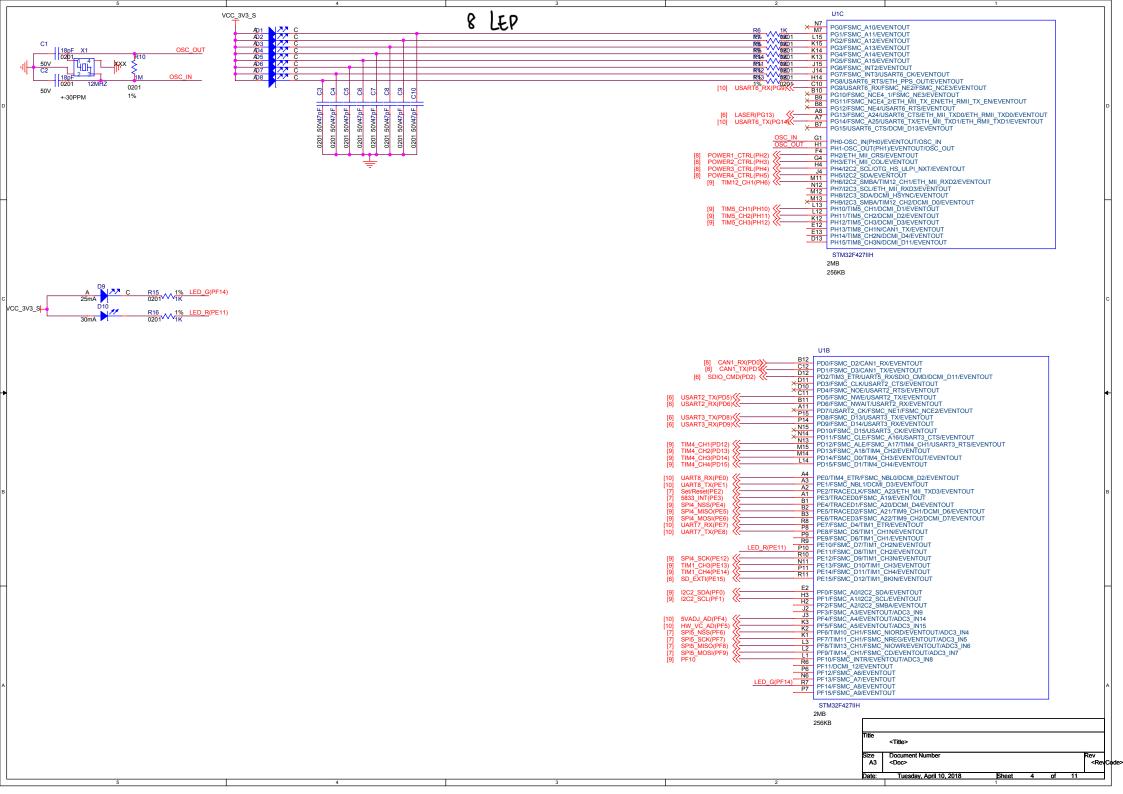


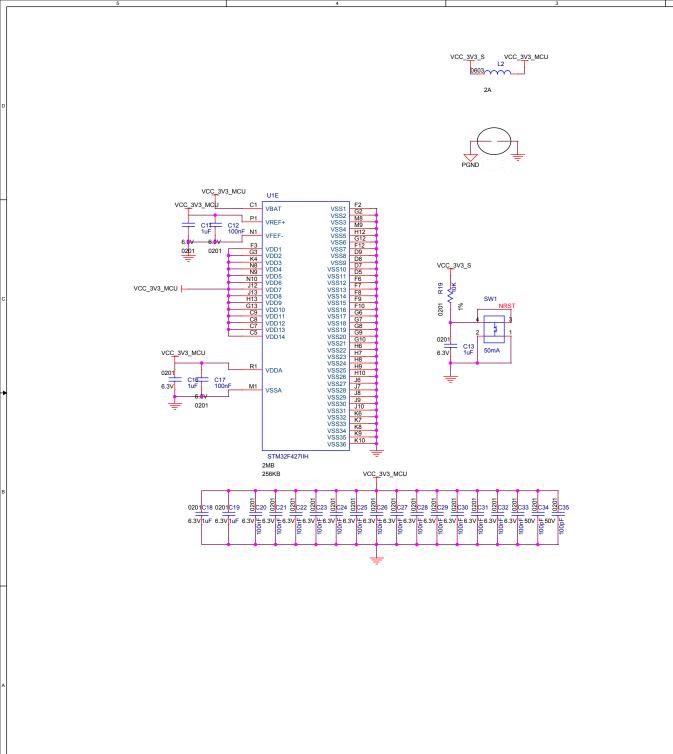
|  | U1A  |
|--|--|
| Section   Sect | PA0_WKUP/USART2_CTS/UART4_TX/ETH_MII_CRS/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/EVENTOUT/ADC123_IN0/WKUP PA1/USART2_RTS/UART4_RX/ETH_RMII_REF_CLE/ETH_MII_RX_CLK/TIM5_CH2/TIM2_CH2/EVENTOUT/ADC123_IN2 PA2/USART2_TX/TIM5_CH3/TIM9_CH3/TIM9_CH3/TIM_DC14/FIXTM_DC14/EXTM_DC14/EXTM_DC14/EXTM_DC14/EXTM_DC123_IN3 PA3/USART2_RX/TIM5_CH3/TIM9_CH2/TIM2_CH3/CTG_HS_USIP_USIP_USIP_USIP_USIP_USIP_USIP_USI  |
| Part   | PB0/TIM3 CH3/TIM8 CH2N/OTG HS ULPI DI/ETH MII RXD2/TIM1 CH2N/EVENTOUT/ADC12 IN8 PB1/TIM3 CH3/TIM8 CH3N/OTG HS_ULPI DI/ETH MII RXD3/OTG_HS_INT/TIM1 CH3N/EVENTOUT/ADC12 IN9 PB2/PB2 B0071/EVENTOUT PB3/JTD0/TRACESWO/JTD0/TRACESWO/SPI3, SCKI/2S3_CK/TIM2_CH2/SPI1_SCK/EVENTOUT PB3/JTD0/TRACESWO/JTD0/TRACESWO/SPI3, SCKI/2S3_CK/TIM2_CH2/SPI1_SCK/EVENTOUT PB4/MITRST/MITRST/SPI3_MSO/TIM3_CH1/SPI1_MISO/IZS3ext_SD/EVENTOUT PB4/JTD1/TRACESWO/JTD0/TRACESWO/SPI3_MSO/TIM2_CH2/SPI1_MOS/SPI3_MOS/DCMI_D10/I2S3_SD/EVENTOUT PB4/JT1/TRACENTED/TRACESWO/JTD0/TIM2_CH2/SPI1_MOS/SPI3_MOS/DCMI_D10/I2S3_SD/EVENTOUT PB4/B1/C1_SDA/FSM6_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2/EVENTOUT PB4/B1/C1_SDA/FSM6_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2/EVENTOUT PB4/B1/M1-CH3/SDI0_D4/TIM10_CH1/DCMI_D6/ETH_MII TX_D1/C1_SDA/CAN1_TX/EVENTOUT PB4/B1/M1-CH3/SDI0_D4/TIM10_CH1/DCMI_D6/ETH_MII TX_D1/C1_SDA/CAN1_TX/EVENTOUT PB4/SPI2_NS/I/SSZ_VS/TIM4_CH4/TIM11_CH1/SDI0_D5/DCMI_D7/I/C1_SDA/CAN1_TX/EVENTOUT PB1/SPI2_SCK/I/SSZ_CK/I/SZ_CK/I/SSZ_CK/I |
| SDIO_DK(PC12)   SDIO_CK(PC12)   SDIO_CK(PC12 | PC0/OTG_HS_ULPI_STP/EVENTOUT/ADC123_IN10 PC1/ETH_MDC/EVENTOUT/ADC123_IN11 PC2/ETH_MDC/EVENTOUT/ADC123_IN11 PC2/SPI2_MISO/OTG_HS_ULPI_DIR7H_MII_TXD2/I2S2ext_SD/EVENTOUT/ADC123_IN12 PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_DIR7H_MII_TX_CLK/EVENTOUT/ADC123_IN13 PC2/ETH_RMII_RX_DISETH_MII_RX_DISEVENTOUT/ADC12_IN14 PC5/ETH_RMII_RX_DISETH_MII_RX_DISEVENTOUT/ADC12_IN15 PC6/IIS2_MCK/TIMB_CH2/ISDIO_DG/IUSART6_RX/IDCM_D0TIMS_CH4/EVENTOUT PC6/IIS2_MCK/TIMB_CH2/ISDIO_DG/IUSART6_RX/IDCM_D1/ITM3_CH2/EVENTOUT PC8/ITM8_CH3/SDIO_DD/IIM3_CH3/USART6_CK/IDCM_D2/EVENTOUT PC8/ITM8_CH3/SDIO_DD/IIM3_CH3/USART6_CK/IDCM_D2/EVENTOUT PC8/IZS_CKIN/MCO2/TIMB_CH4/ISDIO_DT/I/ICS_SDADCMI_D3/TIM3_CH4/EVENTOUT PC1/IU/RATF_RX/SPI5_MISO/SDIO_D3/DCM_D6/IUSART3_RX/ISSSext_SD/EVENTOUT PC1/IU/RATF_RX/SPI5_MISO/SDIO_D3/DCM_D6/IUSART3_RX/ISSSext_SD/EVENTOUT PC1/IU/RATF_TX/SPI5_MISO/SDIO_D3/DCM_D6/IUSART3_RX/ISSSext_SD/EVENTOUT PC13/EVENTOUT/RTC_AFT1 PC14-OSC32_IN/PC14/EVENTOUT/OSC32_IN PC14-OSC32_IN/PC14/EVENTOUT/OSC32_OUT   |

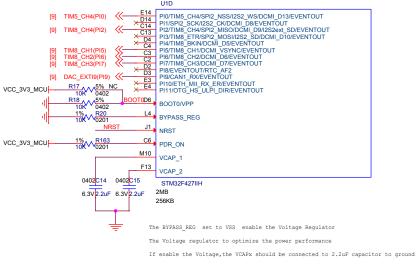
STM32F427IIH 2MB

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<Title> Rev <RevCode> Size Document Number A3 <Doc> Date: Friday, December 15, 2017 Sheet 3 of 11







U1D

change BOOT mode by short NC Register

BOOT SOURCE

User Flash System Memory

BOOT1 BOOT0

<Title> Document Number <Doc> Tuesday, January 02, 2018

