POWER

File: POWER.kicad_sch

USB

UART_RX
UART_TX
ESP_DTR
ESP_RTS

File: USB.kicad_sch

IO_PANEL

GPIO[0..4]
{SDA, SCL, RST}

File: IO_PANEL.kicad_sch

+3.3V
GPIO0
GPIO1
GPIO2
GPIO3
GPIO4
GND
J1
Conn_01x07

SDA
SCL
RST
J4
Conn_01x03

J2
Conn_01x07

R1
10K
R2
10K
J5
Conn_01x03
I2S.SDA
I2S.SCL
I2S.RST

J7
Conn_01x05
RADIO.VOL
RADIO.HI
RADIO.LOW

MCU

ESP_RTS
ESP_DTR
UART_TX
UART_RX
GPIO[0..4]
{LRCK, DIN, BCK, SCK}
I2S{SDA, SCL, RST}
RADIO{VOL, HI, LOW}

File: MCU.kicad_sch

I2S_BUS

DAC_AMP

{LRCK, DIN, BCK, SCK}
XSMT
AMP_SD
AUDIO_SEL
RADIO_AUDIO_IN

VO+
VO−
J3
Conn_01x02

J6
Conn_01x02
D1
SMAJ6.0CA

File: DAC_AMP.kicad_sch

J8
Conn_Coaxial
ANT_IN
C1
15pF
L1
x
C4
39pF
C5
1nF
C7
1nF
C10
6.8pF
Y1
Crystal

U1
LA1185
FM_VCC
VCC
RF_IN
RF_BYPASS
MIX_IN
OSC_MON
OSC
GND
RF_OUT
MIX_OUT

+5V
C2
10uF
C3
100nF
C8
33pF
L2
x
R3
22
L3
x
C11
1nF
C6
15pF
C9
39pF
C12
10nF
ANT_OUT
J9
Conn_01x02

FID1
Fiducial
FID2
Fiducial
FID3
Fiducial

H1
MountingHole_Pad
H2
MountingHole_Pad
H3
MountingHole_Pad
H4
MountingHole_Pad

Sheet: /
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Size: A3 | Date: | **Rev:**
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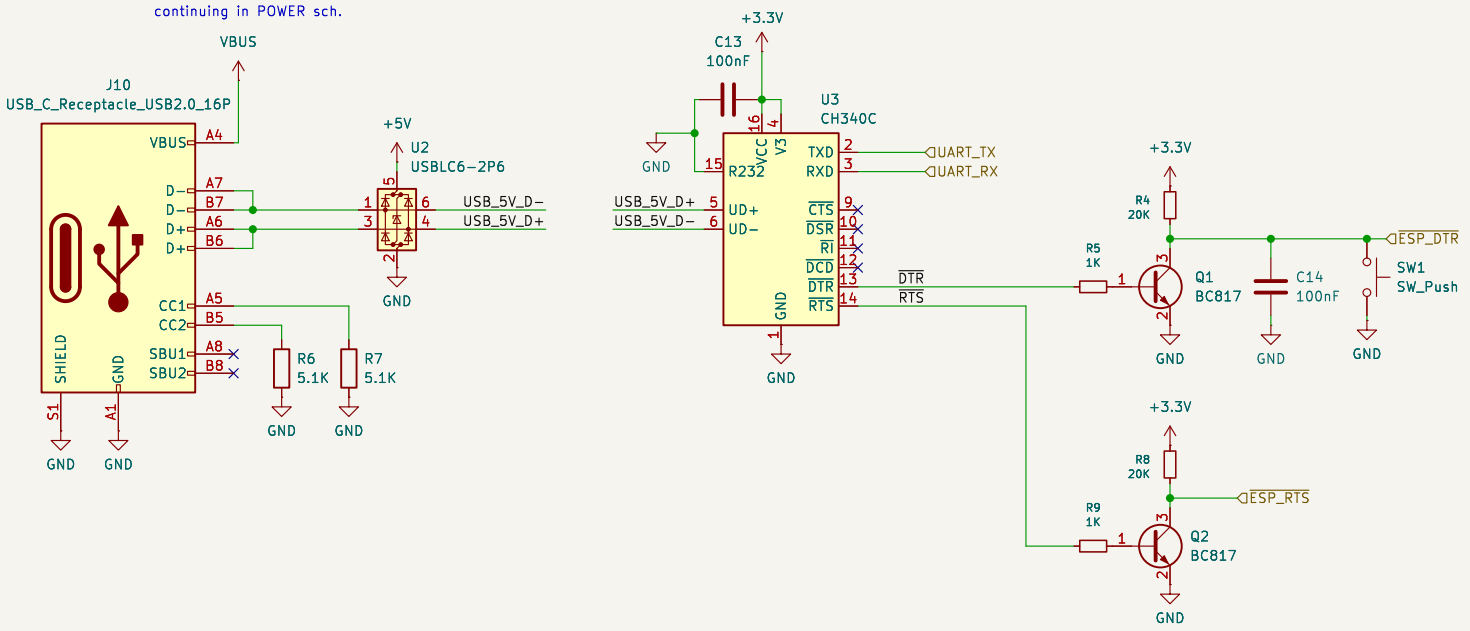

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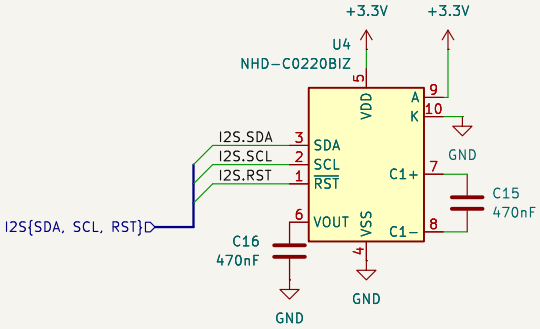

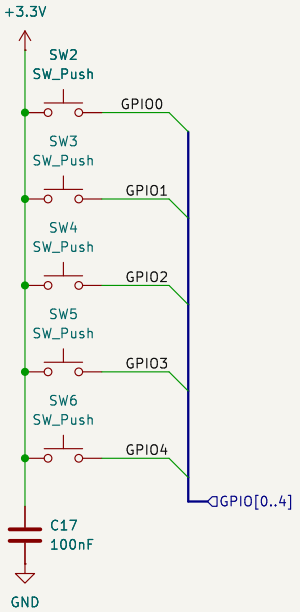

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Title:

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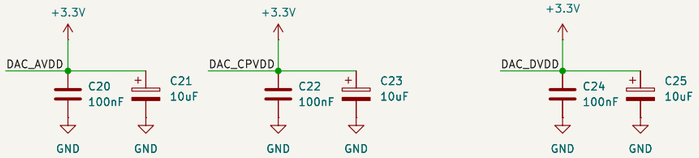
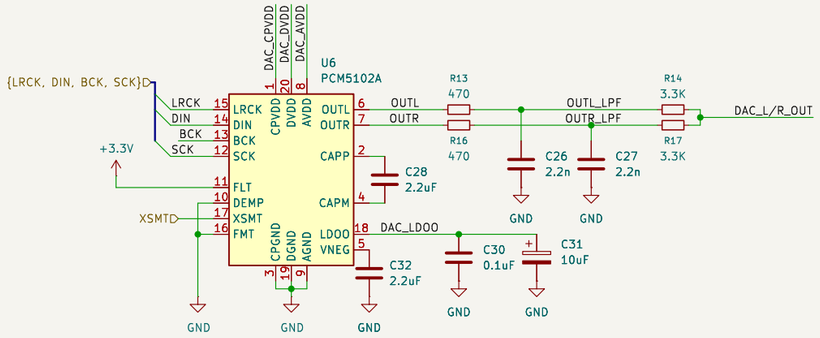
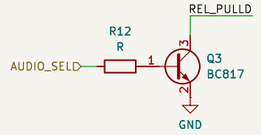
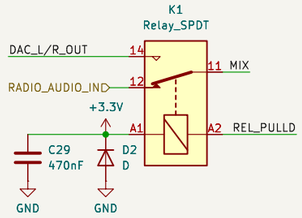
SCK – BCK?

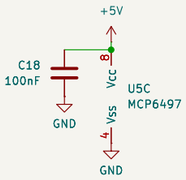
SCK – System clock = sample freq. * 128
BCK – Bit clock / sample freq. = 96KHz

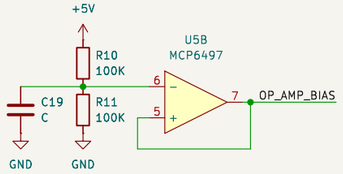


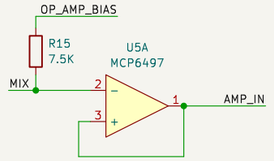


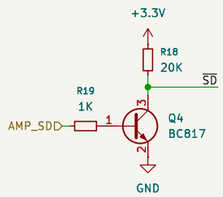


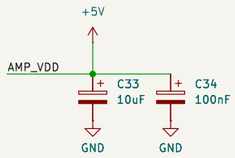


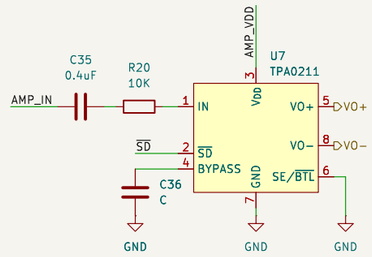












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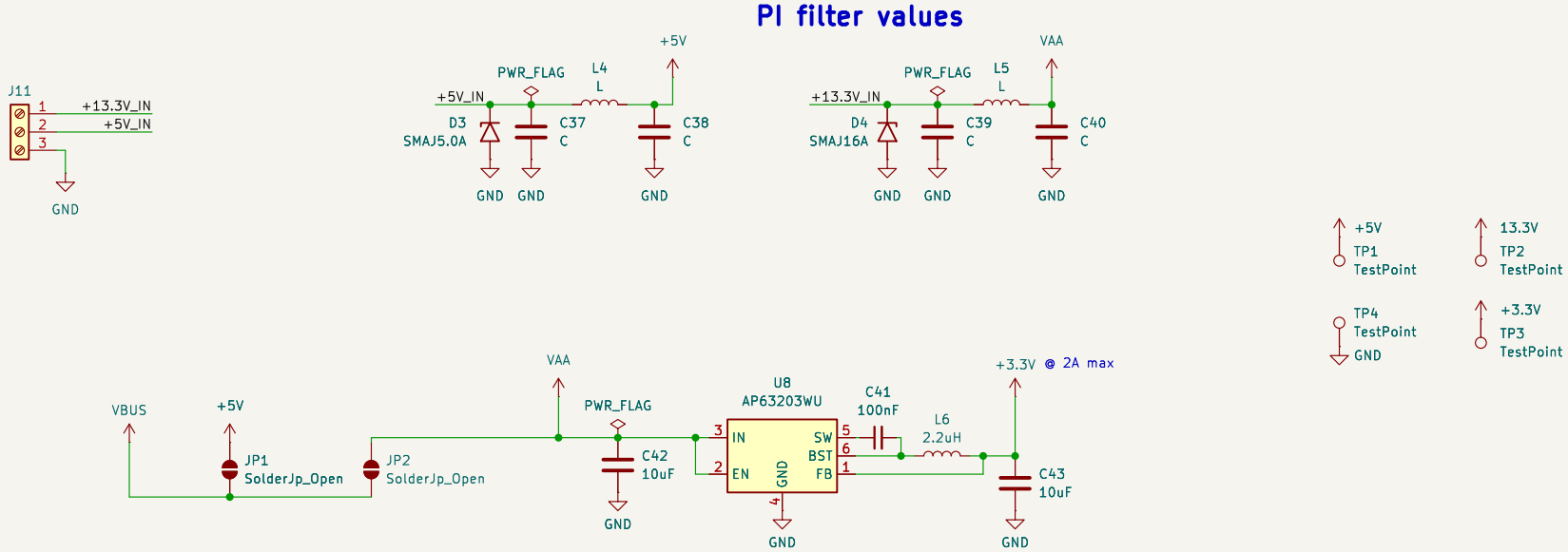

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Size: A4	Date:	**Rev:**
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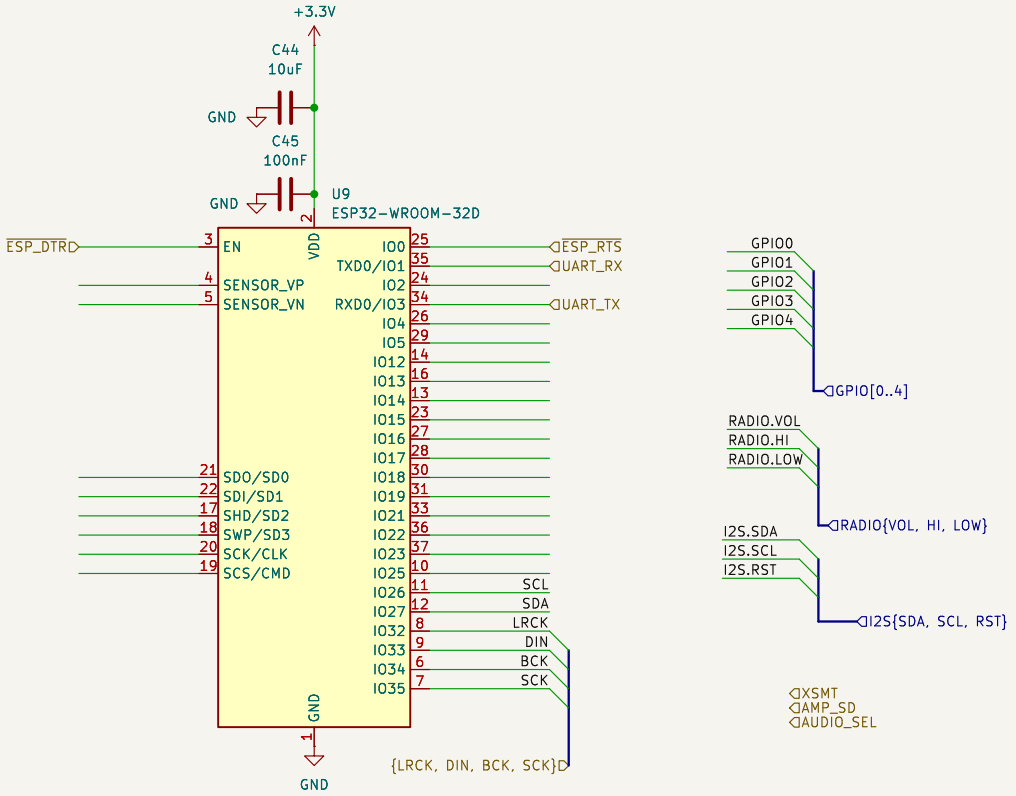

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