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/registers_min_max/din \11100	/registers_min_max/reset	/registers_min_max/clk	/registers_min_max/sel_UU	/registers_min_max/max_out 1000	/registers_min_max/min_out 1000	/registers_min_max/reg_out 0000	/registers_min_max/r0_1	/registers_min_max/r1 1000	/registers_min_max/r2 1000	/registers_min_max/r3_1000	/registers_min_max/min_comb	/registers_min_max/max_comb	·O

Entity:registers_min_max Architecture:true_outputs Date: Fri Nov 17 03:41:55 PM EST 2023 Row: 1 Page: 1