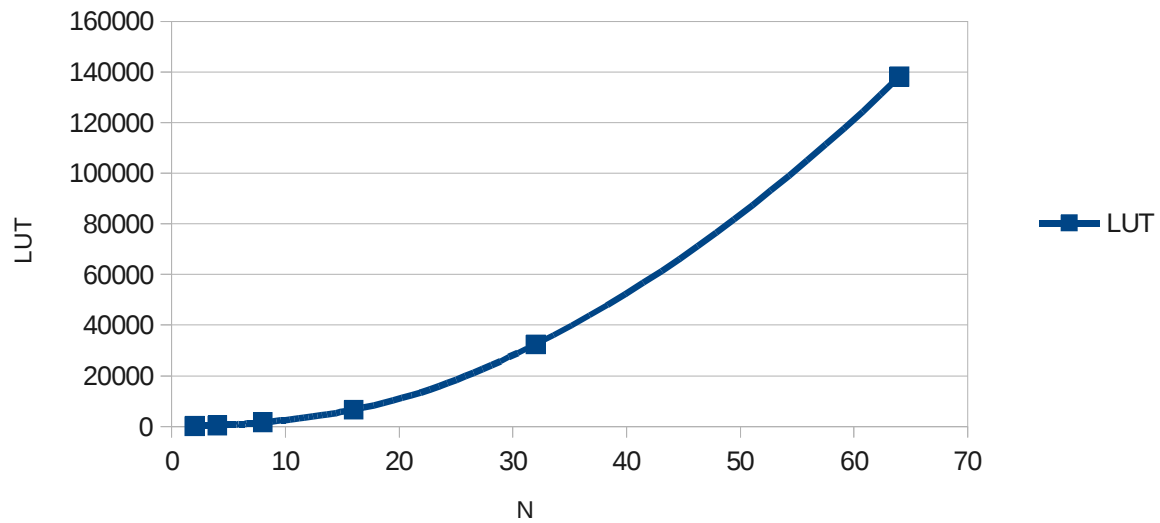


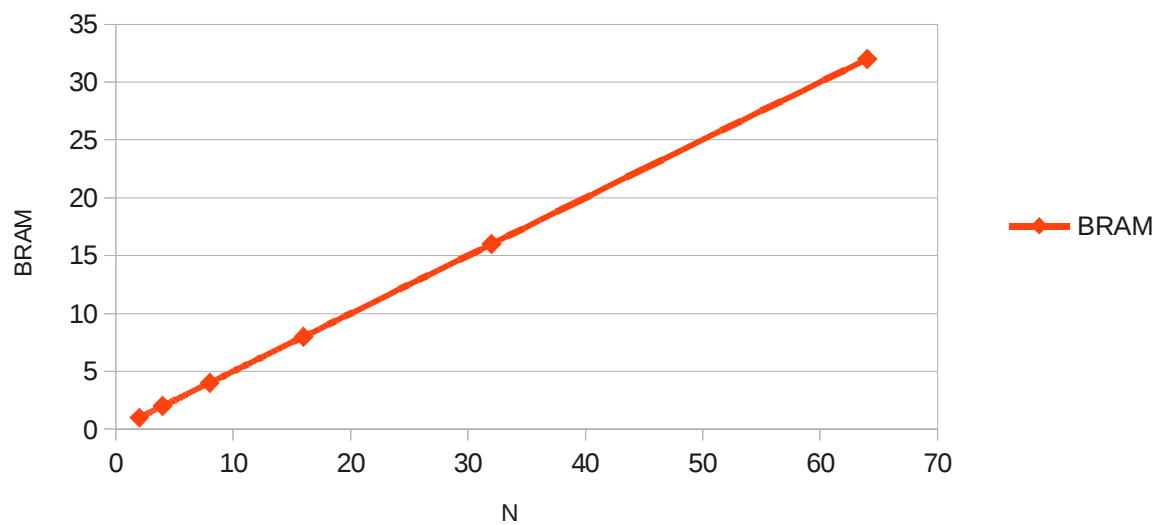
Device utilization

N	LUT	BRAM
2	162	1
4	386	2
8	1617	4
16	6606	8
32	32407	16
64	138162	32

LUT utilization



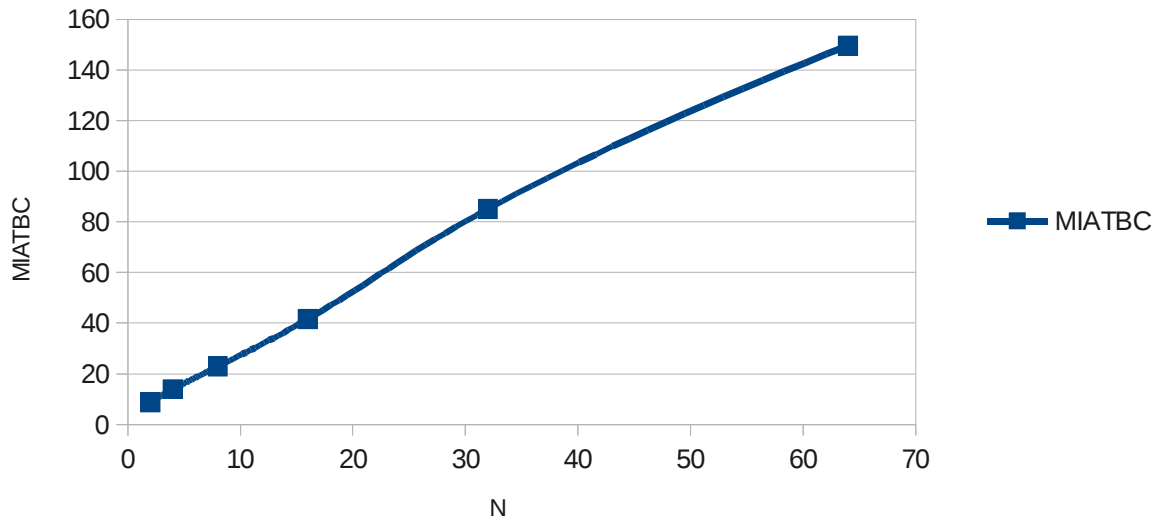
BRAM utilization



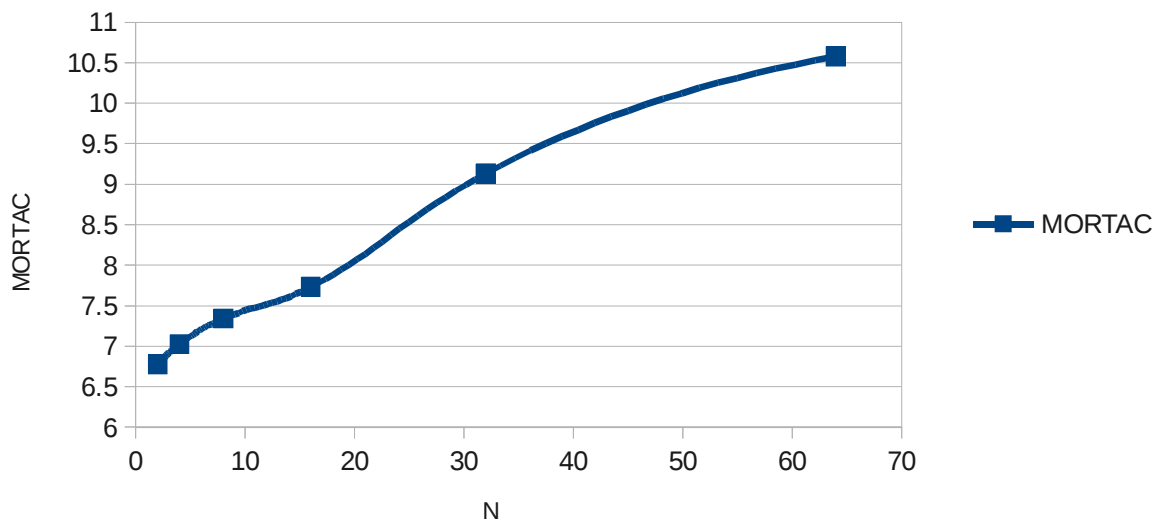
Timing report

N	MIATBC	MORTAC	MACPD
2	8.703	6.777	11.215
4	13.917	7.027	18.496
8	22.897	7.342	29.363
16	41.666	7.732	49.688
32	85.102	9.13	82.3
64	149.616	10.577	159.727

Minimum input arrival time before clock (ns)

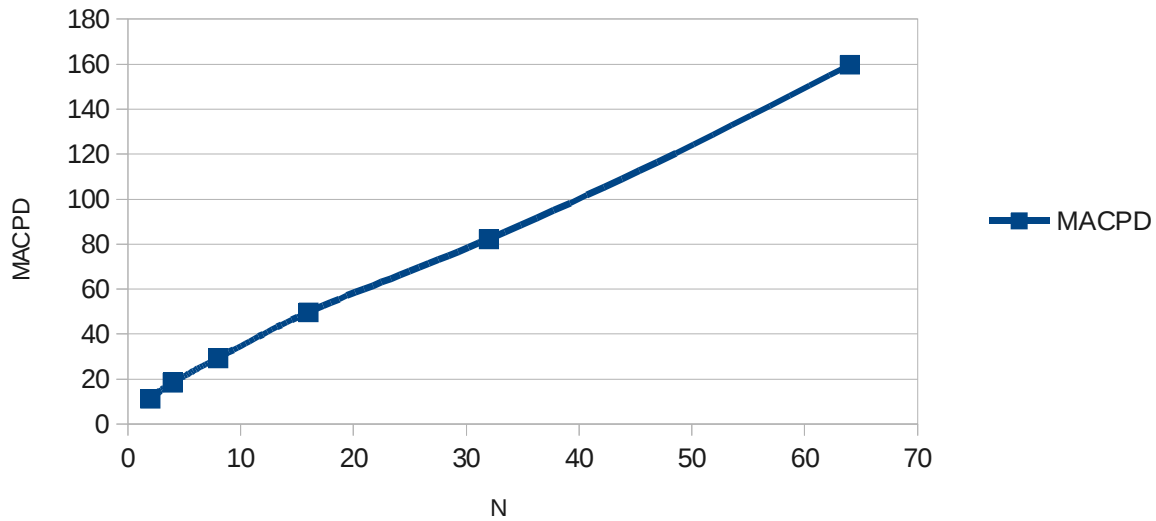


Maximum output required time after clock (ns)



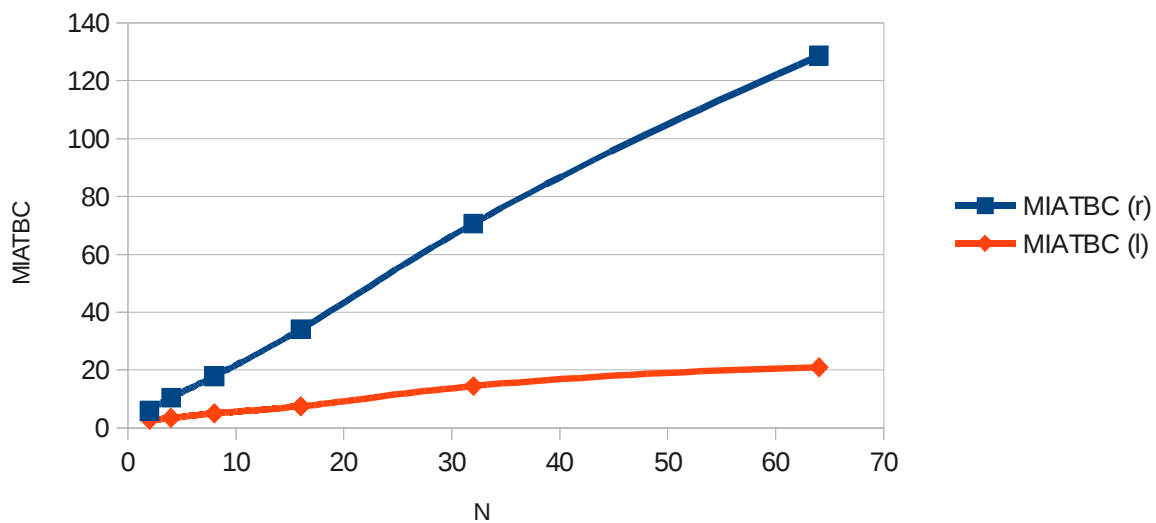
Timing report

Maximum combinational path delay (ns)



N	MIATBC (%r)	MIATBC (r)	MIATBC (l)	MORTAC (%r)	MORTAC (r)	MORTAC (l)
2	68	5.91804	2.78496	22	4.60836	2.16864
4	75	10.43775	3.47925	25	5.27025	1.75675
8	78	17.85966	5.03734	26	5.72676	1.61524
16	82	34.16612	7.49988	27	6.34024	1.39176
32	83	70.63466	14.46734	37	7.5779	1.5521
64	86	128.66976	20.94624	45	9.09622	1.48078

Minimum input arrival time before clock (ns)



Timing report

Maximum output required time after clock (ns)

