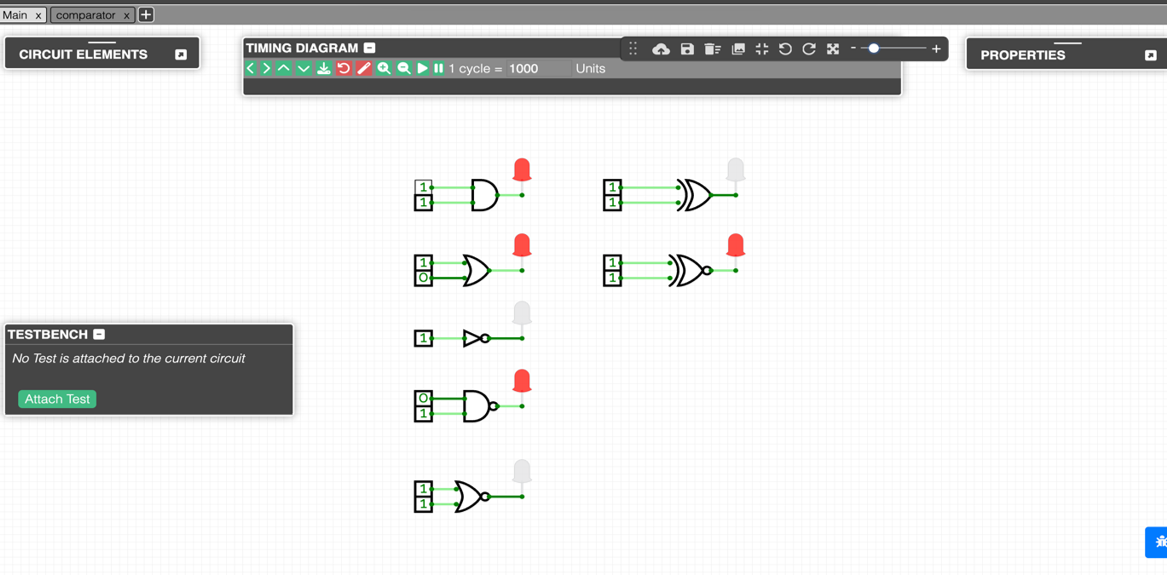
**Name: Shreyans Tatiya**

**Roll no: 16010123325**

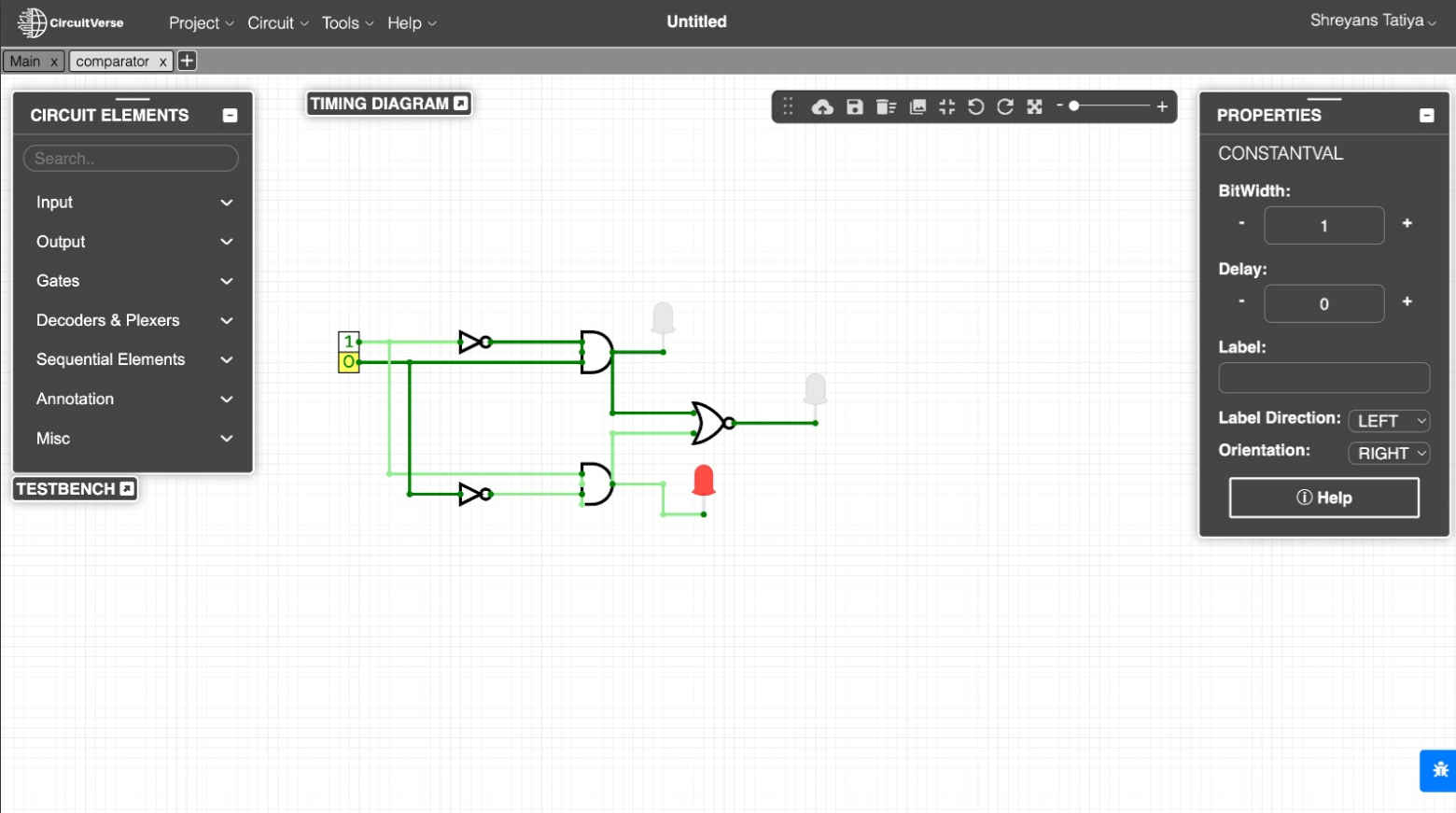
**Batch: E-2**

**Assignment: Circuit Verse**

**Basic Logic Gates [AND, OR, NOT, NAND, NOR, XOR, XNOR]**

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**2 Bit Comparator**

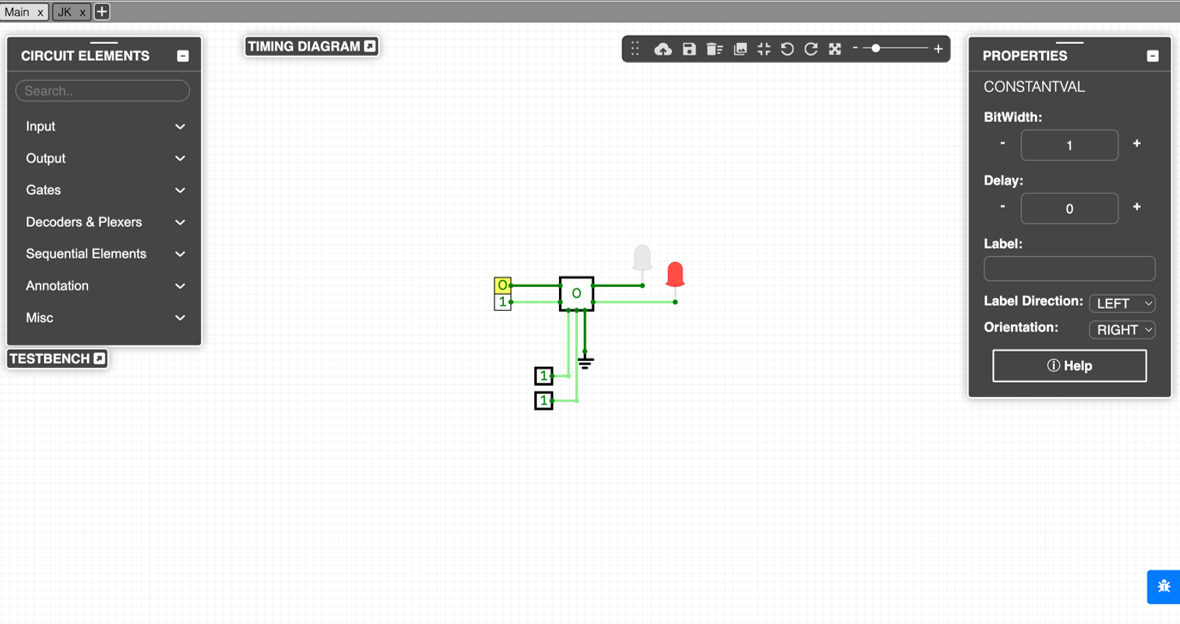
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**JK F/F**

**A screenshot of a computer

Description automatically generated**

**SR**

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