

Unit - 1 & Unit - 2

$R_L \rightarrow$ Load Resistor

$V_m \rightarrow$ Max Voltage [Graphical]

$I_m \rightarrow$ Max Current [Graphical]

$V_k \rightarrow$ Knee Voltage

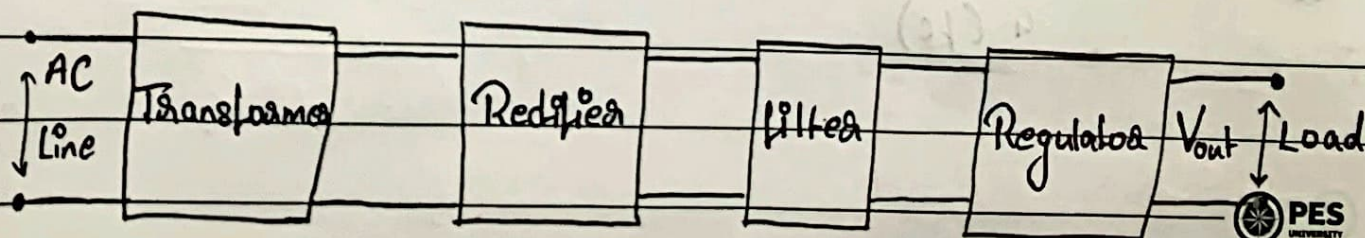
$R_D \rightarrow$ Diode Resistance.

DATE

Comparison b/w Rectified Circuits:

Parameter Name	Half-Wave Rectified	Center-Tapped Full Wave Rect.	Bridged Rectified
V_{DC} [DC Voltage]	$\frac{(V_m - V_k)}{\pi}$	$\frac{2(V_m - V_k)}{\pi}$	$\frac{2V_m - 2V_k}{\pi}$
I_{DC} [DC Current]	$\frac{I_m}{\pi}$	$\frac{2I_m}{\pi}$	$\frac{2I_m}{\pi}$
V_{RMS} [RMS Voltage after rectification]	$\frac{V_m}{2}$	$\frac{V_m}{\sqrt{2}}$	$\frac{V_m}{\sqrt{2}}$
I_{RMS} [RMS Current after rectification]	$\frac{I_m}{2}$	$\frac{I_m}{\sqrt{2}}$	$\frac{I_m}{\sqrt{2}}$
η [Efficiency]	$40.6 \frac{R_L}{(R_D + R_L)}$	$81.2 \frac{R_L}{(R_D + R_L)}$	$81.2 \frac{R_L}{(R_D + R_L)}$
γ (Ripple factor)	1.21	0.48	0.48
PIV Rating	$> V_m$	$> 2V_m - V_k$	$> V_m - V_k$

Regulated Power Supply: [Draw Signal Graphs]



Temperature Effects on Diodes:

* Forward Diode [Biased]:

$$\text{Temp} \propto \frac{1}{V_{knee}}$$

$$2.5 \text{ mV}/1^\circ \text{C}$$

* Reverse Diode [Biased]:

$$\text{Temp} \propto I_{\text{saturation}}$$

$$(I_{\text{sat}})_{\text{new}} = (I_{\text{sat}})_{\text{old}} (2)^{\frac{\Delta T}{10}}$$

* Shockley's Diode Current Equation:

$I_D \rightarrow$ Diode Current

$I_{\text{sat}} \rightarrow$ Saturation Current

$V_D \rightarrow$ Diode Voltage

$V_T \rightarrow$ Thermal Voltage

$n \rightarrow$ Ideality factor

$$I_D = (I_{\text{sat}}) \left[e^{\frac{V_D}{n V_T}} - 1 \right]$$

→ Thermal Voltage:

$$V_T = \frac{kT}{q}$$

$k \rightarrow$ Boltzmann's constant $= 1.38 \times 10^{-23} \text{ J/K}$
 $q \rightarrow$ Charge $= 1.6 \times 10^{-19} \text{ C}$

\Rightarrow for non-conducting Silicon = 2
 everything else = 1

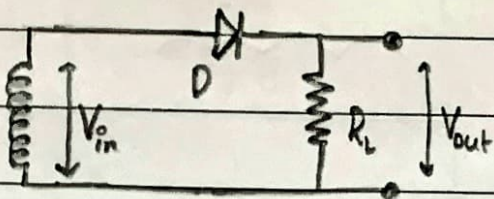
Extra Formulae:

$$\rightarrow \gamma [\text{Ripple factor}] = \frac{(V_{\text{ripple}})_{\text{rms}}}{V_{\text{DC}}} = \frac{I_{\text{DC}}}{4\sqrt{3} (fC)}$$

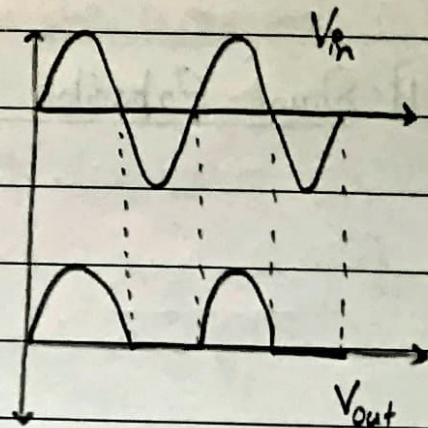
$$\rightarrow V_{\text{DC}} = V_m - \frac{I_{\text{DC}}}{4 (fC)}$$

Rectifiers: [Half-Wave, Center Tapped, Bridged Rectifiers]

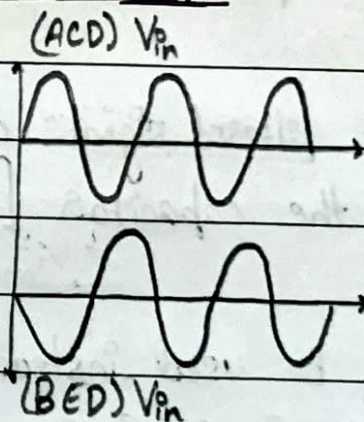
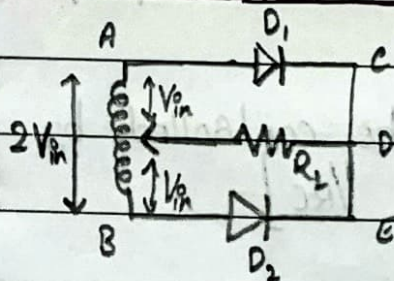
1] Half-Wave Rectifier:



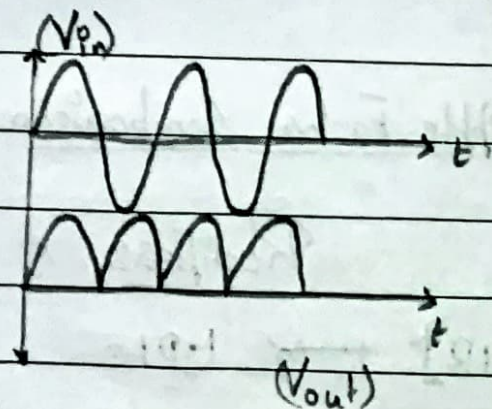
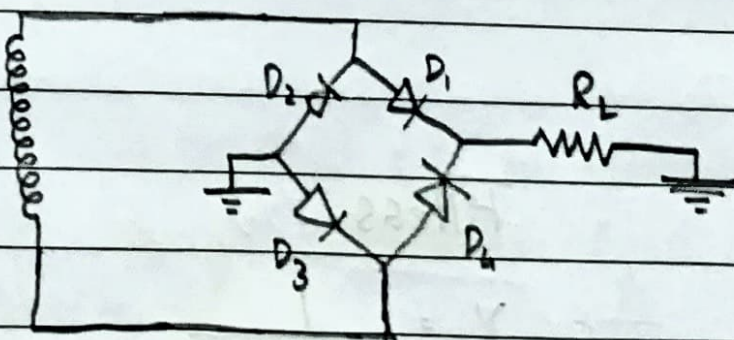
$V_{in} \rightarrow$ Pure AC Signal
 $V_{out} \rightarrow$ Pulsating DC

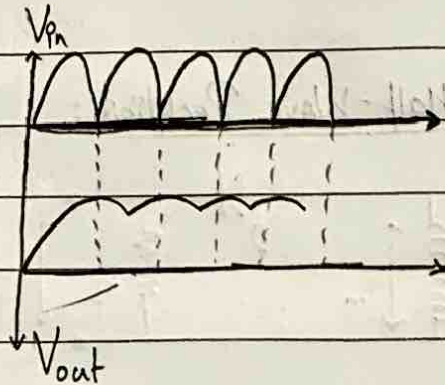
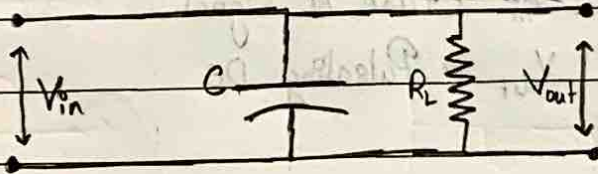


2] Center-Tapped Full-Wave Rectifier:



3] Bridge Rectifier:



Filters:1) Shunt Capacitor Filters: [DC Filter]

- The (V_{out}) Filtered Signal's phase has been shifted while frequency remains the same.
- The 'Ripple amount' in the filtered signal can be controlled by the relaxation time of the capacitor $[\tau = RC]$

Note: An Inductor can be used instead of a capacitor. The inductor is placed in ~~Series~~ Series.
 \Rightarrow Series Inductor Filter

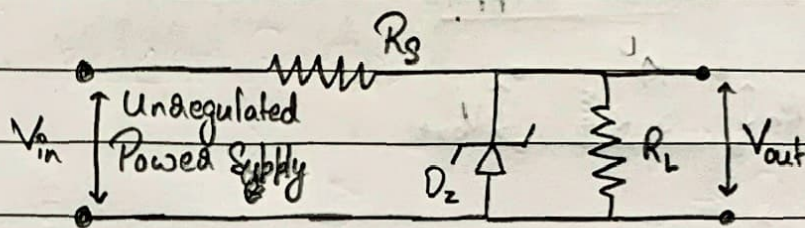
 \Rightarrow Ripple Factor Comparison:Rectifiers (γ)Filters ($\gamma\%$)[HWR] $\rightarrow 1.21$

$$\rightarrow \gamma = \frac{1}{2\sqrt{3}(fL)R_L}$$

[FWR] $\rightarrow 0.48$

$$\rightarrow \gamma = \frac{1}{4\sqrt{3}(fL)R_L}$$

Voltage Regulator [using Zener Diode]



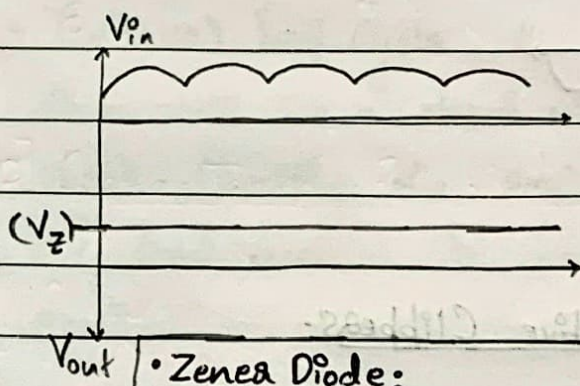
⇒ Conditions to be met:

→ Diode must be R. Biased

→ $V_{in} > V_Z$

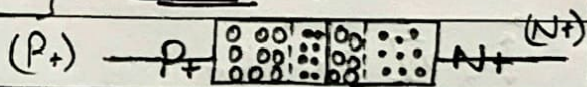
→ $(I_Z)_{min} < I_Z < (I_Z)_{max}$

→ $(V_R)_{max} > V_Z$
(Assume No Diode)

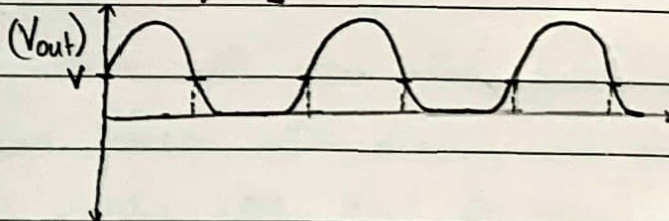
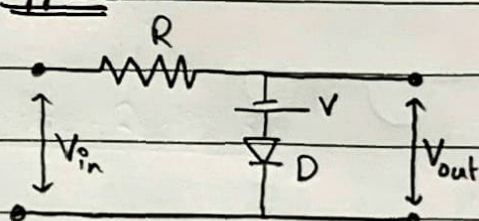


• Zener Diode can accept more current with the same Voltage as it is a non-ohmic Device.

• Zener Diode:



Clippers : [Similar to Half-Wave Rectifier]

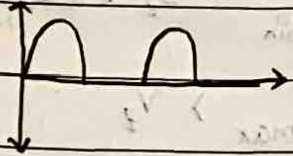
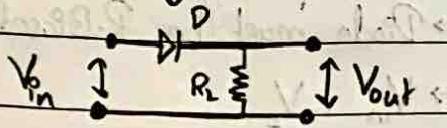


• The Input AC Signal gets an additional DC Voltage 'V'. In the (+ve) half-cycle, both the signal add up to shift the AC signal by 'V'.

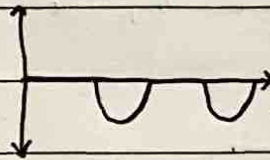
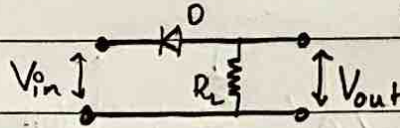
In the (-ve) half-cycle, the DC Voltage keeps the Diode forward biased until the AC signal's voltage is greater than or equal to 'V'.

1) Series Clippers:

(a) Negative Clipper:

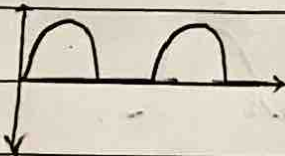
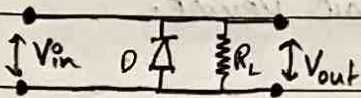


(b) Positive Clipper:

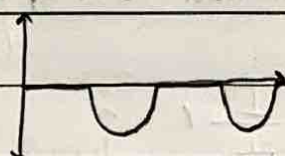
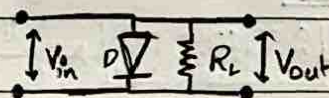


2) Parallel / Shunt Clippers:

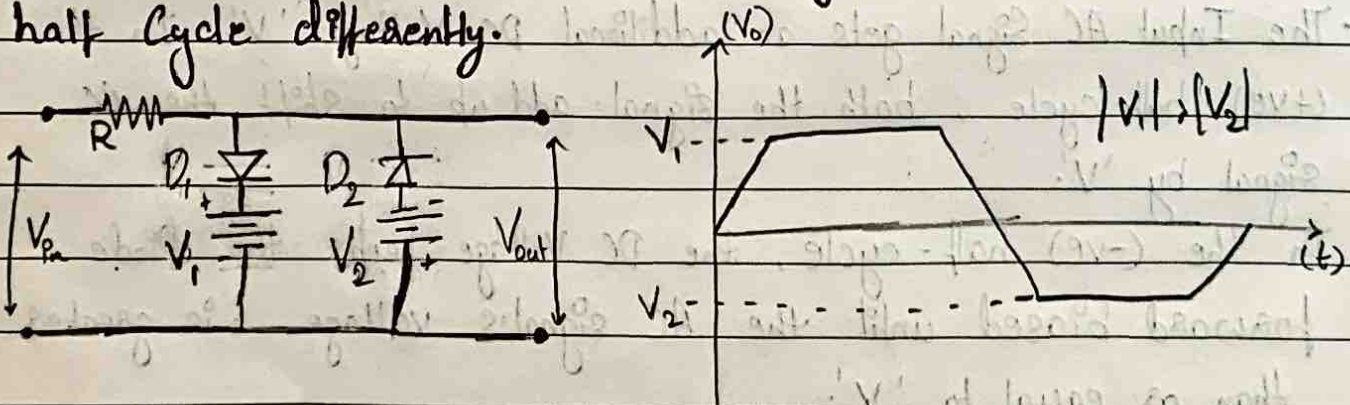
(a) Negative Clipper:



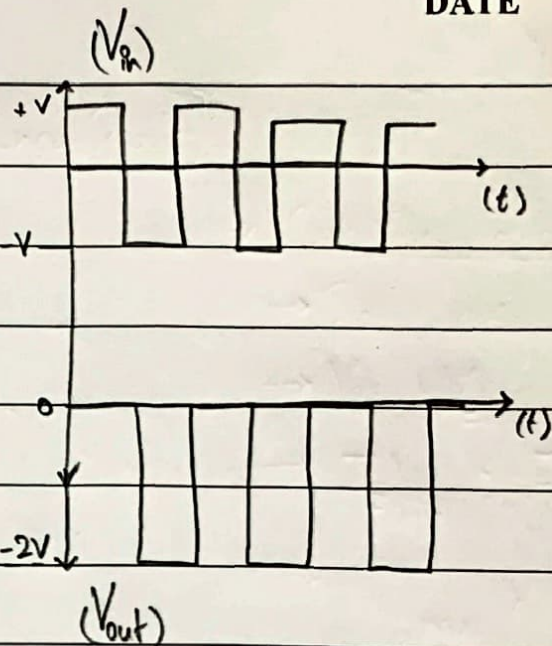
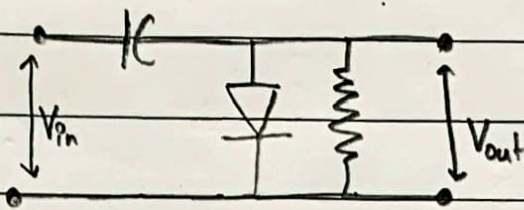
(b) Positive Clipper:



• Clippers can be used to clip the voltages for (+ve) & (-ve) half cycle differently.



• ' V_1 ' is assumed to be greater than ' V_2 '

Clamper:

- The (+ve) half cycle, the diode acts like a closed wire, short-circuiting the resistor & hence the output voltage across the load resistor is zero, while the capacitor charges to reach equilibrium with the source voltage.

→ $Z \Rightarrow 0$, as there is no resistor that is acting in the (+ve) half-cycle. [$Z \rightarrow 0$ [$Z = RC$] as $R = 0$]

- The (-ve) half-cycle, the diode will offer infinite resistance hence we can neglect the branch with diode, and the voltage from the capacitor works with the AC signal.