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# FPGA LAB - Assignment 1

## Priyanka - EE21MTECH12002

Download all c codes from

https://github.com/PeriPriyanka/FPGA\_5811/ Assignment1

and latex-tikz codes from

https://github.com/PeriPriyanka/FPGA\_5811/Assignment1

## 1 Problem

(2015-1-Q.6(a)) Verify the following boolean law:

$$U'+V = U'V'+U'V+UV$$
 (1.0.1)

## 2 Solution

Consider the RHS of the boolean expression given

$$U'V' + U'V + UV$$
 (2.0.1)

$$U'(V'+V) + UV$$
 (2.0.2)

Since V'+V=0, equation (2.0.2) can be written as,

$$U'+UV$$
 (2.0.3)

From the Distributive property of boolean laws,

$$X + X'Y = X + Y$$
 (2.0.4)

Therefore, equation (2.0.3) can be written as,

$$U' + V$$
 (2.0.5)

LHS = RHS, therefore, U'+V = U'V'+U'V+UV.