





# Laurine Leung

 Zionsville, IN  
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Organized, creative, positive, team spirit, independent learner,  
currently enrolled in a web development program to prepare my return in the engineering field.

## Summary

4+ years of experience in the engineering field.

5+ years of experience in the teaching field.

Native French speaker.

Citizen of the United States.

Fluent in spoken and written English.

References and transcripts upon request.

## Work experience

### French Teacher

*L'Ecole Française – Zionsville IN*

*2020/05 – present*

- Teaching a group of middle schoolers. Substituting as needed in elementary schools as well.

*Bon Voyage World Languages Academy (BVWLA) – Online*

*2018/06 – 2019/12*

- Created and lead the French immersion programs whose goal was to maintain and to pursue French knowledge as dispensed in France, following the curriculums of the French Board of Education.

*Education Française Greater Seattle (EFGSeattle) – Seattle WA*

*2015/09 – 2016/05*

- Taught a group of 4- to 6- year- old French-speaking children attending American schools.

### French teacher Assistant

*Education Française Greater Seattle (EFGSeattle) – Seattle WA*

*2013/10 – 2015/06*

- Helped and supported the teacher in educating 5- to 12- year- old children in French.
- Worked with students, individually and in group, to support their learning and enjoyment in French.

### Department Chair for European Languages

*Bon Voyage World Languages Academy (BVWLA) – Online*

*2019/06 – 2019/12*

- Worked closely and cooperatively with other teachers in the department and the director to maintain, implement, and align instruction.
- Assisted with the development of the school and working with department on ideas for promoting particular languages.
- Assisted new teachers in the department with technology questions and training.
- Supervised teachers at least once per semester to make sure they are teaching using the school's method. Work with the teachers not meeting our standards or using our methods to improve.
- Assisted in the interview and hiring process of new teachers.
- Worked with administration to develop a budget for the department and maintaining the budget.

### Test Engineer

*Dynon Avionics – Woodinville WA*

*2013/06 – 2014/09*

- Developed new C# test procedures and test softwares for the company's products and equipment.
- Was responsible of Dynon testing equipment
  - o Maintained testing chambers
  - o Updated testing software codes
  - o Troubleshoot chambers, testing stations
  - o Improved testing stations (connectors, materials, cables, etc)
  - o Trained production technicians to new testing equipment.

- Was part of the electromechanical team, worked closely with the IT/SW teams and the production teams.

### **Applications Engineer**

*Dynon Avionics – Woodinville WA*

*2012/01 – 2013/06*

- Developed and documented embedded software C# applications and tests for radios.
- Designed automated scripts allowing the production team to perform a series of tests on the devices.
- Assembled and tested radio prototypes to provide assembly and calibration procedure instructions to the production team.

### **Applications Engineer**

*Cypress Semiconductor – Europe*

*2010/03 – 2011/12*

- Worked as part of a multi-national/multi-site team in the Automotive and Consumer industries.
- Provided design support and debug for customers and Cypress FAEs in Western European area.
- Developed, documented and implemented embedded solutions using Cypress PSoC microcontrollers.
- Created presentations and trainings for customers and new Cypress personnel on PSoC products.

### **Mixed-Signal Designer (Internship)**

*IMS Chips – Germany*

*2009/02 – 2009/08*

- Worked in ASIC department.
- A/D and D/C converters development for the IMS mixed signal structured ASIC
- Developed an A/D converter in CMOS technology 0.5µm.
  - o Analog design and simulation of the converter's elements: Comparator, Differential pair amplifier, R-2R ladder digital-to-analog converter, Sample/Hold
  - o Digital design of the internal logic of the ADC as well as control of the clock in VHDL
  - o Layout of each element of the ADC

## **Education**

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### **Web Development Certification**

*Eleven Fifty Academy – IN, USA*

*present*

Immersive learning program taught with industry guided curriculum, real-world project based learning and 500+ hours of logged coding time and training.

### **Diplôme d'Aptitude à l'Enseignement du Français Langue Étrangère (DAEFLE)**

*Alliance Française de Paris and Centre National d'Education à Distance*

*2015/06*

Major in teaching French as a Foreign Language (FLE) to children.

### **Masters Degree in Engineering**

*Grenoble National Institute of Technology – France*

*2009/06*

Major in micro- and nano-technologies for integrated systems. GPA: 3.65

Enrolled in a tri degree program where part of the study was also completed at Polytechnic of Turin (Italy) and Swiss Federal Institute of Technology (Switzerland).

### **Bachelors Degree in Engineering Science**

*Grenoble National Institute of Technology – France*

*2007/06*