

Peter J. Pupalaikis

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TECHNICAL EXECUTIVE WITH ACCOMPLISHED DESIGN ENGINEERING EXPERIENCE

A highly versatile engineer and executive who has worked in design, marketing, management and research and development positions. Possess demonstrated capability in delivering on urgent project schedules, strategic planning, inventing, commercializing, researching and developing new ideas to solve a wide variety of problems both technical and business related.

Professional Experience

- Teledyne LeCroy, Chestnut Ridge, NY** Sep 1995 - present
VICE PRESIDENT – TECHNOLOGY DEVELOPMENT Jul 2011 - present
- Manage multi-million dollar R&D budget and personnel in the development of high-speed analog and digital integrated circuits.
 - Manage the development of signal processing technology.
 - Manage intellectual property and other technology development.
- VICE PRESIDENT – PRINCIPAL TECHNOLOGIST** Mar 2009 - Jul 2011
- Managed a team of engineers, marketing and production professionals in an entrepreneurial startup-like group in the development of the SPARQ, a new class of signal-integrity measurement instrumentation. (launched Oct 2010)
 - Led and performed product conception, business case analysis, architecture, design, product requirement and feature definition, strategic partner negotiations, customer visits, algorithm and software development, product positioning and marketing, sales training and manufacturing process development.
- VICE PRESIDENT – STRATEGIC MARKETING** Feb 2008 – Mar 2009
- Managed the product marketing organization for the oscilloscope division, specifically the interface between sales and marketing communications and engineering while representing the marketing organization among the executive team.
 - Responsible for the product road map and all inbound marketing activities including product requirements definition and planning for a \$150M revenue stream.
 - Managed the performance of competitive win/loss analysis, strategy development and execution, business case analysis, technical feasibility, business objectives performance and P&L management.
- MANAGER – SERIAL DATA & SIGNAL INTEGRITY SOLUTIONS** Sep 2007 – Feb 2008
- Managed the engineering and marketing organization in a product group responsible for delivering measurement solutions such as PCI-Express, HDMI, SAS, SATA, UWB, WiMax compliance and debugging tests and jitter analysis and signal integrity measurements.
- PRINCIPAL TECHNOLOGIST** Apr 2004 – Aug 2008
- Worked directly for the Chief Technology Officer in the research and development of new technology in the areas of digital signal processing, RF and microwave technologies and high speed digitizers.
 - Researched and developed advanced tools for high speed serial data measurements such as adaptive equalizer emulation and optimization, s-parameter calculation and system co-simulation, jitter analysis and bit-error-rate testing.
 - Program manager, chief architect and inventor of key technologies for high-speed real-time serial data analyzers including the highest speed real-time analyzer in the world. (Mar 2005, May 2006)
 - Earned the LeCroy Extraordinary Contributor award of \$100K, 6 month research grant and sabbatical (2004 and 2005).
- PRODUCT MARKETING MANAGER - WAVEMASTER OSCILLOSCOPES** Nov 2002 – Apr 2004
- Product Manager for LeCroy's highest performance digital oscilloscope and probes line (product line annual revenues approximately \$35M).
- CHIEF ENGINEER - DSP TECHNOLOGY AND BUSINESS DEVELOPMENT** Dec 2000 - Nov 2002
- Researched and developed digital signal processing technology for digital magnitude and group delay compensation, bandwidth extension, digital phase-locked loops, jitter analysis, time-domain reflectometry and signal fidelity improvement of oscilloscope channels.
- MANAGER - ACQUISITION SYSTEMS SOFTWARE GROUP** Jun 1998 - Dec 2000
- Managed a small team of software engineers in the development of software to control and calibrate the waveform digitizing and acquisition system.
 - Developed the WavePro 960 Oscilloscope (Oct 2000).
 - Lead software engineer in joint development with Japanese test and measurement company (Jan 1999).
- SENIOR PRODUCT DEVELOPMENT ENGINEER** Sep 1995 - Jun 1998
- Developed vertical market software for data storage and optical recording industries.
 - Performed mathematical and statistical analysis of calibration and measurement accuracy.
 - Wrote embedded software in C and C++.

Profesional Experience Cont'd

Honeywell Industrial Automation and Controls, Ft. Washington, PA Aug 1991 - Sep 1995

SENIOR DEVELOPMENT ENGINEER

- Designed and developed electronic hardware and software for industrial measurement instruments and control systems including temperature, pressure and flow measurement and metering devices.
- Wrote embedded real-time software in C and 68HC11 assembly language.
- Lead software engineer in joint product developments with foreign and domestic companies in the development of industrial instrumentation.

Independent Contractor, Philadelphia, PA

May 1986 - Aug 1991

SOLE PROPRIETOR – CONSULTING AND CONTRACTING ENGINEER

- Sole proprietor of independent embedded systems development company
- Designed embedded software for traffic control systems for New York City and Norfolk, VA.
- Designed hardware and software for utility meter test systems and power line analyzers.
- Designed and developed electronic control systems.

Education

University of Pennsylvania - The Wharton School, Philadelphia, PA

Sep 2007 - Oct 2007

ADVANCED MANAGEMENT PROGRAM

Rutgers University, College of Engineering, New Brunswick, NJ

Sep 1984 - Jun 1988

BACHELOR OF SCIENCE, ELECTRICAL ENGINEERING (*magna cum laude*)

- Elected to Tau Beta Pi and Eta Kappa Nu Engineering Honors Societies.

Active Military Service

The United States Army, Seneca Depot, Romulus, NY - two year enlistment

Sep 1982 - Sep 1984

SPECIALIST FOUR (*honorable discharge*)

- Served the depot mission in the deployment of the Pershing II tactical nuclear missile.
- Sharpshooter (M16 Rifle), Army Achievement and Good Conduct Medal, and Cold War Certificate.

Honors and Awards

Institute for Electrical and Electronic Engineers (IEEE)

Jan 2013

IEEE FELLOW

- for contributions to high-speed waveform digitizing Instruments

Patents

43 US Patents with many matching foreign patents issued in the area of measurement instrument design.

Key inventor including continuations and continuations-in-part for a key technology called Digital Bandwidth Interleaving (DBI), a microwave/DSP technology. This technology has enabled LeCroy to produce industry-leading bandwidth oscilloscopes including the latest record of 100 GHz, 240 GS/s digitizer channels.

9,660,661 - High Bandwidth Oscilloscope (continuation)	May 2017
9,366,743 - Time-Domain Network Analyzer	Jun 2016
9,325,342 - High Bandwidth Oscilloscope (continuation)	Apr 2016
9,231,608 - Method and Apparatus for Correction of Time Interleaved ADCs	Jan 2016
9,194,930 - Method for De-embedding in Network Analysis	Nov 2015
8,843,335 - Wavelet Denoising for Time-domain Network Analysis	Sep 2014
8,706,438 - Time-Domain Network Analyzer	Apr 2014
8,706,433 - Time-Domain Reflectometry Step to S-parameter Conversion	Apr 2014
8,659,315 - Method for Printed Circuit Board Trace Characterization	Feb 2014
8,583,390 - High Bandwidth Oscilloscope (continuation)	Nov 2013
8,566,058 - Method for De-embedding Device Measurements	Oct 2013
8,532,953 - Method for Multiple Trigger Path Triggering	Sep 2013
8,390,300 - Time-Domain Reflectometry in a Coherent Interleaved Sampling Timebase	Mar 2013
8,386,208 - Method and Apparatus for Trigger Scanning	Feb 2013
8,190,392 - Method and Apparatus for Multiple Trigger Path Triggering	May 2012
8,170,820 - Virtual Probing (continuation)	May 2012
8,073,656 - High Bandwidth Oscilloscope (continuation)	Dec 2011
7,865,319 - Fixture De-embedding Method	Jan 2011
7,711,510 - Method for Crossover Phase Correction When Combining Frequency Bands	May 2010

Patents Cont'd

7,660,685 - Virtual Probing and Probe Compensation	Feb 2010
7,653,514 - High Bandwidth Oscilloscope (continuation)	Jan 2010
7,535,394 - High Speed Arbitrary Waveform Generator	May 2009
7,519,513 - High Bandwidth Real-time Oscilloscope (continuation)	Apr 2009
7,450,043 - Method for Jitter Compensation for Systems of Interleaved Digitizers	Nov 2008
7,434,113 + 7,437,624 + 7,634,693 - Method and Apparatus for Analyzing Serial Bit Streams	Oct 2008
7,386,409 - Method for Treating Spurs in Systems of Mismatching Interleaved Digitizers	Jun 2008
7,373,281 - High Bandwidth Real-time Oscilloscope (continuation)	May 2008
7,222,055 - High Bandwidth Real-time Oscilloscope (continuation)	May 2007
7,219,037 - Digital Heterodyning Oscilloscope	May 2007
7,139,684 - High Bandwidth Real-time Oscilloscope (continuation)	Nov 2006
7,058,548 - High Bandwidth Real-time Oscilloscope	Jun 2006
7,050,918 - Digital Group Delay Compensator	May 2006
6,819,279 - Method and Apparatus for Matching Digitizers in Interleaved Systems	Nov 2004
6,701,335 + RE40,802 + RE39,693 - Digital Frequency Response Compensation System	Mar 2004
6,567,030 - Sample Synthesis for Matching Digitizers In Interleaved Systems	May 2003
6,542,914 + RE42,809 - Method and Apparatus for Increasing Bandwidth In Sampled Systems	Apr 2003
6,112,160 - Optical Recording Measurement Package	Aug 2000
5,754,452 - Method and Apparatus for Increasing Update Rates In Measurement Instruments	Jul 1995

Books

Co-author (with K. Doshi) of Chapter 11, "TDR-Based S-Parameters" of:

- V. Teppati, A. Ferrero & M. Sayed, "Modern RF and Microwave Measurement Techniques", ISBN:9781107036413, Cambridge University Press, 2013.

Published Papers

- P. Pupalaikis, "Understanding Vertical Resolution in Oscilloscopes", DesignCon, Santa Clara CA, 2017.
- I. Novak & P. Pupalaikis, "A Generic Test Tool for Power Distribution Networks", DesignCon, Santa Clara CA, 2017.
- P. Pupalaikis et al., "The Fastest PAM-4 Signal Ever Generated", DesignCon, Santa Clara CA, 2017.
- P. Pupalaikis et al., "Technologies for Very High Speed Real-time Oscilloscopes", Proceedings of the Bipolar/BiCMOS Circuits and Technology Meeting, 2014, invited paper.
- V. Dmitriev-Zdorov, K. Doshi & P. Pupalaikis, "Computation of Time Domain Impedance Profile from S-parameters: Challenges and Methods", DesignCon, Santa Clara CA, 2014.
- P. Pupalaikis & K. Doshi, "A Fast and Inexpensive Method for PCB Trace Characterization in Production Environments", DesignCon, Santa Clara CA, 2013.
- P. Pupalaikis, "A Patent Application Design Flow in LaTeX and LyX", TUGboat 33(3), 276-281, 2012
- P. Pupalaikis, "The Relationship Between Discrete-Frequency S-Parameters and Continuous-Frequency Responses", DesignCon, Santa Clara CA, 2012.
- K. Doshi, A. Sureka & P. Pupalaikis, "Fast and Optimal Algorithms for Enforcing Reciprocity, Passivity and Causality in S-parameters", DesignCon, Santa Clara CA, 2012.
- P. Pupalaikis, "Wavelet Denoising for TDR Dynamic Range Improvement", DesignCon, Santa Clara CA, 2011.
- D. DeGroot, P. Pupalaikis & J. Shumaker, "Total-Loss: How to Qualify Circuit Boards", DesignCon, Santa Clara CA, 2011.
- P. Pupalaikis & M. Schneck, "A 30 GHz, 80 GS/s Real-Time Waveform Digitizing System", Optical Fiber Communications Conference (OFC), San Diego CA, 2010.
- J. Kenney & P. Pupalaikis, "Timing Measurements in Source Terminated Memory Systems with Inaccessible Probing Points", DesignCon, Santa Clara CA, 2010.
- P. Wittwer & P. Pupalaikis, "A General Closed-Form Solution to Multi-Port Scattering Parameter Calculations", 72nd ARFTG Conference, Portland OR, 2008.
- P. Pupalaikis, "Validation Methods for S-Parameter Based Models of Differential Transmission Lines", DesignCon, Santa Clara CA 2008.
- P. Pupalaikis, "An 18 GHz, 60 GS/s Waveform Digitizing System", IEEE MTT-S International Microwave Symposium (IMS), Honolulu HI, 2007.
- P. Pupalaikis, "Group Delay and its Impact on Serial Data Transmission and Testing", DesignCon, Santa Clara CA, 2006.
- P. Pupalaikis & Eric Yudin, "Eye Patterns in Scopes", DesignCon, Santa Clara CA, 2005.
- P. Pupalaikis, "Bilinear Transformation Made Easy", International Conference on Signal Processing and Technology (ICSPAT), Dallas TX, 2000

Published Papers Cont'd

Co-author on ground-breaking research papers in optical communications with Bell Labs researchers including many optical communications world records using LeCroy high-speed waveform digitizers:

- X. Chen, S. Chandrasekhar, S. Randel, G. Raybon, A. Adamiecki, P. Pupalaikis and P. Winzer, "All-electronic 100-GHz Bandwidth Digital-to-Analog Converter Generating PAM Signals up to 190-GBaud," in OFC Conference Proceedings, Optical Fiber Communication Conference (OFC)/NFOEC, San Diego CA, 2016
- R. Ryf, N. Fontaine, H. Chen, B. Guan, S. Randel, N. Sauer, S. Yoo, A. Koonen, R. Delbue, P. Pupalaikis, A. Sureka, R. Shubochkin, Y. Sun, and R. Lingle, "23 Tbit/s Transmission over 17-km Conventional 50 um Graded-Index Multimode Fiber," in Optical Fiber Communication Conference: Postdeadline Papers, (Optical Society of America, 2014), paper Th5B.1.
- R. Ryf, S. Randel, N. Fontaine, M. Montoliu, E. Burrows, S. Chandrasekhar, A. Gnauck, C. Xie, R. Essiambre, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, L. Gruner-Nielsen, R. Jensen & R. Lingle, "32-bit/s/Hz Spectral Efficiency WDM Transmission over 177-km Few-Mode Fiber", OFC, Anaheim CA, 2013.
- R. Ryf, M. Mestre, S. Randel, X. Palou, A. Gnauck, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, X. Jiang & R. Lingle, "Combined SDM and WDM Transmission Over 700-km Few-Mode Fiber", OFC, Anaheim CA, 2013.
- R. Ryf, N. Fontaine, M. Mestre, S. Randel, X. Palou, C. Bolle, A. Gnauck, S. Chandrasekhar, X. Liu, B. Guan, R. Essiambre, P. Winzer, S. Leon-Saval, J. Bland-Hawthorn, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, L. Gruner-Nielsen, R. Jensen & R. Lingle, "12 x 12 MIMO Transmission over 130-km Few-Mode Fiber", Frontiers in Optics, Rochester NY, 2012.
- S. Randel, R. Ryf, A. Gnauck, M. Mestre, C. Schmidt, R. Essiambre, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, X. Jiang & R. Lingle, "Mode-Multiplexed 6 x 20-GBd QPSK Transmission over 1200-km DGD-Compensated Few-Mode Fiber", National Fiber Optic Engineers Conference (NFOEC), Los Angeles, CA, 2012.
- R. Essiambre, A. Gnauck, S. Randel, M. Mestre, C. Schmidt, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, T. Hayashi, T. Taru & T. Sasaki, "Space-Division Multiplexed Transmission Over 4200-km 3-Core Microstructured Fiber", Optical Fiber Communication Conference (OFC)/NFOEC, Los Angeles CA, 2012.
- R. Ryf, M. Mestre, A. Gnauck, S. Randel, C. Schmidt, R. Essiambre, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, X. Jiang, D. Peckham, A. McCurdy & R. Lingle, "Low-Loss Mode Coupler for Mode-Multiplexed Transmission in Few-Mode Fiber", NFOEC, Los Angeles CA, 2012.
- G. Raybon, P. Winzer, A. Adamiecki, A. Gnauck, A. Konczykowska, F. Jorge, J. Dupuy, A. Sureka, C. Scholz, R. Delbue, P. Pupalaikis, L. Buhl, C. Doerr, S. Chandrasekhar, B. Zhu & D. Peckham, "8 x 320-Gb/s Transmission Over 5600 km Using All-ETDM 80-Gbaud Polarization Multiplexed QPSK Transmitter and Coherent Receiver", OFC, Los Angeles CA, 2012.
- N. Fontaine, G. Raybon, B. Guan, A. Adamiecki, P. Winzer, R. Ryf, A. Konczykowska, F. Jorge, J. Dupuy, L. Buhl, S. Chandrasekhar, R. Delbue, P. Pupalaikis & A. Sureka, "228-GHz Coherent Receiver using Digital Optical Bandwidth Interleaving and Reception of 214-GBd (856-Gb/s) PDM-QPSK", ECOC, Amsterdam NL, 2012.
- R. Ryf, S. Randel, M. Mestre, C. Schmidt, A. Gnauck, R. Essiambre, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, Y. Sun, X. Jiang, A. McCurdy, D. Peckham & R. Lingle, "209-km Single-Span Mode- and Wavelength-Multiplexed Transmission Over Hybrid Few-Mode Fiber", ECOC, Amsterdam NL, 2012
- R. Ryf, A. Sierra, R. Essiambre, A. Gnauck, S. Randel, M. Esmaelpour, S. Mumtaz, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, T. Hayashi, T. Taru & T. Sasaki, "Coherent 1200-km 6 x 6 MIMO Mode-Multiplexed Transmission over 3-Core Microstructured Fiber", ECOC, Geneva CH, 2011.
- R. Ryf, A. Sierra, R. Essiambre, S. Randel, A. Gnauck, C. Bolle, M. Esmaelpour, P. Winzer, R. Delbue, P. Pupalaikis, A. Sureka, D. Peckham, A. McCurdy & R. Lingle, "Mode-Equalized Distributed Raman Amplification in 137-km Few-Mode Fiber", ECOC, Geneva CH, 2011.
- G. Raybon, P. Winzer, A. Adamiecki, A. Gnauck, A. Konczykowska, F. Jorge, J. Dupuy, R. Delbue, P. Pupalaikis, L. Buhl, C. Doerr, S. Chandrasekhar, B. Zhu & D. Peckham, "Transmission over 2400 km Using an All-ETDM 80-Gbaud (160-Gb/s) QPSK Transmitter and Coherent Receiver", ECOC, Geneva CH, 2011.
- A. Gnauck, P. Winzer, G. Raybon, M. Schnecker & P. Pupalaikis, "10 x 224-Gb/s WDM Transmission of 56-Gbaud PDM QPSK Signals over 1890 km of Fiber", IEEE Photonics Technology Letters, San Diego CA, 2010
- P. Winzer, A. Gnauck, G. Raybon, M. Schnecker & P. Pupalaikis, "56-Gbaud PDM-QPSK: Coherent Detection and 2,500- km Transmission", ECOC, Vienna AT, 2009.

Societies & Organizations

IEEE Instrumentation, Signal Processing, Solid-State Circuits and Microwave Society	since 2003
IEC DesignCon Technical Planning and Program Committee Track Organizer	since 2006
Chair and Vice-Chair, North Jersey Section, Instrumentation Society Chapter	since 2013, since 2009
Member T _E X Users Group (TUG) and contributor of the the CTAN package <i>uspatent</i>	Since 2006
Corporate Advisory Board Member of ECEDHA	Since 2013
Corporate Advisory Board Member of Rutgers Electrical and Computer Engineering Department	Since 2015