CSCD70 Compiler Optimization

Tutorial #4 Dataflow Analysis (ii)

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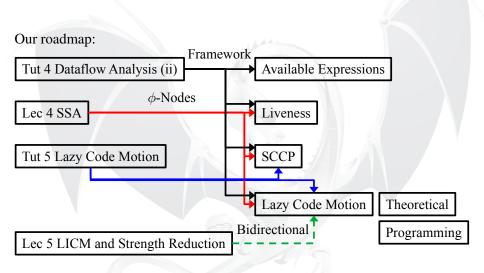
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Announcement

- Assignment 1 has been graded.
- ► Assignment 2 will be released this Friday, due on Mar. 3rd (Friday).
- ► Midterm is tentatively scheduled to be on Mar. 6th.
 - Please plan accordingly.

Announcement



Assignment 1 Feedback

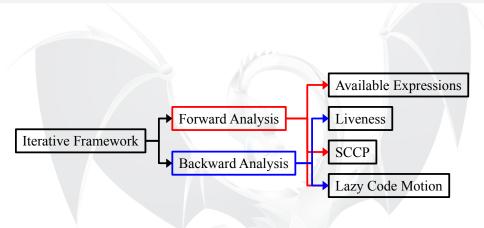
- ▶ Posted on the GitHub classroom PR.
 - ▶ Please do NOT merge it.
- ▶ Please include your name in the report.
- ► Common Problems:
 - Programming
 - CHECK directives have to be completed.
 - ► Theoretical Questions
 - CFG should be illustrated in a hierarchical way.
 - Available expressions (and other dataflow analyses that are on expressions) should be in the form of binary operators, not statements.

TL'DR

 $Iterative \ Framework \rightarrow Available \ Expressions$



Iterative Framework



Iterative Framework

- 1 Domain, Value, and Meet Operator
 - E.g., For available expressions, Domain is binary expressions, Value is boolean (true for available, false otherwise), and Meet Operator is AND.
- 2 Mapping from basic block/instruction to domain values
- 3 Top-level run function

run(const Function&)

Algorithm 1: runOnFunction

Data: Domain D, Instruction-Domain Value Mapping M

initialize *Domain* **0**;

initialize Instruction-Domain Value Mapping 2;

do

traverse through the CFG update $M \forall \text{inst} \in F$;

while NOT converge;

- 1 Fill the domain with binary expressions.
- ❷ How to initialize? ☞ MeetOp::top() -> vector(D.size(), true)

CFG Traversal

Algorithm 2: traverseCFG

```
Data: Domain D, Instruction-Domain Value Mapping M
Arguments: Function F
Return
            :Whether M has been modified
for bb \in \mathrm{BBRange}(F) 0 do
     if bb has no meet operands then
          initialVal ← Boundary Condition;
     else
          initialVal \leftarrow MeetOp(MeetOperands(bb));
     inputVal \leftarrow initialVal;
     for each instruction i \in \text{InstRange}(bb) do
          TransferFunc(i, inputVal, M[i]);
          inputVal \leftarrow M[i];
```

- 1 Traversal order? ™ Reverse post-order for forward analysis
- Apply the boundary condition if there is no predecessor, or the meet operator otherwise.
- **3** Apply the transfer function ∀instruction.



Forward Analysis

```
template <typename TDomainElem, typename TValue
          typename TMeetOp>
class ForwardAnalysis :
    public Framework< 0</pre>
        TDomainElem, TValue, TMeetOp,
        iterator_range<Function::iterator>, // BBRange 2
        iterator_range<BasicBlock::iterator> // InstRange
    > {
  BBRange getBBRange(const Function &F) const {
    return make_range(F.begin(), F.end());
  }
  InstRange getInstRange(const BasicBlock &BB) const {
    return make_range(BB.begin(), BB.end());
```

- 1 Inherit from the Framework class.
- 2 The starter code uses the default iterator. You will have to change the type names and the method definitions.

Domain

```
class Expression {
  const unsigned Opcode;
  const Value *const LHS = nullptr;
  const Value *const RHS = nullptr;

  class Initializer : InstVisitor<Initializer> ① {
    void visitBinaryOperator(BinaryOperator &);
  };
};
```

1 InstVisitor allows us to only handle instructions of interest.

Meet Operator

- **1** Apply the meet operator (i.e., \cap), invoked by the framework on the basic block boundary.
- Return the top element, invoked by the framework when initializing the instruction-domain value mapping.

Meet Operation

Algorithm 3: MeetOp

Data: Domain D, Instruction-Domain Value Mapping M

Arguments : BasicBlock bb

Return : Merged BitVector

 ${\bf 0} \; {\rm MeetOperands}$

return $\bigwedge_{i \in \text{MeetOperands}} (M[\text{back}(\text{pred}(bb))];$

• The meet operands are the outputs (i.e., the domain value that corresponds to the last instruction) of the predecessor basic blocks.

Available Expressions

Transfer Function

 $bv_o \leftarrow bv_o';$ return hasChanges;

Algorithm 4: transferFunc

```
Data: Domain D, Instruction-Domain Value Mapping M
Arguments: Instruction i, BitVector bv_i, bv_o
Return
             : Whether bv_o has been modified
\mathbf{0} bv'_o \leftarrow bv_i;
for each element e \in D do
     if lhs(e) = i or rhs(e) = i then
           bv'_{o}[position(e)] = false;
iter \leftarrow find(D, Expression(i));
if iter \neq end(D) then
     bv'_{o}[position(e)] = true;
hasChanges \leftarrow bv'_o = bv_o?;
```

- Check whether the bitvector has been modified or not.
- 2 Kill expressions whose LHS/RHS are the current assigned value (?).
- Set the expression as generated.

Recap

- ► Iterative Framework
- ► Available Expressions
 - ► Forward Analysis
 - ► Expression: InstVisitor
 - Intersect
 - ► Transfer Function

™ Homework Assignment: Available Expressions