

Peter/Zheyuan HU

University of Cambridge | Jardine Scholar | Research Assistant | Challenge Solver

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Looking forward to research around Graphics / Vision / Visual Computing.

## TECHNICAL SKILLS

**Data Sci:** Prob and Stat, Python, NumPy, ML&DL, PyTorch, Computer Vision.

**Visual:** Computer Graphics, OpenGL, GLSL, XR (AR/VR/MR), Unity, Unreal Engine, 3D Modelling (Blender).

**Prog:** C/C++, Java, OOP, CMake, Gdb, Algorithms and Data Structure, OCaml (Functional Programming).

**Dev Tools:** bash/shell, git, CI/CD pipeline, Docker, VS Code, Pycharm, IntelliJ IDEA.

## LIST OF PUBLICATIONS

Under the supervision of *italic*, † indicates equal contribution.

@ **Cambridge Open Reality and Visual AI Lab**, directed by Prof. **Cengiz Öztireli**. | Details: 📄.

**NeuMaDiff: Neural Material Synthesis via Hyperdiffusion** [arXiv](#) | under ICCV review | 2024-2025

*Chenliang Zhou, Zheyuan Hu, Alejandro Sztrajman, Yancheng Cai, Yaru Liu, Cengiz Öztireli.*

- Computer Graphics (BRDF, real-world materials), Vision (generation via PCA, VAE, diffusion).

**CHORd: Generation of Collision-Free, House Scale, and Organized Digital Twins for 3D Indoor Scenes with Controllable Floor Plans and Optimal Layouts** [arXiv](#) | under ICCV review | 2025

Chong Su†, Yingbin Fu†, **Zheyuan Hu**, *Jing Yang*, Cengiz Öztireli, *Fangcheng Zhong*, et al.

- Indoor Scene Synthesis, Generative Models, Digital Twin Generation. Mentored by Dr **Fangcheng Zhong**.

## INDUSTRY RESEARCH

@ **HiSilicon Kirin Chipsets Dept.**, Huawei Research Center, Cambridge Science Park, UK. Details: 📄

Research Engineer: **Graphics Algorithm/GPU Architecture**

Jun 2023-Jan 2024

- Linear Algebra, Convolution (Bilateral Filter Kernel on Monte Carlo Samples using GBuffer), spatial-temporal locality.
- NN (PyTorch): Train (lr decay, shuffle data 5GB+, dropout) and Infer (conservative loss), 3D Data Encoding, etc.
- Graphics: Key developer for **Ray Tracing simulation** (OpenGL, GLSL, OpenMP, CMake). Host **sharing sessions**.
- Performance Engineer / Data structure design, targeting micro-benchmarks (performance counters, cache hit rate, etc.)
- Supervised by PhD graduate, senior AI researcher and senior GPU Architects.

Research Intern: **CPU Architecture**

Jun-Oct 2023

- **Review** of CPU Scheduling, DVFS policy, Idle Management in terms of energy efficiency. Convex Optimisation, Duality, LP, Pareto Optimality, Stanford CVX, Online Algorithms, Competitive Analysis, Disjoint Set Union-find, etc.
- Set up simulation, event-driven architecture with state machine, taking in runtime profiled task model. Compare different algorithms w.r.t complexity, performance, energy (temperature, thermal), Memory Contention, floor-plan, applications. Python (Numpy, Matplotlib, Networkx, Pandas, DAG, TopologicalSorter, etc.).

Software Engineer: **GPU Driver**

Dec 2022-May 2023

- GPU industry workflow, Linux, Vulkan; GPU driver and verification, Game Engines (UE4), shader debug (RenderDoc).
- Introducing independent full automation tools in the CI/CD, reducing error rate to nearly 0. [Tools](#)

## EDUCATION

🏰 **University of Cambridge**, United Kingdom

Oct 2022-Jul 2025

Computer Science, BA.

Merit-based, fully-funded **Jardine Scholarship**

🌐 **Universitas Amaiensis**, **Project 985** & Top 1 in Southern China

Sep 2021-Jun 2022

Software Engineering, BE First year

rank 1/173 first term, 88/100 overall

C and C++, Object-Oriented Programming, Calculus and Linear Algebra, ACM, SSE.

## HONORS & AWARDS

**Cambridge Summer Internship and Research Award**

Mar 2025

supporting my research at **Cambridge Open Reality and Visual AI lab**.

**Gold Medal**, 3D Data Compression Algorithm, **national Tech Arena**, UK

10 Oct-26 Nov 2022

engineering + research, digesting papers and source code, like RFC1951, etc.

- Responsible for implementation & improvement of LZSS. 6-level / concurrent LZSS Compression. 📄 | 📄
- C with bitwise operators & hash tables, optimization via branch prediction and concurrency.
- In a team of 4, leading the team and engaging in pre-processing, serialization with teammates.

**Top 2 Team**, Maritime Data Science, **Mercuria Hackathon**, Switzerland

16 Dec-18 Dec 2022

regression for Route-Planning and reduce the carbon emissions of the maritime industry. 📄 | 📄

**Third Place**, **High school Science and Technology Innovation Contest**, Shanghai

Apr 2020

deep research thesis into the phenomenon of tire-locking, including pros and cons using Force Analysis.

- Self-made physical simulation test. Introduce Anti-lock braking system into our research with help from mentor.

**Publication twice**, Shanghai Students' Post

Oct 2018, May 2019

topic: *Effective Ways to Overcome Obstacle in Study, Campus Life without Snack Stores.*

**Participant**, Chinese **Physics/Mathematical Olympiad** (ChPO, CMO)

Oct 2019

## LIST OF PROJECTS

### Machine Learning and its applications

Oct 2022-Jan 2024

- DNN in CV [Stanford CS231n](#) kNN, Softmax, SVM, MLP, CNN. Caption: RNN, Attention. Gen: GAN, VAE. [🔗](#) | [📄](#)
- ML [Stanford CS229](#) Linear classifiers (Logistic Regression, GDA), SGD, Regularization, PCA, SVM. [🔗](#)
- [Kaggle](#) DataSci practice & ML model (Regression, MLP, etc), PyTorch DNN Debugging, Visualization, Validation.
- Text Classification via Naive Bayes, HMM, NLP; Social Network and Graph. [🔗](#) | [📄](#)

### Graphics Renderer (C++, OpenGL)

Jul-Sep 2022

*real-time simulation, composite design pattern for 3D objects class hierarchy with transformation.*

- MIT6.837 ray casting, normal visualization, rendering, voxel rendering, super sampling. [🔗](#) | [📄](#)
- large OOP project, with 3D objects, light, camera classes, building over 20 C++ source files from scratch.

#### System

### Operating System (MIT 6.S081)

Oct-Dec 2022

*user-mode and kernel programming of Unix V6 RISC-V multiprocessor.*

- implement Unix utilities, System Call. Process Scheduling, Memory (Segment, Page, VM), I/O, File. [🔗](#)

### Database Design Management System (CMU15-445 Project)

Aug-Oct 2022

*engineering and code style: using C++ STL, Google C++ Style Guide.*

- Memory Management, including Buffer Pool Management System, Replacement policy: LRU
- Concurrency: implement the Parallel Buffer Pool Manager. [🔗](#) | [📄](#)

#### C, C++, OOP

### Multifunctional Supermarket Management System (C++)

Apr 2022

*inheritance polymorphism, operator overloading, read/write files, etc.* [🔗](#) | [📄](#)

#### Typing Game (C, EasyX)

Dec 2021

*a standard keyboard layout, where different modes are provided.* [🔗](#) | [📄](#)

#### Front/Back-end

### Weather App (Flutter)

April-May 2023

*collaborating with team members on an App integrating weather forecast with daily calendar events. I am responsible for:*

- Frontend: Beautiful design with UI components, written in Flutter, with Object-oriented programming.
- Backend: Integration of iCalendar API, asynchronous IO, Computer Networking: HTTP request, get. [🔗](#)

### 🏠 Personal Website and Blog (HTML, CSS, React)

Aug 2022

*project blogs, files, etc; built up from scratch using HTML/ CSS. Deployed by React, with high code reuse.* [🔗](#)

#### Game Dev

### Interactive AR block tower (AR foundation, Unity)

Jan-Mar 2025

*Extended Reality (XR) module video-based AR project.* [🔗](#) | [📄](#) | [Demo](#)

### Priest-Beneath (Unity, C#)

Feb 2023

*2023 Cambridge Game Jam (Group Project).* [🔗](#) | [📄](#) | [WebGL](#)

#### Utility Tools

### URL Finder (Web Crawler, Python, Go)

Apr 2023

*download the web page available at the input URL and extract the URLs of other distinct pages linked to from the HTML.*

- Data Structure: Lists, Sets; Computer Networking: HTTP request, like get; Synchronous File IO. [🔗](#)

### Trace File Parser (Java)

May 2023

*parsing trace files and generate a unique and sorted list in Java.* [🔗](#)

## EXTRACURRICULAR INTEREST

[Photography](#), Music, Gym, Yoga, etc | *Society*: [Ethics in Science](#) | *Econ*: Macro & Micro, Money Banking

## APPENDIX: REFERENCE

"Zheyuan Hu, together with AI team researcher, proposed the ray-prediction algorithm. According to the test results, the ray intersection latency in reflection scenarios can be reduced by 33%, RTU energy consumption can be reduced by 15%, or RTU throughput can be improved by 20%. The results achieved are recognized by the hardware team. This algorithm will be the official delivery technology of the HiMeta project. They have demonstrated strong algorithmic capabilities and have shown typical examples of cross-team collaboration. Well done and congratulations!" Source: [Research Center](#)

"During our time working together, I found Peter to be a highly collaborative and supportive colleague who consistently demonstrated a willingness to share his knowledge and expertise with others. Peter's ability to problem-solve complex C/C++ development issues was invaluable, and his commitment to learning and staying up-to-date with the latest advancements in his field is truly impressive. His passion for ray-tracing is contagious, and I have learned so much from his knowledge sharing." Source: [Linkedin](#)