NYCU-ECE DCS-2023

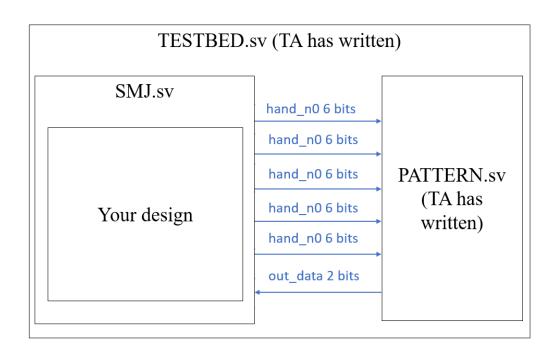
HW01

Design: Simplified Mahjong Judgment

Prepare materials

- 1. Unzip from the TA directory folder: % tar -xvf ~dcsta01/HW01.tar
- 2. Unzip the folder HW01 containing the following:
 - a. 00_TESTBED/
 - b. 01_RTL/
 - c. 02 SYN/
 - d. 03 GATE/
 - e. 09 UPLOAD/

Block Diagram



Design Description:

The goal of this assignment is to complete a simple Mahjong judgment. You will receive five inputs representing five Mahjong tiles respectively, and finally output an out_data to determine whether the winning condition is completed, and if so, what type of win it is (either "sequence plus pair" or "triplets plus pair").





The five Mahjong tiles are composed of six bits, where the first two bits represent the type of tile: 2'b00 represents honor tiles(字牌), 2'b01 represents character tiles(萬子), 2'b10 represents bamboo tiles (索子), and 2'b11 represents dot tiles(筒子).

The remaining four bits represent the number on the tile. For honor tiles, 0~6 represent East, South, West, North, Red, Green, and White respectively. For the other three types, 0~8 represent the numbers 1~9 of that tile type.

When there is no winning hand, output 2'b00.

When the condition is impossible, such as having five same tiles or tiles that are unmentioned (ex: 001000, 111010...), output 2'b01.

When winning with tiles of a sequence and a pair, output 2'b10. When winning with tiles of a triplet and a pair, output 2'b11.

For example: {hand_n0, hand_n1, hand_n2, hand_n3, hand_n4} = {000000, 110001, 110011,000000, 110010} :

represent {East, 2 dots, 4 dots, East, 3 dots}, this meets the winning condition of Mahjong (with sequence). Out_data should be 2'b10

 $\{000000, 111000, 111000, 000000, 111000\}$:

represent {East, 9 dots, 9 dots, East, 9 dots}, this meets the winning condition of Mahjong (with triplet). Out data should be 2'b11

{100010, 010010, 110100,010000, 110110}:

represent { 3 bamboos, 3 characters, 5 dots, 1 character, 5 dots }, this doesn't meet the winning condition of Mahjong.

Out_data should be 2'b00

{001111, 111111, 011100, 010000, 110110}:

represent { unknown, unknown, 1 character, 5 dots }, appearing with unmentioned tiles.

Out data should be 2'b01

Finally, a 2-bit number out_data will be output, and the pattern in testbench will test this 2-bit before the next set of input data comes in.

Inputs

Signal name	Number of bits	Description						
hand_n0	6 bits	A random number in the range 0~63						
		First two bits:						
hand_n1	6 bits	2'b00 : honor tiles						
_		2'b01 : represents character tiles 2'b10 : represents bamboo tiles 2'b11 : represents dot tiles						
band va2	C hite							
hand_n2	6 bits							
		Last four bits:						
hand_n3	6 bits	For honor tiles, 0~6 represent East, South,						
		West, North, Red, Green, and White						
hand_n4	6 bits	respectively. For the other three types, 0~8 represent						
_								
		the numbers 1~9 of that tile type.						

Outputs

Signal name	Number of bits	Description						
		When there is nothing, output 2'b00.						
		When the condition is impossible, such as						
out_data		having five same tiles or tiles that are						
	2 bits	unmentioned, output 2'b01.						
		When winning with tiles of a sequence						
		and a pair, output 2'b10.						
		When winning with tiles of a triplet and a						
		pair, output 2'b11.						

Specifications

- 1. Top module name: SMJ (File name: SMJ.sv)
- 2. Please use Systemverilog to complete your homework.
- 3. Please use combination circuit to complete your homework.
- 5. 02_SYN can not have any error and latch.

Example waveform

clk	0															
hand_n0[5;	1_0011	1_0011	11_1111	10_1101	1_0111	111	11_0001	1101	1_0010	1_1011	101	1_0000	10_1011	1_0110	1_1110	1_0011
hand_n1[5;	11	11	10_0110	11_0111	1	101	11_1000	1_0111	1	101	1101	11	1_0001	10_0010	111	1_0011
hand_n2[5;	11	11	1_0010	11_1000	1000	111	1_0011	1110	10	1_1011	11_1000	100		1_1000	1000	1_1000 10
hand_n3[5;	1_0010	1_0010	11_1010	1_0	010	10_0100	1_0101	1100	1_0010	100	10_1000	1_0000	11_1000	1	110	1_1000 10
hand_n4[5;	1_0001	1_0001	1100	11_0001	11_0101	1_0000	11_0010	1_0111	11	110	10_0110	101	11_0001	11_0000	1_1110	1_0011
out_data[1	10	10					0	1	0			0	1	0	1	11

Upload file

- 1. Please use 09_upload to upload code.
- 2. Please upload report_dcsxx.pdf to new E3, xx is your server account.

Grading policy

- 1. Pass the RTL& Synthesis simulation. 70%
- 2. Area 15%
- 3. Report 15%