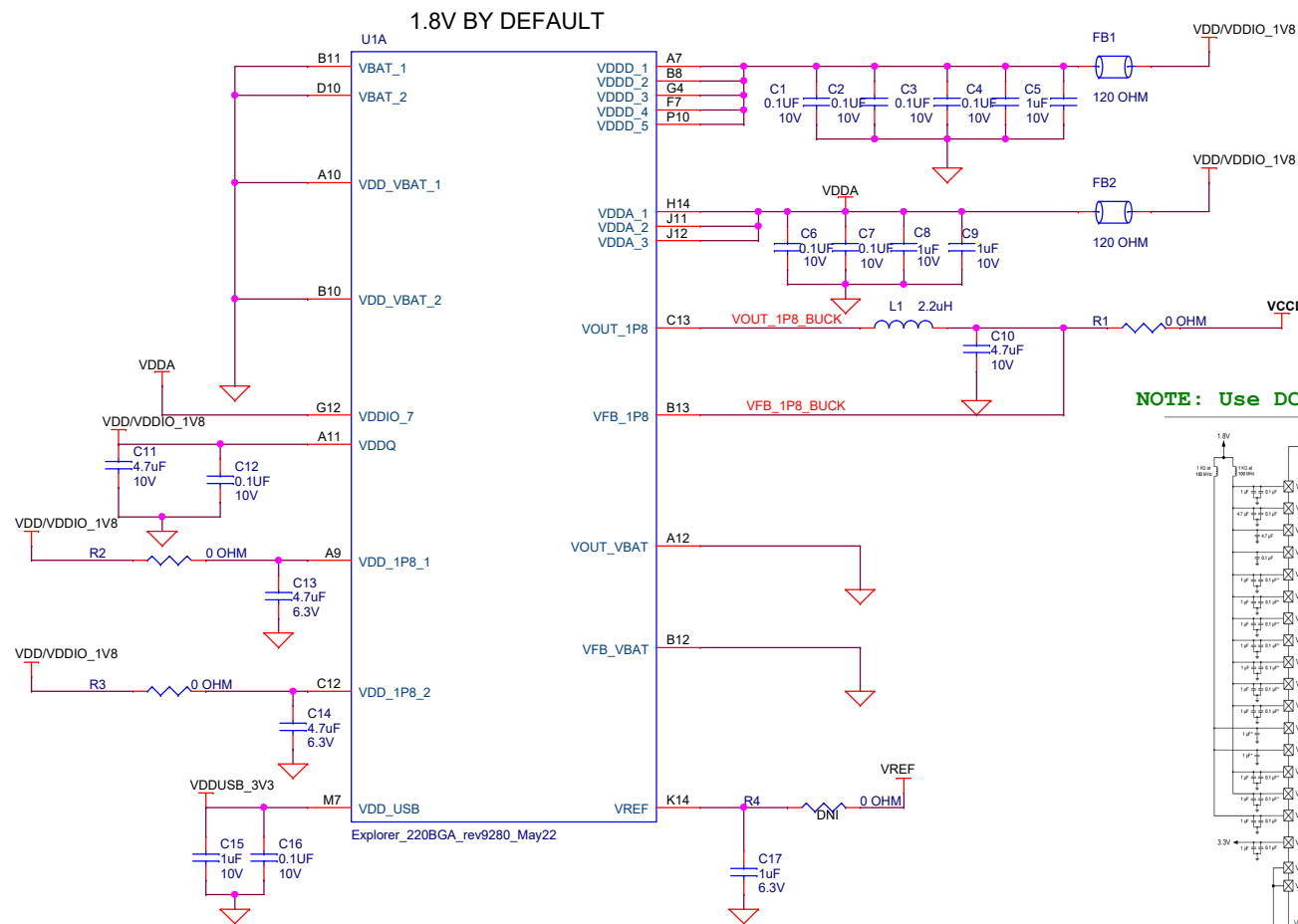
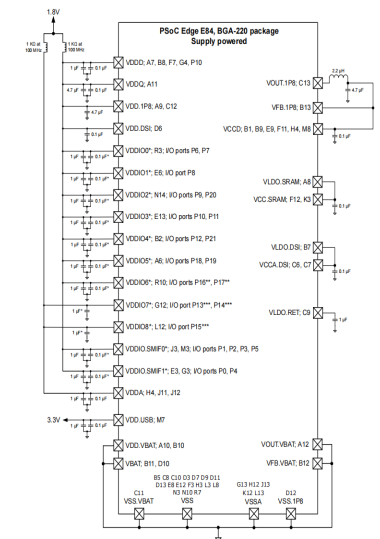


MCU POWER SECTION_1



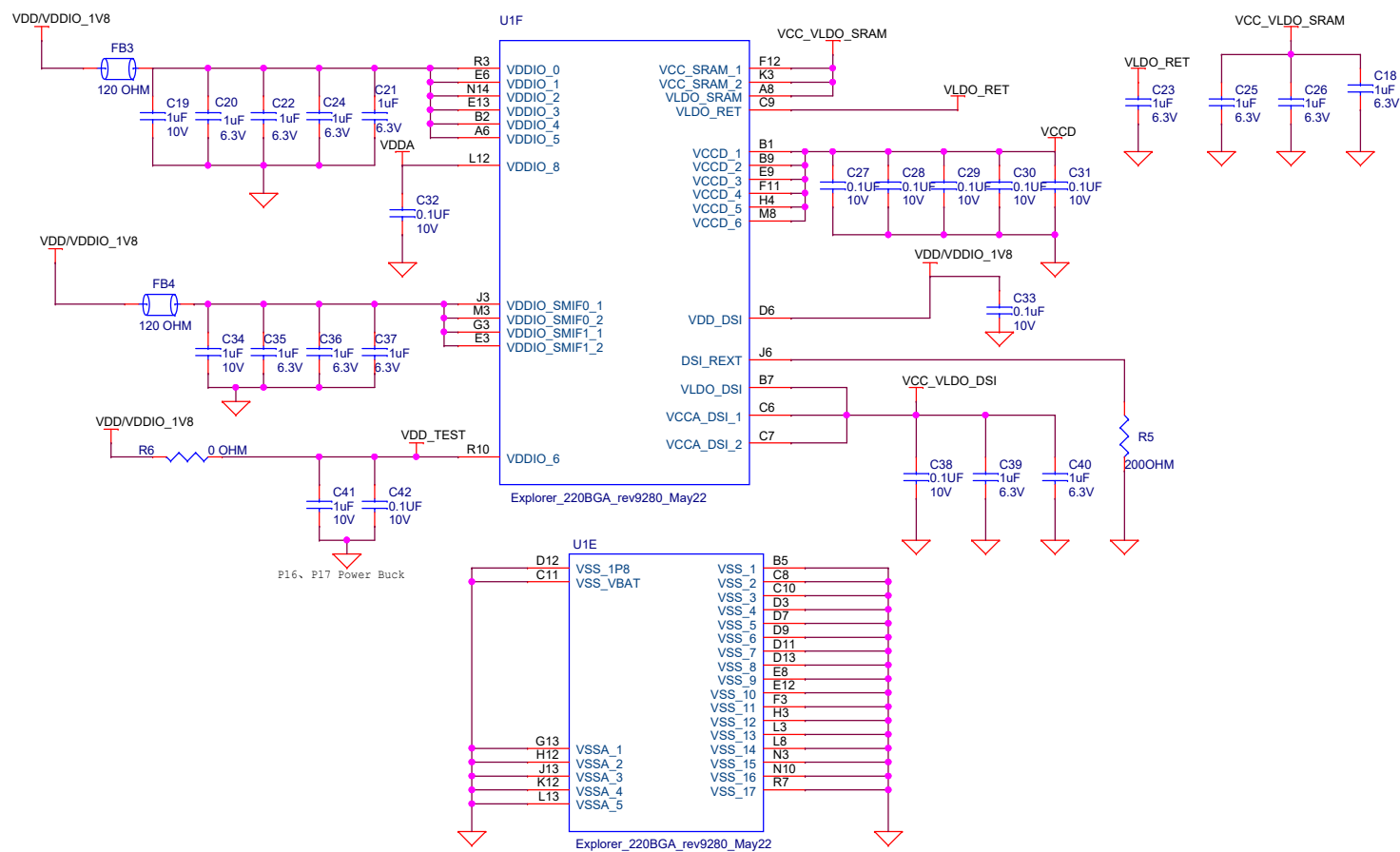
NOTE: Use DCDC1.8V power supply



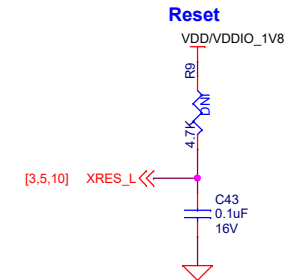
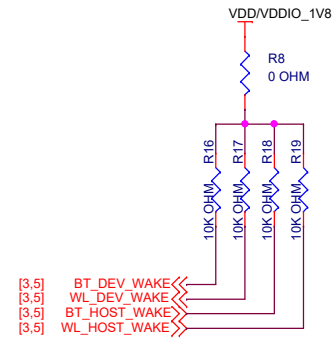
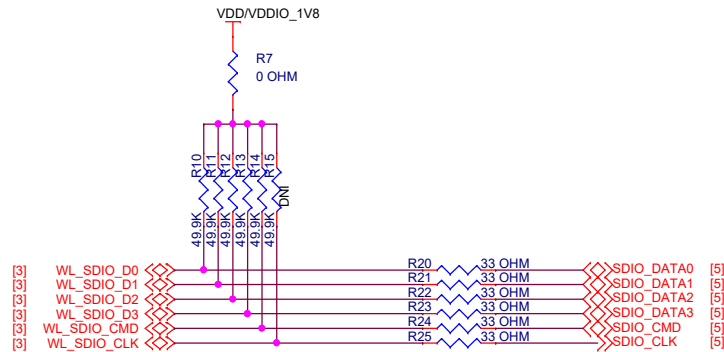
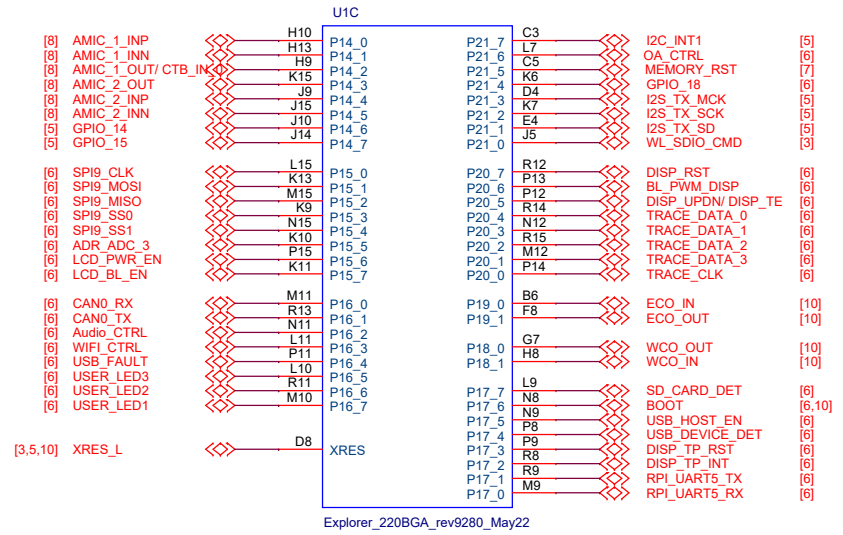
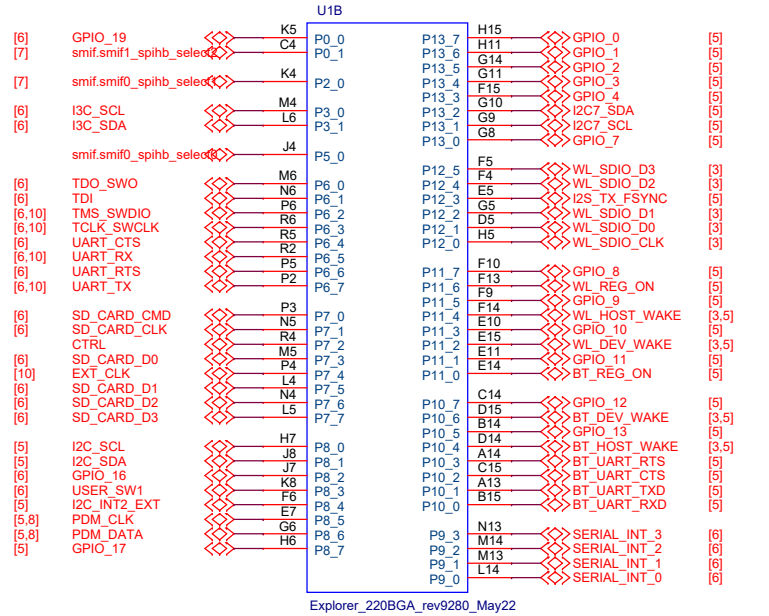
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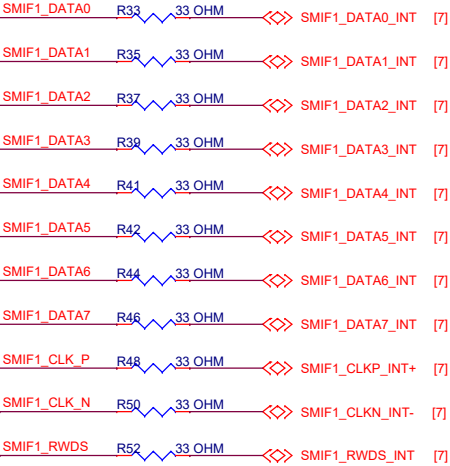
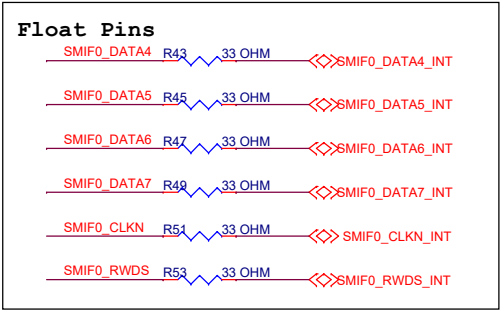
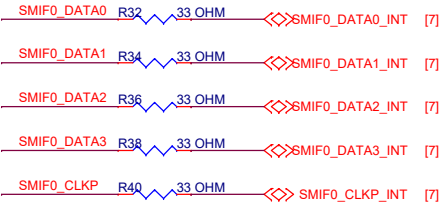
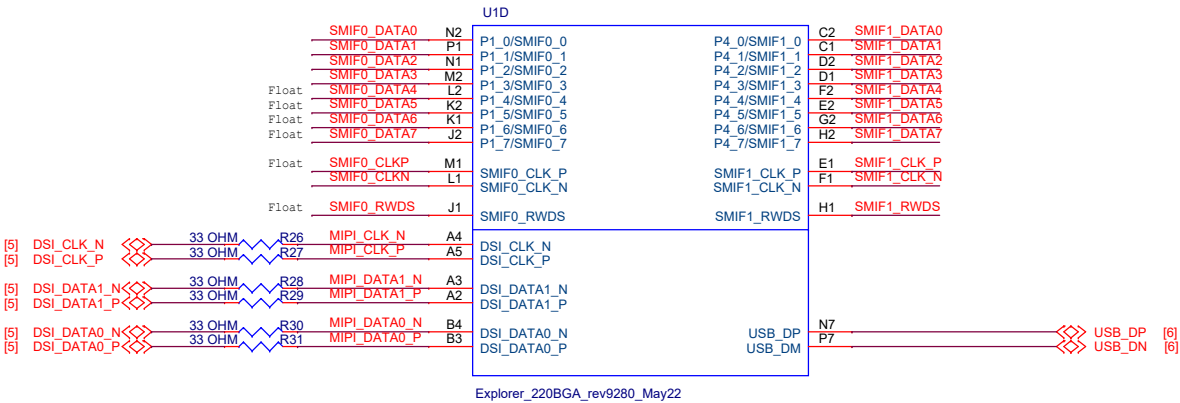
MCU POWER SECTION_2



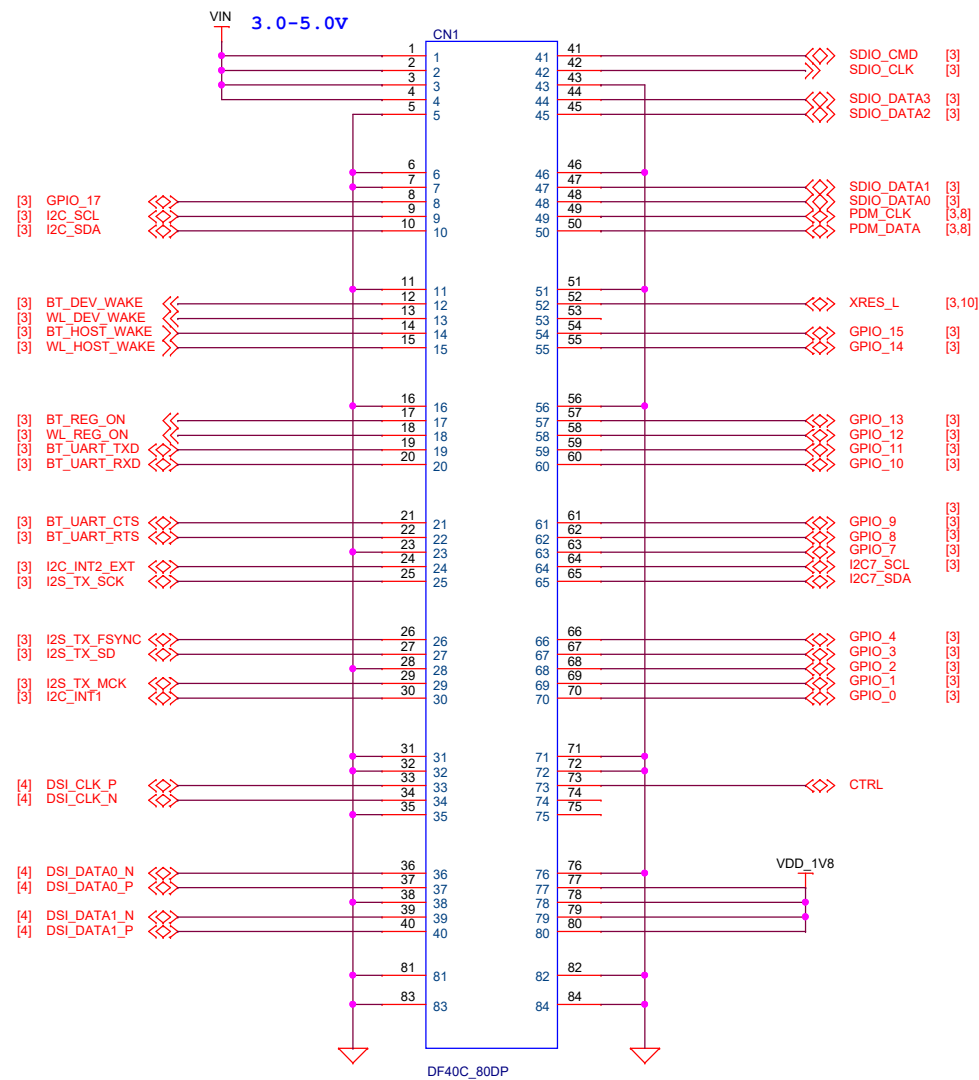
MCU_SIGNAL_1



MCU_SIGNAL_2

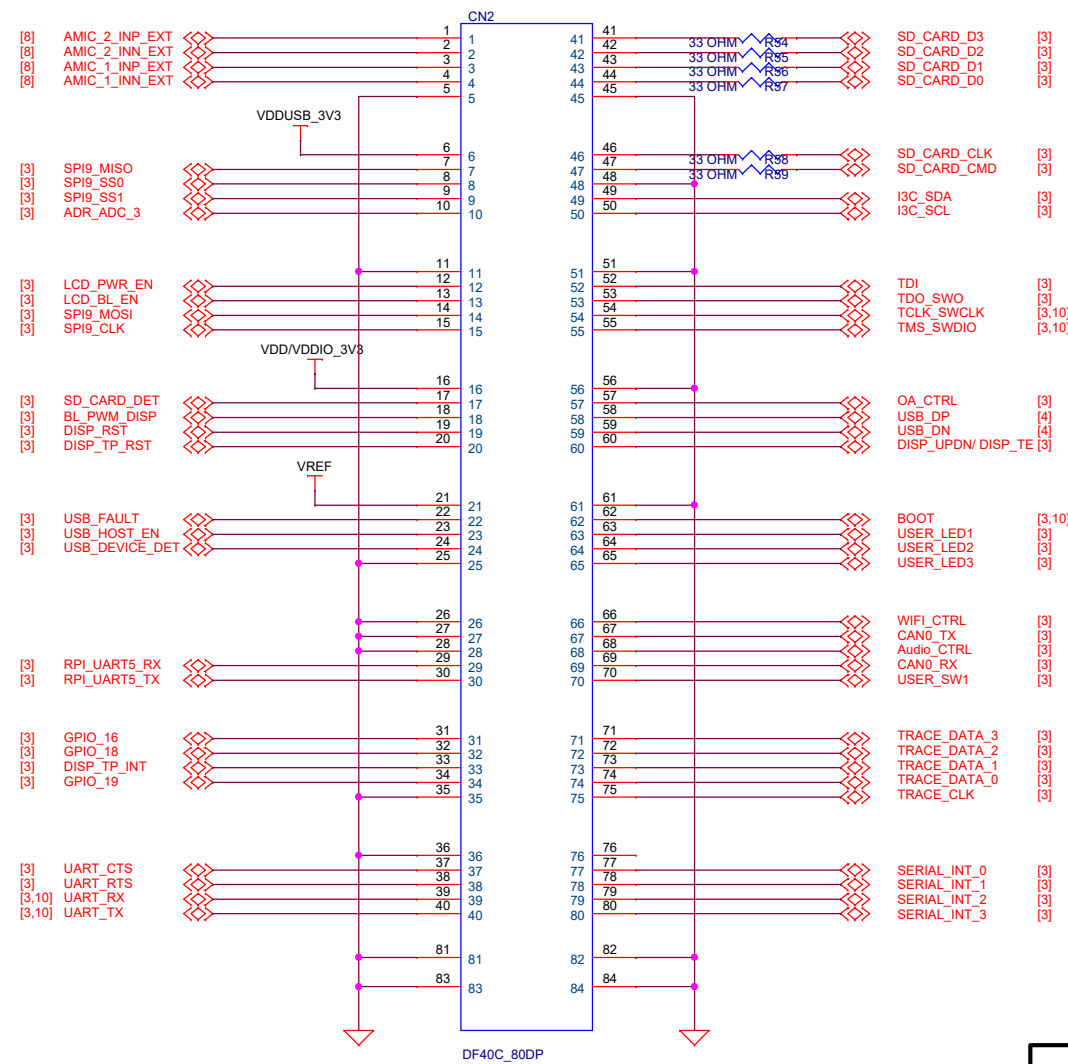


BTB_EDGE_CONNECTOR_1



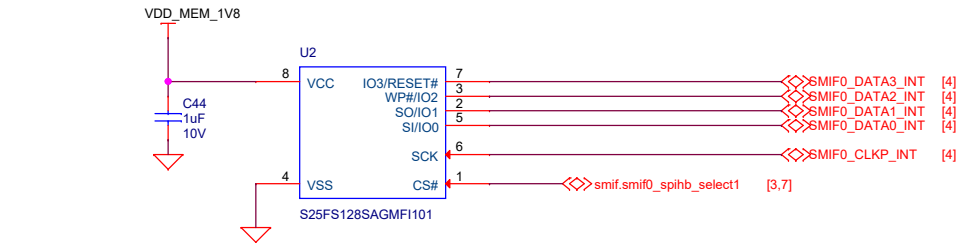
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BTB_EDGE_CONNECTOR_2

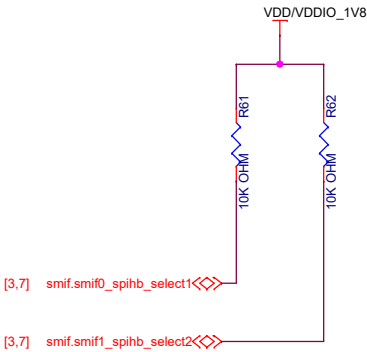
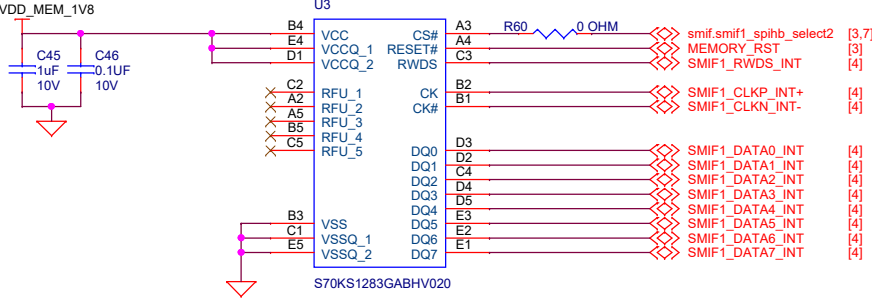


SMIF_INTERFACE

QSPI FLASH

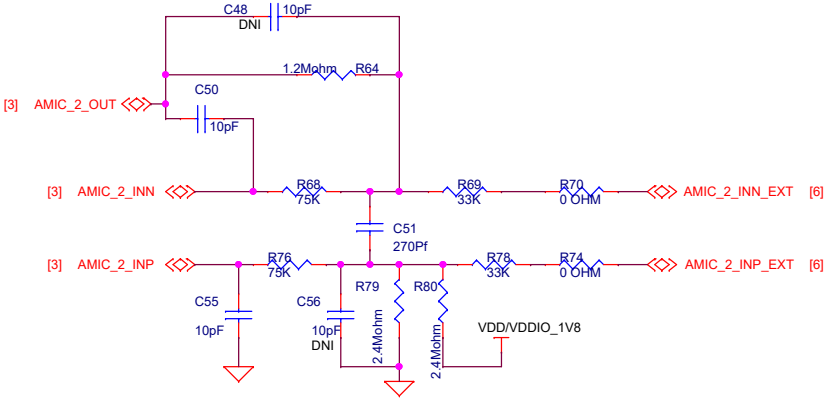
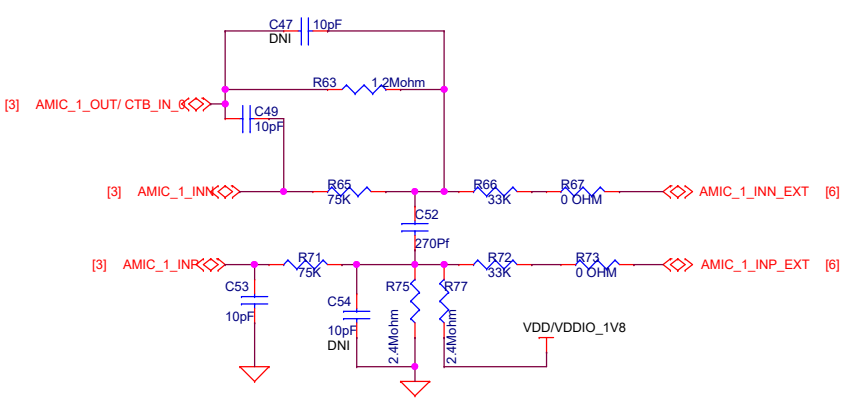


PSRAM

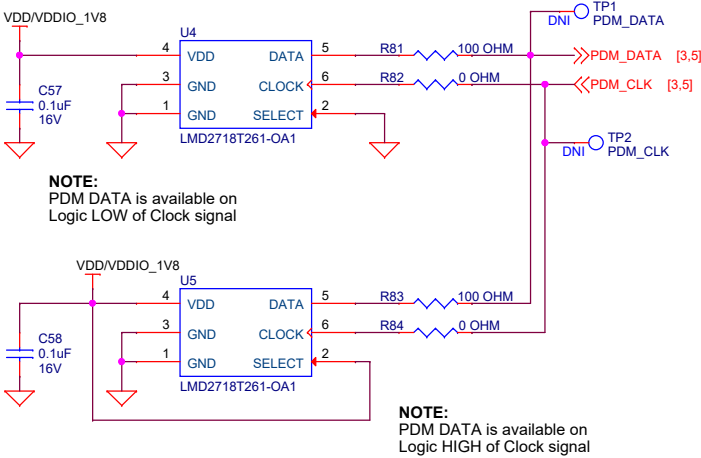


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AMIC&DMIC



PDM MICs

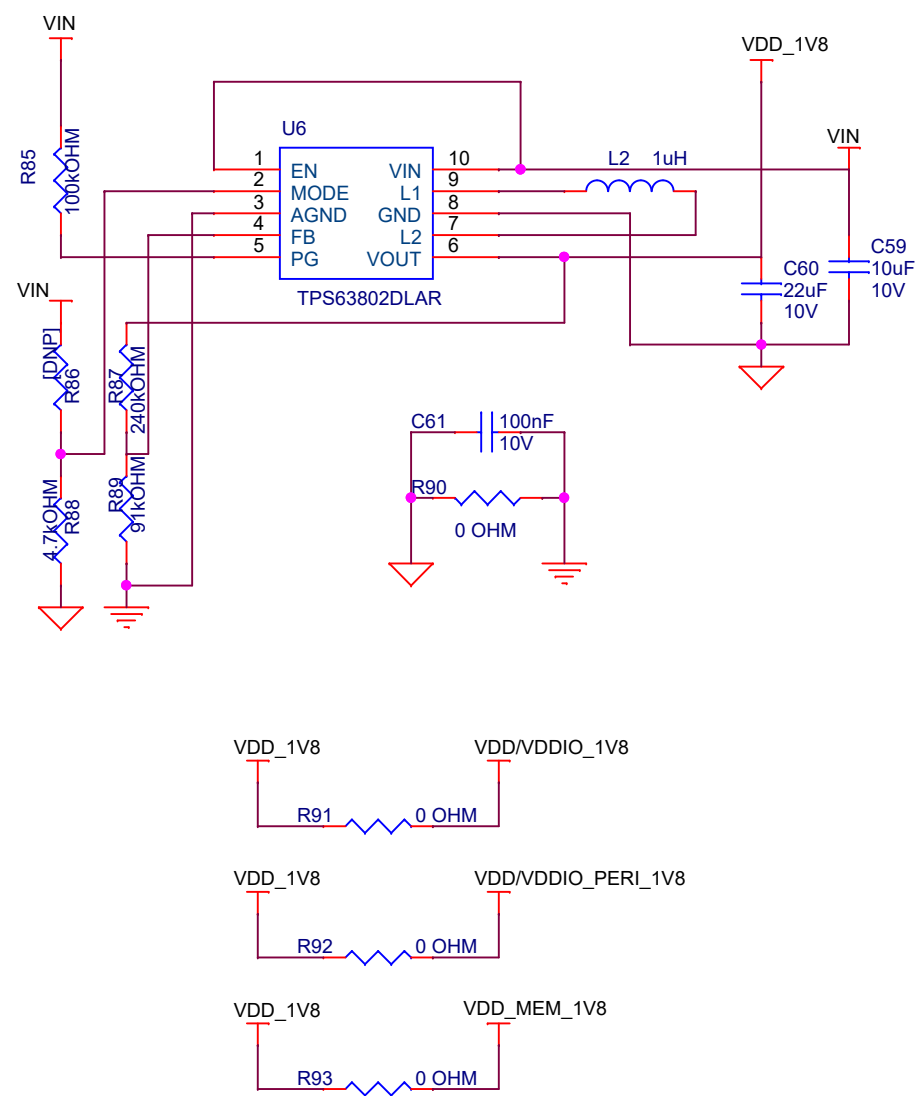


NOTE:
PDM DATA is available on
Logic LOW of Clock signal

NOTE:
PDM DATA is available on
Logic HIGH of Clock signal

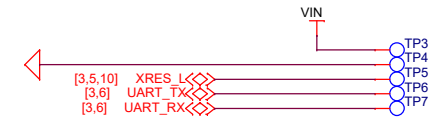
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POWER SUPPLY

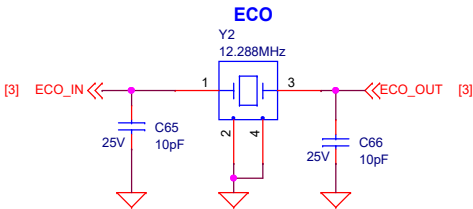
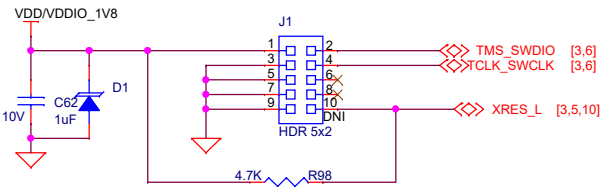


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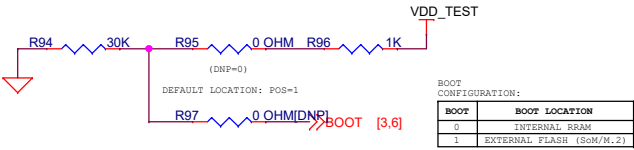
MCU_MISCELLANEOUS



Explorer MCU SWD Header



CORE BOOT TEST



WCO

