Circuits

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Voltage and current laws



Spring 2022

Context



Analyzing a circuit

- we need to determine the **voltages** and **currents** in the circuit
- for resistors: once we know the voltage, we know the current (or conversely)
- for sources: more complicated...

Any kind of circuit can be analyzed

Additional rules

- Kirchhoff's current law (KCL) Charge Corser ration

 Kirchhoff's voltage law (KVL)

 energy conservation.

Context



Wire

The wires connecting the different elements of a circuit will be considered to be zero-resistance conductors il out

It means that there is no voltage drop on a wire (whatever the current value)

Node

Definition: a point at which 2 or more elements are connected to

They share the same voltage.

Path Branch / Loop

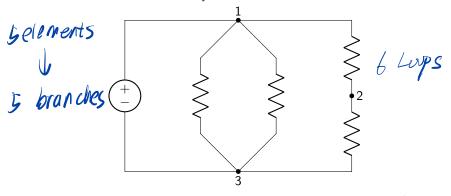
- Path: starting at a node, going through different connected
- Branch: single path, composed of one element only one path & one Loop: path that stops at the starting node (closed note)

Context

上海组约大学 NYU SHANGHAI

THE IT elements should equal the It of branches

How many nodes and branches?



3 nodes

from 1 to 3. 4 paths



a restalement of charge conservation.

KCL

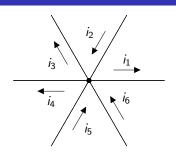
The algebraic sum of the currents **entering a node** (or a closed boundary) is **zero**

Interpretation

Conservation of charge:

- all the electrons have to go somewhere
- the current that goes in, has to come out





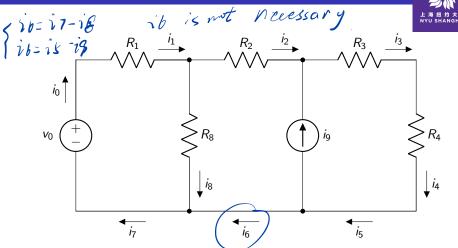
KCL formula

$$-i_1 + i_2 - i_3 - i_4 + i_5 + i_6 = 0$$

or

$$i_2 + i_5 + i_6 = i_1 + i_3 + i_4$$





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How many nodes? How many branches? Apply KCL to each node

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26+28=27

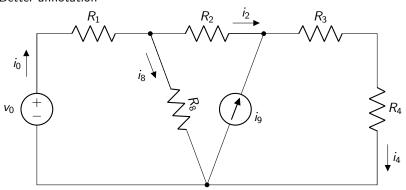


- $i_0 = i_1$
- $i_1 = i_2 + i_8$
- $i_2 + i_9 = i_3$
- $i_3 = i_4$
- $i_4 = i_5$
- $i_5 = i_6 + i_9$
- $i_6 + i_8 = i_7$
- $i_7 = i_0$

Are there some redundant variables? How about i_6 ?



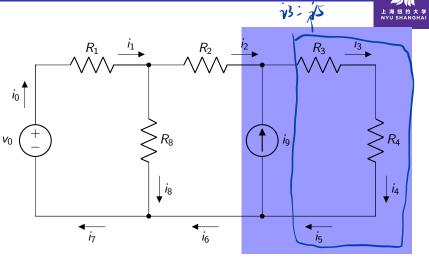
Better annotation





- $i_0 = i_2 + i_8$
- $i_2 + i_9 = i_4$
- $i_4 + i_8 = i_0 + i_9$

Actually, only 2 equations are necessary. The third one can be obtained from the other two.



KCL on closed boundaries: $i_2 = i_6$





KVL

The algebraic sum of voltages around a loop are zero

Interpretation

Conservation of energy:

- voltages are differences of electric potentials
- around a loop, the difference in electrical potential is zero

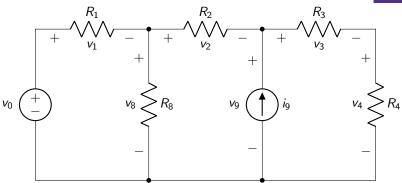
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or

 $-v_1 + v_2 + v_3 - v_4 + v_5 - v_6 = 0$

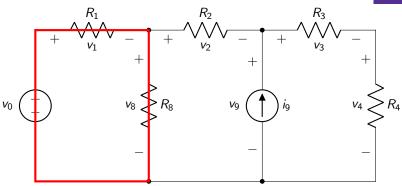
 $v_1 + v_4 + v_6 = v_2 + v_3 + v_5$



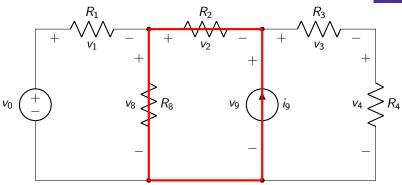


6 Lwps

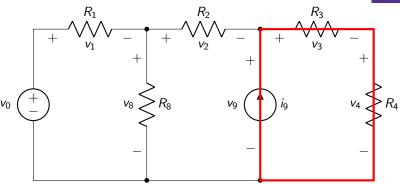




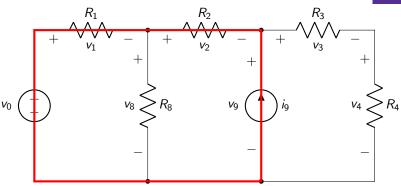




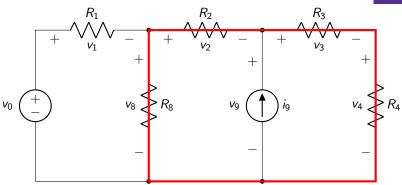




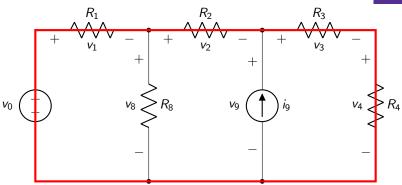














$$v_0 = v_1 + v_8$$

$$v_8 = v_2 + v_9$$

$$v_9 = v_3 + v_4$$

$$v_0 = v_1 + v_2 + v_9$$

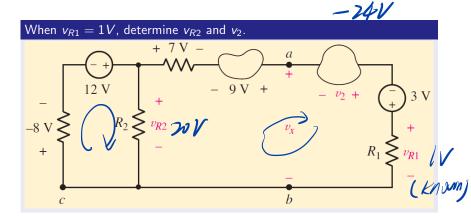
$$v_8 = v_2 + v_3 + v_4$$

$$v_0 = v_1 + v_2 + v_3 + v_4$$

Actually, only 3 equations are necessary. The three others can be deduced from the other ones.

Examples I





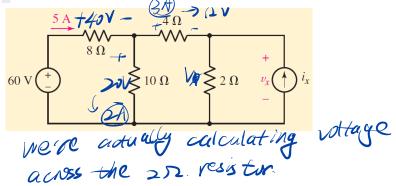
-1+1/2 -7V +9V +12 +3100

Examples II



COMBINE Ohm's Law. EVLX NYUSHANGHAI

Determine v_x in the circuit.



: finally Un=fV

How to analyze circuits by KCL and KVL?



- Identify the goal of the problem.
 Label i or v on the circuit diagram.
- Collect the known information. The nodes, branches, ...
- Oevise a plan KCL and KVL and Ohm's law
- Construct an appropriate set of equations.
 Summing the currents flowing into the node.
- Determine if additional information is required.
- Attempt a solution
- Verify the solution. Is it reasonable or expected? It is always worth the effort to recheck our work.