

05 – Floating Point Numbers

CS1022 – Introduction to Computing II

Dr Jonathan Dukes / jdukes@scss.tcd.ie School of Computer Science and Statistics 32-bits ... 2³² unique values that we can use to represent different things

e.g. unsigned integers

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0 ... 2<sup>32</sup>-1 or
```

0 ... 4,294,967,295

e.g. signed integers using 2's complement

How do we represent real numbers like 3.14 or 2½?

How do we represent values with really large or really small magnitudes?

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e.g. 2.2 x 10<sup>11</sup>
e.g. 1.3 x 10<sup>-8</sup>
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Useful to think first about normalised decimal scientific notation

Convert the following binary numbers to decimal numbers with fractions

```
10010101
1.1
101000.01
```

Convert the following decimal numbers to binary floating point numbers

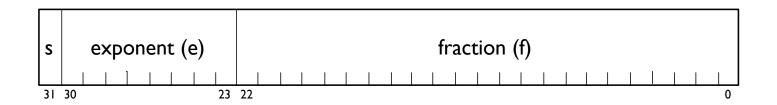
10½

51/4

7.75

2.1

Use a different interpretation of a 32-bit value to represent floating point numbers, e.g. IEEE 754



$$(-1)^s \times f \times 2^e$$

How can we represent ...

... positive and negative values?

... values with positive and negative exponents?

Where is the radix point?

Sign bit?

 $0 \Rightarrow$ positive floating-point number

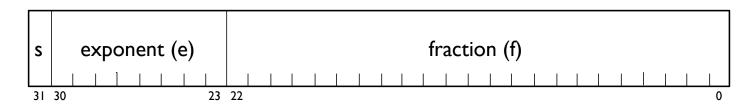
1 ⇒ negative floating-point number

Positive and negative exponents?

Option 1: 2's Complement exponents

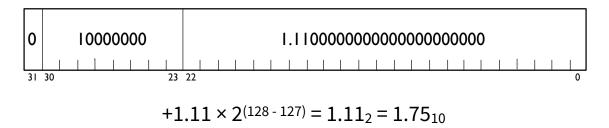
Option 2: Biased exponents

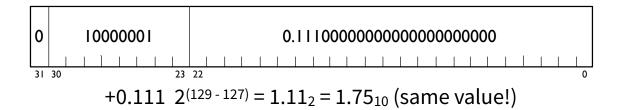
Subtract a constant bias (b) from stored exponent to obtain signed exponent



$$(-1)^s \times f \times 2^{e-b}$$

Assume that the radix point is immediately after the LSB





Don't want multiple representations of the same value! (if $(a == b) \dots$)

Store floating-point numbers in normalised form

1.ddd ... d

Hidden Bit

Normalisation

 0.0101×2^{-4}

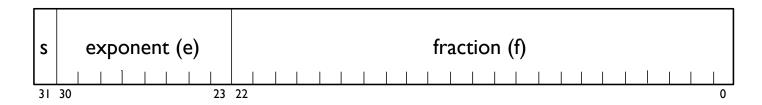
... becomes ...

 1.0100×2^{-6}

adjust fraction so there is a single 1 to left of radix point compensate by adjusting exponent accordingly

If there is always going to be a 1 to the left of the radix point, we don't need to store it!

Increases precision (by one bit) – like not storing the 2 LSBs of a branch target offset!

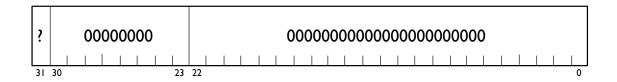


$$(-1)^s \times 1.f \times 2^{(e-b)}$$

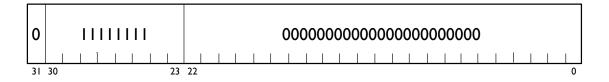
Examples?

Special bit patterns, e.g.

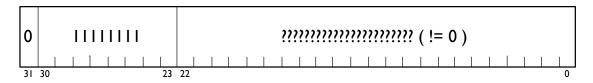
Zero (±)



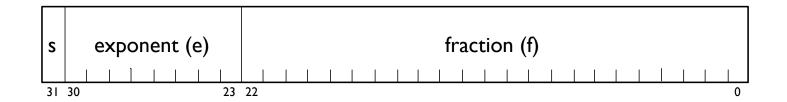
Infinity (±)



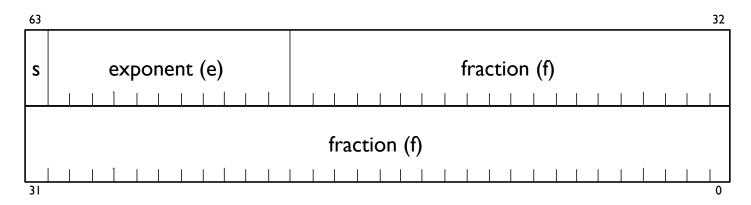
Not a Number (NaN)



32-Bit Single Precision



64-Bit Double Precision



A Closer Loo Warning: Intel

Inside

TALKING BUSINESS

The yield curve has had so many contractions that it about to give birth. The birth ... might be recession.

DIRK VAN DIJK

sorry about Pentium bu

Flawed Chip **Bruises Intel**

Intel Corp. stock dropped sharply yesterday after a minor flaw in the company's Pentium mi-croprocessor triggered a rash of negative publicity for the world's argest chipmaker.

largest chipmaker.

The problem, fixed months ago, received national attention Thursday after two weeks of discussion by researchers and mathematicians on the Internet, the global computer network, and some coverage in high-technology industry rubilizations.

industry publications.

Reacting for the first time, investors sent Intel's stock down 2 percent, or 14, to 63% on the Nasdaq stock market, which was closed for Thanksgiving on Thurs-

"Interesting but irrelevant," said David Wu, an analyst with S.G. Warburg. "These things are quickly forgotten. People won't re-member it by Monday."

The flaw occurred in early ver-sions of the Pentium. Once every few billion calculations, the chip might produce a wrong answer to

San Jose Mercury News

Intel to work on software patch for Pentium bug

will work around the i

Grove says l

Investors react, stock plunges

INTEL'S MISTAKE

But analysts dismissed the idea of a long-term problem for the Santa Clara company.

Faulty FPU flubs math Multithreading gets

Photo finish: Although Photoshop outdoes Picture P Bug Dodge Booed nas an im Intel Knocked For Response On Flaw port issued by Mark Edelstone, an analyst with Prudential Securities Research,

SAN MATEO, CALIF. — have been calculated our or Amid criticism of how it has nine digits beyond the deci-handled a flaw in an early mal point. Such instance arise only in obscure mathematical and aperations for se bandled a flaw in an early version of its Pentium micro-processor, Intel Corp. is prob-lems mounted last week as entific equations and should reports surfaced that the bug could be worse than the com-pany has claimed. The division flaw occurs in The division flaw occurs in

pany has claimed.

The division flaw occurs in tions up to the third signifitions up to the third signifities. cant digit, according to a re-

Intel's Pentium Problem Persist

San Francisco Opronicle

BUSINESS

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at the Uni-s Informa-ne patches L, Page 6F

Chip Shot

Computer Giants' War Over Flaw in Pentium Jolts the PC Industry

ERSONAL TECHNOLOGY

i't Serving Who Bought ium Campaign

Who Is Twisting the Truth? Intel Stands by Product As IBM Halts Shipments

I'd Be Totally Confused'

By Bart Zincurs and Doy CLARY

calling Intel's toll-free number (800-625-6680), even if you're not an MTI professor. Intel doesn't make it easy. But in a test I conducted over



Some Scientists Are Angry Over Flaw CITYLINE: 24 How In Pentium Chip, and Intel's Response

Patterson & Hennessy, "Computer Organisation and Design", 4th edition

Pentium woes continue

in certain equations

lost on PIOO systems

INTEL CORP. PROBABLY FOUND it difficult to find something to be thankful for last week.

IBM to stop shipping **Pentium PCs**

TECHNOLOGY

Intel's Grove Airs Apology for Pentium Over the Internet

But CEO, Defending Policy Not to Replace All Chips, Doesn't Silence Criticism

