MN74HC386/MN74HC386S

Quad 2-Input Exclusive OR Gates

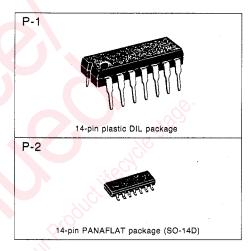
■ Outline

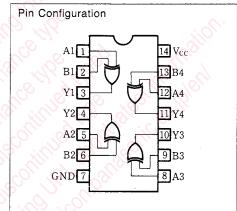
The MN74HC386/MN74HC386S is constituted by 2-input exclusive OR gates having four built-in circuits in one chip. Owing to the silicon gate CMOS process, these OR gates have realized low power consumption and high noise immunity equivalent to those of a standard CMOS and the operation speed as high as of an LS TTL. The respective output can directly drive ten LS TTL inputs.

To protect the input and output against electrostatic breakdown, a resistor and a diode are used for the $V_{\rm CC}$ and the GND. The pin configuration and the function are the same as those of the standard 54LS/74LS logic family.

■ Logic Diagram (1 Gate)







■ Absolute Maximum Ratings

Item			Symbol	Rating	Unit		
Supply voltage			Vcc	-0.5~+7.0	V		
Input output voltage			V _I , V _O	$-0.5 \sim V_{CC} + 0.5$			
Input protective diode current			I_{IK}	±20			
Output parasitic diode current			Іок	±20			
Output current			Io	±25	mA		
Supply current			I _{CC} , I _{GND}	±50	mA		
Storage temperature			T_{stg}	-65~+150	°C		
Power dissipation	MN74HC386	Ta=-40~+60°C	ъ	400	mW		
		$Ta = +60 \sim +85^{\circ}C$	P_D	Decrease to 200mW at the rate of 8mW/°C	7 11144		
	MN74HC386S	Ta=-40~+60°C	D	275	mW		
	WIN /4 TI C 380 S	Ta=+60~+85°C	P_D	Decrease to 200mW at the rate of 3.8mW/°C	111144		

■ Recommended Operating Conditions

Item	Symbol	V _{cc} (V)	Rating	Unit	
Operating power supply voltage	V_{cc}		1.4~6.0	V	
Input output voltage	V _I , V _O		0∼V _{cc}	· V	
Operating temperature	T _A		-40~+85	°C	
		2.0	0~1000	ns	
Input rise, fall time	t _r , t _f	4.5	0~500	. ns	
		6.0	0~400	ns	

■ DC Characteristics (GND=0V)

	Symbol	V _{cc} (V)	Test Condition			Temperature					
Item			Vi	Ţ		Ta=25°C			Ta=-40~+85°C		Unit
				Io	Unit	min.	typ.	max.	min.	max.	
		2.0				1.5			1.5		
Input voltage high level	V_{IH}	4.5				3.15			3.15		V
		6.0				4.2			4.2		
		2.0						0.3		0.3	
Input voltage low level	V_{IL}	4.5					, č	0.9		0.9	V
		6.0					71)	1.2		1.2	
		2.0		-20.0	μΑ	1.9	2.0		1.9		
		4.5	V_{IH}	-20.0	μΑ	4.4	4.5		4.4		
Output voltage high level	V _{OH}	6.0	or	-20.0	μΑ	5.9	6.0		5.9		V
		4.5	V_{1L}	-4.0	mA 💍	3.92	&		3.84		
		6.0		-5.2	mA	5.48	K		5.34	4	
		2.0		20.0	μΑ	CS I	0.0	0.1		0.1	
		4.5		20.0	μΑ		0.0	0.1		0.1	
Output voltage low level	Vol	6.0	V _{IH}	20.0	μ A		0.0	0.1		0.1	V
		4.5		4.0	mA		0	0.26	6	0.33	
		6.0		5.2	mA		, (C	0.26	S (0.33	
Input leakage current	J_{i}	6.0	V _I =V _{CC} or GND		7/1/1	100	±0.1	1.00	±1.0	μΑ	
Static supply current	I_{cc}	6.0	$V_I = V_{CO}$	or GNI	$I_0 = 0$	0, 1	8	2.0	101	20.0	μA

■ AC Characteristics (GND=0V, Input transition time≤6ns, C_L=50pF)

	Symbol	V _{cc} (V)	Test Condition	3° . '\					
Item				Ta=25°C			Ta=-40~+85°C		Unit
			Α,	min.	typ.	max.	min.	max.	
		2.0	110	0	25	75		95	
Output rise time	t _{TLH}	4.5		1,50	8	15		19	ns
		6.0			7	13		16	
		2.0	115, 114		20	75		95	
Output fall time	t _{THL}	4.5			7	15		19	ns
		6.0	16'92 Ville		6	13		16	
		2.0	Q10		25	75		95	
Propagation time $(L\rightarrow H)$	t _{PLH}	4.5			8	15		19	ns
		6.0			7	13		16	
		2.0			25	75		95	
Propagation time (H→L)	t _{PHL}	4.5			8	15		19	ns
		6.0			7	13		16	

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