专业OLED、TFT-LCD模组供应商

业务电话:18601955397 QQ:2534656669

淘宝地址:https://shop73023976.taobao.com

APPROVAL SHEET

承 认 书 记录编号: 版本: v0.0

Customer 客户名称	
Part NO. 产品型号	ZJY144SN005
Product type 产品内容	Mode: Transmissive type .Normally white. TFT LCD Module LCD Module: Graphic 128RGB*128Dot-matrix
Remarks 备注栏	□APPROVAL FOR SEPCIFICATIONS ONLY ■APPROVAL FOR SEPCIFICATIONS AND SAMPLE
Signature by Customer: 客户确认签章	,

中景园确认

核准	审核	定制

客户确认

核准	审核	审核

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1.General Description

Z144SN005 is a 128RGB*128 dots matrix TFT LCD module. It has a TFT panel composed of 384sources and 128gates. The LCM can be easily accessed by micro-controller.

2. Features

Disalar Mada	Transmissive
Display Mode	a-TFT
Display Format	Graphic 128RGB*128 Dot-matrix
Input Data	SPI interface
Viewing Direction	12 o'clock
Drive	ST7735S

3. Mechanical Specification

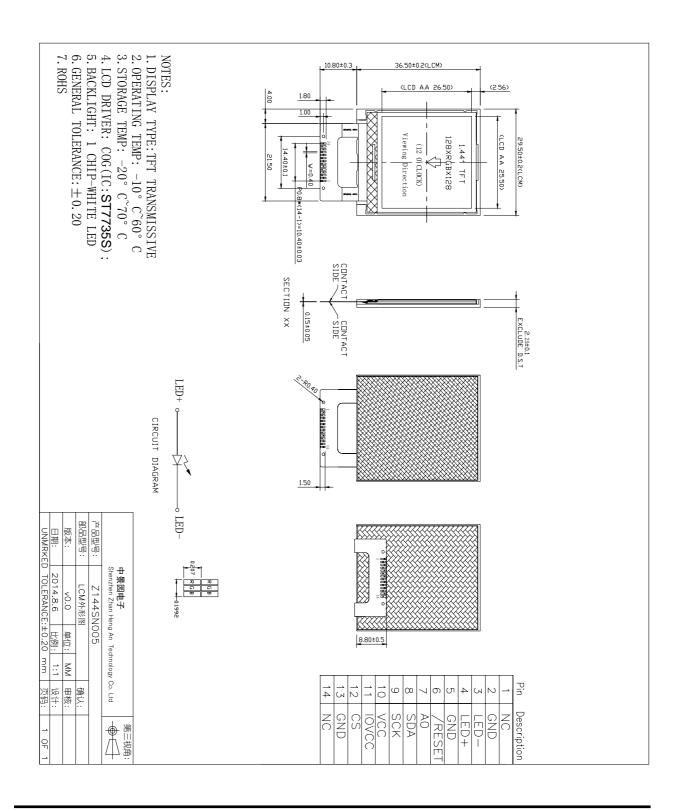
Item	Specifications	Unit
Dimensional south	29.50(W)*36.50(H)*2.25+/-0.1(T)	
Dimensional outline	(FPC not include)	mm
Resolution	128RGB*128	dots
LCD Active area	25.50 (W)*26.50 (H)	mm
Pixel size	0.20(W)*0.20(H)	mm

4. Mechanical Dimension

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5. Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Supply voltage	V	-0.3	4.6	V	
Operating temperature	$\mathbf{V_{T}}$	-0.3	Vcc+0.3	V	
Storage temperature	T _{OPR}	-20	70	°C	
Storage temperature	T _{STR}	-30	80	C	

6. Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Logic	V_{CC}		2.7	2.8	3.3	V
Innut Valtage	H level	T _{IH}		0.8*IOVCC		IOVCC	V
Input Voltage	L level	T_{IL}		-0.3		0.2* IOVCC	v
Storage temp	erature	${ m I_{DD}}$	With internal voltage generation $V_{\rm CC}=2.8V;$ $T_{\rm emp}=25^{\circ}{\rm C}$			TBD	mA

7. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	V_{LED}	3.0	3.2	3.4	V
LED module current	V_{LED}		20		mA
L/G Surface Luminance ★1	$\mathbf{L}_{\mathbf{S}}$	1800			Cd/m³
LCM Surface brightness uniform ★2	L_{D}	80			%

★ 1Test condition is:

- (a) Center point on active area.
- (b)Best Contrast.

★2Uniform measure condition:

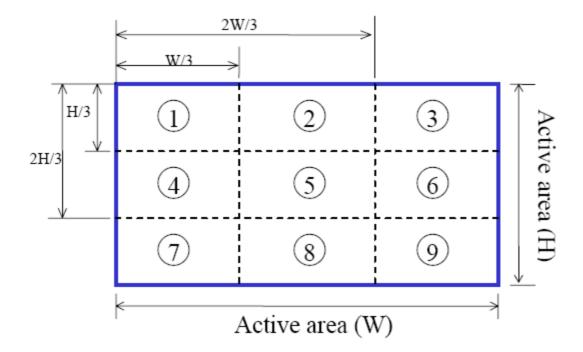
- (1) Measure 9 point. Measure location show below;
- (2)Uniform=(Min. brightness/Max. brightness)*100%

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(3)Best Contrast.



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8. Module Function Description

8.1Pin Descriptions

PIN No.	Symbol	Description	Notes
1	NC	No connection (悬空)	
2	GND	Ground (接地脚)	
3	LED-	Cathode of Backlight (背光负极供电脚)	
4	LED+	Anode of Backlight (3.0V-3.4V) (背光正极供电脚)	
5	GND	Ground (接地脚)	
6	/RESET	LCM Reset pin (屏复位脚)	
7	A0	Register select pin (指令/数据寄存器选择脚) RS='1': Display data. (RS='1':选择数据寄存器) RS='0': Command data. (RS='0':选择指令寄存器)	
8	SDA	Serial data input / output. (串口数据线)	
9	SCK	Serial clock pin. (串口时钟线)	
10	VCC	Power supply for Analog (2.8V-3.3V) (系统电压)	
11	IOVCC	Power supply for interface(1.8V-3.3V) (I/0 口电压)	
12	CS	Chip select pin ("Low" enable) (屏驱动芯片片选脚,低电平有效)	
13	GND	Ground (接地脚)	
14	NC	No connection (悬空)	

NOTE: 1. IOVCC Connected to VCC (应用时可以把 IOVCC 和 VCC 连在一起供 2. 8-3. 3V 的电压。 2. 背光 LED 可以单独供电,也可以和 VCC 共用一组电压供电。

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8.2Timing characteristics.

Serial Interface Characteristics (4-line Serial)

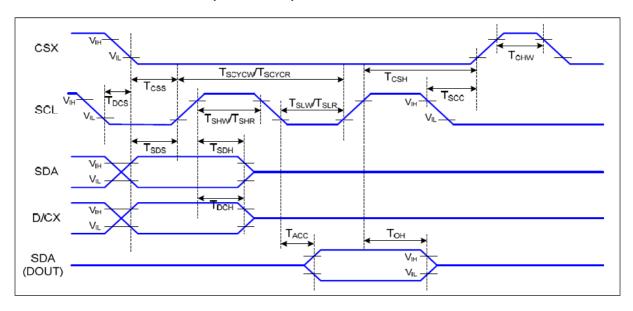


Figure 7 4-line Serial Interface Timing

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Ta=25 °C, VDDI=1.65~3.7V, VDD=2.5~4.8V

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	TCSS	Chip Select Setup Time (Write)	45		ns	
	TCSH	Chip Select Hold Time (Write)	45		ns	
CSX	TCSS	Chip Select Setup Time (Read)	60		ns	
	TSCC	Chip Select Hold Time (Read)	65		ns	
	TCHW	Chip Select "H" Pulse Width	40		ns	
	TSCYCW Serial Clock Cycle (Write)		66		ns	-Write Command &
	TSHW	SCL "H" Pulse Width (Write)	15		ns	Data Ram
SCL	TSLW	SCL "L" Pulse Width (Write)	15		ns	Data Italii
JOL	TSCYCR	Serial Clock Cycle (Read)	150		ns	-Read Command &
	TSHR	SCL "H" Pulse Width (Read)	60		ns	Data Ram
	TSLR	SCL "L" Pulse Width (Read)	60		ns	Data Nam
D/CX	TDCS	D/CX Setup Time	10		ns	
D/CX	TDCH	D/CX Hold Time	10		ns	
SDA	TSDS	Data Setup Time	10		ns	
(DIN)	TSDH	Data Hold Time	10		ns	For Maximum CL=30pF
(DOUT)	TACC	Access Time	10	50	ns	For Minimum CL=8pF
(5001)	ТОН	Output Disable Time	15	50	ns	

Table 7 4-line Serial Interface Characteristics

9. Electro-optical Characteristics

Item	Symbol	Conditions	Tem	p Min.	Typ.	Max.	Unit	Note
Dognongo Timo	T_R	$\theta = \Phi = 0$	25℃		TBD	TBD	msec	NOTE2
Response Time	$T_{\rm F}$				TBD	TBD		NOTEZ
Viewing Angle Range	$\Phi = 0^{0} (6")$	$\phi = 90^{\circ} (3^{\circ})$	")	$\Phi = 180^{\circ}$	(12")	$\Phi = 270^{\circ}$	(9")	NOTE3
θ (25°C) CR≥10	TBD	TBD		TBD	•	TBD		NOTE3

The above "viewing angle" is the measuring position with the largest contrast ratio. Not for good image quality. Viewing direction for good image quality is 12 O'clock.

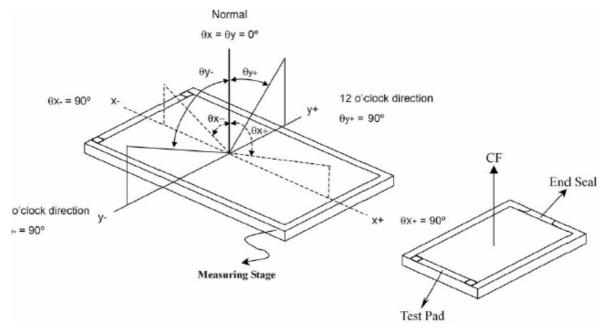
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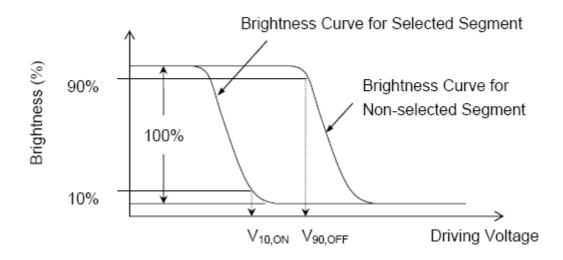
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•For panel only

• Electro-Optical Characteristics Test Method



$$Vop = (V_{10. ON} + V_{90. OFF})/2$$

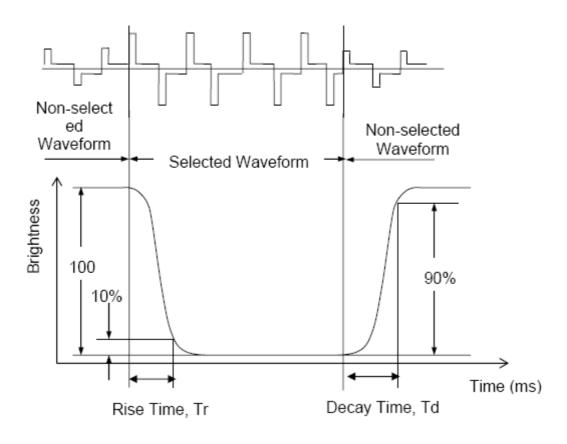


.Note2.Definition of Optical Response Time:

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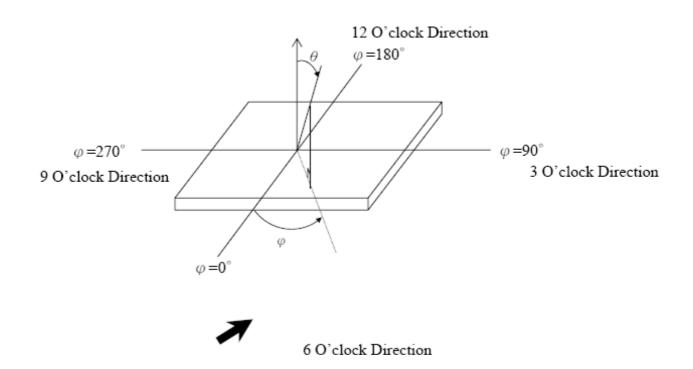
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. Note3.Definition of Viewing Angle θ and Φ :

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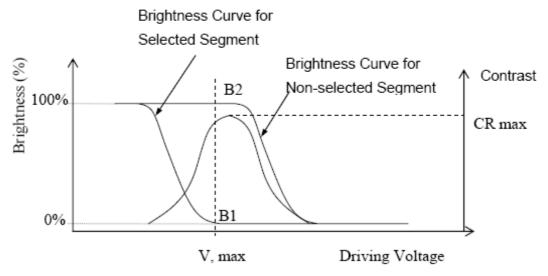


Note4.Definition of Contrast ratio (CR):

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CR = Brightness of Non-selected Segment (B2)
Brightness of Selected Segment (B1)



10. Reliability

10.1Mtbf

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal

10.2Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*240Hrs	No Defect Of Operational
2	Low Temperature Non-Operating Test	-30°C*240Hrs	Function In Room Temperature
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*240Hrs	Are Allowable
4	High Temperature Operating Test	70°C*240Hrs	。 IDD of LCM in Pre-and
5	Low Temperature Operating Test	-20°C*240Hrs	Post-Test Should Follow
	Th	-20°C (30Min) ↔70°C (30Min)	Specification
6	Thermal Shock Test	*10CYCLES	

Notes:

- 1. Judgments should be made after exposure in room temperature for two hours.
- 2. The distill water is used for the high temperature/humidity test.
- 3. The sample above is individually for every reliability tests condition.

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11.Inspection standards

1.AQL(Acceptable Quality Level

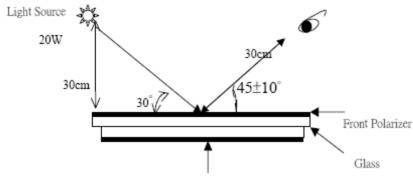
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000 ± 200 .(Darkroom's lux: 100 ± 50), About an angle of incidence 30, a distance of 30 cm with an angle of 45 degree to check the products without uncovering the film!

(As shown below)



Rear Polarizer

3.Inspection item and criteria

3.1 Visual inspection criterion in immobility

3.1.1Glass defect

NO	Defect item	Criteria	Remark
1	Dimension	By Engineering Drawing	
1	Unconformity		
	(Major defect)		
	Cracks	1. Linear cracks panel	N
	(Major defect)	【Reject】	
2		2. Nonlinear crack contrast by	
		limited sample	
			_
	Glass extrude the	a: disregards and no influence	A: Length, b: Width
3	conductive area	assemblage.	
	(minor defect)	1) b≤1/3Pin width(non bonding	

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	Pin-side ,conductive area damaged	area) [Accept] 2)bonding area≤0.5mm [Accept] (a c: disregards) b≤1/3of effective length for bonding electrode	a: length, b: Width, c: Thickness
4	(minor defect)	[Accept]	T D
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark) [Accept] 2)C <t 3)c="T" 3of="" 4)a="" [accept]="" b="" bm1="" disregards<="" glue="" not="" seal="" td="" the="" touch="" width="" ≤=""><td>a: Length, b: Width c: Thickness</td></t>	a: Length, b: Width c: Thickness
6	Non-pin-side damage (minor defect)	c <t 1="" 1)b="" 3bm="" [reject]="" [reject]<="" b="" c="T" exceeds="" glue="" not="" seal="" td="" the="" touch=""><td>c: Thickness b: width of BM 內緣 damage</td></t>	c: Thickness b: width of BM 內緣 damage

3.1.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
	1 cratch, polarizer scratch/folded (minor defect)	Specification	Allowable	note1:L: Length, W: Width
		W≤0.03mm disregard note2:	note2: disregard if out of AA	
		$0.03 \text{mm} < W \le 0.05 \text{mm};$	2	
1		L ≦ 3.0mm		
		0.05 mm $<$ W \leq 0.1mm;	1	
		L ≦ 3.0mm		
		W>0.1mm;L>3.0mm	0	W

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	Polarizer bubble	φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$, L:Length,	
2		0.2 mm $< \Phi \le 0.3$ mm	2	W :Width	
2	concave and convex (minor defect)	0.3 mm $< \Phi \le 0.5$ mm	1	note2:disregard if out of AA	
	(minor defect)	0.5mm< φ	0		
	φ ≦0.15mm		disregard	note2:disregard if out of AA	
3	Black dots, dirty dots, impurities, eye winker	0.15 mm $< \phi \le 0.25$ mm	2	$\bigcirc \qquad \ \ \downarrow \phi$	
		0.25 mm $< \phi \le 0.3$ mm	1		
	(minor defect)	0.3mm< φ	0	φ	
		φ ≤ 0.1mm	disregard	note1: $\Phi = (L+W)/2$, L=Length,	
4	Polarizer prick	0.1 mm $< \Phi \le 0.25$ mm	3	W=Width	
4	(minor defect)	1 > 0 = 0.5	0	note2:the distance between two	
		φ>0.25mm	0	dots>5mm	

3.1.3FPC

NO	Defect item	Criteria		Remark
	Copper screen peel	Copper screen peel		
(minor defect)			【Reject】	
1				
2	No release tape or peel	No release tape or peel		
2			【Reject】	
	Dirty dot and impurity of FPC	Specification	Allowable	Note1: Cannot have stride
3	for customer using side	Φ ≦0.25mm	2	ITO impurities
	(minor defect)	ф>0. 25	0	

3.1.4Black tape &Mara tape

NO	Defect item	Criteria Remark
	FPC or H/S black tape	1. shift spec:
		1) glue to the polarize
		【Reject】 ↓ ↓ × ↓
1	(minor defect)	2) IC bare 【Reject】 y1
1		2. left-and-right spec:
		1)exceed of FPC edge or Mara tape
		H-S edge [Reject]
		2) IC bare [Reject] Hast Sasl

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2	No black tape	No black tape	
2	(major defect)	【Reject】	
Tape position mistake		Not by engineering drawing	
3	(minor defect)		
	Mara tape defect	Peel before pulling the	
4	(minor defect)	protecting film	
		【Reject】	

3.1.5Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon	Uncover the ITO and circuit area	note: compared by engineering
	(major defect)	【Reject】	
2	Taffy glue	1.Uncover the reveal copper area Reject	note: if customer has special
	(major defect)	2.Cover layer 0.3mm(Min)~3.0mm(Max)	requirement, refer to the technical
		【Reject】	document
			3.0mm(Max)
3	Depth of glue covering	Depth of glue covering overtop front	Except of the special requirement
	(major defect)	Polarizer 【Reject】	

3.2Electrical criteria

NO	Defect item	Criteria	Remark
1	No display	No display	
	(major defect)	【Reject】	
2	Missing line	Missing line	
	(major defect)	【Reject】	
3	Seg-com light and dark	Seg-com light and dark	ND filter 2% test
	(major defect)	【Reject】	
4	No display in immobility	No display in immobility	
	(major defect)	【Reject】	
5	Flicker of Pattern	Flicker of Pattern	
	(major defect)	【Reject】	
6	Mura	ND filter 2%test	
	(major defect)		
7	Over current	Over current	

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	(major defect)	【Reject】		
8	Voltage out of specification	Voltage out of		
	(major defect)	specification		
		【Reject】		
9	Pattern blur, error code	Pattern blur, error code		
	(major defect)	【Reject】		
10	Dark light, Flicker	Dark light, Flicker		
	(major defect)	【Reject】		
11	Black/white dots . Dirty	Specification	Allowable	Note1:disregard if out of AA
	dots, eye winker	φ ≦ 0.15mm	disregard	$\bigcirc \qquad \downarrow \phi$
	(major defect)	0.15 mm $< \Phi \le 0.25$ mm	2	
		0.25 mm $< \phi \le 0.3$ mm	1	-
		0.3mm< ф	0	ϕ
12	Fiber glass crutch Polarizer	W ≤ 0.03mm	disregard	Note1:L: Length, W: Width
	scratch/folded	$0.03 \text{mm} < W \le 0.0.05 \text{mm}$	2	Note2: disregard if out of AA
	(major defect)	L≤3.0mm	2	← т →
		0.05 mm $<$ $\mathbb{W} \leq 0.1$ mm	1	
		L≤3.0mm	1	V W
		W>0.1mm;L>3.0mm	0	w

12.Precautions for using LCD modules.

12.1 Safety

- (1)Do mot swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

12.2Srorang Conditions

- (4)Store the panel or module in a dark place where the temperature is 23 ± 5 °C and the humidity is below 45 ±20 %RH.
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.

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(7) Do not place the module near organics solvents or corrosive gases.

(8))Do not crush, shake, or jolt the module.

12.3Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle if very carefully.
- (11) Do not give external shock.
- (12)DO mot apply excessive force on the surface.
- (13) Bo not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

12.4Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.

13.Factory

FACTORY NAME:

FACTORY ADDRESS:

FACTORY PHONE:

14. Revision history

Version	Revise record	Date
v0.0	Original version	2014-06-16

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