

A

B

C

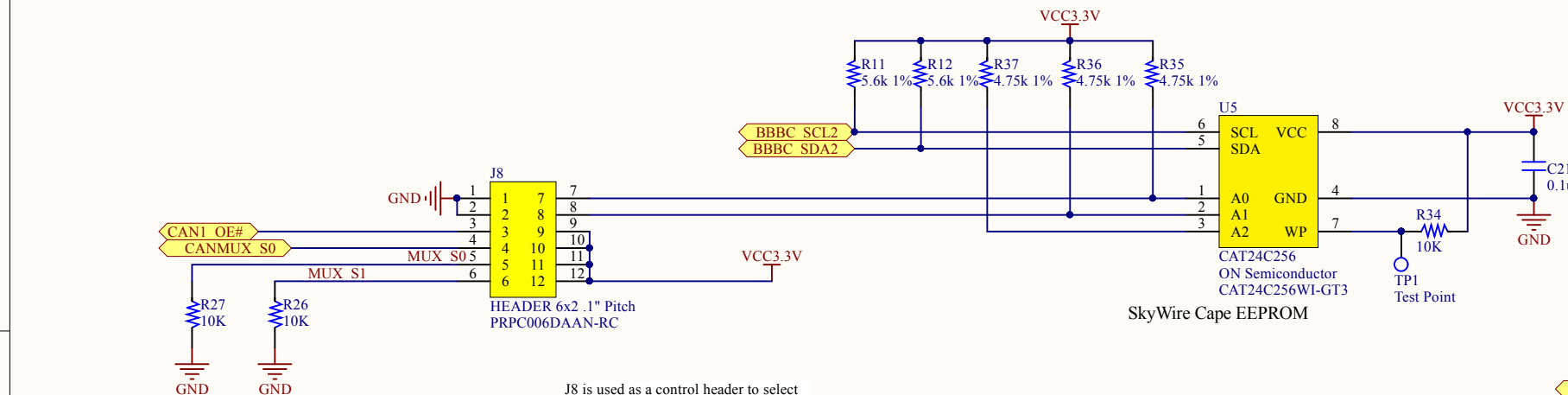
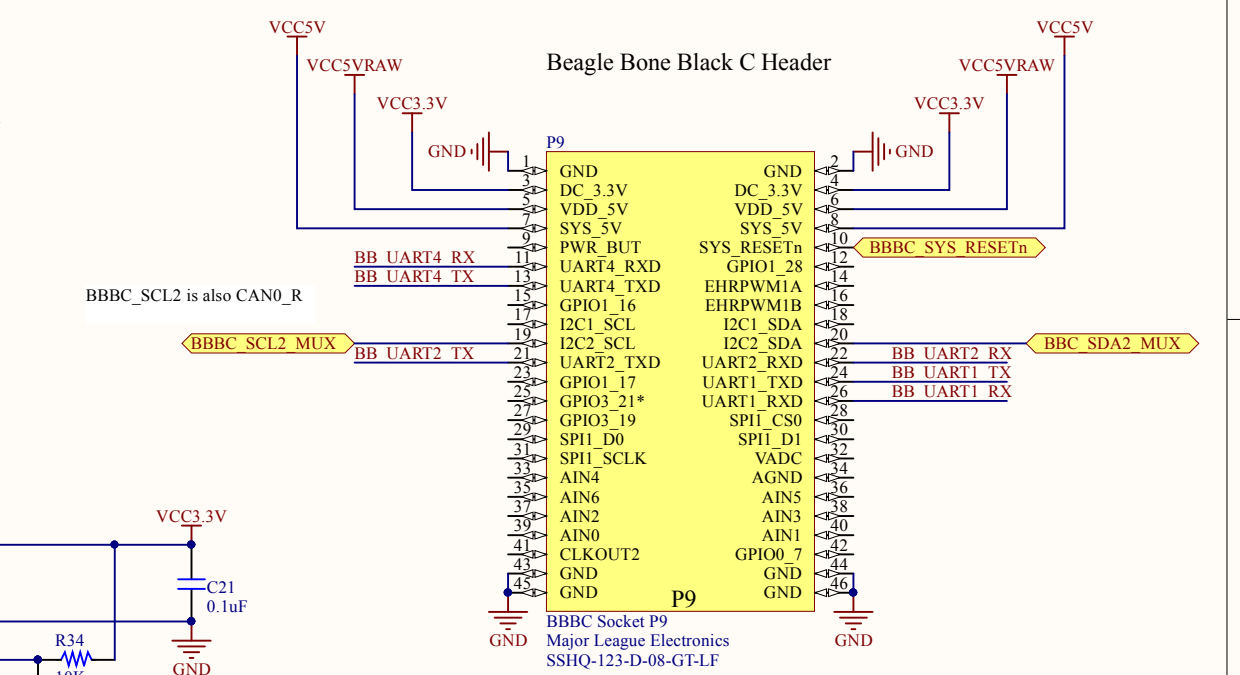
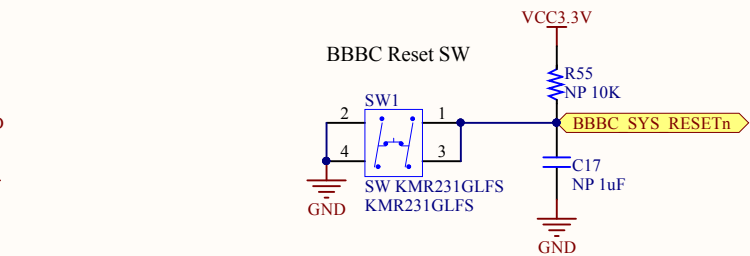
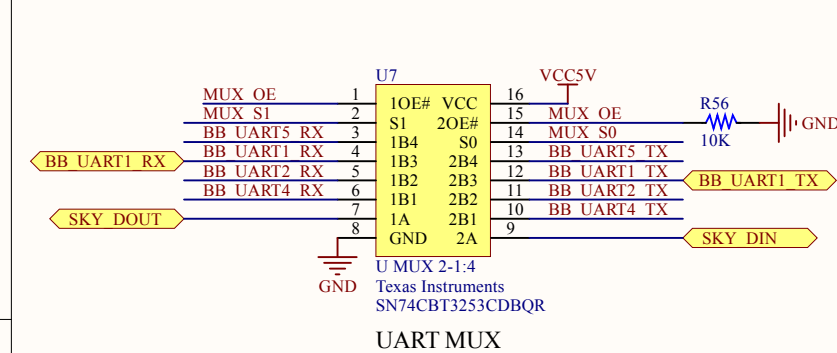
D

A

B

C

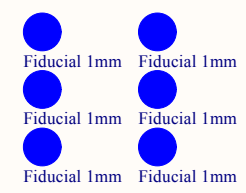
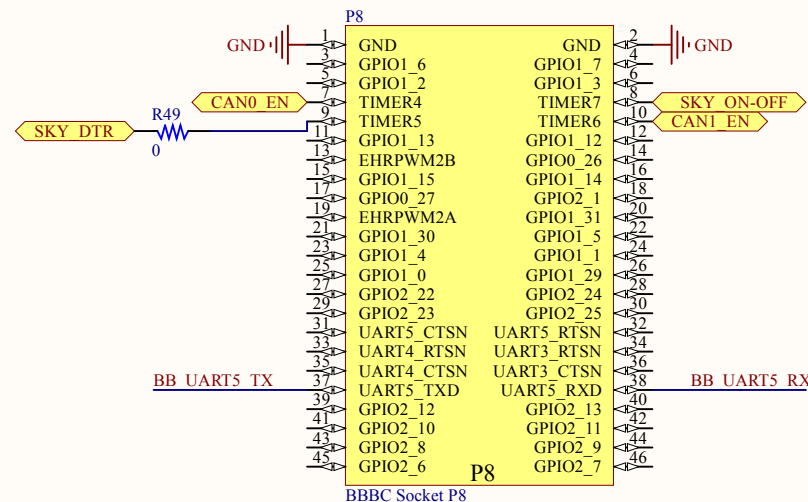
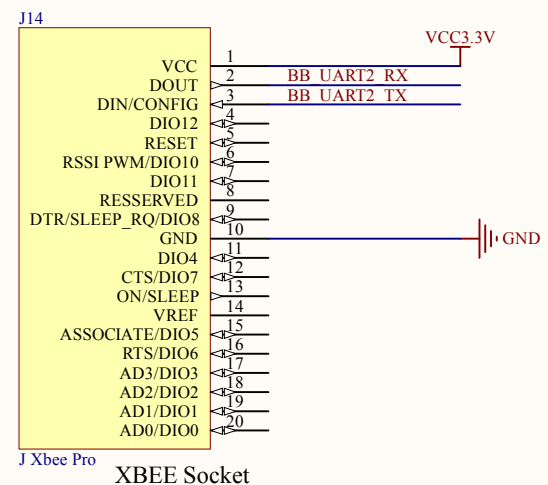
D



UART MUX Channel Connection Truth Table			
Jumper Output			UART CONNECTION *Default connection is UART4
OE#	MUX S1	MUX S0	
L	L	L	UART4
L	L	H	UART2
L	H	L	UART1
L	H	H	UART5

H=High, L=Low

J8 is used as a control header to select the Capes EEPROM address, control configurations of the onboard MUXes (U7/U10), and to control the CAN1 buffer(U11).



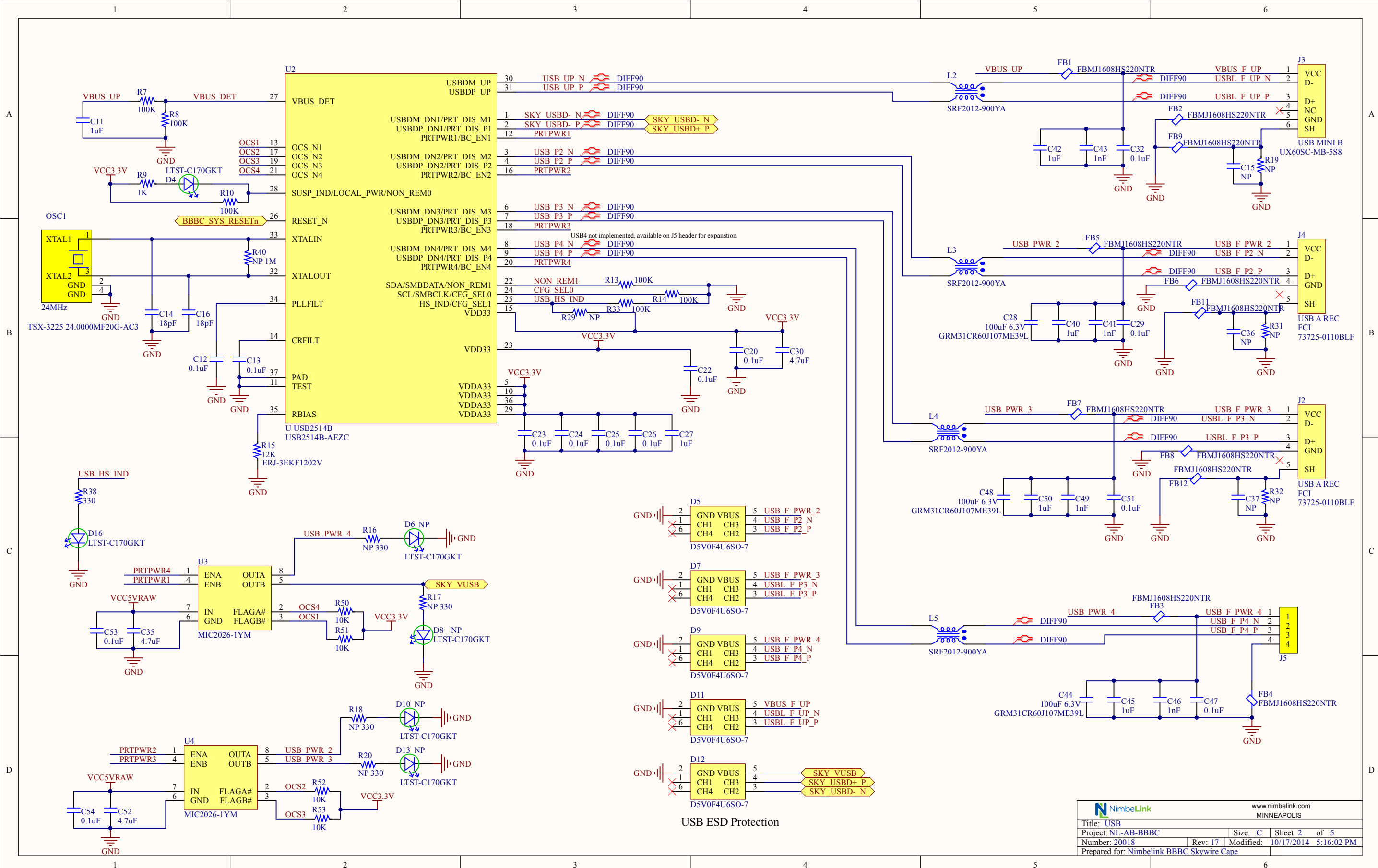
CAN MUX & Buffer Jumpers

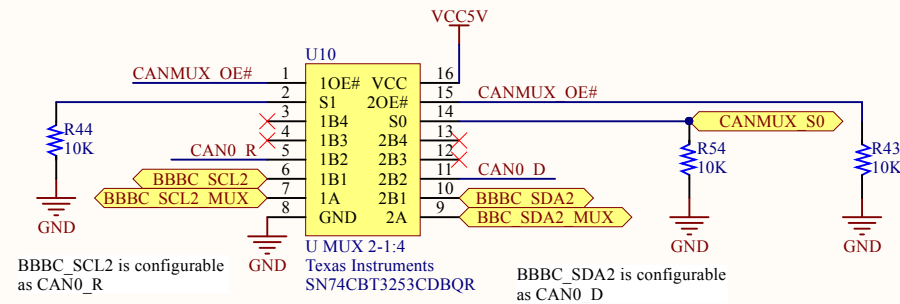
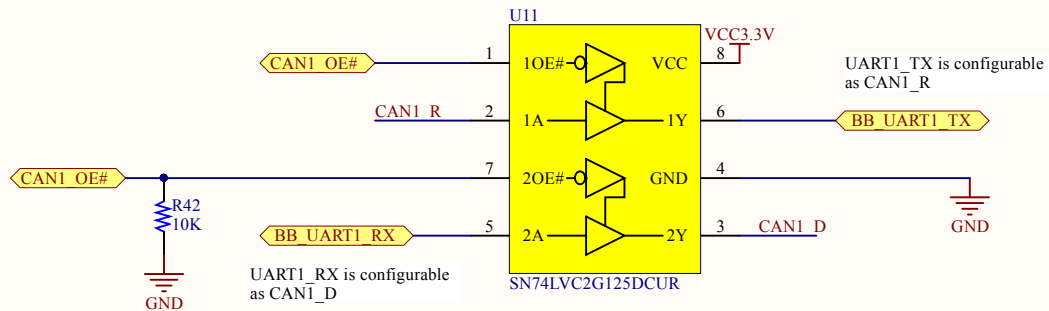
The CAN MUX & buffer is located on page 5 and are controlled by J12 on page 1.

CANMUX is used as a buffer for I2C2, CAN0, and UART5 CTS/RTS to avoid pin contention issues. When CANMUX_OE# is HIGH the connections (CAN0, I2C2) are not connected and are in a high impedance state.

The I2C2 connection connects the capes EPROM to the Beagle Bone Black and is enabled by default.

CAN1_OE# is used as a buffer for the CAN1 transceiver to prevent pin conflicts with UART1. It is enabled by default. Short J12-4 with a jumper to disable.





CAN MUX & Buffer Jumpers

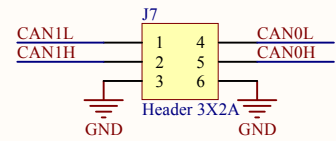
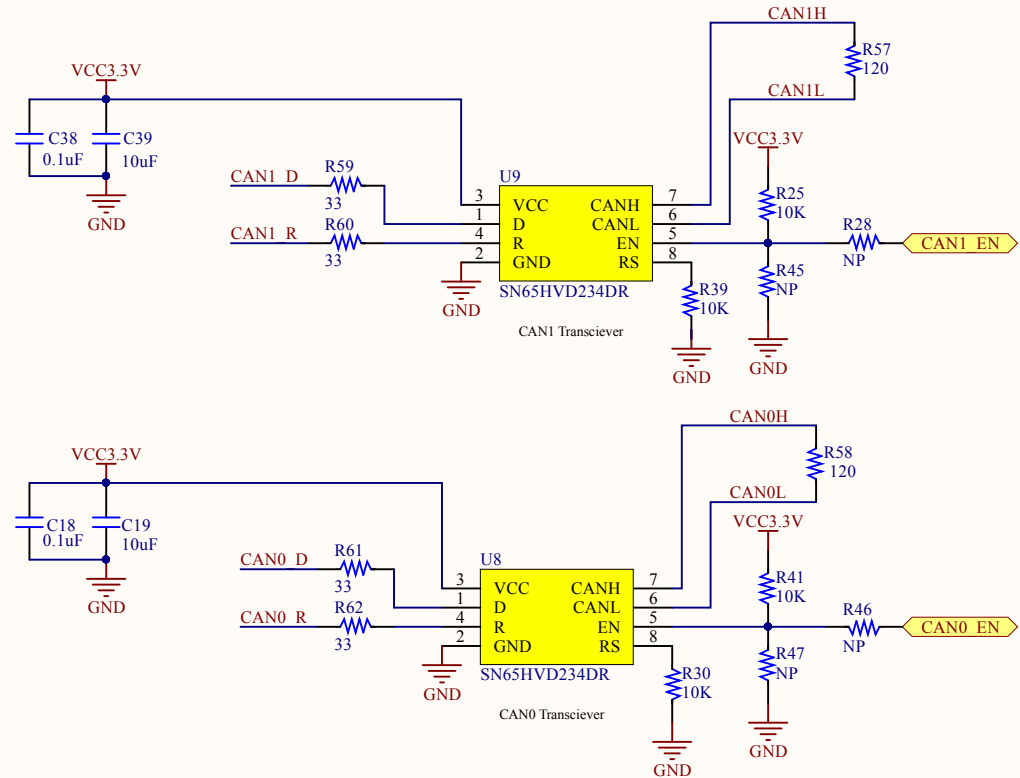
CANMUX is used as a buffer for I2C2, CAN0, and UART5 CTS/RTS to avoid pin contention issues. When CANMUX_OE# is HIGH the connections (CAN0, I2C2) are not connected and are in a high impedance state.

The I2C2 connection connects the capes EPROM to the Beagle Bone Black and is enabled by default.

CAN1_OE# is used as a buffer for the CAN1 transceiver to prevent pin conflicts with UART1.

CANMUX Channel Connection Truth Table	
Jumper Output	UART CONNECTION
S0	*Default connection is I2C2
L	I2C2
H	CAN0

H=High, L= Low, X = Don't care



J7 is an unpopulated 2x3 pin header (0.1" spacing) to connect to CAN0/CAN1

