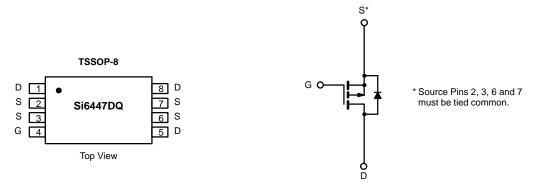


P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)	
-20	0.09 @ V _{GS} = -10 V	±3.2	
	$0.16 @ V_{GS} = -4.5 V$	±2.4	



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter Drain-Source Voltage		Symbol	Limit	Unit		
		V _{DS}	-20	V		
Gate-Source Voltage		V _{GS}	±20			
Continuous Drain Current (T,I = 150°C) ^a	T _A = 25°C	l _D	±3.2			
Continuous Diam Curient (1) = 100 C)	T _A = 70°C	.b	±2.5			
Pulsed Drain Current		I _{DM}	±20			
Continuous Source Current (Diode Conduction) ^a		I _S	-1.7			
Maximum Power Dissipation ^a	T _A = 25°C	PD	1.5	w		
waxiituiti towei Dissipatioti	T _A = 70°C	ט י	1.0	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Maximum Junction-to-Ambient ^a	R _{thJA}	83	°C/W		

Notes

a. Surface Mounted on FR4 Board, $t \le 10$ sec.

 $For \ \ SPICE \ model \ information \ via \ the \ Worldwide \ Web: \ http://www.vishay.com/www/product/spice.htm$

Vishay Siliconix



SPECIFICATIONS (T _J =25°C UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Static	•		•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.0			٧	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zava Cata Valtaga Drain Current	<u> </u>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			-1	1	
Zero Gate Voltage Drain Current	l _{DSS}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70^{\circ}\text{C}$			- 5	μA	
On-State Drain Current ^a	I=	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-14				
On-State Drain Currents	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-2.5			^	
B 1 0 0 0 1 1 0 1 1 0		$V_{GS} = -10 \text{ V}, I_D = 3.2 \text{ A}$		0.060	0.09	Ω	
Drain-Source On-State Resistance ^a	r _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = 2.0 \text{ A}$		0.100	0.16		
Forward Transconductance ^a	9fs	$V_{DS} = -15 \text{ V}, I_D = -3.2 \text{ A}$		4.0		S	
Diode Forward Voltage ^a	V _{SD}	$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		-0.9	-1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			15	25	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, \ V_{GS} = -10 \text{ V}, \ I_D = -3.2 \text{ A}$		3			
Gate-Drain Charge	Q _{gd}			3.5			
Turn-On Delay Time	t _{d(on)}			13	40	ns	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D \cong -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$		13	25		
Turn-Off Delay Time	t _{d(off)}			30	50		
Fall Time	t _f			13	20		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = -1.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		50	100	1	

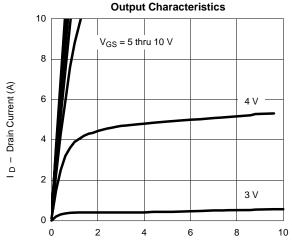
Notes a. Pulse test; pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. b. Guaranteed by design, not subject to production testing.





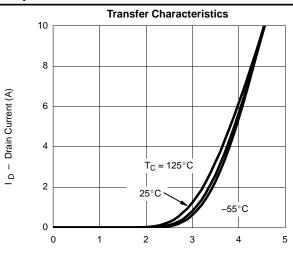


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

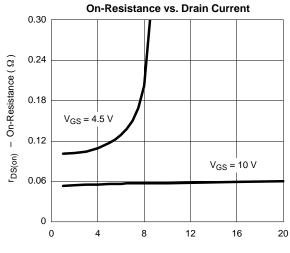


V_{DS} - Drain-to-Source Voltage (V)

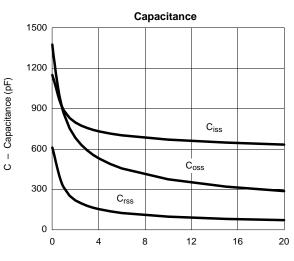




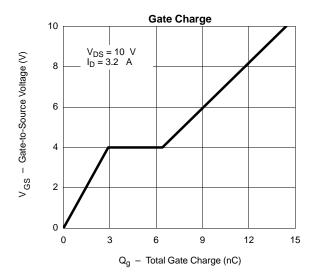
V_{GS} - Gate-to-Source Voltage (V)

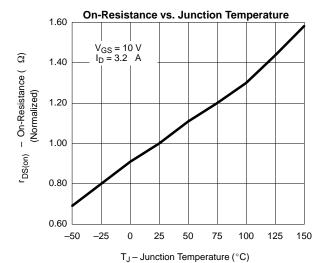


I_D - Drain Current (A)



 V_{DS} - Drain-to-Source Voltage (V)

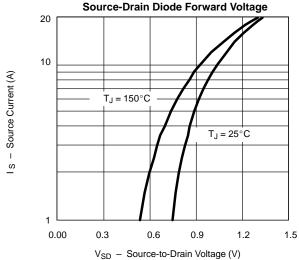


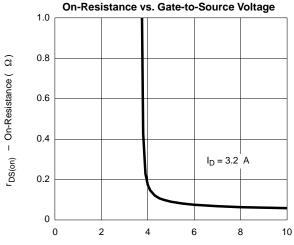


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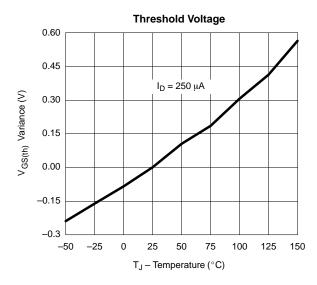


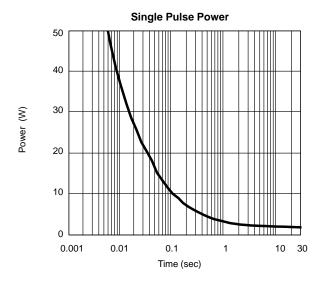
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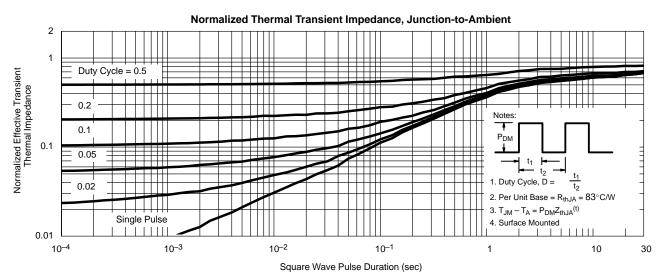














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Document Number: 91000 Revision: 18-Jul-08