

FAB NOTES:

1. ALL DIMENSIONS ARE IN INCHES, UNLESS OTHERWISE NOTED.
2. THE PWB SHALL BE FABRICATED TO PC-6012, CLASS 2 AND WORKMANSHIP SHALL CONFORM TO PC-A-600, CLASS 2, CURRENT REVISIONS.
3. BOARD MATERIAL SHALL BE 180 Tg/350 Td ISOLA FR-370HR OR EQUIVALENT, ROHS COMPLIANT AND LEAD FREE ASSEMBLY CAPABLE. BOARD MATERIAL SHALL MEET OR EXCEED PC-4101B, COLOR: NATURAL.
4. BOARD MATERIAL & CONSTRUCTION TO BE ULL, APPROVED AND MARKED ON THE FINISHED BOARD.
5. MINIMUM COPPER THICKNESS OF PLATED-THRU HOLES TO BE .001 INCH WITH A MINIMUM ANNUAL RING OF .0003 INCH.
6. OVERALL BOARD THICKNESS TO BE .125 \pm .010 INCH.
7. MAX. WARP & TWIST TO BE .0075 INCHES PER INCH.
8. BOARD MUST BE ELECTRICALLY TESTED USING SUPPLIED PC-D-356 NETLIST.
9. ALL VIAS TO HAVE SOLDERMASK.

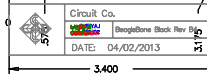
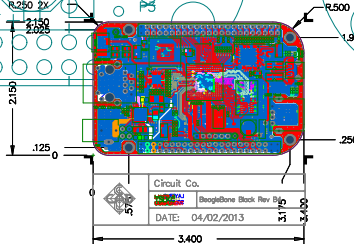
FINISHED AS SMOOTH WALL BY VEDOR. PROCESS NOTES:

1. PLATE ALL EXPOSED AREAS WITH ELECTROLESS AMERSON GOLD, INCHES 150 MICRONS THK MIN. GOLD 5-15 MICRONS THK MIN.
2. APPLY UPI SOLDERMASK OVER BARE COPPER (SMOBC), COLOR: BLACK. SOLDERMASK SHALL CONFORM TO PC-SM-840, CLASS H, CURRENT REV.
3. SOLDERMASK ARTWORK HAS ZERO (0) OVERSIZED PADS. FABRICATION VENDOR IS ALLOWED TO ADJUST THE COMPONENT SOLDERMASK PADS TO MEET UPI TOLLING REQUIREMENTS.
4. APPLY UPI SILKSCREEN OR EQUIVALENT PER THE ARTWORK. COLOR: WHITE.

LAYER STACK-UP - ALL DIMENSIONS IN INCHES

LAYER	COPPER WEIGHT	THICKNESS	THICKNESS	THICKNESS
LAYER 1 - PRIMARY SIDE - SIGNAL	HAUT-PLATING	4.75	4.5/6.5	3.75/7.25
LAYER 2 - GROUND PLANE	1	5.25	5.0/7.0	4.00/8.00
LAYER 3 - SIGNAL	1	5.25	5.0/7.0	4.00/8.00
LAYER 4 - SIGNAL	1	5.25	5.0/7.0	4.00/8.00
LAYER 5 - SPLIT POWER PLANE	1	5.25	5.0/7.0	4.00/8.00
LAYER 6 - SECONDARY SIDE - SIGNAL	HAUT-PLATING	4.75	4.5/6.5	3.75/7.25

ASSEMBLY COMPONENT SIDE



ASSEMBLY SOLDER SIDE

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APPROVED	Circuit Co.
CHECKED	Circuit Co.
DRAFTING	Circuit Co.
ENGR	Circuit Co.
DESIGN ENGR	Circuit Co.
PROJECT ENGR	Circuit Co.
ENGR MGR	Circuit Co.
DATE	04/02/13
SCALE	NONE
SHEET	1 OF 1
PCB REV	B4
SIZE	D