

A Bit-String Longest-Common-Subsequence Algorithm

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Abstract: A longest-common-subsequence algorithm is described which operates in terms of bit or bit-string operations. It offers a speedup of the order of the word-length on a conventional computer.

Keywords: longest common subsequence, edit distance, bit string

1. Introduction.

The *longest-common-subsequence* (LCS) problem is to find the maximum possible length of a common subsequence of two strings, 'a' of length l_a and 'b' of length l_b . Usually an actual LCS is also required. For example, using the alphabet A, C, G and T of genetic *bases*, an LCS of 'GCTAT' and 'CGATTA' is 'GTT' of length three.

Here an algorithm which requires $O(l_a \times l_b)$ operations on single bits or $O(\lceil l_a/w \rceil \times l_b)$ operations on w -bit computer words or $O(l_b)$ operations on l_a -bit bit-strings, for a fixed finite alphabet, is presented. Although falling into the same complexity class as simple LCS algorithms, if w is greater than any additional multiplicative cost, this algorithm will be faster. If $l_a \leq w$ the algorithm is effectively linear in l_b . (An alphabet larger than l_b can be effectively reduced to l_b by sorting 'b' in time $O(l_b \times \log(l_b))$ and using index positions in the sorted string.)

The LCS problem is related to the *edit-distance* [11] or *evolutionary-distance* problem [9] [10] where the minimum cost of editing string 'a' to string 'b' must be found. The elementary edit operations are to insert or delete one character or to change one character. There is a cost function, $d(.,.)$, which can be extended to strings in the obvious way. A common choice for the elementary edit-costs is

$$\begin{aligned} d(\alpha, \alpha) &= 0 \\ d(\alpha, \beta) &= 2 \text{ if } \alpha \neq \beta && \text{cost of changing character } \alpha \text{ to } \beta \\ d(\alpha, -) &= 1 && \text{cost of deleting } \alpha \\ d(-, \alpha) &= 1 && \text{cost of inserting } \alpha \end{aligned}$$

and then

$$d(a, b) = l_a + l_b - 2 \times \text{LCS}(a, b)$$

The LCS problem and the edit-distance problem find applications in computer-science and molecular-biology [8] [9] [10] [11] [12]. An LCS algorithm can compare two files and, by finding what they have in common, compute their differences. It can be used to correct spelling errors and to compare two genetic sequences for homology (similarity).

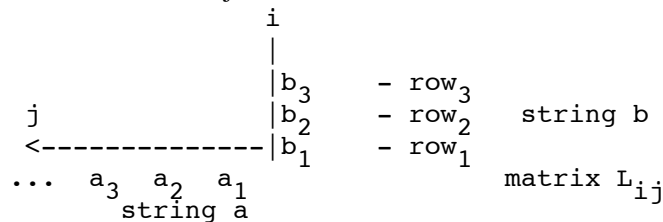
Under plausible assumptions [1] [13], the worst case complexity of an LCS or edit-distance algorithm on strings over an infinite alphabet must be $O(l_a \times l_b)$ time. Masek and Paterson [5] give an $O(l_a \times l_b / \log(\min(l_a, l_b)))$ edit-distance algorithm for strings over a finite alphabet with modest restrictions

on $d(.,.)$. However this is faster than simple algorithms only for strings longer than about 200,000 characters. For special cases there are faster algorithms: for similar strings [3] [6], and for strings with few matching characters [4], but their worst-case running time is at least $O(|a| \times |b|)$.

The algorithm presented here certainly has complexity $O(|a| \times |b|)$ for a finite alphabet, but asymptotically the improvement in speed is close to w . The algorithm is insensitive to the number of matches between strings and to their similarity.

1.1. Matrix L.

Conventionally a matrix L_{ij} is defined:



L_{ij} equals the length of an LCS of $a_{1..j}$ and $b_{1..i}$

$$L_{ij} = 1 + L_{i-1, j-1} \quad \text{if } a_j = b_i$$

$$L_{ij} = \max(L_{i-1, j}, L_{i, j-1}) \quad \text{otherwise}$$

This leads to simple dynamic-programming algorithms [8] upon which this one is based. A reflected representation of matrix L is typically used; the above representation is chosen to make the reading of certain bit-strings easier.

L has many interesting properties:

$$L_{i-1, j-1} \leq L_{i, j-1}, L_{i-1, j} \leq L_{i, j}$$

$$|L_{ij} - L_{i-1, j-1}| \leq 1$$

The rows and columns of L_{ij} contain ascending values for increasing i and j . This prompted Hunt and Szymanski [4] and Hirschberg [3] to "contour" L and develop fast algorithms for special cases.

2. Bit-String Algorithm.

The values in the rows of L, increase by at most one. This makes it possible to represent the information in L by a bit-matrix:

[illegible]

$$L_{ij} = \sum_{k=1..j} M_{ik}$$

Row₁ has either the same number of bits set or one more bit set than the previous row, row_{i-1}. New bits are set towards the left of a row. The length of an LCS of 'a' and 'b' is the number of bits in the top row. The 1's in a particular row tend to drift to the right as we look (up) at the next row. This is because as more of 'b' is used, an LCS of a given length can be found using no more of, and possibly less of, 'a'. The 1's mark the contour lines of matrix L.

2.1. Alphabet-Strings.

Each letter in the alphabet defines a bit-string when it is compared with the elements of 'a':

a: G T C T T A C A T C C G T T C G

A-string: 0 0 0 0 0 1 0 1 0 0 0 0 0 0 0 0

C-string: 0 0 1 0 0 0 1 0 0 1 1 0 0 0 1 0

G-string: 1 0 0 0 0 0 0 0 0 0 0 1 0 0 0 1

T-string: 0 1 0 1 1 0 0 0 1 0 0 0 1 1 0 0

Precomputing these *alphabet-strings* contributes $O(|\text{alphabet}| \times |\text{al}/\text{w}| + |\text{al}|)$ to the time complexity. For a fixed alphabet this is $O(|\text{al}|)$; for a non-fixed alphabet this could be $O(|\text{al}| \times |\text{bl}|)$ at worst. If the alphabet is small, $|\text{alphabet}| \ll |\text{bl}|$, the contribution to the total time can be ignored.

2.2. Matrix M

To calculate row_i , we use the i^{th} character in string 'b', b_i , to select the b_i -string from the set of alphabet-strings. The 1's in row_{i-1} "cut" the b_i -string into segments:

```
row10:    1 0 0 0 0 0 0    1 0 0 0    1    1    1    1    1
T-string:  0 1 0 1 1 0 0    0 1 0 0    0    1    1    0    0
```

Each segment extends from the position of a 1 in row_{*i-1*} rightward to the position to the left of the next 1. If the left-hand bit of row_{*i-1*} is zero, the left-most segment extends from the left of the *b_i*-string to the position left of the first 1. Row_{*i*} is formed by setting only the right-most 1 bits in each segment of the *b_i*-string. If a segment is all zero, the bit defining the left end of the segment should be set in row_{*i*} (that is the segmenting bit in row_{*i-1*}). This can be ensured by first or-ing row_{*i-1*} into the *b_i*-string.

T-string Or row₁₀:

1	1	0	1	1	*	0	0	1	1	*	0	0	1	*	1	*	1	*	1	*	1	*
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

row₁₁:

0	0	0	0	1	0	0	0	0	1	0	0	0	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

* indicates a right-most 1

It may be convenient to imagine an invisible 1 at position $l+1$ for a left-most segment which is zero, but this is unnecessary for the algorithm.

A 1 in row _{$i-1$} marks the shortest piece of 'a' that can be used to make an LCS of a certain length with $b_{1..i-1}$. Bringing b_i into use, the best that can be done to extend that LCS is to use the first b_i in 'a' to the left of the 1, if possible. This is marked by the right-most 1 bit in the next segment to the left.

In more detail, let $x = \text{row}_{i-1} \text{ Or } b_i\text{-string}$.

eg. $x = \text{row}_{10} \text{ Or } \text{T-string}$

x:	1	1	0	1	1	0	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1
----	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

Each segment of x can be read as a binary number. There is a "folk-technique" that decrementing a binary number changes the low-order bits [7] up to and including the least-significant 1. The correct decrement for each segment can be found by a logical left-shift of row _{$i-1$} with a 1 carry-in:

x:	1	1	0	1	1	0	0	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1
-	0	0	0	0	0	0	1	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	1	1	0	1	0	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0

A non-equivalence of the result and x sets the changed bits:

0	0	0	0	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

And-ing this with x gives the right-most bits in each segment:

0	0	0	0	1	0	0	0	1	0	0	1	1	1	1	1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

This is row _{i} , row₁₁ in this example.

In summary:

$\text{row}_i = x \text{ And } ((x - (\text{row}_{i-1} \ll 1)) \neq x)$
 where $x = \text{row}_{i-1} \text{ Or } b_i\text{-string}$

Or And \neq bit-string operations
 \ll logical left-shift bit-string; right-hand bit set
 $-$ l -bit integer subtraction

The bit-string operations can be done in units of a word-length. The shift and subtraction can also be done one word at a time, taking care that the carry-out-bit and the borrow-bit are propagated.

The number of bits set in a word can be counted in $O(\log_2(w))$ time; this is attributed to D.Muller in 1954 in [7]. The number of 1's in the final row therefore can be calculated in $O(\lceil l/w \rceil \times \log_2(w))$.

2.3. An LCS.

An LCS, as opposed to just its length, can be obtained in at least two ways. Hirschberg's recursive technique [2] can be used to find an LCS in linear-space at the cost of slowing the algorithm by a factor of two.

Alternatively all rows of M can be kept, the space required is $l \times b$ bits, $\lceil l/w \rceil \times b$ words. This is quite practical for strings of the order of 1000 characters. Then an LCS can be recovered by finding the "corners" of the contours in L which stand out as patterns in M :

matrix M_{ij}

3. Simulation Results.

$ a \times b $	Time Ratio (Simple/Bit)
32×32	6
64×64	10
100×100	11
500×500	25
1000×1000	26
4000×4000	27

$ a \times b $	Time Ratio (Simple/Bit)
32×32	2
64×64	5
100×100	6
500×500	19
1000×1000	21
4000×4000	27

4. Conclusions.

The time complexity of the bit-string LCS algorithm on a computer with w -bit words for a fixed finite alphabet is

$$O(\lceil \text{la}/w \rceil \times \text{lb})$$

This gives a speedup over simple $O(\text{la} \times \text{lb})$ algorithms. The time does not depend on properties of 'a' and 'b'. For random strings and moderate alphabets, the bit-string LCS algorithm will be faster than the special case algorithms.

The algorithm might also be programmed on a vector-computer if carry-out and borrow bits can be propagated from word to word in a vector-shift and a vector-subtract operation. It could be "pipelined" in a diagonal fashion on a vector or parallel machine because the least significant bit (or word) of row_{i+1} can be calculated as soon as the least significant bit (or word) of row_i has been calculated.

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