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INFORMATION DISTRIBUTION ASPECTS OF DESIGN METHODOLOGY *

D.L. PARNAS

Computer Science Department, Carnegie-Mellon University, Pittsburgh, Pennsylvania, USA

The role of documentation in the design and implementation of complex systems is explored, resulting in suggestions in sharp contrast with current practice. The concept of system structure is studied by examining the meaning of the phrase "connections between modules". It is shown that several system design goals (each suggesting a partial time ordering of the decisions) may be inconsistent. Some properties of programmers are discussed. System documentation which makes all information accessible to anyone working on the project, is discussed. The thesis that such information "broadcasting" is harmful, that it is helpful if most system information can be hidden from most programmers, is supported by use of the above mentioned considerations as well as by examples.

1. INTRODUCTION

Papers on design methodology assume (1) that the methods used in system design strongly affect the quality of the final product; and (2) by selecting an appropriate methodology we can avoid many of the problems previously encountered in constructing large systems.

Under the heading "Design Methodology" a number of separate topics can be distinguished:

1. The order in which design decisions are made [1, 2, 3, 6].
2. The characteristics of the final product (e.g., what constitutes "good structure" for a system) [4, 5, 6, 7].
3. Methods of detecting errors in design decisions shortly after they are made [1, 2, 3, 5, 8, 9].
4. Specification techniques [12, 13].
5. Tools for system designers [1, 2, 3, 10, 11].

This paper emphasizes another topic named "information distribution". Design and development are a series of decisions. Each decision results in information about the system which can be used in making later decisions. We eventually want to discuss the distribution of that information among those working on the system and to deal with its organization in documentation. To prepare for this discussion we deal first with (1) the concept of system structure, (2) constraints on the order of decisions, and (3) some observed characteristics of good programmers.

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2. STRUCTURE DEFINED

The word "structure" is used to refer to a partial description of a system. A structure description shows the system divided into a set of *modules*, gives some characteristics of each module, and specifies some *connections* between the modules. Any given system admits many such descriptions. Since structure descriptions are not unique, our usage of "module" does not allow a precise definition parallel to that of "subroutine" in software or "card" in hardware. The definitions of those words delineate a class of object, but not the definition of "module". Nevertheless, "module" is useful in the same manner that "unit" is in military or economic discussions. We shall continue to use "module" without a precise definition. It refers to portions of a system indicated in a description of that system. Its precise definition is not only system dependent but also dependent upon the particular description under discussion.

The term "connection" is usually accepted more readily. Many assume that the "connections" are control transfer points, passed parameters, and shared data for software, wires or other physical connections for hardware. Such a definition of "connection" is a highly dangerous oversimplification which results in misleading structure descriptions. *The connections between modules are the assumptions which the modules make about each other.* In most systems we find that these connections are much more extensive than the calling sequences and control block formats usually shown in system structure descriptions.

The meaning of the above remark can be ex-

hibited by considering two situations in which the structure of a system is terribly important: (1) making of changes in a system and (2) proving system correctness. (I feel no need to argue the necessity of proving programs correct, or to support the necessity of making changes. I wish to use those hypothetical situations to exhibit the meaning of "connection".)

Correctness proofs for programs can become so complex that their own correctness is in question (e.g., [14], [15]). For large systems we must make use of the structure of the programs in producing the proofs. We must examine the programs comprising each module separately. For each module we will identify (1) the system properties that it is expected to guarantee, and (2) the properties which it expects of other modules. The correctness proof for each module will take (1) as the set of theorems to be proven and (2) as a set of axioms which may be used in proving that the programs do indeed guarantee the proofs of the theorems proven about each module will be used in proving the correctness of the whole system. The statements (1) and (2) constitute the connections between the various modules of the system. The task of proving system correctness will be facilitated by this process if the amount of information in the statement sets (1) and (2) is significantly less than the information in the complete description of the programs which implement the connected module.

We now consider making a change in the completed system. We ask, "What changes can be made to one module without involving change to other modules?" We may make only those changes which do not violate the assumptions made by other modules about the module being changed. In other words, a single module may be changed only while the "connections" still "fit". Here, too, we have a strong argument for making the connections contain as little information as possible.

3. FACTORS INFLUENCING THE ORDER OF DECISION MAKING

Progress in a design is marked by decisions which eliminate some possibilities for system structure. The fact that those possibilities have been eliminated can be apart of the rationale for subsequent decisions. If the information is used, the order of decision making (in time) affects the structure of the resulting product. Examples of interest can be found in [4]. We can

identify three considerations, each suggesting a partial ordering on the decisions.

3.1. *Obtaining 'good' external characteristics*

All systems have characteristics which are not pleasing to the users. Usually they were not determined by explicit deliberations; they were the unnoticed implications of decisions about other aspects of system structure. To consistently avoid such errors we can make the decisions about external characteristics first and use the resulting information to make the later decisions. The internal decisions would be either derived from or checked against the complete specifications of the external factors. This is the basis of the "top down" or "outside in" approach discussed in [1, 2, 3, 4].

3.2. *Reducing the time interval between initiation and completion of the project*

Competitive pressures may require the use of large groups to produce a system in a sharply limited period of time. Additional men speed up a project significantly only *after* the project has been divided into sub-projects in such a way that separate groups can work with little interaction (i.e., spending significantly less time in inter-group decisions than in intra-group decisions). This consideration affects the order of decisions in that it encourages very early splitting of the system into modules which are then designed completely independently. The desire to make the split early and "get on with it" encourages a splitting along familiar lines and in agreement with existing personnel classifications.

Time pressures encourage groups to make the split *before* the external are defined. Consequently we find some adverse effect on the useability of the product. Haste also makes poor internal structure likely.

3.3. *Obtaining an easily changed system*

Systems are changed after construction either because their original characteristics proved insufficient or because another application was found. We have already noted that the difficulties in changing systems are related to the assumptions which each of the modules makes about its environment. Since each decision is usually made on the assumption that the previous decisions will hold, the most difficult decisions to change are usually the earliest. The last piece of code inserted may be changed easily, but a piece of code inserted several months earlier may have "wormed" itself into the program

and become difficult to extract. These considerations suggest that the early decisions should be those which are the least likely to change; i.e., those based on "universal" truths or reasoning which takes into account little about a particular environment. The remaining facts must be used eventually, but the possibility of change suggests using the most general information first.

Since such external characteristics as job control language and file commands are very frequently changed, the "outside-in" approach may make the system harder to change. Further, those decisions which should be made early on this basis are not usually those which allow the project to be quickly subdivided into independent assignments. As a rule, decisions which do not use all the available information about a system (i.e., the general decisions) take more time.

In summary, each of the three considerations suggests a partial ordering of the decisions. Those orderings are usually inconsistent in that it will be impossible to satisfy them simultaneously.

4. DOCUMENTATION SYSTEMS

For any complex system there must be documentation about the system for use by the human beings who must complete it. Programs and wiring diagrams do completely define the algorithm which they will execute, but this form of documentation is not usually appropriate for use by people. Consequently there are always papers which attempt to answer the questions most likely to be asked. There is usually no attempt to make the documentation complete (i.e., equivalent to the code for software), thus certain questions must be answered by reference to the code.

When a system is strongly connected, this documentation must be read by persons not closely involved with the module being documented. Because each working group develops a unique module organization *and a corresponding set of concepts and terms*, the documents which they write are difficult for outsiders to read.

The natural response is to require all documentation to be written with a standard organization and vocabulary [16]. A standard is made company-wide to allow anyone in the organization to find some piece of information without needing to learn the concepts and vocabulary peculiar to one system or module.

Such approaches raise several questions:

1. It is really desirable to have all information equally accessible to all in the company (or project)?
2. What is the effect of documentation standards on the resulting system?
3. What is the result of a non-standard system being described using a standard document organization?

Documentation standards tend to force system structure into a standard mold. A standard for document organization and vocabulary makes some assumptions about the structure of the system to be described. If those assumptions are violated, the document organization fits poorly and the vocabulary must be stretched or misused. Consider the following example. In most operating systems there exists a module which handles all job control statements from the time they are read in until the job is completed. As a result, most documentation systems can insist that there be a section describing such a module. Now consider an organization (such as that of the T.H.E. system [18]) in which there is no such module because most of the processing is handled in modules which are also used for other purposes. If we adhere to the documentation standard we will duplicate information and describe one module in the documentation of another.

If there are to be standard documentation organizations, they must be designed to make the minimum number of assumptions about the system being documented. If so, they will be of little help in making the document readable to people who do not understand the structure of the system.

5. ON SOME PROPERTIES OF GOOD PROGRAMMERS

The following observation is essential to the remainder of this paper:

"A good programmer makes use of the usable information given him!"

The good programmer will try to use his machine well. He is actually programming for a "virtual machine" defined by the hardware *and* his knowledge of the other software on the machine. His straining and his nature lead him to make *full* use of that extended machine.

Sometimes the uses are obvious. The programmer makes use of a subroutine from some other module,

or a table of constants already present for some other piece of code. Sometimes these uses are so marginal as to be laughable, e.g., the use of a 3-instruction subroutine or the borrowing of a single constant. In the terms of our previous discussions, such extreme cases increase the connectivity of the structure without appreciably improving its performance.

Sometimes the uses are less obvious. For example, a programmer may make use of his knowledge that a list is searched in a certain order to eliminate a check or an extra queue. In the area of application programming we may find a programmer who introduces an erroneous value for π knowing that because of an error in the sine routine the erroneous value will cause his program to converge more rapidly.

Such uses of information have been so costly that we observe a strange reaction. The industry has started to encourage bad programming. Derogatory names such as "kludger", "hacker" and "bit twiddler" are used for the sort of fellow who writes terribly clever programs which cause trouble later on. Such programmers are subtly but effectively discouraged by being assigned to work on small independent projects such as application routines (the Siberia of the software world) or hardware diagnostic routines (the coal mines). In both situations the programmer has little opportunity to make use of information about other modules.

Those that remain (the non-bit-twiddlers) are usually poor programmers. While a few refrain from using information because they know it will cause trouble, most refrain because they do not notice that the information can be used. Such people also miss opportunities to use facts which should be used. Poor programs result. Since even a poor programmer sometimes has a "flash of brilliance" (e.g., noticing that two bytes in a control block can be simultaneously set with one instruction because they are adjacent and in the same word) we still have no control of the structure.

We have found that a programmer can disastrously increase the connectivity of the system structure by using information he possesses about other modules. We wish to have the structure of the system determined by the designers explicitly before programming begins, rather than inadvertently by a programmer's use of information. Consequently, we discourage the bit twiddlers and pay a price in poor programming *without obtaining complete control of the structure.*

6. THE USE OF DESIGNER CONTROLLED INFORMATION DISTRIBUTION

We can avoid many of the problems discussed here by rejecting the notion that design information should be accessible to everyone. Instead we should allow the designers, those who specify the structure, to *control* the distribution of design information as it is developed.

Our concerns about the inconsistent decision orderings were based on the assumption that information would be used shortly after the corresponding decision. The restrictions placed by the three considerations are considerably relaxed if we have the possibility of hiding some decisions from each group. For example, we have noted a conflict between the desire to produce an external specification early and the desire to produce a system for which the external interface is easily changed. We can avoid that conflict by designing the external interface, using it as a check on the remaining work, but hiding the details that we think likely to change from those who should not use them.

If we want the structure to be determined by the designers, they must be able to control it by controlling the distribution of the information. We should not expect a programmer to decide not to use a piece of information, rather he should not possess information that he should not use. The decision is part of the design, not the programming.

Reflection will show that such a policy expects a great deal from the designers. We currently release all the information about a module; to do so is considerably easier than (1) deciding which information should be released and (2) finding a way of expressing precisely the information needed by other modules. Preliminary experience has shown that making appropriate definitions is quite difficult. Acquiring skill in making those definitions is vital because we will be able to successfully build systems while restricting programmers' information only if we learn to provide them with precisely the information they need.

7. EXAMPLES

I believe it worthwhile to give some concrete examples of information which is now widely disseminated within a project and should instead be sharply restricted.

7.1. Control block formats

Every system contains small amounts of information in pre-formatted areas of storage called control blocks. These are used for passing information between modules and are considered to be the interfaces. For this reason formats are usually specified early in the project and distributed to all who are working on the project. The formats are changed many times during the project. Few programmers on any project need to know such formats. They need a means for referring to a specific item, but not more. They need not even know which information is grouped into one control block.

7.2. Memory maps

It is common to begin a description of an operating system by (1) describing the main modules and (2) showing how the core storage is divided among those main modules. Soon there is a complete map of the memory showing how that resource is allocated. Reasonably sophisticated designers show the borders of allocated areas as symbolic rather than absolute addresses, but the order of memory assignment is specified. Only a small portion of this information derives from hardware decisions. There is no legitimate way to use the map information. It would be frightening if someone developed code that would not work if the map were changed. Such maps are almost invariably changed because something which was fixed becomes variable or vice versa. The information is only needed at assembly time. We could survive if it were input to the assembler and not known by anyone else.

Where there is a virtual memory or other mechanism for swapping built into the system, the distinction between resident and non-resident items should not be broadcast. If there are several kinds of core storage, the allocation of modules and data among those storage types should not be known to those who are writing the modules. If partial preloading of certain programs is envisaged, the decision as to which modules will be preloaded should be hidden. Each of these decisions is worthy of attention, but few should know the result.

7.3. Calling sequences

Calling sequences are the secret hobby of every system programmer. We begin to look at new hardware by inventing a calling sequence. Throughout the design and implementation, the calling sequence is simplified, generalized, made more efficient, etc. Each time we face a decision. Either modules all over

the system are altered or the new sequence is added to a growing set of calling sequences. In the latter case generating a call to a routine requires determining which sequence it uses.

Most routines can be written, and *written well* without knowledge of the calling sequence *if* the programmer is provided with a programming tool which allows him to postpone decisions about register allocation for parameters, return addresses, and results. Such features can be provided in an assembler with macro facilities.

7.4. JCL formats

One characteristic which should be easy to change is the syntax of the so called Job Control Language, the means by which the user describes his job's gross characteristics to the operating system. The design of a JCL implies assumptions about the way that the system will be used which may later prove to be false or too restrictive. There exist systems in which JCL format information has been used so much that reasonable changes require user provision of duplicate information and/or the maintenance of duplicate tables. (See, for example, [17].)

Most of the people working on an operating system need very little knowledge about the JCL. The only people who need to know the format are those who are writing the syntax analyzer for the language.

7.5. Location of I/O device addresses

It is widely recognized that device addresses should not be built into code but stored in tables associated with each job. However, it is usual that all programmers are given knowledge sufficient to allow them to find and use the table. For example, many modules will send messages to a user at his teletype. If later one wishes to intercept those messages and re-interpret or suppress them for a special class of users, the job is horrendous. Most programs did not need that information. Access to a module which would send messages for them is sufficient.

7.6. Character codes

Some hardware information should not be released. I have seen one compiler in which the association made by the hardware between card characters and integers was so widely used that a second version of the compiler (for a new machine) contained a module which translated from the new character code to the old one and back again.

The efficiency gained by using the character code information (e.g., by using arithmetic test to deter-

mine if a given character is a delimiter) is often not worth the price paid. Where it is worthwhile, the knowledge can be closely restricted *if* the designers pay attention to the problem. Certainly the decision to use or not use the information should not be left up to an individual programmer.

8. CONCLUSION

The inescapable conclusion is that manufactures who wish to produce software in which the structure is under the control of the designers, must develop a documentation system which enables designer control of the distribution of information. Further, they must find and/or train designers who are able to define or specify modules in a way which provides exactly the information that they want the programmers to use. Until we can completely staff a project with men who have the intellectual capacity and training to make that decision for themselves, some must make the decision for others. An assembler which allows the insertion of some hidden information at "assembly time" will aid in maintaining efficiency.

I consider the internal restriction of information within development groups to be of far more importance than its restriction from users or competitors. Much of the information in a system document would only harm a competitor if he had it. (*He* might use it!)

It is worth repeating that the decision about *which* information to restrict is a design decision, not a management one. The management responsibility ends with providing the appropriate information distribution mechanism. The use of that mechanism remains a design function because it determines the structure of the product.

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REFERENCES

- [1] D.L. Parnas and J.A. Darringer, SODAS and a methodology for system design, Proc. AFIPS FJCC (1967) 449-474.
- [2] F.W. Zurcher and B. Randell, Multilevel modeling - a methodology for computer system design, Proc. IFIP Conference (1968).
- [3] D.L. Parnas, More on simulation languages and design methodology for computer systems, Proc. SJCC (1969) 739-743.
- [4] E.W. Dijkstra, Notes on structured programming, publication of the Technical University of Eindhoven, Netherlands.
- [5] E.W. Dijkstra, Complexity controlled by hierarchical ordering of function and variability, in: Software Engineering, Proc. of a meeting at Garmisch, Germany, Oct. 1968.
- [6] S. Gill, Thoughts on the sequence of writing software, in: Software Engineering, Proc. of a meeting at Garmisch, Germany, Oct. 1968.
- [7] E.W. Dijkstra, Structured programming, in: Software Engineering Techniques, Proc. of meeting in Rome, Oct. 1969.
- [8] E.W. Dijkstra, A constructive approach to the problem of program correctness, BIT 8, vol. 3 (1968).
- [9] P. Naur, Proof of algorithms by general snapshot, BIT 6 (1966).
- [10] Wulf et al., BLISS Users Manual, publication of Carnegie-Mellon U., Pittsburgh, Pa.
- [11] V.M. Waite, The mobile programming system: STAGE 2, CACM 13, 7 (July 1970) 411-421.
- [12] D.L. Parnas, On the use of transition diagrams in the design of a user interface for an interactive computer system, Proc. 1969 Nat'l. ACM Conference, 379-386.
- [13] P.H. Hartman and D.H. Owens, How to write software specifications, Proc. FJCC (1967) 779-790.
- [14] R.M. Balzer, Studies concerning minimal time solutions to the firing squad synchronization problem, Ph. D. thesis, Carnegie Institute of Technology (1966).
- [15] R. London, Certification of treesort 3, CACM, June 1970.
- [16] F. Selig, Documentation standards, in: Software Engineering, Proc. of meeting Garmisch, Germany, Oct. 1968.
- [17] Braden, et al., An Implementation of MVT, publication of the University of Calif., L.A.
- [18] E.W. Dijkstra, Structure of the T.H.E. multiprogramming system, CACM, May 1967.