

Yilin Li

Tel: (+86)18888922967 **E-mail:** liyilin@zju.edu.cn

Date of birth: 1999-04-01 **Nationality:** China

Address: No. 515, Building 32, Yuquan Campus, Zhejiang University, Xihu District, Hangzhou

City, Zhejiang Province, China

EDUCATIONAL BACKGROUND

ZHEJIANG University

Hangzhou, China

Senior student, bachelor of engineering in electronic information

Expected Jul. 2021

- Overall GPA: 3.67/4.0 (top 40%, junior year 49/143), 167 credits
- Relevant Courses: Numerical Analysis Methods (4.0), Computer Composition and Design (4.0), Data Analysis and Algorithm Design (4.0), Artificial Intelligence (4.0)
- English Proficiency: TOEFL IBT 81 (the latest scores will be higher)

ACADEMIC EXPERIENCES

• Summer internship in Huawei Hangzhou Research Institute

July. 2019-August. 2020

- Learn the LLVM architecture
- Update compiler dependent package versions and fix bugs
- Learn compiler basics and develop new pass
- University-level Student Research Training Program

May. 2019-June. 2020

- Facial Makeup Transfer using traditional digital image processing method including dividing layers and weighting blending
- Facial Makeup Transfer using Deep Generative Adversarial Network
- Summer school in Tokyo Institute of Technology

Information Experiments III

Jul. 22 2019-Aug. 12 2019

- Assemble program design
- Verilog analysis and simulation
- FPGA implementation

HONORS & AWARDS

- Zhejiang University Scholarship-Third Prize(2017-2018)
- University Student Scholarship of Texas Instruments (2018-2019)
- Cyrus Tang Scholarship
- Zhejiang University Five-star Volunteer
- Outstanding Volunteer of Zhejiang University (2017-2019 academic year)
- Meritorious Winner in Mathematical Contest In Modeling (2020)

OTHER EXPERIENCES

Monitor of the class
Deputy director of the sports department of the college student union
Deputy minister of public relations department of Caring Heart Club
2018-2019
2018-2019

SKILLS & INTERESTS

Technical Skills: programming in python, c and assembly language

Research Interests: computer architecture, FPGA, embedded systems, machine learning