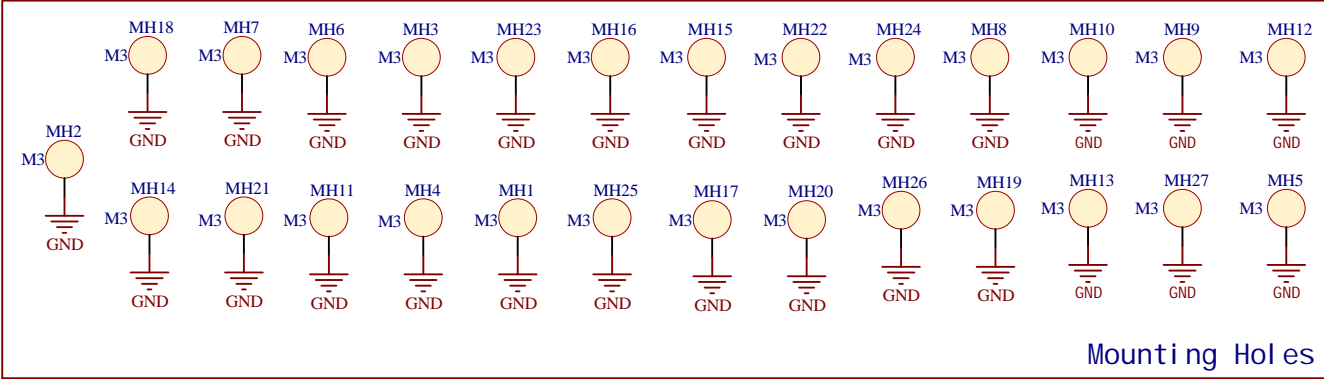
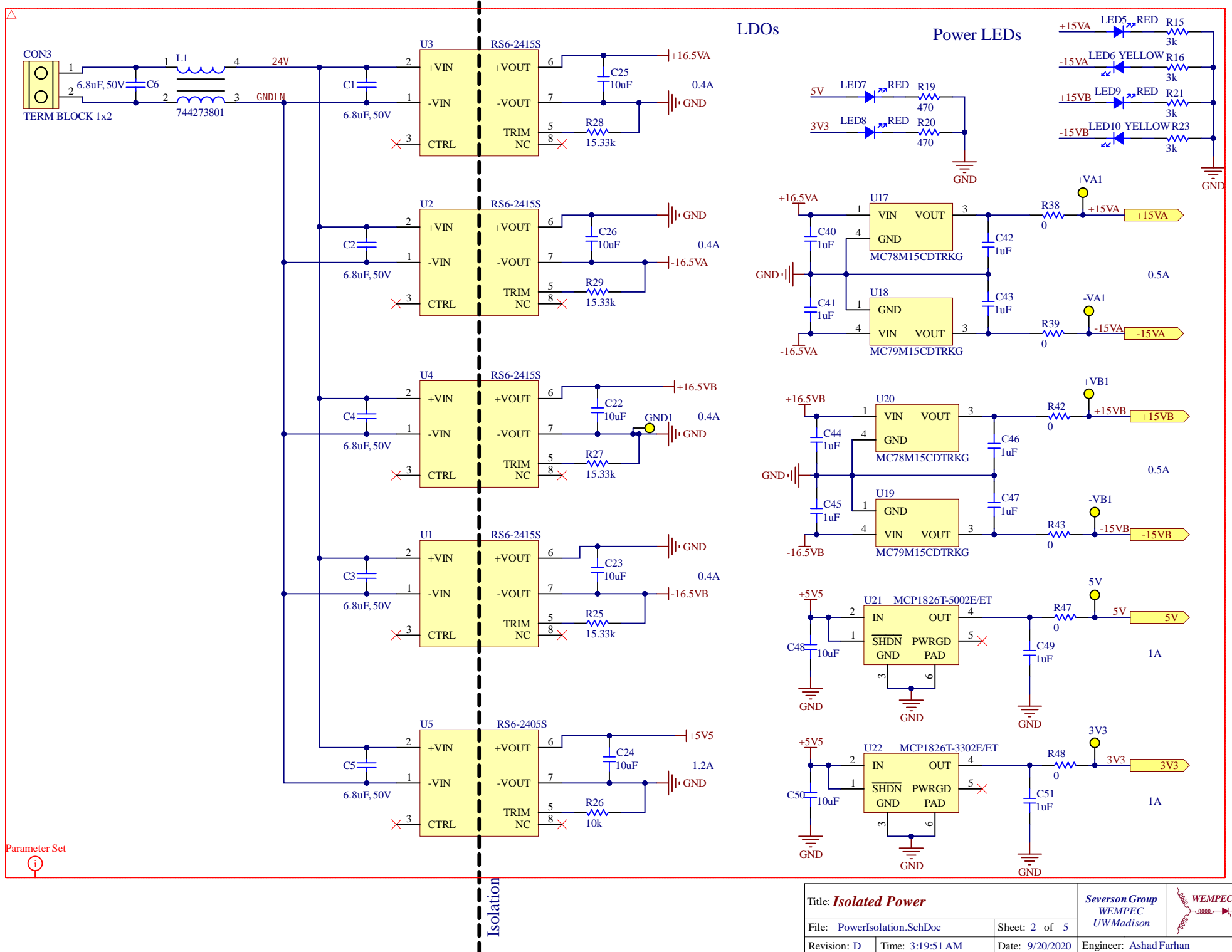


Daughterboard Mounting





A

A

B

B

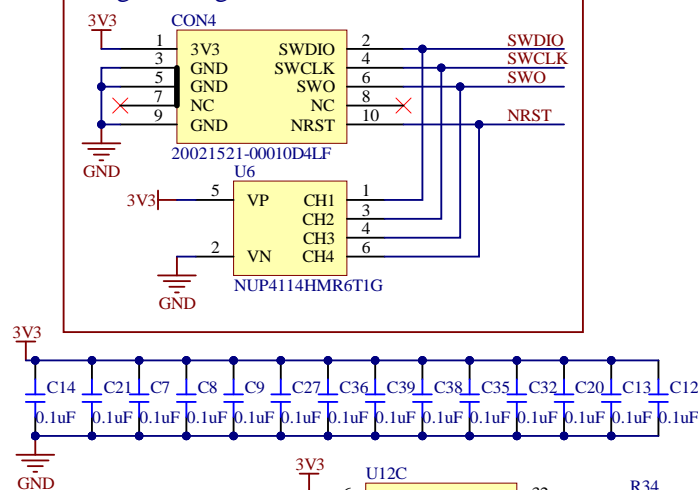
C

C

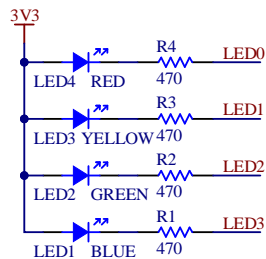
D

D

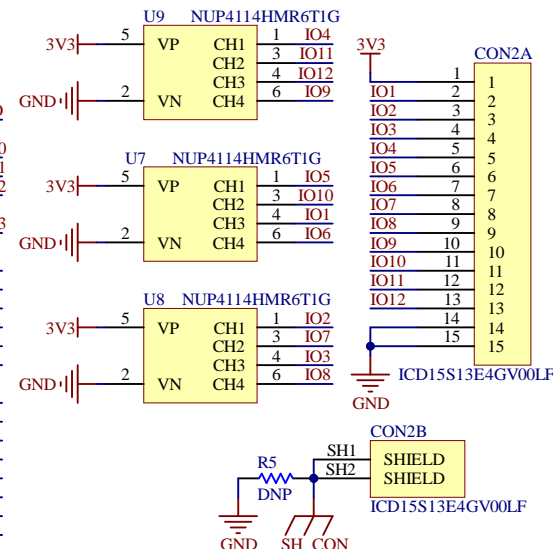
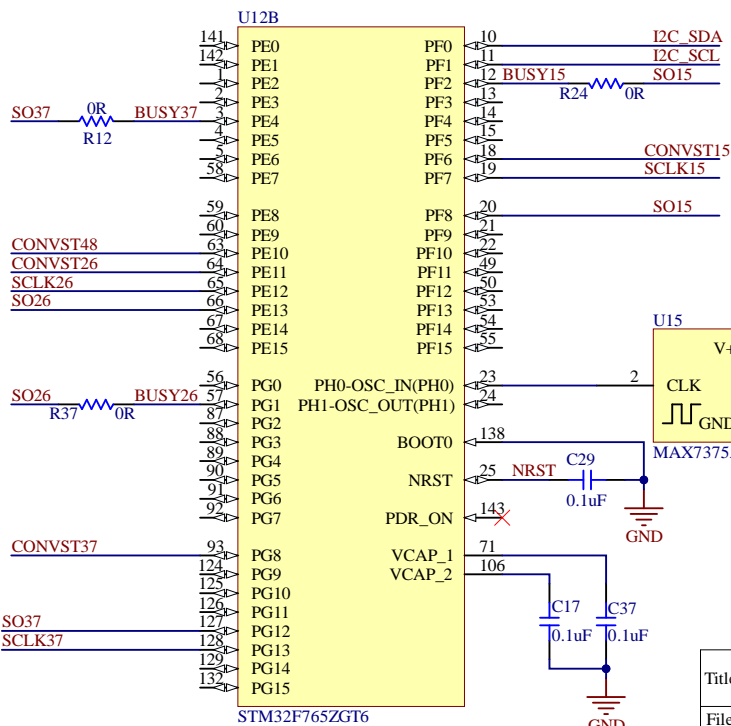
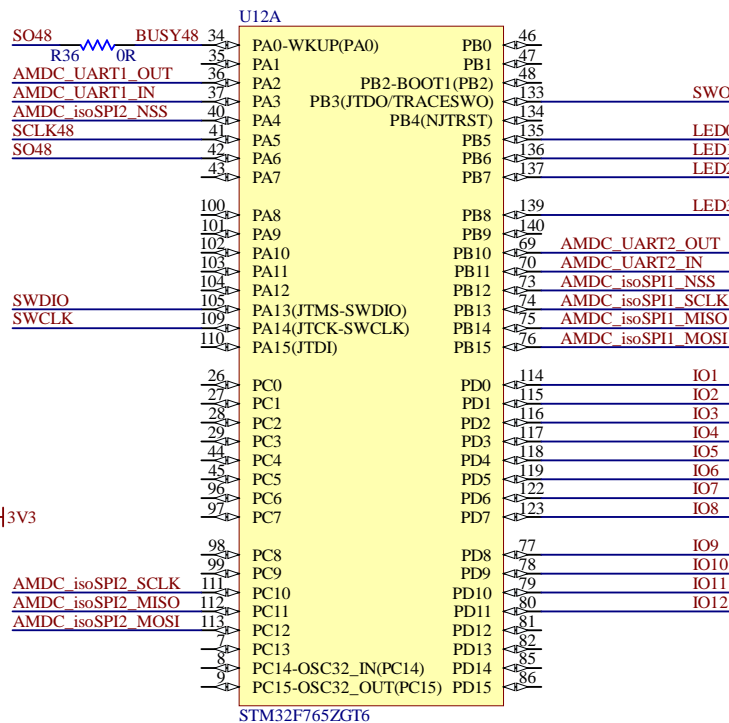
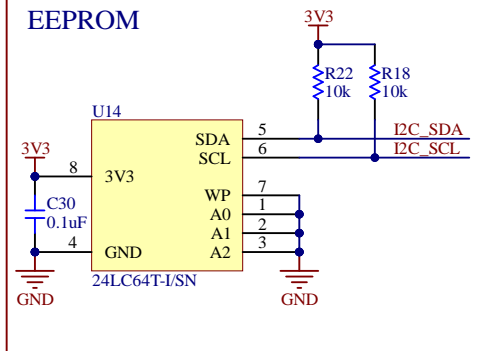
Programming Interface



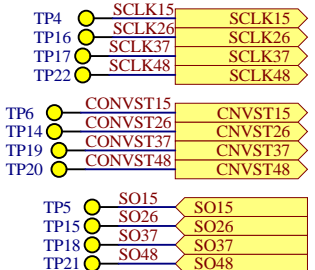
Status LEDs



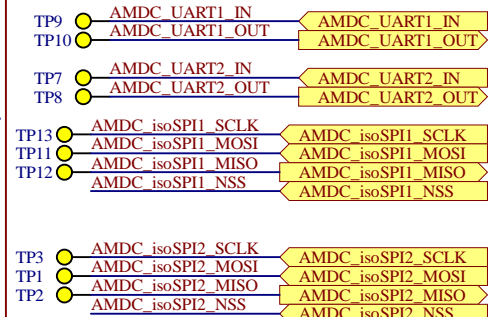
EEPROM



Sensor Card Interface



AMDC Interface



Title: **Processor**

File: Processor.SchDoc

Revision: D

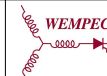
Time: 3:19:51 AM

Sheet: 4 of 5

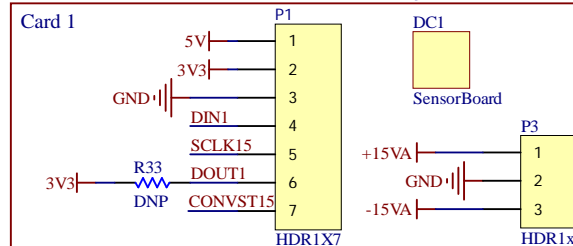
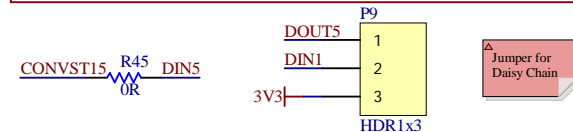
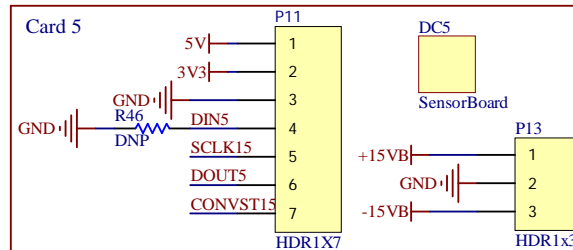
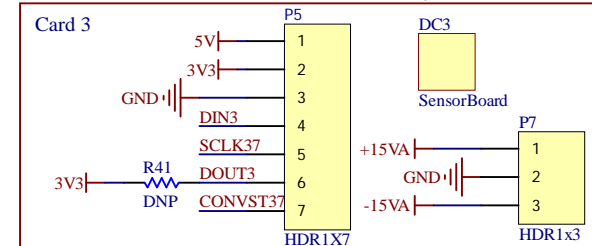
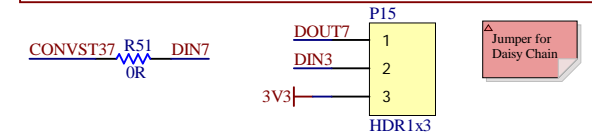
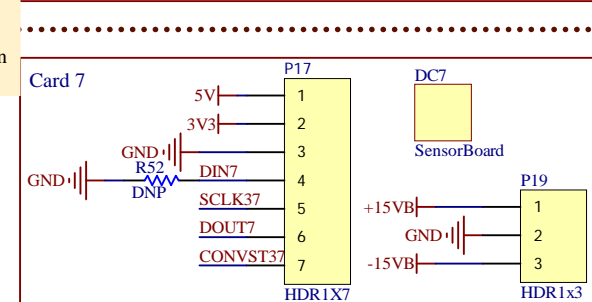
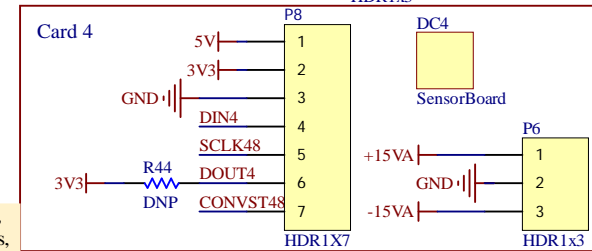
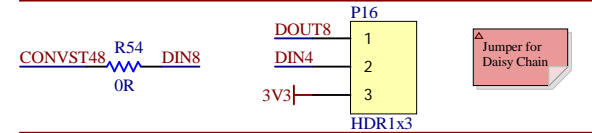
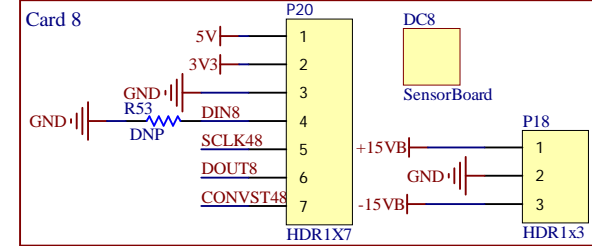
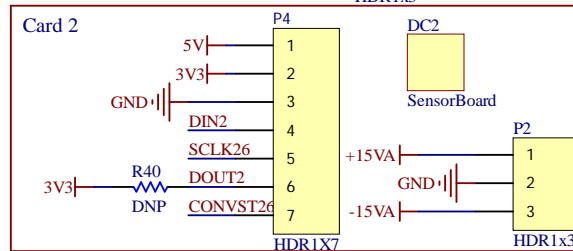
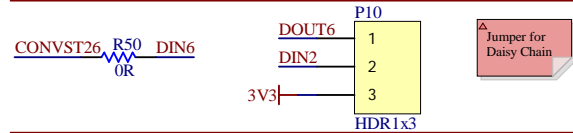
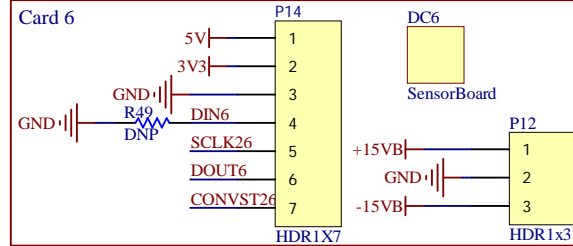
Date: 9/20/2020

Severson Group
WEMPEC
UWMadison

Engineer: Ashad Farhan



Daughterboard Connections



Eight daughterboards, divided into four pairs, each pair has two boards connected in daisy chain connection

Processor Interface

