

VSYS VSYS

C1 C2 10u

₩ GND GND

V<u>S</u>YS

= GND

I2S_SD GAIN_SLOT

SD_MODE#

U3 MAX98357AETE+T

₩ GND

GAIN_SLOT

SD_MODE#

GND

VDD

BCLK GND LRCLK N.C. N.C. GND OUTN OUTP

를 GND

I2S_SCK

I2S_WS

J1 HDR-M-2.54_1x2

GAIN_SLOT	I ² S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V_{DD} through $100k\Omega$ ±5% resistor	3

SD_MODE STATUS		SELECTED CHANNEL
High	V _{SD_MODE} > B2 trip point	Left
Pullup through R _{SMALL}	B2 trip point > V _{SD_MODE} > B1 trip point	Right
Pullup through R _{LARGE}	B1 trip point > V _{SD_MODE} > B0 trip point	(Left/2 + right/2)
Low	B0 trip point > V _{SD_MODE}	Shutdown

Table 6. Examples of SD_MODE Pullup Resistor Values

LOGIC VOLTAGE LEVEL (V _{DDIO}) (V)	R _{SMALL} (kΩ)	R _{LARGE} (kΩ)
1.8	69.8	300
3.3	210.2	634

TITLE: Pico audio maker **REV:** 1.0 Sheet: 1/1 Company: 2024-07-08

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