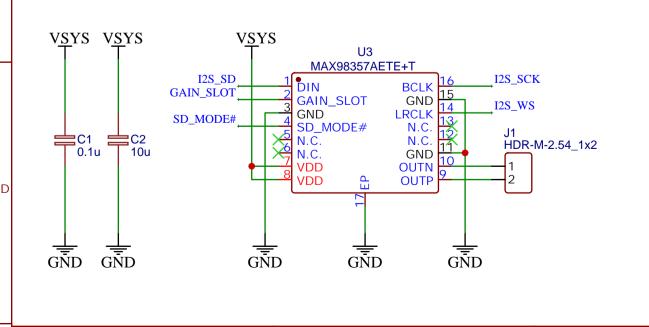| GAIN_SLOT	I²S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V$_{DD}$	6
Connect to V$_{DD}$ through 100kΩ ±5% resistor	3

Table 5. $\overline{SD\_MODE}$ Control

$\overline{SD\_MODE}$ STATUS		SELECTED CHANNEL
High	V$_{\overline{SD\_MODE}}$ > B2 trip point	Left
Pullup through R$_{SMALL}$	B2 trip point > V$_{\overline{SD\_MODE}}$ > B1 trip point	Right
Pullup through R$_{LARGE}$	B1 trip point > V$_{\overline{SD\_MODE}}$ > B0 trip point	(Left/2 + right/2)
Low	B0 trip point > V$_{\overline{SD\_MODE}}$	Shutdown

Table 6. Examples of $\overline{SD\_MODE}$ Pullup Resistor Values

LOGIC VOLTAGE LEVEL (V$_{DDIO}$) (V)	R$_{SMALL}$ (kΩ)	R$_{LARGE}$ (kΩ)
1.8	69.8	300
3.3	210.2	634

TITLE: Pico audio maker

REV: 1.1

Company:

Sheet: 1/1

Date: 2024-07-08   Drawn By: philbobagins