

Table 32-6. Fuse High Byte

High Fuse Byte	Bit No.	Description	Default Value
RSTDISBL ⁽¹⁾	7	External Reset Disable	1 (unprogrammed)
DWEN	6	debugWIRE Enable	1 (unprogrammed)
SPIEN ⁽²⁾	5	Enable Serial Program and Data Downloading	0 (programmed, SPI programming enabled)
WDTON ⁽³⁾	4	Watchdog Timer Always On	1 (unprogrammed)
EESAVE	3	EEPROM memory is preserved through the Chip Erase	1 (unprogrammed), EEPROM not preserved
BOOTSZ1	2	Select Boot Size (see Boot Loader Parameters)	0 (programmed) ⁽⁴⁾
BOOTSZ0	1	Select Boot Size (see Boot Loader Parameters)	0 (programmed) ⁽⁴⁾
BOOTRST	0	Select Reset Vector	1 (unprogrammed)

Note:

1. Refer to *Alternate Functions of Port C* in I/O-Ports chapter for description of RSTDISBL Fuse.
2. The SPIEN Fuse is not accessible in serial programming mode.
3. Refer to *WDTCSR – Watchdog Timer Control Register* for details.
4. The default value of BOOTSZ[1:0] results in maximum Boot Size. See table Boot Size Configuration in subsection Boot Loader Parameters in the previous chapter for details.