

# Experiment Report

Start of automated test report 2024-08-30 01:02:36

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## Machine Information

sysname=Linux

nodename=yons-MS-7E06

release=6.8.0-40-generic

version=#40~22.04.3-Ubuntu SMP PREEMPT\_DYNAMIC Tue Jul 30 17:30:19 UTC 2

machine=x86\_64

CPU arch : X86\_64

CPU bits : 64

CPU brand : Intel(R) Core(TM) i9-14900KF

CPU cores : 32

CPU base clock : 5.6850 GHz

CPU boost clock : 5.6850 GHz

System Memory : 94.13GB

Nvidia driver version : 550.90.07

Device 0 : NVIDIA GeForce RTX 4090

Device 0 : 23.99GB

## Library Information

python : 3.8.19

torch : 1.13.1+cu117

optuna : 3.2.0

numpy : 1.23.3

pandas : 1.5.3

matplotlib : 3.7.1

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist\_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

## Hpyerparameters

/home/yons/work/RL\_PCB-main/tests/23\_226\_sac/hyperparameters/hp\_sac.json

learning\_rate:0.0003

buffer\_size:25000

n\_steps:2048

batch\_size:256

gamma:0.99

net\_arch: {'pi': [400, 300], 'qf': [400, 300]}

activation\_fn:relu

expl\_noise:0.1

tau:0.005

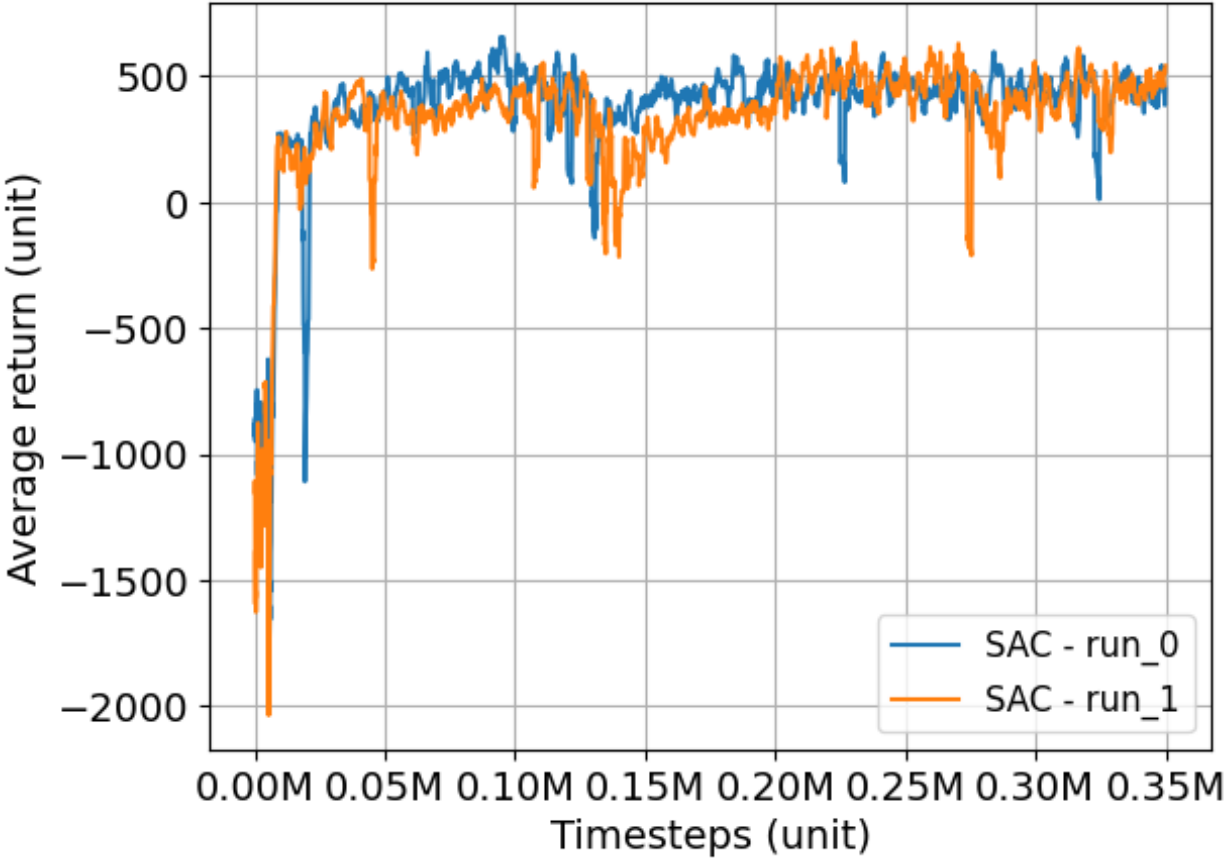
policy\_noise:0.2

noise\_clip:0.5

policy\_freq:1

experiment=parameter\_test\_226  
experiments=['parameter\_experiment\_226']  
algorithms=['SAC']  
averaging window=10 (user assigned)

Parameter test w/ emphasis on wirelength (W=2, H=2, O=6)



title	experiment_226:SAC
run #0	436.9622 ± 257.3173
run #1	415.1710 ± 242.7178
mean	426.0666 ± 250.0176

runs\_involved=['1724926285\_0', '1724926285\_1']

End of automated test report 2024-08-30 01:02:38