

# Experiment Report

Start of automated test report 2024-08-23 12:25:06

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## Machine Information

sysname=Linux

nodename=yons-MS-7E06

release=6.8.0-40-generic

version=#40~22.04.3-Ubuntu SMP PREEMPT\_DYNAMIC Tue Jul 30 17:30:19 UTC 2

machine=x86\_64

CPU arch : X86\_64

CPU bits : 64

CPU brand : Intel(R) Core(TM) i9-14900KF

CPU cores : 32

CPU base clock : 5.7001 GHz

CPU boost clock : 5.7001 GHz

System Memory : 94.13GB

Nvidia driver version : 550.90.07

Device 0 : NVIDIA GeForce RTX 4090

Device 0 : 23.99GB

## Library Information

python : 3.8.19

torch : 1.13.1+cu117

optuna : 3.2.0

numpy : 1.23.3

pandas : 1.5.3

matplotlib : 3.7.1

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist\_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

## Hpyerparameters

/home/yons/work/RL\_PCB-main/tests/05\_training\_td3\_cuda\_fast/hyperparameters/hp\_td3.json

learning\_rate:0.001

buffer\_size:25000

n\_steps:2048

batch\_size:128

gamma:0.99

net\_arch: {'pi': [400, 300], 'qf': [400, 300]}

activation\_fn:relu

expl\_noise:0.1

tau:0.005

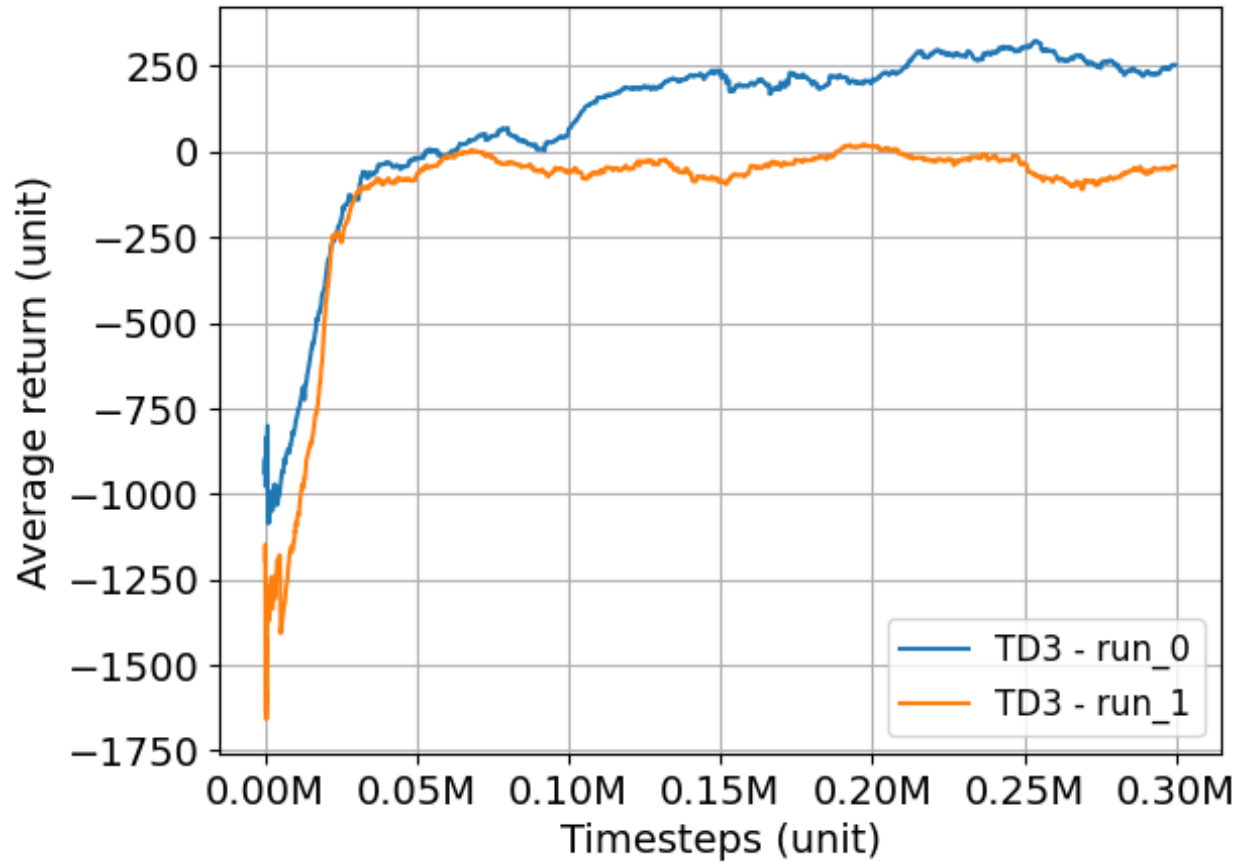
policy\_noise:0.2

noise\_clip:0.5

policy\_freq:2

```
experiment=parameter_test_262
experiments=['parameter_experiment_262']
algorithms=['TD3']
averaging window=100 (user assigned)
```

Parameter test w/ emphasis on wirelength (W=2, H=6, O=2)



title	experiment_262:TD3
run #0	269.5181 ± 221.1844
run #1	-66.2663 ± 221.1352
mean	101.6259 ± 221.1598

```
runs_involved=['1724376927_0', '1724376927_1']
```

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