

Experiment Report

Start of automated test report 2024-08-29 23:10:43

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Machine Information

sysname=Linux

nodename=yons-MS-7E06

release=6.8.0-40-generic

version=#40~22.04.3-Ubuntu SMP PREEMPT_DYNAMIC Tue Jul 30 17:30:19 UTC 2

machine=x86_64

CPU arch : X86_64

CPU bits : 64

CPU brand : Intel(R) Core(TM) i9-14900KF

CPU cores : 32

CPU base clock : 5.3455 GHz

CPU boost clock : 5.3455 GHz

System Memory : 94.13GB

Nvidia driver version : 550.90.07

Device 0 : NVIDIA GeForce RTX 4090

Device 0 : 23.99GB

Library Information

python : 3.8.19

torch : 1.13.1+cu117

optuna : 3.2.0

numpy : 1.23.3

pandas : 1.5.3

matplotlib : 3.7.1

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

Hpyerparameters

/home/yons/work/RL_PCB-main/tests/23_226_td3/hyperparameters/hp_td3.json

learning_rate:0.001

buffer_size:25000

n_steps:2048

batch_size:128

gamma:0.99

net_arch: {'pi': [400, 300], 'qf': [400, 300]}

activation_fn:relu

expl_noise:0.1

tau:0.005

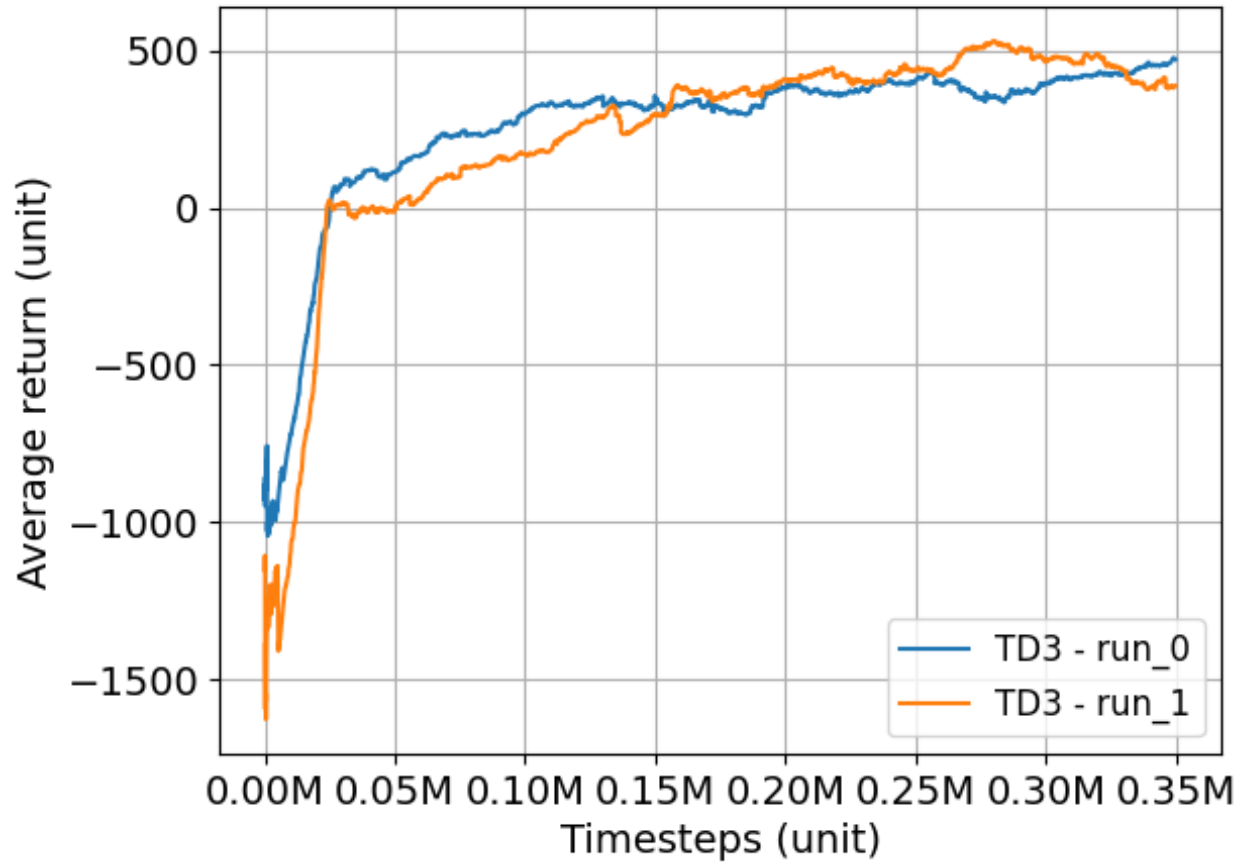
policy_noise:0.2

noise_clip:0.5

policy_freq:2

experiment=parameter_test_226
experiments=['parameter_experiment_226']
algorithms=['TD3']
averaging window=100 (user assigned)

Parameter test w/ emphasis on wirelength (W=2, H=2, O=6)



title	experiment_226:TD3
run #0	426.3772 ± 168.3647
run #1	444.1736 ± 294.6644
mean	435.2754 ± 231.5145

runs_involved=['1724926293_0', '1724926293_1']

End of automated test report 2024-08-29 23:10:45