

Experiment Report

Start of automated test report 2024-08-17 16:40:02

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Machine Information

sysname=Linux

nodename=LabYu

release=6.5.0-44-generic

version=#44~22.04.1-Ubuntu SMP PREEMPT_DYNAMIC Tue Jun 18 14:36:16 UTC 2

machine=x86_64

CPU arch : X86_64

CPU bits : 64

CPU brand : 11th Gen Intel(R) Core(TM) i7-1165G7 @ 2.80GHz

CPU cores : 2

CPU base clock : 2.8000 GHz

CPU boost clock : 2.8032 GHz

System Memory : 7.71GB

Without use Cuda

Library Information

python : 3.8.19

torch : 1.13.1+cpu

optuna : 3.2.0

numpy : 1.23.3

pandas : 1.5.3

matplotlib : 3.7.1

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

Hpyerparameters

/home/yu/Work/RL_PCB-main/tests/09_training_ddpg_cpu_fast/hyperparameters/hp_ddpg.json

learning_rate:0.001

buffer_size:25000

n_steps:2048

batch_size:128

gamma:0.99

net_arch: {'pi': [300, 100], 'qf': [300, 100]}

activation_fn:relu

expl_noise:0.1

tau:0.005

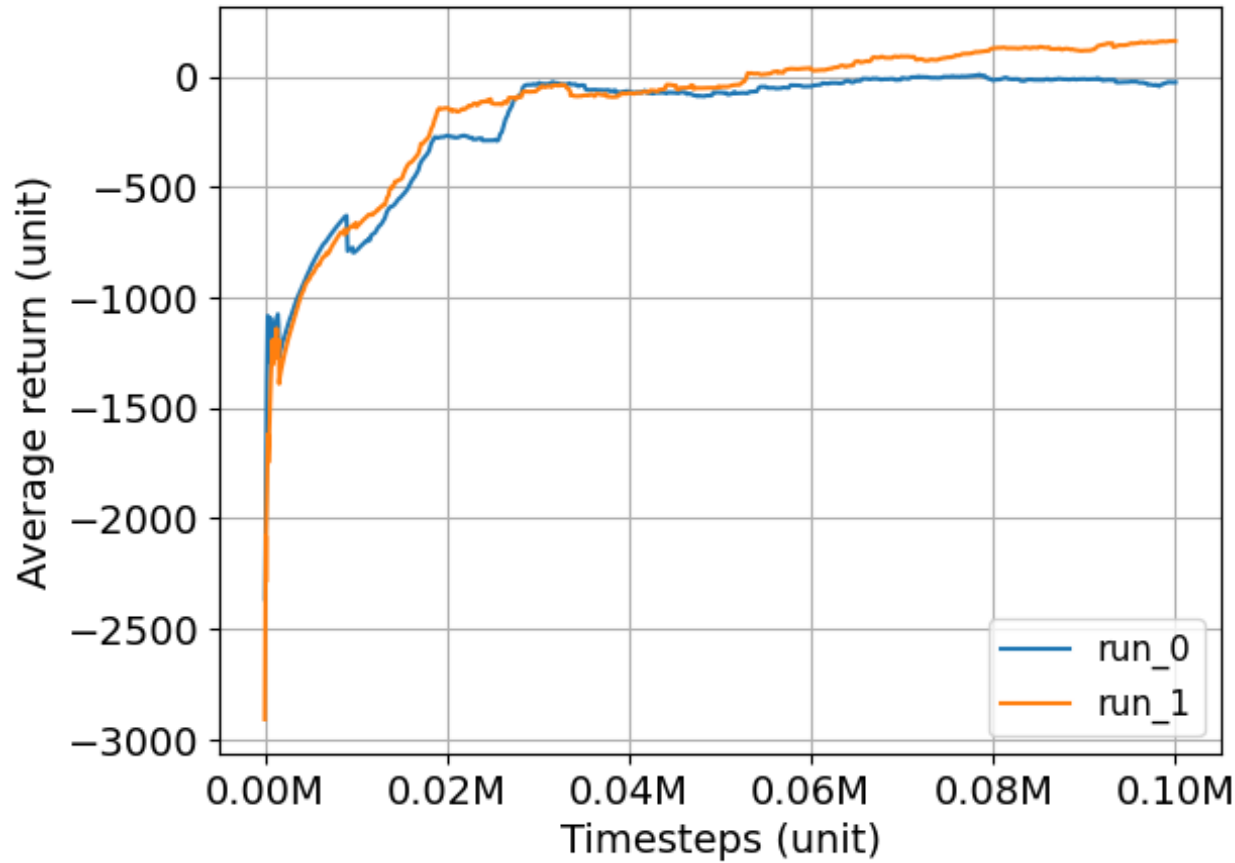
policy_noise:0.2

noise_clip:0.5

policy_freq:2

experiment=09_training_ddpg_cuda_fast_262
experiments=['training_ddpg_cpu_262']
algorithms=['DDPG']
averaging window=100 (user assigned)

Parameter test w/ emphasis on wirelength (W=2, H=6, O=2)



| | |
|--------|---------------------|
| title | ddpg_cpu_262:DDPG |
| run #0 | -38.3373 ± 180.7085 |
| run #1 | 162.6034 ± 218.3466 |
| mean | 62.1330 ± 199.5276 |

runs_involved=['1723856790_0', '1723856790_1']

End of automated test report 2024-08-17 16:40:04