2024-08-29 23:14 Page 1 of 4

Experiment Report

Start of automated test report 2024-08-29 23:14:29 Author=yons@yons-MS-7E06 obo YiFei Yu

Machine Information

sysname=Linux nodename=yons-MS-7E06 release=6.8.0-40-generic version=#40~22.04.3-Ubuntu SMP PREEMPT_DYNAMIC Tue Jul 30 17:30:19 UTC 2

CPU arch: X86 64

machine=x86 64

CPU bits: 64

CPU brand: Intel(R) Core(TM) i9-14900KF

CPU cores: 32

CPU base clock: 5.4000 GHz CPU boost clock: 5.4000 GHz System Memory: 94.13GB

Nvidia driver version: 550.90.07

Device 0: NVIDIA GeForce RTX 4090

Device 0: 23.99GB

Library Information

python: 3.8.19

torch: 1.13.1+cu117

optuna: 3.2.0 numpy: 1.23.3 pandas: 1.5.3 matplotlib: 3.7.1 seaborn: 0.12.2

pcb library: generation of .pcb files.

Library version: 0.0.12 Library built with: C++14

Library built on: Mar 3 2023 23:10:31

netlist_graph: Graph pre-processing library for PCB component placement.

Library version: 0.1.16 Library built with: C++14

Library built on: Mar 3 2023 23:10:32

2024-08-29 23:14 Page 2 of 4

Hpyerparameters

/home/yons/work/RL_PCB-main/tests/23_226_ddpg/hyperparameters/hp_dd

pg.json

learning_rate:0.001 buffer_size:25000

n_steps:2048 batch_size:128 gamma:0.99

net_arch:{'pi': [400, 300], 'qf': [400, 300]}

activation_fn:relu expl_noise:0.1

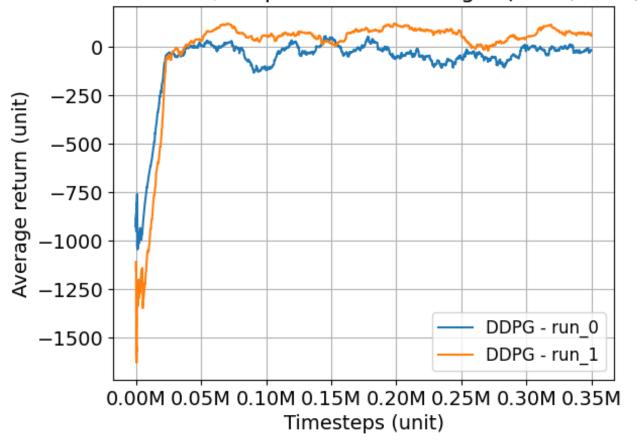
tau:0.005

policy_noise:0.2
noise_clip:0.5
policy_freq:2

2024-08-29 23:14 Page 3 of 4

experiment=parameter_test_226
experiments=['parameter_experiment_226']
algorithms=['DDPG']
averaging window=100 (user assigned)

Parameter test w/ emphasis on wirelength (W=2, H=2, O=6)



title	experiment_226:DDPG
run #0	-12.8410 ± 326.7339
run #1	70.0275 ± 203.2227
mean	28.5932 ± 264.9783

runs_involved=['1724926278_0', '1724926278_1']

2024-08-29 23:14 Page 4 of 4

End of automated test report 2024-08-29 23:14:31