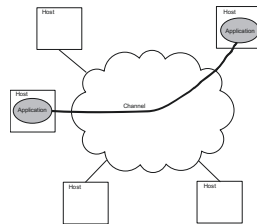


Requirements: (3) Process-to-Process Channels

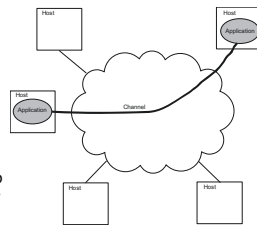


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Requirements: (3) Process-to-Process Channels

- The application programs running on the hosts connected to the network must be able to communicate in a meaningful and efficient way
- Network supports common process-to-process channels; e.g.,
 - Reliable (no loss, no errors, no duplication, in-order): for file access and digital libraries
 - Secure (privacy, authentication, message integrity)
 - Delay-bounded: for real-time voice and video



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Requirements: (3) e2e Channels

- What Goes Wrong in the Network?
 - Bit-level errors (electrical interference)
 - Packet-level errors (bit errors, congestion)
 - Link and node failures
 - Packets are delayed
 - Packets are delivered out-of-order
 - Third parties eavesdrop

The key problem is to fill in the gap between what applications expect and what the underlying technology provides

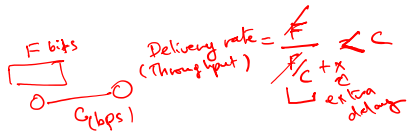
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Requirements: (3) e2e Channels

□ Performance: Bit Rate (throughput)

- m Amount of data that can be transmitted per time unit
- m Example: 10Mbps
- m link versus end-to-end



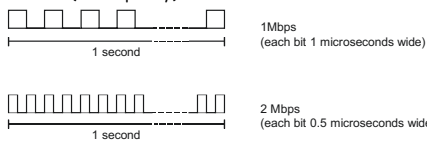
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Requirements: (3) e2e Channels

□ Performance: Bit Rate (throughput)

- m Amount of data that can be transmitted per time unit
- m Example: 10Mbps
- m link versus end-to-end
- m Notation
 - KB = 2^{10} bytes
 - Mbps = 10^6 bits per second
- m Bit rate (aka capacity) related to ``bit width''



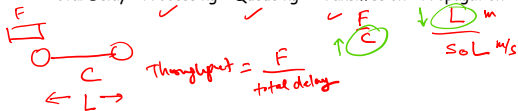
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Requirements: (3) e2e Channels

□ Performance: Delay

- m Time it takes to send message from point A to point B
- m Example: 24 milliseconds (ms)
- m Sometimes interested in round-trip delay (response time)
- m Components of delay
 - Total Delay = Processing + Queueing + Transmission + Propagation



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Requirements: (3) e2e Channels

□ Performance: Delay

- m Time it takes to send message from point A to point B
- m Example: 24 milliseconds (ms)
- m Sometimes interested in round-trip delay (response time)
- m Components of delay
 - Total Delay = Processing + Propagation + Transmit + Queue
 - Propagation Delay = Distance / SpeedOfLight
 - Transmission = Size / Bit Rate
- m Speed of light
 - 3.0×10^8 meters/second in a vacuum
 - 2.3×10^8 meters/second in a cable
 - 2.0×10^8 meters/second in a fiber

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Requirements: (3) e2e Channels

□ Relative importance of bit rate and propagation delay

- m small message (e.g., 1 byte): 1ms vs 100ms dominates 1Mbps vs 100Mbps
- m large message (e.g., 25 MB): 1Mbps vs 100Mbps dominates 1ms vs 100ms

Handwritten calculations:

US: $\frac{1B \times 8}{1Mbps} = 8\mu s$

VS: $\frac{1B}{100Mbps} = 0.008\mu s$

VS: $\frac{25MB \times 8}{1Mbps} = 200s$

VS: $\frac{25MB}{100Mbps} = 2s$

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Requirements: (3) e2e Channels

□ Relative importance of bit rate and propagation delay

- m small message (e.g., 1 byte): 1ms vs 100ms dominates 1Mbps vs 100Mbps
- m large message (e.g., 25 MB): 1Mbps vs 100Mbps dominates 1ms vs 100ms

□ Bandwidth (Bit Rate) x Delay Product (BxD)

Handwritten formula: $B \times D \uparrow$

Handwritten formula: $(bps) \times (\frac{s}{b})$

Diagram: A cylinder representing a pipe. The vertical axis is labeled "bandwidth" and the horizontal axis is labeled "delay".

- Example: 100ms round-trip propagation delay/time (RTT) and 45Mbps Bit Rate = 4,500,000 bits ~ 550 KB of data

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Where do we go from here?

"The secret of getting ahead is getting started. The secret to getting started is breaking your complex overwhelming tasks into small manageable tasks and then starting on the first one."

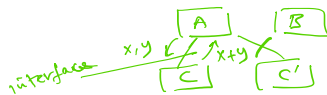
--Mark Twain

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Standards

- By having computers comply to the same standards, they can ``interoperate'' even if they are of different type or connected to different types of networks



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Standards

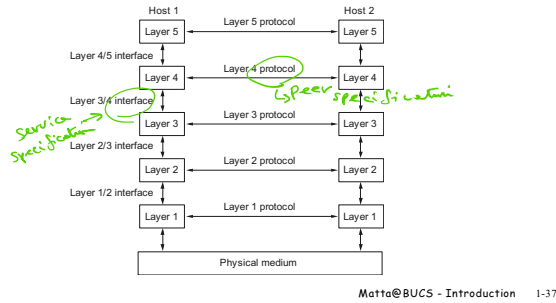
- By having computers comply to the same standards, they can ``interoperate'' even if they are of different type or connected to different types of networks
- Standards Organizations
 - In Europe:
 - ITU-T (formerly CCITT), e.g. publications X.25, V.24, etc.
 - X-series define how to connect a host to PSDN (Data)
 - V-series define how to connect a host to PSTN (Telephone)
 - I-series define how to connect a host to ISDN (Integrated)
 - ISO, developed OSI architecture
 - In US: IETF, IEEE, ANSI, NIST, ...
 - IETF RFCs define Internet standards
 - IEEE 802 define standards for links, e.g. Ethernet, WiFi

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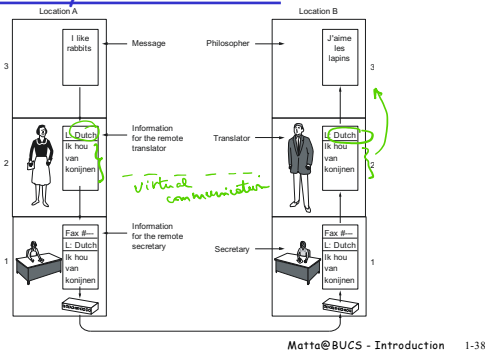
ISO/OSI Architecture

- Based on divide-and-conquer concept



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The Philosopher-translator-secretary Architecture



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ISO/OSI Architecture (cont'd)

Invite a friend to a dinner via postal mail

1. Trucks travels from one city to next
 2. Place mail on truck
 3. Build itineraries for delivery trucks
 4. P.O. collects mail
 5. Drop it in mail box
 6. write a letter
 7. Decide on specifics
- Handwritten notes: 'app-dependent' for steps 6 and 7; 'interface' for step 4; 'receiver' for step 5; 'net-dependent' for steps 1 and 2.

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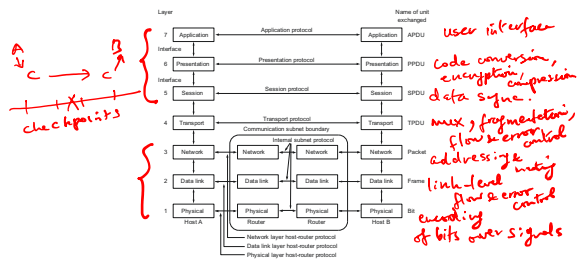
ISO/OSI Architecture (cont'd)

- The ISO/OSI model consists of seven layers:
 - ▢ Layers 5-7 are *application-oriented*
 - ▢ Layers 1-3 are *network-dependent*
 - ▢ Layer 4 provides the interface between 5-7 and 1-3

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The OSI Reference Model



- A **protocol** defines message types and associated actions
- MAC sublayer to handle multi-access (shared) links
- Internetwork sublayer to connect networks

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ISO/OSI Architecture (cont'd)

- Seven layers with following typical functions:
 - m **application**: user interface
 - m **presentation**: code conversion, encryption, compression
 - m **session**: organizes and synchronizes the data exchange
 - m **transport**: multiplexing/demultiplexing, fragmentation/reassembly, end-to-end flow control, congestion control and error control
 - m **network**: addressing and routing
 - m **data link**: link-level flow and error control
 - m **physical**: physical and electrical interfaces (normally 100% hardware)

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