| | | | | | | MEMOR' | Y MAPPING | | | | | | |
|---------|-----------------------------|------------|---|---------|--|--------|-----------|---|--|---|-----------------------|---|---------------|
| Address | addr[11] | addr[10:8] | | | addr[7 : 2] | | | | | | addr[1:0] | | |
| | х | х | х | х | х | х | х | х | х | х | х | х | Reserved |
| 0x9FF | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| | 1 | 0 | 0 | 1 | х | х | x | x | x | x | x | х | |
| 0x900 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | In Peripheral |
| 0x8FF | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| ••• | 1 | 0 | 0 | 0 | х | х | х | x | x | x | х | х | |
| 0x800 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Out Periphera |
| 0x7FF | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| | 0 | x | x | | x | x | х | x | x | × | x | х | |
| 0x000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | D_MEM |
| | sel_in | | X | sel_out | 6-bit addrs for array IP_mem and OP_mem | | | | | | | | |
| | 9-bit addrs for array D_mem | | | | | | | , | | | 2-bit byte-address | | |
| | | | | | | | | | | | | | |
| 0x | 8 | | | | 0 | | | | 0 | | | | |
| | - | | | | - | | | | , and the second | | | | |