ĐẠI HỌC BÁCH KHOA TP. HỒ CHÍ MINH KHOA KHOA HỌC VÀ KỸ THUẬT MÁY TÍNH



BÁO CÁO HỆ THỐNG NHÚNG

Bài thực hành số 4

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| Họ và tên | MSSV | Đóng góp |
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GitHub:

PhuLoi-1911545/school-LAB-embedded-ESP-IDF (github.com)

1. Thực hiện

Tạo một Project ESP IDF, sau đó code file main với nội dung như sau

```
main.c
    #include <stdio.h>
    #include <string.h>
    #include <unistd.h>
    #include "esp_log.h"
    #include "esp timer.h"
    #include "esp_sleep.h"
    #include "sdkconfig.h"
    #include "freertos/FreeRTOS.h"
    #include "freertos/task.h"
    #include "freertos/timers.h"
    #include "esp_system.h"
    #include "esp_spi_flash.h"
    #include "driver/gpio.h"
    #include "driver/uart.h"
    #define TIMER 1 MAX 10
    #define TIMER 2 MAX 5
    TimerHandle t xTimer1;
    TimerHandle t xTimer2;
```

```
TimerHandle t xTimer1;
  TimerHandle t xTimer2;
      const char *name = pcTimerGetTimerName(arg);
     uint32_t count = (uint32_t)pvTimerGetTimerID(arg);
      if(strcmp(name, "Timer 1")) {
          if(count > TIMER 1 MAX) {
              xTimerStop(arg,0);
              printf("ahihi - Nhom 3 L01 - HCMUT K19\n");
              vTimerSetTimerID(arg,(void*)count);
      else if (strcmp(name, "Timer 2")) {
          if(count > TIMER_2_MAX) {
             printf("STOP Timer 2\n");
             xTimerStop(arg,0);
              vTimerSetTimerID(arg,(void*)count);
void app_main(void)
                                 Parameter passed as input of the task */
```

```
printf("Timer 2 - Nhom 3 L01 - HCMUT K19\n");
                      vTimerSetTimerID(arg,(void*)count);
       void app_main(void)
            xTimer1 = xTimerCreate(
                // NULL, /* Task function. */
"Timer 1", /* String with name of task. */
                 periodic_timer_callback
            xTimer2 = xTimerCreate(
               // NULL, /* Task function. */
"Timer 2", /* String with name of task. */
                 pdTRUE, /* Parameter passed as input of the task */
0, /* Priority of the task. */
                 periodic timer callback
            xTimerStart(xTimer1, 0);
            xTimerStart(xTimer2,0);
                                         TERMINAL
Total sizes:
Used static DRAM: 11568 bytes ( 169168 remain, 6.4% used)
      .data size: 8976 bytes
.bss size: 2592 bytes
Used static IRAM: 48594 bytes ( 82478 remain, 37.1% used)
.text size: 47567 bytes
   .vectors size: 1027 bytes
Used Flash size: 115999 bytes
.text: 84547 bytes
.rodata: 31196 bytes
Total image size: 173569 bytes (.bin may be padded larger)
```

Giải thích:

- Hàm callback được sử dụng chung của 2 timer hàm void periodic_timer_callback
- Hàm sẽ xét tên của timer gọi nó và thực hiện tác vụ tương ứng
- 2 timer ở đây là Timer 1 và Timer 2

2. Nạp và kết quả

• Nạp vào esp, ta thấy kết quả hiện lên màn hình serial

```
xTimerStop(arg,0);
                            printf("ahihi - Nhom 3 L01 - HCMUT K19\n");
                            vTimerSetTimerID(arg,(void*)count);
               else if (strcmp(name, "Timer 2")) {
                      if(count > TIMER_2_MAX) {
              OUTPUT DEBUG CONSOLE TERMINAL
I (61) boot: ## Label
I (68) boot: 0 nvs
                                              Usage
WiFi data
                                                                     Type ST Offset Length
                                        WiFi data
RF data
factory app
                                                                   01 02 00009000 00006000
I (76) boot: 1 phy_init
I (83) boot: 2 factory
                                                                        01 01 0000f000 00001000
                                                                        00 00 00010000 00100000
  (91) boot: End of partition table
(95) boot_comm: chip revision: 1, min. application chip revision: 0
(102) esp_image: segment 0: paddr=00010020 vaddr=3f400020 size=07ad4h (
                                                                                                            31444) map
   (122) esp_image: segment 1: paddr=00017afc vaddr=3ffb0000 size=02310h ((126) esp_image: segment 2: paddr=00019e14 vaddr=40080000 size=06204h (
   (140) esp_image: segment 3: paddr=00020020 vaddr=40000020 size=14a44h ( 84548) map (171) esp_image: segment 4: paddr=00034a6c vaddr=40086204 size=05bd0h ( 23504) load
  (181) esp_image: segment 5: paddr=0003a644 vaddr=50000000 size=00010h ((187) boot: Loaded app from partition at offset 0x10000
                                                                                                                16) load
I (202) cpu_start: Starting app cpu, entry point is 0x40081090
0x40081090: call_start_cpu1 at C:/Users/luong/esp/esp-idf/components/esp_system/port/cpu_start.c:148
   (216) cpu_start: Application information:
   (221) cpu_start: Project name: template-app
   (226) cpu start: App version:
   (230) cpu start: Compile time:
                                                     Dec 12 2022 21:53:29
   (236) cpu_start: ELF file SHA256: 7da3ef58e92f3916...
(242) cpu_start: ESP-IDF: v4.4.3
   (248) heap_init: Initializing. RAM available for dynamic allocation:
   (255) heap_init: At 3FFAE6E0 len 00001920 (6 KiB): DRAM
(261) heap_init: At 3FFB2D30 len 0002D2D0 (180 KiB): DRAM
   (267) heap init: At 3FFE0440 len 00003AE0 (14 KiB): D/IRAM (273) heap init: At 3FFE4350 len 0001BCB0 (111 KiB): D/IRAM (280) heap init: At 4008BDD4 len 0001422C (80 KiB): IRAM
   (287) spi_flash: detected chip: generic
   (291) spi flash: flash io: dio
   (296) cpu_start: Starting scheduler on PRO CPU.
I (0) cpu_start: Starting scheduler on APP CPU.
ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
ihaha - Nhom 3 L01 - HCMUT K19
```

```
I (0) cpu start: App cpu up.
I (216) cpu start: Pro cpu start user code
I (216) cpu start: cpu freq: 160000000
I (216) cpu start: Application information:
I (221) cpu_start: Project name: template-app
I (226) cpu_start: App version: 1
I (230) cpu_start: Compile time: Dec 12 2022 21:53:29
I (236) cpu_start: FUT 6:3- cure
I (236) cpu_start: ELF file SHA256: 7da3ef58e92f3916...
I (242) cpu start: ESP-IDF: v4.4.3
I (248) heap_init: Initializing. RAM available for dynamic allocation:
I (255) heap init: At 3FFAE6E0 len 00001920 (6 KiB): DRAM
I (261) heap init: At 3FFB2D30 len 0002D2D0 (180 KiB): DRAM
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ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
ihaha - Nhom 3 LØ1 - HCMUT K19
ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
ihaha - Nhom 3 L01 - HCMUT K19
ahihi - Nhom 3 L01 - HCMUT K19
STOP Timer 2
ahihi - Nhom 3 L01 - HCMUT K19
STOP Timer 1
```

- Chuỗi "Timer 2 Nhom 3 L01 HCMUT K19" được in ra 3 giây sau mỗi lần và in ra 5 lần
- Chuỗi "Timer 1 Nhom 3 L01 HCMUT K19 "được in ra 2 giây sau mỗi lần và in ra 10 lần