

Why NOT withdraw my resources?

Huu-Phuc Vo

– **From 2017-08 to 2018- 02**

- I DID NOT have any supervision
- I DID NOT have any ISP.

– **From 2018-02-06 to 2019-06-12**

- The net full-time study and research is **9 months**.

– **The link to the folder that contains all these evidence and ISP is here**

(https://github.com/PhucVH888/why_not_withdraw_resources).

1. **isp_2015_2019 folder**: Individual study plan from 2015 to 2019.

(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/isp_2015_2019)

2. **timeline.pdf**: Timeline from 11-2015 until now.

(https://github.com/PhucVH888/why_not_withdraw_resources/blob/master/timeline.pdf)

3. **why_not_withdraw_resources.pdf**: Includes exchanged discussions via email with supervisors. Evidence of working continuously on ISP.

(https://github.com/PhucVH888/why_not_withdraw_resources/blob/master/why_not_withdraw_resources.pdf)

4. **work_on_isp_goals**: Evidence of working on ISP from 2015 to 2019.

(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals)

5. **workLog_2018_2019.pdf**: Worklog in details from 2018-2019.

(https://github.com/PhucVH888/why_not_withdraw_resources/blob/master/workLog_2018_2019.pdf)

6. **work_at_weekends folder**: Evidence shows that the ISP's goals and publication are identical.

(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_at_weekends)

– **From 2018-02 until so far, I have three 3-month ISPs since I changed my research topic from February 2018.**

- Due to sick leave, parental leave, and VAB leave, the last revision date of 3-month ISP was compensated and extended.
- This recent ISP is planned for 3 months (12 weeks) including holidays.

- However, the total amount of time to work on these goals and courses are **more than 3 months (12 weeks)**. If the supervisors clearly ask me to work extra time, I would consider to do that.
 - Goal 1: applied logic course: 10 weeks.
 - Goal 2: repository: 2 weeks.
 - Academic teacher training course: 5 weeks (25 full-time working days).
 - Survey: 2 weeks, interleaving with doing above goals.
 - 1DL451 course: ??? weeks.
- **According to the current ISP, there are 2 goals and 2 remained goals from previous ISP.**
 - Goal 1: “Applied Logic” course (10 weeks), there are 4 chapters with approximately 100 exercises to finish, I am still working on this goal by submitting solutions of exercises to my supervisors. Please refer to these evidence.
 (https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals/goal1). This folder contains the emails show that I am working on this goal by submitting solutions and getting feedback from my supervisor.
 - Goal 2: “Repository” (2 weeks), I already submitted the source codes of models to my supervisors. After waiting for approximately a month, I reminded him to give me feedback. I am still working on this goal. Please refer to these evidence.
 (https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals/goal2). This folder contains the emails that I discussed and submitted the work to my supervisor.
 - Extra goal: “Academic Teacher Training Course”, approved by the head of department.
 - I am still working on the course with the approval from the head of department.
 However, I have to work extra because the time for taking this course since the ISP plans the time for goal 1 (10 weeks) and goal 2 (2 weeks). The approval from the head of department could be found here:
https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals/attc_7.5hp
 - Remained_1DL451 goal: Since goal 1 and 2 in the current ISP take 12 weeks (3 months), I had to discuss with the ombudsman and union when my supervisors asked me to retake this course. It means that the total time for these goals is more than 3 month. So far, I have contacted teacher of the course for retaking when we construct next ISP.

Please refer to the link

(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals/more_1DL451). I took this project course 2 times before.

- In the first time, the problem specification requires 1 solution with the given example. I submitted the solution. But the teacher did not rely on the specification, and asked me to do more than the given specification which was not specified in the problem specification before.
- In the second time, the teacher failed me because of the typo, grammar errors, and writing.
- In both cases, I asked for detailed explanation and evaluation criteria, but I did not get the measurable criteria to pass the course from the teacher.
- **Remained_survey:** The requirement of the survey is 20 pages with good quality. In the first ISP, after the first 3-month full-time study, I submitted a 40 pages survey with 150 related papers in the references. However, my supervisors concluded it is not fulfill the requirements. They refused to provide the measurement and evaluation criteria for a survey which must be finished in 3 months.
 - While doing goal 1 and 2 in total 12 weeks, I had to work extra on revising the survey. Please refer to the link (https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_on_isp_goals/more_survey).
 - In the very beginning, before working on the survey, I asked my supervisors about the “good quality” criteria within three months in case there are so many related work, but they did not give any measurement for this 3 months goal.
- **My publications:** These publications are the extension and inline with the 1DL451 course goal, and thesis title in my ISP. I just wonder whether something wrong if a PhD student spend his time at weekend to continue to extend the goals in his ISP.
 - At weekends, I extend the results of the course 1DL451, and develop my research plan from the thesis title in the ISP. Then I submitted these results to conferences. Since my supervisors numerous times refuses to help me in writing papers, so I have to do it at weekends, and I am the sole author. So far, two papers and one poster were accepted.
 - Publication #1: Poster at ETAPS conferences “Towards Efficient Algorithms for Constraint Satisfaction Problems”: this is the result of 1DL451 course final report.

Please refer to the link

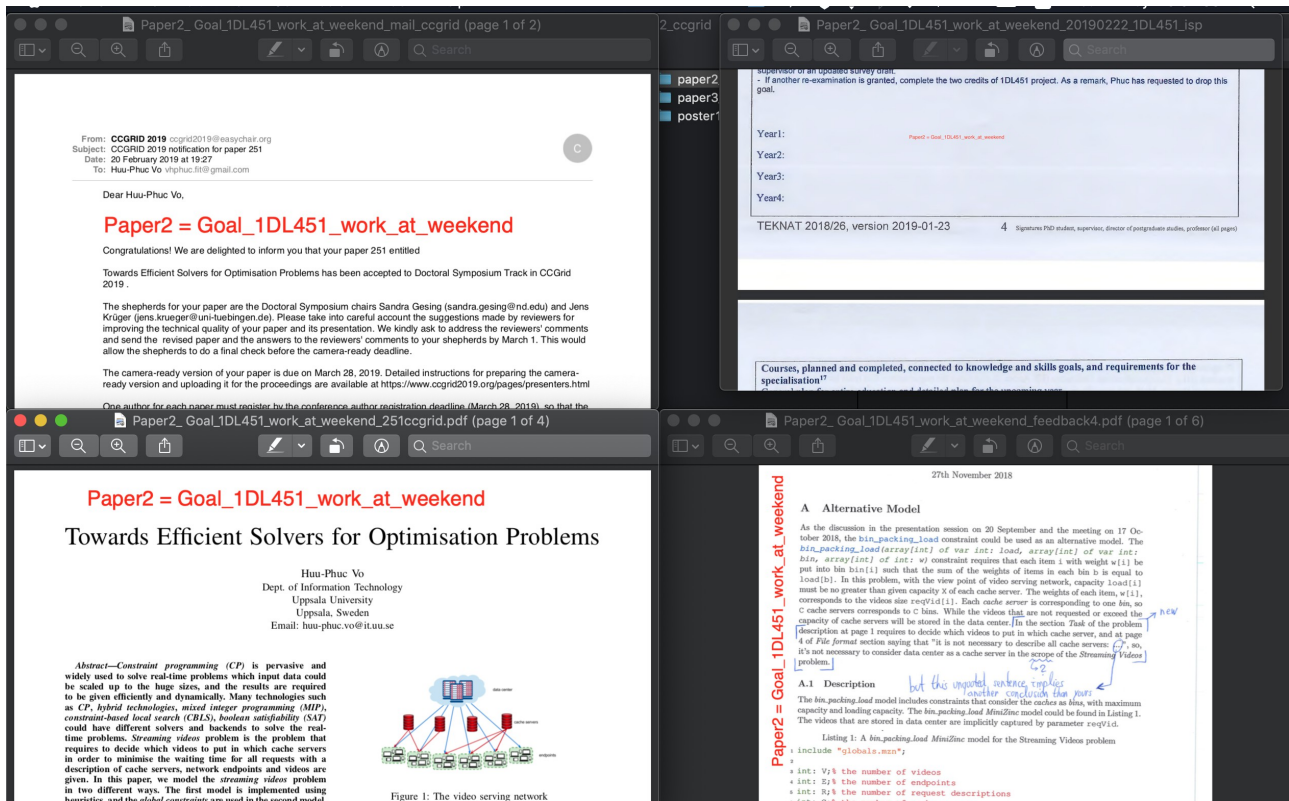
(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_at_weekends/poster1_etaps). This folder contains:

- (1) 1DL451 course goal in the ISP;
 - (2) 1DL451 project report (version 1 and 4);
 - (3) etaps poster which is the summary of revised version of 1DL451 course report;
 - (4) letter of acceptance from the ETAPS committee.
- Publication #2: Paper at CCGRID conference “Towards Efficient Solvers for Optimisation Problems”: this is the result of 1DL451 course final report. Please refer to the link
(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_at_weekends/paper2_ccgrid). This folder of publication 2 contains
 - (1) 1DL451 course goal in the ISP;
 - (2) 1DL451 project report (version 1 and 4);
 - (3) CCGRID paper which is the revised version of 1DL451 course report;
 - (4) letter of acceptance from the CCGRID committee.
 - Publication #3: Paper at ICAC conference “Machine-Assisted Reformulation for MiniZinc”: this paper is about the research plan which is written in my current ISP. Please refer to the link
(https://github.com/PhucVH888/why_not_withdraw_resources/tree/master/work_at_weekends/paper3_icac). This folder of publication 3 contains
 - (1) thesis title in the ISP;
 - (2) ICAC paper which is the plan, goals to proceed my degree in the ISP;
 - (4) letter of acceptance from the ICAC committee.

- Poster: Poster at ETAPS conferences “Towards Efficient Algorithms for Constraint Satisfaction Problems”

such as CP, MIP [5], CBLS, satisfiability modulo theories (SMT) [6], [7], and SAT [6] could have different solvers and backends to solve the real-time problems. Each technology has its scope of application, and none of the technology is dominant all problems. CP [8] is pervasive and widely used to solve real-time problems which input data could be scaled up to the enormous sizes, and results are required to be given efficiently and dynamically.

● Paper 1: Paper at CCGRID conference “Towards Efficient Solvers for Optimisation Problems”



● Paper 2: ICAC = thesis title: “Machine-Assisted Reformulation for MiniZinc”

