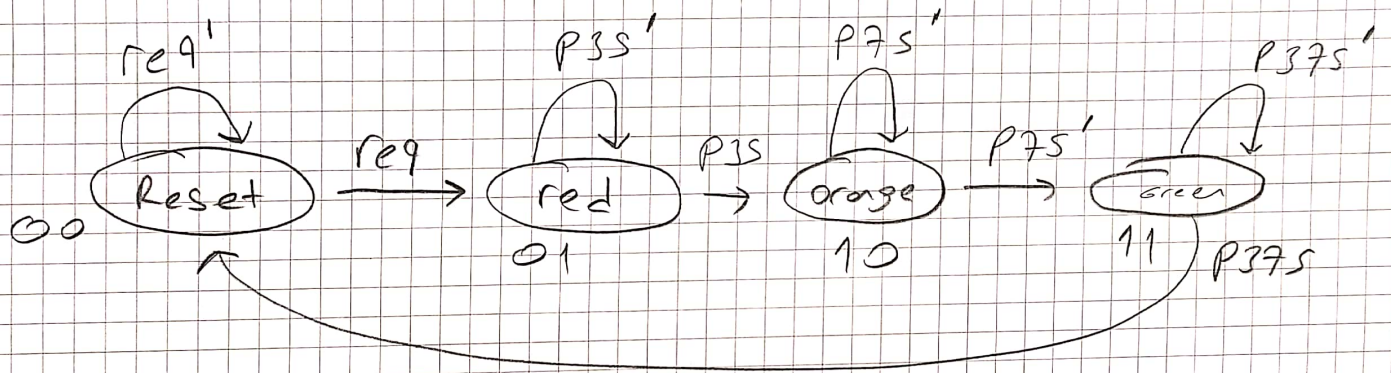


State diagram



Inputs						Outputs					
S1	S0	req	P3S	P7S	P37S	resetT	redL	orangeL	greenL	Y1	Y0
0	0	0	X	X	X	1	1	0	0	0	0
0	0	1	X	X	X	1	1	0	0	0	1
0	1	X	0	X	X	0	1	0	0	0	1
0	1	X	1	X	X	0	1	0	0	1	0
1	0	X	X	0	X	0	0	1	0	1	0
1	0	X	X	1	X	0	0	1	0	1	1
1	1	X	X	X	0	0	0	0	1	1	1
1	1	X	X	X	1	0	0	0	1	0	0

Boolean

$$\text{reset} = S_1' S_0' \text{req}' + S_1' S_0' \text{req} = S_1' S_0' (\text{req}' + \text{req}) \\ = S_1' S_0'$$

$$\text{redL} = \underbrace{S_1' S_0' \text{req}' + S_1' S_0' \text{req}}_{S_1' S_0' (\text{req}' + \text{req})} + \underbrace{S_1' S_0 P_3 S' + S_1' S_0 P_3 S}_{S_1' S_0 (P_3 S' + P_3 S)} = \\ \underbrace{S_1' S_0'}_{\text{redL}} + \underbrace{S_1' S_0}_{\text{redR}}$$

$$\text{orangeL} = S_1 S_0' P_7 S' + S_1 S_0' P_7 S = S_1 S_0' (P_7 S' + P_7 S) = \\ = S_1 S_0'$$

$$\text{greenL} = S_1 S_0 P_3 S' + S_1 S_0 P_3 S = S_1 S_0 (P_3 S' + P_3 S) \\ = S_1 S_0$$

$$n1 = s1's0p3s + \underbrace{s1s0'p7s' + s1s0'p7s + s1s0p37s'}_{s1s0'(p7s' + p7s) = s1s0'}$$

$$n0 = s1's0'req + s1's0p3s' + s1s0'p7s + s1s0p37s'$$
