

RABv520

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Test Suite Overview

Test Suite Structure			
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CombinationOnDPCH/Conversational_Speech/	tc_14_2_4a		Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1260
CombinationOnDPCH/Conversational_Speech/	tc_14_2_5a	RB_Speech_C57	Conversational / speech / UL:(10.2, 6.7, 5.9, 4.75) DL:(10.2, 6.7, 5.9, 4.75) kbps / CS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH	1262
CombinationOnDPCH/Conversational_Speech/	tc_14_2_7a	RB_Speech_C58	Conversational / speech / UL:7.4 DL:7.4 kbps / CS RAB+ UL:3.4 DL:3.4 kbps SRBs for DCCH	1264
CombinationOnDPCH/Conversational_Speech/	tc_14_2_9	RB_Speech_C115	Conversational / speech / UL:5.9 DL:5.9 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1266
CombinationOnDPCH/Conversational_unknown/	tc_14_2_12	RB_Conversational_C118	Conversational / unknown / UL:28.8 DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1267
CombinationOnDPCH/Conversational_unknown/	tc_14_2_13_1	RB_Conversational_C119	Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI	1268
CombinationOnDPCH/Conversational_unknown/	tc_14_2_13_2	RB_Conversational_C120	Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI	1269
CombinationOnDPCH/Conversational_unknown/	tc_14_2_14_1	RB_Conversational_C121	Conversational / unknown / UL:32 DL:32 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:32 DL:32 kbps / CS RAB / 20 ms TTI	1270

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CombinationOnDPCH/Streaming_unknown/	tc_14_2_15	RB_Streaming_C123	Streaming / unknown / UL:14.4/DL:14.4 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1272
CombinationOnDPCH/Streaming_unknown/	tc_14_2_16	RB_Streaming_C124	Streaming / unknown / UL:28.8/DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1273
CombinationOnDPCH/Streaming_unknown/	tc_14_2_17	RB_Streaming_C125	Streaming / unknown / UL:57.6/DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1274
CombinationOnDPCH/Interactive_Background/	tc_14_2_23a1	RB_Intract_Backgr_C397	Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.	1275
CombinationOnDPCH/Interactive_Background/	tc_14_2_23a2		Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.	1277
CombinationOnDPCH/Interactive_Background/	tc_14_2_23b		Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1279
CombinationOnDPCH/Interactive_Background/	tc_14_2_23c	RB_Intract_Backgr_C131	Interactive or Background / UL:32/DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH (TC,40ms TTI)	1281
CombinationOnDPCH/Interactive_Background/	tc_14_2_26	RB_Intract_Backgr_C140	Interactive or background / UL:64 DL: 64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1283
CombinationOnDPCH/Interactive_Background/	tc_14_2_27	RB_Intract_Backgr_C141	Interactive or background / UL:64 DL: 128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1285
CombinationOnDPCH/Interactive_Background/	tc_14_2_28	RB_Intract_Backgr_C142	Interactive or Background / UL:128/DL: 128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1287
CombinationOnDPCH/Interactive_Background/	tc_14_2_29	RB_Intract_Backgr_C143	Interactive or Background / UL:64/DL: 144kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1289

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CombinationOnDPCH/Interactive_Background/	tc_14_2_31_1	RB_Intract_Backgr_C14 5	nteractive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI	1291
CombinationOnDPCH/Interactive_Background/	tc_14_2_32_1	RB_Intract_Backgr_C14 7	Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI	1293
CombinationOnDPCH/Interactive_Background/	tc_14_2_32_2	RB_Intract_Backgr_C14 8	Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI	1295
CombinationOnDPCH/Interactive_Background/	tc_14_2_34_1	RB_Intract_Backgr_C15 1	Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI	1298
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_38a		Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1300
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_38b		Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1302
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_38c		Conversational / speech / UL: 12.2 kbps DL: 12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1304
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_38e		Conversational / speech / UL : (12.2 7.95 5.9 4.75) kbps DL : (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1306
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_38f		Conversational / speech / (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1308
CombinationOnDPCH/Convspeech_InteractBackground/	tc_14_2_40	RB_Speech_IB_C167	Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB	1310

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CombinationOnDPCH/Convspeech_InteractBackgrnd/	tc_14_2_41	RB_Speech_IB_C168	Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1312
CombinationOnDPCH/Convspeech_InteractBackgrnd/	tc_14_2_43_1	RB_Speech_IB_C171	Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 10 ms TTI	1314
CombinationOnDPCH/Convspeech_InteractBackgrnd/	tc_14_2_43_2	RB_Speech_IB_C172	Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 20 ms TTI	1316
CombinationOnDPCH/Convspeech_ConvUnknown/	tc_14_2_49_1	RB_Speech_Conv_C179	Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI.	1318
CombinationOnDPCH/Convspeech_ConvUnknown/	tc_14_2_51_1	RB_Conv_IB_C183	Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL:64 DL:64 kbps / PS RAB	1320
CombinationOnDPCH/Convspeech_ConvUnknown/	tc_14_2_51a_1		Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB	1321
CombinationOnDPCH/Convspeech_ConvUnknown/	tc_14_2_51b_1		Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 16 DL: 64 kbps / PS RAB	1324
CombinationOnDPCH/InteractBackgrnd_StreamUnknown/	tc_14_2_58		Streaming / unknown / UL: 16 DL: 64 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1325
CombinationOnDPCH/InteractBackgrnd_StreamUnknown/	tc_14_2_58a		Streaming / unknown / UL: 16 DL: 128 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH	1327

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CombinationsOnSCCPC H/	tc_14_4_2_1	RB_Intract_Backgr_C204	One SCCPCH: Interactive/Background 32 kbps PS RAB + SRBs for CCCH + SRB for DCCH + SRB for BCCH	1330
CombinationsOnSCCPC H/	tc_14_4_2_2	RB_Intract_Backgr_C204	Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.	1332
CombinationsOnSCCPC H/	tc_14_4_2_3	RB_Intract_Backgr_C204	Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.	1334
CombinationsOnSCCPC H/	tc_14_4_2a_1	RB_Intract_Backgr_C64	One SCCPCH: Interactive/Background 32 kbps PS RAB + Interactive/Background 32 kbps PS RAB + SRBs for CCCH + SRB for DCCH + SRB for BCCH	1336
CombinationsOnSCCPC H/	tc_14_4_2a_2	RB_Intract_Backgr_C64	Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.	1338

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CombinationsOnSCCPC H/	tc_14_4_2a_3	RB_Intract_Backgr_C64	Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for two Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.	1340
CombinationsOnSCCPC H/	tc_14_4_3	RB_Intract_Backgr_C20 5	Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.	1342
Detailed Comments :				

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Detailed Comments :			

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NAS_Defaults/	NAS_OtherwiseFailActRB_TM		2319
RRC_Defaults/	RRC_Def1		2320
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SS_Defaults/	SS_Def		2328
Detailed Comments :			

II

Declarations Part

Simple Type Definitions			
Type Name	Type Definition	Type Encoding	Comments
AccessPtN	OCTETSTRING[1..100]		This is the DNS name. This can take values as per 24.008, 10.5.6.1
AddressInfo	OCTETSTRING[0..16]		Address information, this is the DNS server name. this will be converted into
AM_SeqNum	BITSTRING[12]		Sequence number for an AM RLC PDU. Ref 3G TS 25.322 clause 9.2.2.3
AuthRsp	BITSTRING [32]		Authentication Response Parameter 3G TS 24.008 cl. 10.5.3.2
B1	BITSTRING [1]		Generic type for 1 bit value
B128	BITSTRING [128]		Generic type for 128 bit value
B16	BITSTRING [16]		Generic type for 16 bits value
B18	BITSTRING [18]		Generic type for 18 bits value
B2	BITSTRING [2]		Generic type for 2 bits value
B20	BITSTRING [20]		Generic type for 20 bits value
B3	BITSTRING [3]		Generic type for 3 bits value
B4	BITSTRING [4]		Generic type for 4 bits value
B48	BITSTRING [48]		Generic type for 48 bit value
B5	BITSTRING [5]		Generic type for 5 bits value
B6	BITSTRING [6]		Generic type for 6 bits value
B7	BITSTRING [7]		Generic type for 7 bits value
B8	BITSTRING [8]		Generic type for 8 bits value
B80	BITSTRING [80]		Generic type for 80 bit value
Bitmap	OCTETSTRING[1..16]		The bitmap within a Bitmap super-field. Ref 3G TS 25.322 clause 9.2.2.11.5
BitmapLen	BITSTRING[4]		The length of a bitmap super field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.11.5
BitRate	OCTETSTRING[1]		Maximum bit rates supported
CauseValue	OCTETSTRING[1]		SM Cause Value
CLIR_Invocation	BITSTRING('10100010'B)		CLIR invocation 3G TS 24.008 cl. 10.5.4.11b
CLIR_Suppression	BITSTRING('10100001'B)		CLIR suppression 3G TS 24.008 cl. 10.5.4.11a
CtrlPDU_Type	BITSTRING[3]		PDU type for AM STATUS PDUs. Ref 3G TS 25.322 clause 9.2.2.2
CTSPerm	IEI8		CTSPerm 3G TS 24.008 cl. 10.5.3.10 value "10100010"B
CW_NumberPart	BITSTRING[3]		The number part (X1X2X3) of a codeword in an RLIST SUFI. Ref 3G TS 25.322 clause 9.2.2.11.6

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Simple Type Definitions			
Type Name	Type Definition	Type Encoding	Comments
DC_Field	BITSTRING[1]		Control / Data type indicator for AM RLC PDU. Ref 3G TS 25.322 clause 9.2.2.1
FollowOnProceed	IEI8		Follow On Proceed 3G TS 24.008 cl. 10.5.3.7 value "10100001"B
Fresh	BITSTRING[32]		
GSM_CipheringKey	BITSTRING [64]		
IEI4	B4		information element identifier, type 1
IEI8	B8		information element identifier, type 2–4
IntegrityKey	BITSTRING[128]		
KeySeq	B3		ciphering key sequence 3G TS 24008 cl. 10.5.1.2
Length	OCTETSTRING [1]		IE length
LIST_Len	BITSTRING[4]		The length of a LIST super field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.11.4
LogicChGERAN	IA5String		Logical channel (used for Interworking with GERAN)
MaxBitRate	OCTETSTRING[1]		Gaurented Bit rate
MaxSDU_Size	OCTETSTRING[1]		Maximum SDU size
MM_RAND	BITSTRING [128]		Authentication parameter RAND 3G TS 24.008 cl. 10.5.3.1
MRW_Len	BITSTRING[4]		The length of a MRW super field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.11.8
MsgType	B8		Message Type 3G TS 24.008 cl. 10.4. This type is also used by the BMC protocol.
N_Length	BITSTRING[4]		The N_Length field within an MRW superfield. Ref 3G TS 25.322 clause 9.2.2.11.8
O0_8	OCTETSTRING[0..8]		Generic type for 0 to 8 byte value
O3	OCTETSTRING [3]		Generic type for 3 byte value
Padding	HEXSTRING[2..8192]		Padding for RLC UM or AM PDU. Ref 3G TS 25.322 clause 9.2.2.10 Note that this type should ideally be an OCTETSTRING[1..4096], but since TTCN does not have a predefined operator for INT_TO_OCT, a HEXSTRING is used instead.

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Simple Type Definitions			
Type Name	Type Definition	Type Encoding	Comments
PDP_TypeNo	OCTETSTRING[1]		PDP Type number, this will take values 00000001: PDP Type PPP 00000010: PDP Type IHOSS 01000001: IPv5 01010111: IPv6 This can take values as per 24.008, clause 10.5.6.4
ProtocolDiscriminator	B4		Protocol Discriminator 3G TS 24.008 cl. 10.2
ProtoldContents	OCTETSTRING[1..251]		3G TS 24.008 cl. 10.5.6.3
RejCau	OCTETSTRING[1]		Reject Cause 3G TS 24.008 cl. 10.5.3.6
RLIST_Len	BITSTRING[4]		The length of a RLIST super field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.11.6
SapId	OCTETSTRING [1]		SAP Identifier ??? defined in several places GERAN 04.06 and 3G TS 24.008 as bitstring[2] ?
SkipIndicator	B4		Skip Indicator 3G TS 24.008 cl. 10.3.1
SS_CN_DomainIdentity	INTEGER (0..1)		CN domain identity type in TTCN tabular format
SS_RB_Identity	INTEGER (-31..32)		RB identity type in TTCN tabular format
SUFI_ListLi	BITSTRING[4]		Number of consecutive PDUs not correctly received following PDU with sequence number SNi. Used in List super field. Ref 3G TS 25.322 clause 9.2.2.11.4
SUFI_SN_MRWi	BITSTRING[12]		SN_MRWi field to be used within move receiving window super fields. Ref 3G TS 25.322 clause 9.2.2.11.7
SUFI_Type	BITSTRING[4]		The type of a super field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.11
UE_TestLoopMode	OCTETSTRING[1]		UETestLoopMode 3G TS 34.109 cl. 6.2
UTRAN_GERAN	IA5String ("UTRAN and GERAN", "UTRAN only")		
Detailed Comments :			

Structured Type Definition			
Type Name : CellInfoCfg Encoding Variation : Comments : This structure contains relevant information describing a cell configuration as needed for basic test Steps like ts_CreateCell and ts_IdleUpdated. NOTE: this information is not related to any PDUs, IEs or ASPs			
Element Name	Type Definition	Field Encoding	Comments
cellId	INTEGER		Cell Id
frequencyInfo	FrequencyInfo		Frequency information for current cell
attenuationLevel	INTEGER		Value of RF attenuator
priScrmCode	PrimaryScramblingCode		Primary Scrambling Code
powerpCPICH	DL_TxPower_PCPICH		Absolute Tx Power of (primary) CPICH
powerpSCH	DL_TxPower		Tx power level of primary SCH relative to CPICH
powersSCH	DL_TxPower		Tx power level of secondary SCH relative to CPICH
powerpCCPCH	DL_TxPower		Tx power level of primary CCPCH relative to CPICH
powersCCPCH	DL_TxPower		Tx power level of secondary CCPCH relative to CPICH
powersCCPCH1	DL_TxPower		Tx power level of secondary CCPCH1 relative to CPICH
timingsCCPCH1	INTEGER		Timing offset for secondary CCPCH1
powerAICH	AICH_PowerOffset		Tx power level of AICH relative to CPICH
powerPICH	PICH_PowerOffset		Tx power level of PICH relative to CPICH
cellTxPowerLevel	CellTxPowerLevel		Total cell power level (\geq sum of all configured physical channels)
tCell	Tcell		Cell timing offset (in chips)
sfnOffset	INTEGER		SFN offset (in frames)
puncLimit	PuncturingLimit		Puncturing limit for PRACH
sf_PRACH	SF_PRACH		Spreading factor for PRACH
slotFormatsCCPCH1	SCCPCHSlotFormat		Slot format for secondary CCPCH1
mcc	HEXSTRING		MCC
mnc	HEXSTRING		MNC
lac	OCTETSTRING		LAC
rac	OCTETSTRING		RAC
attFlag	INTEGER		Attach flag (as broadcasted in BCCH)
nmo	OCTETSTRING		Network mode of operation
ura_Identity	BITSTRING		URA Identity (3GPP 25.331 clause 10.3.2.6)
t3212	OCTETSTRING		T3212 value

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Structured Type Definition			
Element Name	Type Definition	Field Encoding	Comments
cRNTI	C_RNTI		cell radio network temporary identity assigned to the UE under test, this initial value is for ts_SS_CreateCellFACH. (from 34.123-1)
uRNTI	U_RNTI		smc_Identity and s_RNTI
cellConfig	RB_ConfigType		Current configuration of the cell
dRX_CycleLength	DRX_CycleLengthStructure		To hold the 3 dRX_CycleLength
uL_ScramblingCode	UL_ScramblingCode		To hold the UL scrambling to be used in the cell
DL_DPCH_SHO	BOOLEAN		To be used in SHO (Active set update) test cases to indicate whether a DL DPCH is configured
UL_DPCH_SHO	BOOLEAN		To be used in SHO (Active set update) test cases to indicate whether a DL DPCH is configured
dl_DPCH_2ndScrCode	SecondaryScramblingCode		Secondary scrambling code for the DL DPCH
Detailed Comments :			

Structured Type Definition			
Type Name : DRX_CycleLengthStructure Encoding Variation : Comments : Structure taht contains CN DRX cycle length (CS and PS) and the UTRAN DRX Cycle length. This type is to be used in the CellInfoCfg in order to keep all DRX values.			
Element Name	Type Definition	Field Encoding	Comments
cN_CS_DRX_CycleLength	CN_DRX_CycleLengthCoefficient		
cN_PS_DRX_CycleLength	CN_DRX_CycleLengthCoefficient		
uTRAN_DRX_CycleLength	UTRAN_DRX_CycleLengthCoefficient		
Detailed Comments :			

Structured Type Definition			
Type Name : AuthenticationFailureParameter Encoding Variation : Comments : Authentication Failure Parameter (TLV) 3G TS 24.008 cl. 10.5.3.2.2			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100010'B for MM (22 hex)
iel	Length		'00110000'B for GMM (30 hex)
auts	BITSTRING[112]		M 1 octet AUTS, 14 octets
Detailed Comments :			

Structured Type Definition			
Type Name : AC_ReferenceNumber Encoding Variation : Comments : Cipher Algorithm 3GPP 24.008 / 10.5.5.19			
Element Name	Type Definition	Field Encoding	Comments
value	B4		
Detailed Comments :			

Structured Type Definition			
Type Name : AUTN Encoding Variation : Comments : Authentication Parameter AUTN 3G TS 24.008 cl. 10.5.3.1.1			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100000'B
iel	Length		'10'O
aUTN	BITSTRING[128]		Authentication Parameter AUTN
Detailed Comments :			

Structured Type Definition			
Type Name : AccessPtName Encoding Variation : Comments : 24.007, section 10.5.6.1			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00101000'B (28hex)
length	Length		
accessPtName	AccessPtN		Access point name value 3–102 octets
Detailed Comments :			

Structured Type Definition			
Type Name : AttachResult Encoding Variation : Comments : Attach result 3GPP 24.008 / 10.5.5.1			
Element Name	Type Definition	Field Encoding	Comments
spare	B1		
result	B3		Attach result
Detailed Comments :			

Structured Type Definition			
Type Name : AttachType Encoding Variation : Comments : Attach type 3GPP 24.008 / 10.5.5.2			
Element Name	Type Definition	Field Encoding	Comments
for	B1		Follow-on request
type	B3		Type of attach
Detailed Comments :			

Structured Type Definition			
Type Name : AuthRspExt Encoding Variation : Comments : Authentication Response parameter (extension) 3G TS 24.008 cl. 10.5.3.2.1			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100001'B for MM (21 hex) '00101001'B for GMM (29 hex)
iel rES	Length BITSTRING[1..96]		Authentication Parameter RES
Detailed Comments :			

Structured Type Definition			
Type Name : AuthRsp_tv Encoding Variation : Comments : Authentication Response parameter (TV, 5 octets) 3G TS 24.008 cl. 10.5.3.2			
Element Name	Type Definition	Field Encoding	Comments
iei value	IEI8 BITSTRING[32]		'00100010'B (22 hex) Authentication Parameter RES
Detailed Comments :			

Structured Type Definition			
Type Name : AuthenticationParamterRAND Encoding Variation : Comments : Authentication Parameter RAND (TV, 17 octets) 3G TS 24.008 cl. 10.5.3.1			
Element Name	Type Definition	Field Encoding	Comments
iei randValue	IEI8 BITSTRING[128]		'00100001'B (21 hex) Authentication Parameter RAND value
Detailed Comments :			

Structured Type Definition			
Type Name : Bcap Encoding Variation : Comments : Bearer capability (CC information element) 3G TS 24.008 cl. 10.5.4.5			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '00000100'B
iel	Length		length
extBit3	B1		extension bit
radioChRequi	B2		radio channel requirements ue->n, GSM, octet 3
codingStd	B1		coding standard, '0'B, octet 3
transferMode	B1		transfer mode octet 3
itc	B3		information transfer capability, octet 3
bcap3aEtc1	Bcap3aEtc		octet 3a etc no.1
bcap3aEtc2	Bcap3aEtc		octet 3a etc no.2
bcap3aEtc3	Bcap3aEtc		octet 3a etc no.3
bcap3aEtc4	Bcap3aEtc		octet 3a etc no.4
bcap3aEtc5	Bcap3aEtc		octet 3a etc no.5
bcap3aEtc6	Bcap3aEtc		octet 3a etc no.6
extBit4	B1		extension bit, octet 4
compress	B1		compression, ue->network, octet 4
structure	B2		structure, '0, '3', octet 4
duplexMode	B1		duplex mode, '1'B, octet 4
cfg	B1		configuration, '0'B, octet 4
nirr	B1		negotiation of intermediate rate requested, GSM, octet 4
establish	B1		establishment, '0'B, octet 4
extBit5	B1		extension bit, octet 5
accessId	B2		access identify, '00'B, octet 5
rateAdapt	B2		rate adaption, octet 5
sacp	B3		signalling access protocol, '1 – 6', octet 5
extBit5a	B1		extension bit, octet 5a
OtherItc	B2		Other ITC , octet 5a
OtherRateAdapt	B2		Other Rate adaptation, octet5a
spare3	B3		spare bits – 3 bits
extBit5b	B1		extension bit, '1'B, octet 5b
rateAdaptHeader	B1		rate adaptation header, octet 5b
multiFrame	B1		Multi frame, octet 5b
mode	B1		Mode of operation, octet 5b
logLinkId	B1		logical link identifier negotiation, octet 5b

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Structured Type Definition			
Element Name	Type Definition	Field Encoding	Comments
assignorAssignee	B1		assignor/assignee, octet 5b
inBandOutBand	B1		in band/out band negotiation, octet 5b
spare1	B1		spare bit – 1 bit
extBit6	B1		extension bit, octet 6
layer1Id	B2		Layer 1 identity, '01'B, octet 6
userInfoLayer1	B4		user information Layer 1 protocol, '0000'B, octet 6
syncAsync	B1		synchronous bit, octet 6
extBit6a	B1		extension bit, octet 6a
numStopBits	B1		number of stop bits, octet 6a
nego	B1		negotiation bit, '0'B, octet 6a
numDataBits	B1		number of data bits, octet 6a
userRate	B4		user rate, GSM, octet 6a
extBit6b	B1		extension bit, octet 6b
intermRate	B2		intermediate rate, octet 6b
nicTx	B1		network independent clock on transmission, GSM, octet 6b
nicRx	B1		network independent clock on reception, GSM, octet 6b
parity	B3		parity information, octet 6b
extBit6c	B1		extension bit, octet 6c
connectElem	B2		connection element, octet 6c
modemType	B5		modem type, octet 6c
extBit6d	B1		extension bit, octet 6d
OtherModemType	B2		Other Modem type, octet 6d
FixedNtwUserRate	B5		Fixed Network user rate, octet 6d
extBit6e	B1		extension bit, octet 6e
acceptChCoding	B4		acceptable channel coding, octet 6e
maxNumTrafficCh	B3		maximum number of traffic channel, octet 6e
extBit6f	B1		extension bit, octet 6f
ulMI	B3		User initiated modification indication, octet 6f
wAIUR	B4		wanted air interface user rate, octet 6f
extBit6g	B1		extension bit, octet 6g
acceptChCodingExt	B3		acceptable channel coding extended, octet 6g
asymInd	B2		asymmetry indication, octet 6g
spare2	B2		spare 2 bits , octet 6g

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Structured Type Definition			
Element Name	Type Definition	Field Encoding	Comments
extBit7	B1		extension bit, octet 7
layer2id	B2		L2 identity, octet 7
userInfoLayer2	B5		user information L2 protocol, octet 7
Detailed Comments :			

Structured Type Definition			
Type Name : Bcap3aEtc			
Encoding Variation :			
Comments : Speech Versions preferences as supported by the EFR mobile – see 3G TS 24.008 cl. 10.5.4.5/octet 3a etc/"speech version indication"			
Element Name	Type Definition	Field Encoding	Comments
extBit	B1		extension bit ('0'B)
coding	B1		Octet a1, Coding ('0'B)
spare2	B2		Octet a1, Spare bits ('00'B)
speechVersion	B4		Octet a1, Speech version indication
Detailed Comments :			

Structured Type Definition			
Type Name : CC_Capabilities			
Encoding Variation :			
Comments : Call Control Capabilities 3G TS 24.008 cl. 10.5.4.5a			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '00010101'B
iel	Length		length
maxNumBearer	B4		maximum number o fudported bearer
spare2	B2		spare bits
pcp	B1		PCP
dtmf	B1		indication of supporting DTMF
spare4	B4		spare bits
maxNumSpeechBearer	B4		maximum number of speech bearers
Detailed Comments :			

Structured Type Definition			
Type Name : CDPN Encoding Variation : Comments : Called party BCD number (CC information element) 3G TS 24.008 cl. 10.5.4.7			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01011110'B
iel	Length		length
typeOfNumPlan	TypeOfNumPlan		type of number and numbering plan identification
digits	OCTETSTRING[0..40]		BCD numbers
Detailed Comments :			

Structured Type Definition			
Type Name : CDPS Encoding Variation : Comments : Called party subaddress (CC information element) 3G TS cl. 10.5.4.8			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01101101'B
iel	Length		length
subadrs	Subadrs		Subaddress
Detailed Comments :			

Structured Type Definition			
Type Name : CGPS Encoding Variation : Comments : Calling party subaddress (CC information element) 3G TS 24.008 cl. 10.5.4.10			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01011101'B
iel	Length		length
subadrs	Subadrs		Subaddress
Detailed Comments :			

Structured Type Definition			
Type Name : CellIndependentInfo Encoding Variation : Comments : This structure contains relevant information independant from the cell			
Element Name	Type Definition	Field Encoding	Comments
cs_cipheringStarted	BOOLEAN		Set to TRUE when ciphering is started, to be initiated in Security Steps and used in RAB establishment Steps
ps_cipheringStarted	BOOLEAN		Set to TRUE when ciphering is started, to be initiated in Security Steps and used in RAB establishment Steps
recentSecureDomain	CN_DomainIdentity		the domain on which security was recently started, and hence the SRB are ciphered and Integrity protected with this domain.
dL_CipherMode	CipheringModeCommand		To hold the DL cipher mode to be used in RAB Steps.
uL_CipherMode	RB_ActivationTimeInfoList		To hold the UL cipher mode to be used in RAB Steps.
cipheringAlgorithmCapability	BITSTRING		BITSTRING that holds the ciphering algorithm capability supported by UE
integrityStarted	BOOLEAN		Set to TRUE when integrity is started
dL_Integrity	IntegrityProtectionModelInfo		To hold the DL integrity protection active during a test case
uL_Integrity	IntegrityProtActivationInfo		To hold the UL integrity protection active during a test case
dl_IntegrityCheckInfo	IntegrityCheckInfo		To hold the integrity information to be sent
start_CS	START_Value		To hold the START value for CS Domain
start_PS	START_Value		To hold the START value for PS Domain
Detailed Comments :			

Structured Type Definition			
Type Name : CellNotification Encoding Variation : Comments : Cell notification 3GPP 24.008 / 10.5.5.21			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'10001100'B (8C hex)
Detailed Comments :			

Structured Type Definition			
Type Name : CiphAlgorithm Encoding Variation : Comments : Cipher Algorithm 3GPP 24.008 / 10.5.5.3			
Element Name	Type Definition	Field Encoding	Comments
spare1	B1		
algorithm	B3		
Detailed Comments :			

Structured Type Definition			
Type Name : CiphKeySeqNum Encoding Variation : Comments : Ciphering Key Sequence Number 3G TS 24.008 cl. 10.5.1.2			
Element Name	Type Definition	Field Encoding	Comments
spare1	B1		
keySeq	KeySeq		
Detailed Comments :			

Structured Type Definition			
Type Name : CiphKeySeqNum_tv			
Encoding Variation :			
Comments : Ciphering Key Sequence Number 3G TS 24.008 cl. 10.5.1.2			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI4		'1000'B
spare1	B1		
keySeq	KeySeq		
Detailed Comments :			

Structured Type Definition			
Type Name : Codec Encoding Variation : Comments : Codec 3G TS 24.008 cl. 10.5.4.32			
Element Name	Type Definition	Field Encoding	Comments
sysId	OCTETSTRING[1]		system identification
len	Length		length
bitMap1to8	BITSTRING[8]		codec bitmap bits 1–8
bitMap9to16	BITSTRING[8]		codec bitmap bits 9–16
Detailed Comments :			

Structured Type Definition			
Type Name : CodecList Encoding Variation : Comments : Supported Codec List 3G TS 24.008 cl. 10.5.3.32			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'01000000'B (40 hex)
iel	Length		length
codec1	Codec		Codec
codec2	Codec		Codec
codec3	Codec		Codec
codec4	Codec		Codec
codec5	Codec		Codec
Detailed Comments :			

Structured Type Definition			
Type Name : CodeWord Encoding Variation : Comments : Code word within an RList super field. Ref 3G TS 25.322 clause 9.2.2.11.6			
Element Name	Type Definition	Field Encoding	Comments
numberPart	CW_NumberPart		
statusInd	BITSTRING[1]		
Detailed Comments :			

Structured Type Definition			
Type Name : DRXparamter Encoding Variation : Comments : DRX paramter 3GPP 24.008 / 10.5.5.6			
Element Name	Type Definition	Field Encoding	Comments
splitPGcycleCode	B8		Split PG cycle code
cnDRXcoef	B4		CN specific DRX cycle length coefficient
splitOnCCCH	B1		Split on CCCCH
nonDRXtimer	B3		non-DRX timer
Detailed Comments :			

Structured Type Definition			
Type Name : DRXparamter_tv Encoding Variation : Comments : DRX paramter 3GPP 24.008 / 10.5.5.6			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100111'B (hex 27)
splitPGcycleCode	B8		Split PG cycle code
cnDRXcoef	B4		CN specific DRX cycle length coefficient
splitOnCCCH	B1		Split on CCCCH
nonDRXtimer	B3		non-DRX timer
Detailed Comments :			

Structured Type Definition			
Type Name : DetachType Encoding Variation : Comments : Detach type 3GPP 24.008 / 10.5.5.5			
Element Name	Type Definition	Field Encoding	Comments
powOff	B1		
type	B3		
Detailed Comments :			

Structured Type Definition			
Type Name : EmergNum Encoding Variation : Comments : Emergency Number 3G TS 24.008 cl. 10.5.3.13			
Element Name	Type Definition	Field Encoding	Comments
len	Length		length
emergServCat	EmergServCat		Emergency Service Category
digits	OCTETSTRING[0..10]		BCD numbers
Detailed Comments :			

Structured Type Definition			
Type Name : EmergNumList Encoding Variation : Comments : Emergency Number List 3G TS 24.008 cl. 10.5.3.13			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00110100'B (34 hex)
iel	Length		length
emergNum1	EmergNum		Emergency Number
emergNum2	EmergNum		Emergency Number
emergNum3	EmergNum		Emergency Number
emergNum4	EmergNum		Emergency Number
emergNum5	EmergNum		Emergency Number
Detailed Comments :			

Structured Type Definition			
Type Name : EmergServCat Encoding Variation : Comments : Emergency Service Category 3GPP 24.008 / 10.5.4.33			
Element Name	Type Definition	Field Encoding	Comments
spare	B3		
value	B5		Emergency Service Category value
Detailed Comments :			

Structured Type Definition			
Type Name : Facility Encoding Variation : Comments : Facility information element 3G TS 24.008 cl. 10.5.4.15			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '00011100'B
iel	Length		length
comps	OCTETSTRING		Component
Detailed Comments :			

Structured Type Definition			
Type Name : ForceToStandby Encoding Variation : Comments : Force to standby 3GPP 24.008 / 10.5.5.7			
Element Name	Type Definition	Field Encoding	Comments
spare	B1		
value	B3		Force to standby value
Detailed Comments :			

Structured Type Definition			
Type Name : GMM_AUTN Encoding Variation : Comments : Authentication Parameter AUTN– to be used in GMM constraints 3G TS 24.008 cl. 10.5.3.1.1			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00101000'B
iel	Length		'10'O
aUTN	BITSTRING[128]		Authentication Parameter AUTN
Detailed Comments :			

Structured Type Definition			
Type Name : GMM_Cause Encoding Variation : Comments : GMM cause 3GPP 24.008 / 10.5.5.14			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100101'B (25 hex)
value	B8		Cause value
Detailed Comments :			

Structured Type Definition			
Type Name : GMM_MS_Identity Encoding Variation : Comments : Mobile Identity 3G TS 24.008 cl. 10.5.1.4			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00100011'B
iel	Length		
iDigit1	B4		1st identity digit
oddEvenInd	B1		Odd/even indicator
typeOfId	B3		Type of identity
otherDigits	OCTETSTRING[0..8]		Other identity digits
Detailed Comments : Maximum number of digits is 16 (IMEISV). Filler may be used.			

Structured Type Definition			
Type Name : GMM_MS_IdentityPTMSI Encoding Variation : Comments : Mobile Identity 3G TS 24.008 cl. 10.5.1.4			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00011000'B
iel	Length		
iDigit1	B4		1st identity digit
oddEvenInd	B1		Odd/even indicator
typeOfId	B3		Type of identity
otherDigits	OCTETSTRING[0..8]		Other identity digits
Detailed Comments : Maximum number of digits is 16 (IMEISV). Filler may be used.			

Structured Type Definition			
Type Name : GPRS_Timer Encoding Variation : Comments : GPRS timer 3GPP 24.008 / 10.5.7.3			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		00010111'B (17 hex)
unit	B3		Unit
value	B5		Timer value
Detailed Comments :			

Structured Type Definition			
Type Name : GPRS_Timer2 Encoding Variation : Comments : GPRS timer 3GPP 24.008 / 10.5.7.4			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00101010'B (2A hex)
iel	Length		length
unit	B3		Unit
value	B5		Timer value
Detailed Comments :			

Structured Type Definition			
Type Name : GPRS_Timer_v Encoding Variation : Comments : GPRS timer 3GPP 24.008 / 10.5.7.3			
Element Name	Type Definition	Field Encoding	Comments
unit	B3		Unit
value	B5		Timer value
Detailed Comments :			

Structured Type Definition			
Type Name : HLC Encoding Variation : Comments : High layer compatibility (CC information element) 3G TS 24.008 cl. 10.5.4.16, ITU Q.931			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01111101'B
iel	Length		length
extBit3	B1		extension bit ('1'B)
codingStd	B2		coding standard
interpretation	B3		interpretation
presentModeProtocolProfile	B2		presentation method of protocol profile
extBit4	B1		extension bit, octet 4
hlclcd	B7		high layer characteristics identification
extBit4a	B1		extension bit, '1'B, octet 4a
exteHlclcd	B7		extended high layer characteristics identification
Detailed Comments :			

Structured Type Definition			
Type Name : IMEISVRequest Encoding Variation : Comments : IMEISV Request 3GPP 24.008 / 10.5.5.10			
Element Name	Type Definition	Field Encoding	Comments
spare1	B1		
value	B3		
Detailed Comments :			

Structured Type Definition			
Type Name : LB_SetupRB_IE			
Encoding Variation :			
Comments : LB Setup RAB Subflow 3G TS 34.109 cl. 6.2			
Element Name	Type Definition	Field Encoding	Comments
rLC_SDU_Size	BITSTRING [16]		RLC SDU size
spare_2	BITSTRING [3]		
rB_Identity	BITSTRING[5]		
Detailed Comments :			

Structured Type Definition			
Type Name : LLC Encoding Variation : Comments : Low layer compatibility (CC information element) 3G TS 24.008 cl. 10.5.4.18 and ETS DE/SPS-5034-1 (update of ETS 300 102-1)			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01111100'B
iel	Length		length
extBit3	B1		extension bit
codingStd	B2		coding standard, octet 3
itc	B5		information transfer capability, octet 3
extBit3a	B1		extension bit, octet 3a
negoInd	B1		negotiation indicator, octet 3a
spare6	B6		6 spare bits
extBit4	B1		extension bit, octet 4
transferMode	B2		transfer mode
infoTransferRate	B5		information transfer rate, octet 4
extBit4_1	B1		extension bit, octet 4.1
rateMultiplier	B7		rate multiplier, octet 4.1
extBit5	B1		extension bit, octet 5
layer1Id	B2		Layer 1 identity, '01'B, octet 5
userInfoLayer1	B5		user information Layer 1 protocol, '0000'B, octet 5
extBit5a	B1		extension bit, octet 5a
syncAsync	B1		synchronous bit, octet 5a
nego	B1		negotiation bit, octet 5a
userRate	B5		user rate, octet 5a
extBit5b1	B1		extension bit, octet 5b1
intermRate	B2		intermediate rate, octet 5b1
nicTx	B1		network independent clock on transmission, octet 5b1
nicRx	B1		network independent clock on reception, octet 5b1
flowCtrlTx	B1		flow control on transmission, octet 5b1
flowCtrlRx	B1		flow control on reception, octet 5b1
spare1	B1		1 spare bit, '0'B, octet 5b1
extBit5b2	B1		extension bit, octet 5b2
rateAdaptHeader	B1		rate adaptation header, octet 5b2
multiFrame	B1		Multi frame, octet 5b2
mode	B1		Mode of operation, octet 5b2
logLinkId	B1		logical link identifier negotiation, octet 5b2
assignorAssignee	B1		assignor/assignee, octet 5b2

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Structured Type Definition			
Element Name	Type Definition	Field Encoding	Comments
inBandOutBand	B1		in band/out band negotiation, octet 5b2
spare1_5b2	B1		sapre bit – 1 bit, octet 5b2
extBit5c	B1		extension bit, octet 5c
numStopBits	B2		number of stop bits, octet 5c
numDataBits	B2		number of data bits, octet 5c
parity	B3		parity, octet 5c
extBit5d	B1		extension bit, octet 5d
duplexMode	B1		duplex mode, octet 5d
modemType	B6		modem type, octet 5d
extBit6	B1		extension bit, octet 6
layer2id	B2		L2 identity, octet 6
userInfoLayer2	B5		user information L2 protocol, octet 6
extBit6a1	B1		extension bit, octet 6a1
modeLayer2	B2		mode, octet 6a1
spare3	B3		spare bits , '000'B, octet 6a1
q933	B2		Q.933 use, octet 6a1
extBit6a2	B1		extension bit, octet 6a2
userSpecifLayer2	B7		user specified layer 2 protocol information, octet 6a2
extBit6b	B1		extension bit, octet 6b
windowSize	B7		window size, octet 6b
extBit7	B1		extension bit, octet 7
layer3id	B2		L3 identity, octet 7
userInfoLayer3	B5		user information L3 protocol, octet 7
extBit7a1	B1		extension bit, octet 7a2
OptionUserSpecifLayer3	B7		user specified layer 3 protocol information, octet 7a2
extBit7a2	B1		extension bit, '1'B, octet 7a2
modeLayer3	B2		mode, octet 7a2
spare5	B5		spare bits , '00000'B, octet 7a2
extb7b	B1		extension bit, octet 7b
spare3_7b	B3		spare bits , '000'B, octet 7b
defaultPacketSize	B4		default packet size, octet 7b
extBit7c	B1		extension bit, '1'B, octet 7c
packetWindowSize	B7		packet window size, octet 7bc
extBit7a3	B1		extension bit, octet 7a3
spare3_7a3	B3		3 spare bits

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Structured Type Definition			
Element Name	Type Definition	Field Encoding	Comments
addLayer3ProtocolInfo	B4		additional layer 3 protocol information (most significant bits), octet 7a3
extBit7a4	B1		extension bit, octet 7a4
spare3_7a4	B3		3 spare bits
addLayer3ProtocolInfoL	B4		additional layer 3 protocol information (least significant bits), octet 7a4
Detailed Comments :			

Structured Type Definition			
Type Name : LLC_SAPI_v			
Encoding Variation :			
Comments : 24.007, clause 10.5.6.9			
Element Name	Type Definition	Field Encoding	Comments
spare	B4		
ILC_SAPI_Value	B4		LLC SAPI Value
Detailed Comments :			

Structured Type Definition			
Type Name : LocAreald_v			
Encoding Variation :			
Comments : Location Area Identification Value 3G TS 24.008 cl. 10.5.1.3			
Element Name	Type Definition	Field Encoding	Comments
plmn	OCTETSTRING[3]		MCC + MNC 3 digits each
lac	OCTETSTRING[2]		LAC
Detailed Comments :			

Structured Type Definition			
Type Name : LocUpdType			
Encoding Variation :			
Comments : Location Updating Type 3G TS 24.008 cl. 10.5.3.5			
Element Name	Type Definition	Field Encoding	Comments
fOR	B1		Follow-On Request
spare1	B1		
IUT	B2		Location Updating Type
Detailed Comments :			

Structured Type Definition			
Type Name : MM_MS_Identity Encoding Variation : Comments : Mobile Identity 3G TS 24.008 cl. 10.5.1.4			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00010111'B
iel	Length		
iDigit1	B4		1st identity digit
oddEvenInd	B1		Odd/even indicator
typeOfId	B3		Type of identity
otherDigits	OCTETSTRING[0..8]		Other identity digits
Detailed Comments : Maximum number of digits is 16 (IMEISV). Filler may be used.			

Structured Type Definition			
Type Name : MSRadioAccessCap_Iv Encoding Variation : Comments : MS radio access capability 3GPP 24.008 / 10.5.5.12a			
Element Name	Type Definition	Field Encoding	Comments
iel	Length		
value	OCTETSTRING[0..50]		MS radio access capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Definition			
Type Name : MS_Clsmk1 Encoding Variation : Comments : Mobile Station Classmark 1 3G TS 24.008 cl. 10.5.1.5			
Element Name	Type Definition	Field Encoding	Comments
spare1	B1		Spare bit
revLvl	B2		Revision Level
eSIND	B1		Early Sending Indication
a5_1	B1		Algorithm A5/1 supported
rFPwrCap	B3		RF Power Capability
Detailed Comments :			

Structured Type Definition			
Type Name : MS_Clsmk2 Encoding Variation : Comments : Mobile Station Classmark 2 3G TS 24.008 cl. 10.5.1.6			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		
iel	Length		
spare1_1	B1		Spare bit
revLvl	B2		Revision Level
eSIND	B1		Early Sending Indication
a5_1	B1		Algorithm A5/1 Support
rFPwrCap	B3		RF Power Capability
spare1_2	B1		Spare bit
pSCap	B1		Pseudo Synchronisation Capability
sSSI	B2		SS Screen Indicator
sMCap	B1		Short Message Capability
vBS	B1		VBS Capability
VGCS	B1		VGCS Capability
fC	B1		Frequency Capability
cM3	B1		Classmark 3 Indicator
spare1_3	B1		Spare bit
ICCSVA	B1		LCS VA Capability
uCS2	B1		UCS2 Encoding Support
soLSA	B1		SoLSA Support
cMSP	B1		CM Service Prompt Support
a5_3	B1		Algorithm A5/3 Support
a5_2	B1		Algorithm A5/2 Support
Detailed Comments :			

Structured Type Definition			
Type Name : MS_Clsmk2_lv Encoding Variation : Comments : Mobile Station Classmark 2 3G TS 24.008 cl. 10.5.1.6			
Element Name	Type Definition	Field Encoding	Comments
iel	Length		
spare1_1	B1		Spare bit
revLvl	B2		Revision Level
eSIND	B1		Early Sending Indication
a5_1	B1		Algorithm A5/1 Support
rFPwrCap	B3		RF Power Capability
spare1_2	B1		Spare bit
pSCap	B1		Pseudo Synchronisation Capability
sSSI	B2		SS Screen Indicator
sMCap	B1		Short Message Capability
vBS	B1		VBS Capability
vGCS	B1		VGCS Capability
fC	B1		Frequency Capability
cM3	B1		Classmark 3 Indicator
spare1_3	B1		Spare bit
ICSVA	B1		LCS VA Capability
uCS2	B1		UCS2 Encoding Support
soLSA	B1		SoLSA Support
cMSP	B1		CM Service Prompt Support
a5_3	B1		Algorithm A5/3 Support
a5_2	B1		Algorithm A5/2 Support
Detailed Comments :			

Structured Type Definition			
Type Name : MS_Identity_lv Encoding Variation : Comments : Mobile Identity LV 3G TS 24.008 cl. 10.5.1.4			
Element Name	Type Definition	Field Encoding	Comments
iel	Length		
iDigit1	B4		1st identity digit
oddEvenInd	B1		Odd/even indicator
typeOfId	B3		Type of identity
otherDigits	OCTETSTRING[0..8]		Other identity digits
Detailed Comments : Maximum number of digits is 16 (IMEISV). Filler may be used.			

Structured Type Definition			
Type Name : MS_NetworkCap_lv Encoding Variation : Comments : MS network capabilityt LV 3GPP 24.008 / 10.5.5.12			
Element Name	Type Definition	Field Encoding	Comments
iei value	Length OCTETSTRING[0..8]		MS network capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Definition			
Type Name : MS_NetworkCap_tlv Encoding Variation : Comments : MS network capabilityt T LV 3GPP 24.008 / 10.5.5.12			
Element Name	Type Definition	Field Encoding	Comments
iei iei value	IEI8 Length OCTETSTRING[0..8]		'00110001'B (hex 31) MS network capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Definition			
Type Name : NSAPI_v Encoding Variation : Comments : 24.007, clause 10.5.6.2			
Element Name	Type Definition	Field Encoding	Comments
spare nSAPI_Value	B4 B4		NSAPI value
Detailed Comments :			

Structured Type Definition			
Type Name : NtwFeatureSupport_tv Encoding Variation : Comments : Network Feature Support 3G TS 24.008 cl. 10.5.5.23			
Element Name	Type Definition	Field Encoding	Comments
iei ICS_MOLR spare3	IEI4 B1 B3		'1011'B (B- hex)
Detailed Comments :			

Structured Type Definition			
Type Name : PDP_ContextStatus			
Encoding Variation :			
Comments : PDP_ContextStatus 3G TS 24.008 cl. 10.5.7.1			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00110010'B
iel	Length		
nSAPI	BITSTRING[16]		
Detailed Comments :			

Structured Type Definition			
Type Name : PLMN_List Encoding Variation : Comments : PLMN list 3G TS 24.008 cl. 10.5.1.13			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'01001010'B
iel	Length		
plmn1	OCTETSTRING[3]		
plmn2	OCTETSTRING[3]		PLMN 1
plmn3	OCTETSTRING[3]		PLMN 2
plmn4	OCTETSTRING[3]		PLMN 3
plmn5	OCTETSTRING[3]		PLMN 4
			PLMN 5
Detailed Comments :			

Structured Type Definition			
Type Name : PS_LCS_Capability			
Encoding Variation :			
Comments : PS LCS Capability 3GPP 24.008 / 10.5.5.22			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00110011'B (33 hex) '01'O
iel	Length		
spare	B3		
oTD_A	B1		
oTD_B	B1		
gPS_A	B1		
gPS_B	B1		
gPS_C	B1		
Detailed Comments :			

Structured Type Definition			
Type Name : PTMSI_Signature Encoding Variation : Comments : P-TMSI signature 3GPP 24.008 / 10.5.5.8			
Element Name	Type Definition	Field Encoding	Comments
iei value	IEI8 OCTETSTRING[3]		'00011001'B (19 hex) P-TMSI signature value
Detailed Comments :			

Structured Type Definition			
Type Name : PTMSI_Signature_tlv Encoding Variation : Comments : P-TMSI signature 3GPP 24.008 / 10.5.5.8			
Element Name	Type Definition	Field Encoding	Comments
iei iel value	IEI8 Length OCTETSTRING[3]		'00011001'B (19 hex) P-TMSI signature value
Detailed Comments :			

Structured Type Definition			
Type Name : PktDataProtoAddr_lv Encoding Variation : Comments : 24.007, clause 10.5.6.4			
Element Name	Type Definition	Field Encoding	Comments
length spare pDP_TypeOrg pDP_TypeNo addrInfo	Length B4 B4 PDP_TypeNo AddressInfo		PDP type organisation PDP type number Address Information
Detailed Comments :			

Structured Type Definition			
Type Name : ProtoCfgOpt Encoding Variation : Comments : 24.007, clause 10.5.6.3			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		'00101000'B (27hex)
length	Length		
ext	B1		extension bit
spare	B4		
configprotocol	B3		configuration protocol
protocolldContents	ProtoldContents		information related to additional external protocols
Detailed Comments :			

Structured Type Definition			
Type Name : QualityOfService_lv Encoding Variation : Comments : 24.008, clause 10.5.6.5			
Element Name	Type Definition	Field Encoding	Comments
length	Length		
spare	B2		
dlyClass	B3		Delay Class
reliabilityClass	B3		Reliability Class
peakThroughput	B4		Peak Throughput
spare1	B1		
precedenceClass	B3		Precedence Class
spare2	B3		
meanThroughput	B5		Mean Throughput
trafficClass	B3		Traffic Class
deliveryOrder	B2		Delivery Order
deliveryErrorSDU	B3		Delivery of erroneous SDU
maxSDUSize	MaxSDU_Size		Maximum SDU Size
maxBitRateUplink	MaxBitRate		Maximum Bit Rate for Uplink
maxBitRateDnlink	MaxBitRate		Maximum Bit Rate for Downlink
residualBER	B4		Residual BER
sduErrRatio	B4		SDU Error Ratio
transDly	B6		Transfer Delay
trafficHandpro	B2		Traffic Handling Priority
bitRateUplink	BitRate		Guaranteed bit rate for uplink
bitRateDnlink	BitRate		Guaranteed bit rate for downlink
spare3	B3		
signallingInd	B1		Signalling Indication
srcStatsDescr	B4		Source Statistics Descriptor
maxBitRateDnlinkExt	BitRate		Maximum bit rate for downlink (extended)
bitRateDnlinkExt	BitRate		Guaranteed bit rate for downlink (extended)
Detailed Comments :			

Structured Type Definition			
Type Name : RAI_v Encoding Variation : Comments : Routing Area Identification 3GPP 24.008 / 10.5.5.15			
Element Name	Type Definition	Field Encoding	Comments
plmn	OCTETSTRING[3]		MCC + MNC 3 digits each
lac	OCTETSTRING[2]		LAC
rac	OCTETSTRING[1]		RAC
Detailed Comments :			

Structured Type Definition			
Type Name : RadioPriority_v			
Encoding Variation :			
Comments : Radio priority 3GPP 24.008 / 10.5.7.2			
Element Name	Type Definition	Field Encoding	Comments
spare	B1		Radio priority value
value	B3		
Detailed Comments :			

Structured Type Definition			
Type Name : RadioPriority2_v			
Encoding Variation :			
Comments : Radio priority 2 3GPP 24.008 / 10.5.7.5			
Element Name	Type Definition	Field Encoding	Comments
spare	B1		Radio priority value
value	B3		
Detailed Comments :			

Structured Type Definition			
Type Name : RepeatInd			
Encoding Variation :			
Comments : Repeat indicator 3G TS 24.008 cl. 10.5.4.22			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI4		
repeatInd	B4		
Detailed Comments :			

Structured Type Definition			
Type Name : SM_Cause_v Encoding Variation : Comments : Ref 24.008, 10.5.6.6			
Element Name	Type Definition	Field Encoding	Comments
causeValue	CauseValue		
Detailed Comments :			

Structured Type Definition			
Type Name : SNiLiPair Encoding Variation : Comments :			
Element Name	Type Definition	Field Encoding	Comments
sNi	AM_SeqNum		
li	SUFI_ListLi		
Detailed Comments :			

Structured Type Definition			
Type Name : SS_VersionInd Encoding Variation : Comments : SS version indicator 3G TS 24.008 cl. 10.5.4.24			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '01111111'B
iel	Length		length
sS_VersionInfo	OCTETSTRING [1]		ss version information 1.
Detailed Comments : 1. Usually this IE has only one octet of content. Exact definition see TS 24.080			

Structured Type Definition			
Type Name : SUFI_Ack Encoding Variation : Comments : Acknowledgement super field. Ref 3G TS 25.322 clause 9.2.2.11.2. The ACK SUFI shall always be placed as the last SUFI if it is included in a STATUS PDU.			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_Ack
lsn	AM_SeqNum		1.
Detailed Comments : 1. Acknowledges the reception of all PDUs with sequence numbers < LSN that are not indicated to be erroneous in earlier parts of the STATUS PDU.			

Structured Type Definition			
Type Name : SUFI_Bitmap Encoding Variation : Comments : Bitmap super field. Ref 3G TS 25.322 clause 9.2.2.11.5			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_Bitmap
len	BitmapLen		1.
fsn	AM_SeqNum		2.
bitmap	Bitmap		3.
Detailed Comments : 1. The size of the bitmap in octets = len+1. len='0000'B means that the bitmap is one octet, and len='1111'B means that the bitmap is the maximum size of 16 octets 2. The sequence number for the first bit in the bitmap. 3. Status of the SNs in the interval [FSN, FSN + (len + 1) * 8 -1] where each bit_position can take on the following values:- * '1'B indicates that FSN + bit_position has been correctly received * '0'B indicates that FSN + bit_position has not been correctly received			

Structured Type Definition			
Type Name : SUFI_List Encoding Variation : Comments : List super field. Ref 3G TS 25.322 clause 9.2.2.11.4 This type definition assumes that a maximum of 3 (SNI, Li) pairs will be required for RLC test purposes.			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_List
len	LIST_Len		1.
sN1L1	SNiLiPair		2.
sN2L2	SNiLiPair		2.
sN3L3	SNiLiPair		2.
Detailed Comments : 1. The number of (SNI, Li) pairs in the super field. 2. SNI: Sequence number of PDU which was not correctly received; Li: The number of consecutive PDUs not correctly received following PDU with sequence number SNI.			

Structured Type Definition			
Type Name : SUFI_MRW Encoding Variation : Comments : Move receiving window super field. Ref 3G TS 25.322 clause 9.2.2.11.8 This type definition assumes that a maximum of three SN_MRWi will be required for RLC testing.			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_MRW
len	MRW_Len		1.
sN_MRW1	SUFI_SN_MRWi		2.
sN_MRW2	SUFI_SN_MRWi		2.
sN_MRW3	SUFI_SN_MRWi		2.
nLength	N_Length		3.
Detailed Comments : 1. The number of SN_MRWi fields in the super-field. 2. Each SN_MRWi is used to indicate the end of each discarded SDU. SN_MRWi is the sequence number of the PDU that contains the LI of the i:th discarded SDU. 3. nLength is used together with SN_MRW_Length to indicate the end of the last discarded SDU			

Structured Type Definition			
Type Name : SUFI_MRW_ACK Encoding Variation : Comments : Move receiving window acknowledgement super field. Ref 3G TS 25.322 clause 9.2.2.11.7			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_MRW_ACK
n	N_Length		1.
sN_ACK	AM_SeqNum		2.
Detailed Comments : 1. The N field shall be set equal to the N_Length field in the received MRW SUFI if the SN_ACK field is equal to the SN_MRW_Length field. Otherwise N shall be set to 0. 2. The SN_ACK field indicates the updated value of VR(R) after the reception of the MRW SUFI.			

Structured Type Definition			
Type Name : SUFI_NoMore Encoding Variation : Comments : No more data super field. Ref 3G TS 25.322 clause 9.2.2.11.1			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_NoMore
Detailed Comments :			

Structured Type Definition			
Type Name : SUFI_RList Encoding Variation : Comments : Relative list super field. Ref 3G TS 25.322 clause 9.2.2.11.6 This type definition assumes that a maximum of three codewords will be required for RLC testing			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_RList
len	RLIST_Len		1.
fsn	AM_SeqNum		2.
CW1	CodeWord		3.
CW2	CodeWord		3.
CW3	CodeWord		3.
Detailed Comments : 1. The number of codewords in the super-field 2. The sequence number of the first erroneous PDU in the RLIST. Note that len = '0000'B means that only FSN is present in the SUFI. 3. Each CW consists of 4 bits where the first three bits are part of a number, and the last bit is a status indicator. see 3G TS 25.25.322 clause 9.2.2.11.6 for a detailed description.			

Structured Type Definition			
Type Name : SUFI_WindowSize Encoding Variation : Comments : Window size super field. Ref 3G TS 25.322 clause 9.2.2.11.3			
Element Name	Type Definition	Field Encoding	Comments
type	SUFI_Type		Always tsc_SUFI_WindowSize
wsn	BITSTRING[12]		1.
Detailed Comments : 1. The allowed Tx window size to be used by the transmitter.			

Structured Type Definition			
Type Name : ServiceType_v Encoding Variation : Comments : Service type 3GPP 24.008 / 10.5.5.20			
Element Name	Type Definition	Field Encoding	Comments
spare1	B1		
type	B3		type
Detailed Comments :			

Structured Type Definition			
Type Name : StreamId Encoding Variation : Comments : stream identifier 3G TS 24.008 cl. 10.5.4.28			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier '00101101'B
iel	Length		length
val	B8		stream identifier value
Detailed Comments :			

Structured Type Definition			
Type Name : Subadrs Encoding Variation : Comments : Subaddress 3G TS 24.008 cl. 10.5.4.8, 10.5.4.10, 10.5.4.14			
Element Name	Type Definition	Field Encoding	Comments
extBit	B1		extension bit
typrOfSubadrs	B3		Type of subaddress
oddEven	B1		odd/even indicator
spare3	B3		3 spare bits
subadrsInfo	OCTETSTRING [0..20]		subaddress information
Detailed Comments :			

Structured Type Definition			
Type Name : SuperFields Encoding Variation : Comments : This type is used to represent the set of super-fields within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.1.5.			
Element Name	Type Definition	Field Encoding	Comments
windowSize	SUFI_WindowSize		1
list	SUFI_List		1
rList	SUFI_RList		1
bitmap	SUFI_Bitmap		1
mRW	SUFI_MRW		1
mRW_Ack	SUFI_MRW_ACK		1
noMore	SUFI_NoMore		2
ack	SUFI_Ack		2, 3
Detailed Comments : 1. These super fields may arrive in any order, and may or may not be present. This type definition only supports a single super-field, followed by either a NO_MORE SUFI, or an ACK SUFI. 2. The NO_MORE SUFI, and the ACK SUFI are mutually exclusive, and should never both be present in the same STATUS PDU. 3. The ACK SUFI is used to acknowledge reception of all PDUs up to LSN, EXCEPT for any PDUs indicated as incorrectly received in the previous SUFIs.			

Structured Type Definition			
Type Name : TI Encoding Variation : Comments : Transaction identifier 3G TS 24.007 cl. 11.2.3.1.3			
Element Name	Type Definition	Field Encoding	Comments
tiFlag	B1		Flag
tiVal	B3		TIO
Detailed Comments :			

Structured Type Definition			
Type Name : TMSI_Status Encoding Variation : Comments : TMSI status 3GPP 24.008 cl. 10.5.5.4			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI4		'1001'B
spare3	B3		
flag	B1		Flag
Detailed Comments :			

Structured Type Definition			
Type Name : TearDwnInd_tv Encoding Variation : Comments : Ref 24.008, clause 10.5.6.10			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI4		'1001'B (9 hex)
spare	B3		
tdiflag	B1		TDI Flag
Detailed Comments :			

Structured Type Definition			
Type Name : TypeOfNumPlan Encoding Variation : Comments : Type of number and numbering plan 3G TS 24.008 cl.10.5.4.7, 10.5.4.9, 10.5.4.13			
Element Name	Type Definition	Field Encoding	Comments
extBit	B1		extension bit
typeOfNum	B3		Type of number
numbPlanId	B4		Numbering plan id.
Detailed Comments :			

Structured Type Definition			
Type Name : UE_TestLoopMode1LB_Setup Encoding Variation : Comments : UE Test Loop Mode 1 LB Setup 3G TS 34.109 cl. 6.2			
Element Name	Type Definition	Field Encoding	Comments
iel	Length		length
IB_SetupRB_IE1	LB_SetupRB_IE		LB Setup RB IE #1
IB_SetupRB_IE2	LB_SetupRB_IE		LB Setup RB IE #2
IB_SetupRB_IE3	LB_SetupRB_IE		LB Setup RB IE #3
IB_SetupRB_IE4	LB_SetupRB_IE		LB Setup RB IE #4
IB_SetupRB_IE5	LB_SetupRB_IE		LB Setup RB IE #5
Detailed Comments : The maximum number of LB entities in the LB setup list is less than or equal to 5.			

Structured Type Definition			
Type Name : UpdateType_v Encoding Variation : Comments : Update result 3GPP 24.008 / 10.5.5.18			
Element Name	Type Definition	Field Encoding	Comments
for	B1		Follow-on request
value	B3		Update type value
Detailed Comments :			

Structured Type Definition			
Type Name : UserUser Encoding Variation : Comments : User-user 3G TS 24.008 cl. 10.5.4.25			
Element Name	Type Definition	Field Encoding	Comments
iei	IEI8		information element identifier 01111110'B
iel	Length		length
userUserProtocolDiscr	B8		user-user protocol discriminator
userUserInfo	OCTETSTRING [1..128]		user user information
Detailed Comments : In SETUP, ALERTING, CONNECT, DISCONNECT, RELEASE and RELEASE COMPLETE messages the userUserInfo length is of 0 – 32 bytes. In USER INFORMATION messages the userUserInfo length is of 1 – 128.			

ASN.1 Type Definition	
Type Name	: RB_Type
Encoding Variation	:
Comments	:
Type Definition	
<pre> ENUMERATED { speech_12_2k (0), speech_10_2k (1), speech_7_95k (2), speech_7_4k (3), speech_6_7k (4), speech_5_9k (5), speech_5_15k (6), speech_4_75k (7), convUnknown_28_8k (8), convUnknown_64k_20 (9), convUnknown_32k_20 (10), convUnknown_64k_40 (11), convUnknown_32k_40 (12), streamUnknown_14_4k (13), streamUnknown_28_8k (14), streamUnknown_57_6k (15), streamUnknown_0k_64k (16), streamUnknown_64k_0k (17), streamUnknown_0k_128k (18), streamUnknown_128k_0k (19), streamUnknown_0k_384k (20), interact_32k_8k_TC_10 (21), backgrnd_32k_8k_TC_10 (22), interact_32k_8k_TC_20 (23), backgrnd_32k_8k_TC_20 (24), interact_32k_8k_CC_10 (25), backgrnd_32k_8k_CC_10 (26), interact_32k_8k_CC_20 (27), backgrnd_32k_8k_CC_20 (28), interact_64k_8k (29), backgrnd_64k_8k (30), interact_32k_64k_TC_10 (31), backgrnd_32k_64k_TC_10 (32), interact_32k_64k_TC_20 (33), backgrnd_32k_64k_TC_20 (34), interact_32k_64k_CC_10 (35), backgrnd_32k_64k_CC_10 (36), interact_32k_64k_CC_20 (37), backgrnd_32k_64k_CC_20 (38), interact_64k (39), backgrnd_64k (40), interact_64k_128k (41), backgrnd_64k_128k (42), interact_128k (43), backgrnd_128k (44), interact_64k_144k (45), backgrnd_64k_144k (46), interact_144k (47), backgrnd_144k (48), interact_64k_256k_10 (49), backgrnd_64k_256k_10 (50), interact_64k_256k_20 (51), backgrnd_64k_256k_20 (52), interact_64k_384k_10 (53), backgrnd_64k_384k_10 (54), interact_64k_384k_20 (55), backgrnd_64k_384k_20 (56), interact_128k_384k_10 (57), </pre>	

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ASN.1 Type Definition
Type Definition
<p> backgrnd_128k_384k_10 (58), interact_128k_384k_20 (59), backgrnd_128k_384k_20 (60), interact_384k_10 (61), backgrnd_384k_10 (62), interact_384k_20 (63), backgrnd_384k_20 (64), interact_64k_2048k_10 (65), backgrnd_64k_2048k_10 (66), interact_64k_2048k_20 (67), backgrnd_64k_2048k_20 (68), interact_128k_2048k_10 (69), backgrnd_128k_2048k_10 (70), interact_128k_2048k_20 (71), backgrnd_128k_2048k_20 (72), interact_384k_2048k_10 (73), backgrnd_384k_2048k_10 (74), interact_384k_2048k_20 (75), backgrnd_384k_2048k_20 (76), speech_12_2k_Interact_32k_8k_TC_20 (77) , speech_12_2k_Backgrnd_32k_8k_TC_20 (78), speech_12_2k_Interact_32k_8k_TC_10 (79), speech_12_2k_Backgrnd_32k_8k_TC_10 (80), speech_12_2k_Interact_32k_8k_CC_20 (81), speech_12_2k_Backgrnd_32k_8k_CC_20 (82), speech_12_2k_Interact_32k_8k_CC_10 (83) , speech_12_2k_Backgrnd_32k_8k_CC_10 (84), speech_12_2k_Interact_32k_64k_TC_10 (85) , speech_12_2k_Backgrnd_32k_64k_TC_10 (86), speech_12_2k_Interact_32k_64k_TC_20 (87) , speech_12_2k_Backgrnd_32k_64k_TC_20 (88), speech_12_2k_Interact_32k_64k_CC_10 (89), speech_12_2k_Backgrnd_32k_64k_CC_10 (90), speech_12_2k_Interact_32k_64k_CC_20 (91), speech_12_2k_Backgrnd_32k_64k_CC_20 (92), speech_12_2k_Interact_64k(93), speech_12_2k_Backgrnd_64k(94), speech_12_2k_Interact_64k_128k(95), speech_12_2k_Backgrnd_64k_128k (96), speech_12_2k_Interact_64k_256k_10 (97), speech_12_2k_Backgrnd_64k_256k_10(98), speech_12_2k_Interact_64k_256k_20(99), speech_12_2k_Backgrnd_64k_256k_20(100), speech_12_2k_Interact_64k_384k_10 (101), speech_12_2k_Backgrnd_64k_384k_10 (102) , speech_12_2k_Interact_64k_384k_20 (103), speech_12_2k_Backgrnd_64k_384k_20 (104), speech_12_2k_Interact_128k_2048k_10 (105), speech_12_2k_Backgrnd_128k_2048k_10 (106), speech_12_2k_Interact_128k_2048k_20 (107), speech_12_2k_Backgrnd_128k_2048k_20 (108), speech_12_2k_StreamUnknown_57_6k (109) , speech_12_2k_StreamUnknown_0_64k (110) , speech_12_2k_StreamUnknown_0_128k (111) , speech_12_2k_StreamUnknown_0_384k (112) , speech_12_2k_ConvUnknown_64k_20(113), speech_12_2k_ConvUnknown_64k_40(114), convUnknown_64k_ConvUnknown_64k_20(115), convUnknown_64k_ConvUnknown_64k_40(116) , convUnknown_64k_Interact_64k_20 (117), convUnknown_64k_Interact_64k_40 (118), convUnknown_64k_Backgrnd_64k_20 (119), </p>

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ASN.1 Type Definition
Type Definition
<pre> convUnknown_64k_Backgrnd_64k_40 (120), convUnknown_64k_Interact_64k_128k_20 (121), convUnknown_64k_Interact_64k_128k_40 (122), convUnknown_64k_Backgrnd_64k_128k_20 (123), convUnknown_64k_Backgrnd_64k_128k_40 (124), convUnknown_64k_Interact_128k_128k_20 (125), convUnknown_64k_Interact_128k_128k_40 (126), convUnknown_64k_Backgrnd_128k_128k_20 (127), convUnknown_64k_Backgrnd_128k_128k_40 (128), interact_64k_128k_StreamingUnknown_0k_64k(129), backgrnd_64k_128k_StreamingUnknown_0k_64k (130), interact_64k_128k_StreamingUnknown_0k_128k (131), backgrnd_64k_128k_StreamingUnknown_0k_128k (132), interact_64k_8k_CC (133), backgrnd_64k_8k_CC (134) , interact_32k_TC_40 (135), backgrnd_32k_TC_40 (136) , speech_12_2k_7_95k_5_9k_4_75k (137), interact_16k_TC_40(138), backgrnd_16k_TC_40(139), speech_12_2k_7_95k_5_9k_4_75k_Interact_8k_8k_40 (140) , speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_8k_8k_40 (141), speech_12_2k_7_95k_5_9k_4_75k_Interact_16k_16k_40 (142) , speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_16k_16k_40 (143), speech_12_2k_7_95k_5_9k_4_75k_Interact_32k_32k_40(144), speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_32k_32k_40(145), speech_12_2k_7_95k_5_9k_4_75k_Interact_64k_64k_20(146), speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_64k_64k_20(147), speech_12_2k_7_95k_5_9k_4_75k_Interact_64k_128k_20(148), speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_64k_128k_20(149), speech_10_2k_6_7k_5_9k_4_75k (150), speech_7_4k_6_7k_5_9k_4_75k (151), interact_8k_TC_40(152), backgrnd_8k_TC_40(153), speech_12_2k_Interact_0k_0k_CC_20 (154) , speech_12_2k_Backgrnd_0k_0k_CC_20 (155), speech_12_2k_Interact_8k_8k_TC_40 (156) , speech_12_2k_Backgrnd_8k_8k_TC_40 (157), speech_12_2k_7_95k_5_9k_4_75k_Interact_0k_0k_CC_20(158), speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_0k_0k_CC_20(159), convUnknown_64k_Interact_8k_20(160), convUnknown_64k_Backgrnd_8k_20(161), convUnknown_64k_Interact_8k_40(162), convUnknown_64k_Backgrnd_8k_40(163), convUnknown_64k_Interact_16k_64k_20(164), convUnknown_64k_Backgrnd_16k_64k_20(165), convUnknown_64k_Interact_16k_64k_40(166), convUnknown_64k_Backgrnd_16k_64k_40(167) , interact_32k_TC_20 (168), backgrnd_32k_TC_20 (169), speech_12_2k_7_95k_5_9k_4_75k_ConvUnknown_64k_20(170), speech_12_2k_Interact_32k_32k_40(171), speech_12_2k_Backgrnd_32k_32k_40(172), interact_64k_TC_20(173), backgrnd_64k_TC_20(174), interact_8k_Interact_8k_TC_40(175), backgrnd_8k_Backgrnd_8k_TC_40(176), streamUnknown_16k_64k_Interact_8k(177), streamUnknown_16k_64k_Backgrnd_8k(178), speech_12_2k_Interact_64k_64k_TC_20(179), </pre>

ASN.1 Type Definition
Type Definition
<pre> speech_12_2k_Backgrnd_64k_64k_TC_20(180), speech_12_2k_7_95k_5_9k_4_75k_ConvUnknown_64k_40(181) , interact_64k_384k_DSCH_10 (182), backgrnd_64k_384k_DSCH_10(183), interact_64k_384k_DSCH_20(184), backgrnd_64k_384k_DSCH_20(185), interact_64k_2048k_DSCH_10(186), backgrnd_64k_2048k_DSCH_10(187), interact_64k_2048k_DSCH_20(188), backgrnd_64k_2048k_DSCH_20(189), speech_12_2k_Interact_64k_384k_DSCH_10 (190), speech_12_2k_Backgrnd_64k_384k_DSCH_10 (191), speech_12_2k_Interact_64k_384k_DSCH_20 (192), speech_12_2k_Backgrnd_64k_384k_DSCH_20 (193), speech_12_2k_Interact_64k_2048k_DSCH_10 (194), speech_12_2k_Backgrnd_64k_2048k_DSCH_10 (195), speech_12_2k_Interact_64k_2048k_DSCH_20 (196), speech_12_2k_Backgrnd_64k_2048k_DSCH_20 (197), interact_8k_CC_40(198), backgrnd_8k_CC_40(199), interact_64k_768k_10(200), interact_64k_768k_20(201), backgrnd_64k_768k_10(202), backgrnd_64k_768k_20(203), streamUnknown_16k_128k_Interact_8k(204), streamUnknown_16k_128k_Backgrnd_8k(205) } </pre>
Detailed Comments :

ASN.1 Type Definition	
Type Name	: RB_ConfigType
Encoding Variation	:
Comments	:
Type Definition	
<pre> ENUMERATED { cell_NotConfigured (0), -- Configurations on DPCH cell_DCH_StandAloneSRB_NoConn (1), cell_NoDPCH (2), cell_DCH_StandAloneSRB (3), cell_DCH_Speech (4), cell_DCH_64kCS_RAB_SRB (5), cell_DCH_57_6kCS_RAB_SRB (6), cell_DCH_64kPS_RAB_SRB (7), cell_RLC_DCH_AM_RAB_15Lis (8), cell_RLC_DCH_AM_RAB_7Lis (9), cell_RLC_DCH_UM_RAB_15Lis (10), cell_RLC_DCH_UM_RAB_7Lis (11), cell_PDCP_AM_RAB (12), cell_PDCP_UM_RAB (13), cell_PDCP_AM_UM_RAB (14), cell_DCH_MAC_SRB_NoConn (15), cell_DCH_MAC_SRB (16), cell_DCH_2AM_PS (17), -- Configurations on FACH cell_FACH_NoConn (18), cell_FACH (19), cell_FACH_NoDedicated (20), cell_FACH_PS (21), cell_FACH_BMC (22), cell_FACH_BMC_NoConn (23), cell_FACH_2_PRACH_NoConn (24), --no RAB cell_FACH_2_PRACH (25), -- no RAB cell_FACH_2_SCCPCH_NoConn (26), --used in BMC cell_FACH_2_SCCPCH (27), --Used in BMC cell_FACH_MAC_SRB_NoConn (28), cell_FACH_MAC_SRB (29), cell_FACH_MAC_SRB0_NoConn (30), cell_FACH_MAC_SRB0 (31), -- Configurations for RAB test cases -- for these configurations Security step does not handle RAB established situation, as in RAB test procedure -- Security procedure is called before RAB establishment cell_FACH_2SCCPCH_StandAlonePCH_NoConn (32), cell_FACH_2SCCPCH_StandAlonePCH (33), cell_FACH_2SCCPCH_StandAlonePCH_PS (34), cell_Two_DTCH (35), cell_Four_DTCH_CS (36), cell_Two_DTCH_CS_PS (37), cell_Four_DTCH_CS_PS (38), cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn (39), cell_FACH_3_SCCPCH_4_FACH_Cnfg1 (40), cell_FACH_3_SCCPCH_3_FACH_CTCH_NoConn (41), cell_FACH_3_SCCPCH_3_FACH_CTCH (42), cell_Two_DTCH_PS_CS (43), cell_Four_DTCH_PS_CS (44) , cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn (45), cell_FACH_3_SCCPCH_4_FACH_Cnfg2 (46), cell_DCH_2_PS_Call(47), -- Configurations for DSCH RAB test cases cell_DCH_DSCH_PS (48), cell_DCH_DSCH_CS_PS (49), cell_FACH_2SCCPCH_StandAlonePCH_PS_2a(50), </pre>	

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ASN.1 Type Definition	
Type Definition	
<pre> cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_NoConn (51), cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1 (52), cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_NoConn (53), cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2 (54), cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_NoConn (55), cell_FACH_3_SCCPCH_3_FACH_2a_CTCH (56), cell_Two_DTCH_CS_PS_Init (57), cell_Four_DTCH_CS_PS_Init (58), cell_FACH_2SCCPCH_StandAlonePCH_2a_NoConn (59), cell_FACH_2SCCPCH_StandAlonePCH_2a (60), cell_Two_DTCH_PS_CS_Init (61), cell_Four_DTCH_PS_CS_Init (62), cell_DCH_HS_DSCH(63), cell_FACH_HS(64), cell_DCH_64kPS_RAB_SRB_HS(65), cell_One_DTCH_HS_DSCH_MAC(66), cell_5_UM_DCH_HS_DSCH (67), cell_DCH_Speech_WAMR (68), cell_Four_DTCH_HS_CS_Init (69), cell_Four_DTCH_HS_CS (70), cell_Two_DTCH_HS_CS_Init (71), cell_Two_DTCH_HS_CS (72), cell_Four_DTCH_CS_HS_Init (73), cell_DCH_2AM_HS_DSCH (74) } </pre>	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: AICHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { aichinfo AICH_Info, dl_TxPower AICH_PowerOffset } </pre>	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: AmConfirmationRequest
Encoding Variation	:
Comments	: If the noConfirmationRequested option is used, then an RLC_AM_DATA_CNF is not expected from the RLC AM entity. If the confirmationRequested option is used, then the RLC AM entity is being requested to provide an RLC_DATA_CNF primitive containing the same mui value.
Type Definition	
CHOICE { noConfirmationRequest NULL, confirmationRequested Mui }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: COUNT_I_MSB
Encoding Variation	:
Comments	: 28 bits long
Type Definition	
INTEGER (0..268435455)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CellTxPowerLevel
Encoding Variation	:
Comments	: The defaultCellTxPowerLvl is a default setting and is used for the most signalling tests. The real total cell DL Tx power level equals to the sum of the DL Tx power of the individual physical channels configured. The totalCellTxPowerLvl applies to e.g. the idle mode tests in a non–default multi–cell radio environment.
Type Definition	
CHOICE { defaultCellTxPowerLvl NULL, totalCellTxPowerLvl DL_TxPower }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CiphActivationInfo
Encoding Variation	:
Comments	: DL or UL ciphering activation info If RB is omitted in rB_UL_CiphActivationTimeInfo the SS takes no action on this RB and the ciphering configuration keeps unchanged on this RB. CipheringModeCommand = dummy NULL means no ciphering.
Type Definition	
<pre>CHOICE { cipheringModeInfo CipheringModeInfo, rb_UL_CipheringActivationTimeInfo RB_ActivationTimeInfoList }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CmacConfigReq
Encoding Variation	:
Comments	: To request to configure MAC
Type Definition	
<pre>SEQUENCE { activationTime SS_ActivationTime, uE_Info UE_Info, trCHInfo TrCHInfo, trCH_LogCHMapping TrCH_LogCHMappingList1 -- RACHTransmissionCtrlElements -- CPCHTransmissionControlElements }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CmacPagingConfigReq
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { pl_BitMapInfo CHOICE { e18 BIT STRING (SIZE (18)), e36 BIT STRING (SIZE (36)), e72 BIT STRING (SIZE (72)), e144 BIT STRING (SIZE (144))}, dRX_CycleLength INTEGER (3..9), iMSI IMSI_GSM_MAP, t_pich_T_sccpch BOOLEAN -- T_pich > T_sccpch then FALSE }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CmacSysinfoConfigReq
Encoding Variation	:
Comments	: if bcch_ModificationTime = OMIT SS shall modify the Sysinfo immediately
Type Definition	
<pre> SEQUENCE { sg_REP INTEGER (2..12), -- Repetition period is the sg_REP-th power of 2. sg_POS INTEGER (0..2047), -- The position of each segment is 2 * sg_POS. bcch_ModificationTime BCCH_ModificationTime OPTIONAL } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CommonOrDedicatedTFS
Encoding Variation	:
Comments	: Transport Format Set
Type Definition	
<pre> SEQUENCE { tti CHOICE { tti10 CommonOrDedicatedTF_InfoList, tti20 CommonOrDedicatedTF_InfoList, tti40 CommonOrDedicatedTF_InfoList, tti80 CommonOrDedicatedTF_InfoList, dynamic CommonOrDedicatedTF_InfoList_DynamicTTI }, semistaticTF_Information SemistaticTF_Information } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CommonOrDedicatedTF_Info
Encoding Variation	:
Comments	: Transport Format Set
Type Definition	
<pre> SEQUENCE { tb_Size INTEGER (0..5035), numberOfTbSizeList SEQUENCE (SIZE (1..maxTF)) OF NumberOfTransportBlocks, logicalChannelList LogicalChannelList } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CommonOrDedicatedTF_InfoList
Encoding Variation	:
Comments	: Transport Format Set
Type Definition	
SEQUENCE (SIZE (1..maxTF)) OF CommonOrDedicatedTF_Info	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CommonOrDedicatedTF_InfoList_DynamicTTI
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { tb_Size INTEGER (0..5035), numberOfTbSizeList SEQUENCE (SIZE (1..maxTF)) OF NumberOfTransportBlocks, logicalChannelList LogicalChannelList }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CphyRIModifyReq
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { activationTime SS_ActivationTime, physicalChannelInfo CHOICE { dpch_CompressedModeStatusInfo DPCH_CompressedModeStatusInfo, secondaryCCPCHInfo SecondaryCCPCHInfo, pRACHInfo PRACHInfo, dPCHInfo DPCHInfo, dPCHInfo_r5 DPCHInfo_r5, -- Rel-5 or later hS_PDSCHInfo HS_PDSCHInfo -- Rel-5 or later }, trchConfigToFollow BOOLEAN DEFAULT TRUE }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CphyRISetupReq
Encoding Variation	:
Comments	: To request to setup the Radio Link
Type Definition	
<pre> SEQUENCE { physicalChannelInfo CHOICE { primaryCPICHInfo PrimaryCPICHInfo, secondaryCPICHInfo SecondaryCPICHInfo, primarySCHInfo PrimarySCHInfo, secondarySCHInfo SecondarySCHInfo, primaryCCPCHInfo PrimaryCCPCHInfo, secondaryCCPCHInfo SecondaryCCPCHInfo, pRACHInfo PRACHInfo, pICHInfo PICHInfo, aICHInfo AICHInfo, dPCHInfo DPCHInfo, pDSCHInfo PDSCHInfo, dPCHInfo_r5 DPCHInfo_r5, -- Rel-5 or later hS_PDSCHInfo HS_PDSCHInfo -- Rel-5 or later } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CphyTrchConfigReq
Encoding Variation	:
Comments	: To request to configure the transport channel. The same TFCS information should be provided to the PHY and MAC layers at all times. When a CPHY_TrCH_Config_REQ is used to configure the PHY layer, a corresponding CMAC_Config_REQ should be sent to the MAC layer to ensure that the configuration is consistent. For configuring HS-DSCH transport channel, the ulconnectedTrCHList, ulTFCS, dlconnectedTrCHList and dlTFCS shall be omitted.
Type Definition	
<pre> SEQUENCE { activationTime SS_ActivationTime, ulconnectedTrCHList SEQUENCE (SIZE (0..maxTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, ul_TransportChannelType SS_UL_TransportChannelType, transportChannelInfo CommonOrDedicatedTFS } OPTIONAL, ulTFCS TFCS OPTIONAL, dlconnectedTrCHList SEQUENCE (SIZE (0..maxTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, dl_TransportChannelType SS_DL_TransportChannelType, transportChannelInfo CommonOrDedicatedTFS } OPTIONAL, dlTFCS TFCS OPTIONAL, hsDSCHMacdFlows HS_DSCHMACdFlows OPTIONAL -- Rel-5 or later } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: CrlcConfigReq
Encoding Variation	:
Comments	: To request to setup, re_configure or release RLC entityThe Stop parameter indicates that the RLC entity shall not transmit or receive RLC PDUs. The Continue parameter indicates that the RLC entity shall continue transmission and reception of RLC PDUs. When the RLC entity is stopped, the RLC timers are not affected. Triggered polls and status transmissions are delayed until the RLC entity is continued.
Type Definition	
<pre>CHOICE { setup RBInfo, reconfigure RBInfo, release NULL, sS_stop NULL, sS_continue NULL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DL_DPCHInfo
Encoding Variation	:
Comments	: The range for powerOffsetOfTPC_PO2 and powerOffsetOfTFCI_PO1 and powerOffsetOfPILOT_PO3 is 0–6 dB, 0.25 dB per step.
Type Definition	
<pre>SEQUENCE { dl_CommonInformation DL_CommonInformation, dl_DPCH_InfoPerRL DL_DPCH_InfoPerRL, powerOffsetOfTFCI_PO1 INTEGER (0..24), powerOffsetOfTPC_PO2 INTEGER (0..24), powerOffsetOfPILOT_PO3 INTEGER (0..24), dl_TxPower DL_TxPower, dl_TxPowerMax DL_TxPower, dl_TxPowerMin DL_TxPower }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DL_DPCHInfo_r5
Encoding Variation	:
Comments	: Applicable Rel-5 or later
Type Definition	
<pre> SEQUENCE { dl_CommonInformation DL_CommonInformation_r5, dl_DPCH_InfoPerRL DL_DPCH_InfoPerRL_r5, powerOffsetOfTFCI_PO1 INTEGER (0..24), powerOffsetOfTPC_PO2 INTEGER (0..24), powerOffsetOfPILOT_PO3 INTEGER (0..24), dl_TxPower DL_TxPower, dl_TxPowerMax DL_TxPower, dl_TxPowerMin DL_TxPower } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DL_TxPower
Encoding Variation	:
Comments	: Downlink Tx Power relative to PCPICH
Type Definition	
INTEGER (-35..15)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DL_TxPower_PCPICH
Encoding Variation	:
Comments	: Absolute Tx Power of PCPICH
Type Definition	
INTEGER (-60..-30)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DPCHInfo
Encoding Variation	:
Comments	: The range for powerOffsetOfTPC_PO2 and powerOffsetOfTFCI_PO1 and powerOffsetOfPILOT_PO3 is 0 dB to 6 dB, 0,25 dB per step.
Type Definition	
<pre> SEQUENCE { ul_DPCHInfo UL_DPCH_Info OPTIONAL, dl_DPCHInfo DL_DPCHInfo OPTIONAL } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: DPCHInfo_r5
Encoding Variation	:
Comments	: Applicable Rel-5 or later At least one of the first two fields shall be present. Presence of hs_DPCCHInd (value = truevalue) means that the HS-DPCCH shall be configured in the uplink DPCH. If hs_DPCCHInd is absent no HS-DPCCH shall be configured in the uplink DPCH, or the configured HS-DPCCH shall be removed in the modify ASP. In the active set which has radio links from more than one cell the HS-DPCCH is configured only in the HS-DSCH serving cell. Three combinations are valid: ul_DPCH_Info only, dl_DPCHInfo only and ul_DPCH_Info + hs_DPCCHInd.
Type Definition	
<pre>SEQUENCE { ul_DPCHInfo UL_DPCH_Info_r5 OPTIONAL, dl_DPCHInfo DL_DPCHInfo_r5 OPTIONAL, hs_DPCCHInd HS_DPCCHInfo OPTIONAL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: HS_DPCCHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { cqi_RepetitionFactor CQI_RepetitionFactor, ackNackRepetitionFactor ACK_NACK_repetitionFactor }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: HS_DSCHMACdFlows
Encoding Variation	:
Comments	: Applicable Rel-5 or later. Within the ACK/NACK repetition period indicated by ackNackRepetitionFactor the SS shall not transmit MAC-hs PDU's on HS-PDSCH.
Type Definition	
<pre>SEQUENCE { harqInfo HARQ_Info OPTIONAL, addOrReconfMACdFlow SS_AddOrReconfMAC_dFlow OPTIONAL, ackNackRepetitionFactor ACK_NACK_repetitionFactor OPTIONAL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: HS_PDSCHInfo
Encoding Variation	:
Comments	: Applicable Rel-5 or later When CHY_RL_Setup_REQ is called with CHOICE of hS_PDSCHInfo the SS shall not only configure the HS_PDSCH but also the HS-SCCH; The following HS-DSCH related parameters are passed to the SS implicitly by HSDSCH_physical_layer_category: "Maximum number of HS-DSCH codes can be received by UE", "Minimum inter-TTI interval", "Maximum number of bits of an HS-DSCH transport block within an HS-DSCH TTI" and "Total number of soft channel bits". The HSDSCH_physical_Layer_category is also used for interpret the meaning of CQI value.
Type Definition	
<pre>SEQUENCE{ hSDSCHPhysicalLayerCategory HSDSCH_physical_layer_category, h_RNTI H_RNTI, dlHSPDSCHInformation DL_HSPDSCH_Information, sttd_Indicator BOOLEAN, hs_SCCH_TxPower DL_TxPower -- offset related to pilot bits on DL-DPCCH -- (TS 25.433 subcluse 9.2.2.18l) }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: HyperFrameNumber
Encoding Variation	:
Comments	:
Type Definition	
BIT STRING (SIZE (20))	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Increment_Mode
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED {incPerCFN_Cycle(0), notInc(1), incByOne_IncPerCFN_Cycle(2)}	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: IntegrityActivationInfo
Encoding Variation	:
Comments	: Comment DL or UL integrity activation info. At the RRC message sequence numbers specified in the ul_IntegProtActivationInfo the SS shall initialise COUNT-I for the SRB's indicated in the ul_IntegProtActivationInfo and start using the new configuration on uplink for the indicated SRB's. If the START value is omitted in the CRLC_SecurityMode_Config_REQ above COUNT-I initialisation shall not be performed. Type Definition
Type Definition	
CHOICE { integrityProtectionModelInfo IntegrityProtectionModelInfo, ul_IntegProtActivationInfo IntegrityProtActivationInfoList }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: IntegrityProtActivationInfoList
Encoding Variation	:
Comments	: List of SS IntegrityProtActivationInfo
Type Definition	
SEQUENCE (SIZE (1..maxRB)) OF SS_IntegrityProtActivationTimeInfo	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: IntegrityResult
Encoding Variation	:
Comments	:
Type Definition	
CHOICE { integrityNotUsed NULL, integrityUsed IntegrityStatus }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: IntegrityStatus
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED { i_pass(0), i_fail(1) }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_ActiveSetUpdate
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { activeSetUpdate_r3 ActiveSetUpdate_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_CCCH_MsgShort
Encoding Variation	:
Comments	:
Type Definition	
NULL	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_CellUpdateConfirm
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { cellUpdateConfirm_r3 CellUpdateConfirm_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { cellUpdateConfirm_v3a0ext CellUpdateConfirm_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_DCCH_MsgShort
Encoding Variation	:
Comments	:
Type Definition	
NULL	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_DL_CCCH_MsgType
Encoding Variation	:
Comments	:
Type Definition	
<pre> CHOICE { invalid_cellUpdateConfirm [0] Invalid_CCCH_MsgShort, invalid_rrcConnectionReject [1] Invalid_CCCH_MsgShort, invalid_rrcConnectionRelease [2] Invalid_CCCH_MsgShort, invalid_rrcConnectionSetup [3] Invalid_RRCCConnectionSetup, --- Specific invalid message invalid_uraUpdateConfirm [4] Invalid_UraUpdateConfirm, invalid_extension [5] NULL, unkown_Type_CCCH_message [6] Invalid_CCCH_MsgShort } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_DL_DCCH_MsgType
Encoding Variation	:
Comments	:
Type Definition	
<pre> CHOICE { invalid_activeSetUpdate [0] Invalid_ActiveSetUpdate, invalid_assistanceDataDelivery [1] Invalid_DCCH_MsgShort, invalid_cellChangeOrderFromUTRAN [2] Invalid_DCCH_MsgShort, invalid_cellUpdateConfirm [3] Invalid_CellUpdateConfirm , invalid_counterCheck [4] Invalid_DCCH_MsgShort, invalid_downlinkDirectTransfer [5] Invalid_DownlinkDirectTransfer, invalid_handoverFromUTRANCommand_GSM [6] Invalid_HandoverFromUTRANCommand_GSM, invalid_handoverFromUTRANCommand_CDMA2000 [7] Invalid_DCCH_MsgShort, invalid_measurementControl [8] Invalid_MeasurementControl, invalid_pagingType2 [9] Invalid_DCCH_MsgShort, invalid_physicalChannelReconfiguration [10] Invalid_PhysicalChannelReconfiguration, invalid_physicalSharedChannelAllocation [11] Invalid_DCCH_MsgShort, invalid_radioBearerReconfiguration [12] Invalid_RadioBearerReconfiguration, invalid_radioBearerRelease [13] Invalid_RadioBearerRelease, invalid_radioBearerSetup [14] Invalid_RadioBearerSetup, invalid_rrcConnectionRelease [15] Invalid_RRC_ConnectionRelease, invalid_securityModeCommand [16] Invalid_SecurityModeCommand, invalid_signallingConnectionRelease [17] Invalid_DCCH_MsgShort, invalid_transportChannelReconfiguration [18] Invalid_TransportChannelReconfiguration, invalid_transportFormatCombinationControl [19] Invalid_DCCH_MsgShort, invalid_ueCapabilityEnquiry [20] Invalid_UECapabilityEnquiry, -- Specific invalid message invalid_ueCapabilityInformationConfirm [21] Invalid_UECapabilityInformationConfirm, -- Specific invalid message invalid_uplinkPhysicalChannelControl [22] Invalid_DCCH_MsgShort, invalid_uraUpdateConfirm [23] Invalid_UraUpdateConfirm, invalid_utranMobilityInformation [24] Invalid_UtranMobilityInformation, invalid_extension [25] NULL, unkown_Type_DCCH_message [26] Invalid_DCCH_MsgShort } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_DL_SHCCH_MsgType
Encoding Variation	:
Comments	:
Type Definition	
<pre> CHOICE { invalid_physicalSharedChannelAllocation [0] Invalid_SHCCH_Message_short, extension [1] NULL, unkown_Type_SHCCH_message [2] Invalid_SHCCH_Message_short } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_DownlinkDirectTransfer
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { downlinkDirectTransfer_r3 DownlinkDirectTransfer_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_HandoverFromUTRANCommand_GSM
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { handoverFromUTRANCommand_GSM_r3 HandoverFromUTRANCommand_GSM_r3_IEs, laterNonCriticalExtensions SEQUENCE { handoverFromUTRANCommand_GSM_r3_add_ext BIT STRING OPTIONAL, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_MeasurementControl
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { measurementControl_r3 MeasurementControl_r3_IEs, v390nonCriticalExtensions SEQUENCE { measurementControl_v390ext MeasurementControl_v390ext, v3a0NonCriticalExtensions SEQUENCE { measurementControl_v3a0ext MeasurementControl_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_PhysicalChannelReconfiguration
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { physicalChannelReconfiguration_r3 PhysicalChannelReconfiguration_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { physicalChannelReconfiguration_v3a0ext PhysicalChannelReconfiguration_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_RRCConnectionSetup
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { rrcConnectionSetup_r3 RRCConnectionSetup_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_RRC_ConnectionRelease
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { rrcConnectionRelease_r3 RRCConnectionRelease_r3_IEs, laterNonCriticalExtensions SEQUENCE { rrcConnectionRelease_r3_add_ext BIT STRING OPTIONAL, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- instead of SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_RadioBearerReconfiguration
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { radioBearerReconfiguration_r3 RadioBearerReconfiguration_r3_IEs, v3aoNonCriticalExtensions SEQUENCE { radioBearerReconfiguration_v3a0ext RadioBearerReconfiguration_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_RadioBearerRelease
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { radioBearerRelease_r3 RadioBearerRelease_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { radioBearerRelease_v3a0ext RadioBearerRelease_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_RadioBearerSetup
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { radioBearerSetup_r3 RadioBearerSetup_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { radioBearerSetup_v3a0ext RadioBearerSetup_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_SHCCH_Message_short
Encoding Variation	:
Comments	:
Type Definition	
CounterCheckResponse	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_SecurityModeCommand
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { securityModeCommand_r3 SecurityModeCommand_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_TransportChannelReconfiguration
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { transportChannelReconfiguration_r3 TransportChannelReconfiguration_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { transportChannelReconfiguration_v3a0ext TransportChannelReconfiguration_v3a0ext, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_UECapabilityEnquiry
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { ueCapabilityEnquiry_r3 UECapabilityEnquiry_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_UECapabilityInformationConfirm
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { ueCapabilityInformationConfirm_r3 UECapabilityInformationConfirm_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_UraUpdateConfirm
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { uraUpdateConfirm_r3 URAUpdateConfirm_r3_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Invalid_UtranMobilityInformation
Encoding Variation	:
Comments	: This invalid type should cause an undefined critical extension error
Type Definition	
<pre> CHOICE { r3 SEQUENCE { utranMobilityInformation_r3 UTRANMobilityInformation_r3_IEs, v3a0NonCriticalExtensions SEQUENCE { utranMobilityInformation_v3a0ext UTRANMobilityInformation_v3a0ext_IEs, nonCriticalExtensions SEQUENCE {} OPTIONAL } OPTIONAL }, later_than_r3 SEQUENCE { rrc_TransactionIdentifier RRC_TransactionIdentifier, criticalExtensions BIT STRING (SIZE(8)) -- INSTEAD OF SEQUENCE {} } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: KeyCiphering
Encoding Variation	:
Comments	:
Type Definition	
BIT STRING (SIZE (128))	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: LogicalChannelType
Encoding Variation	:
Comments	:
Type Definition	
<pre> ENUMERATED { bCCH (0), pCCH (1), cCCH (2), cTCH (3), dCCH (4), dTCH (5), sHCCH (6) } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: MAC_HeaderManipulation
Encoding Variation	:
Comments	:
Type Definition	
<pre> ENUMERATED { normalMacHeader (0), omitMacHeader (1) } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: Mui
Encoding Variation	:
Comments	:
Type Definition	
INTEGER (0..4095)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PDSCHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { fdd_tdd CHOICE { fdd SEQUENCE { pdsch_CodeMapping PDSCH_CodeMapping }, tdd SEQUENCE { -- pdsch-Identity PDSCH-Identity, -- pdsch-Info PDSCH-Info, -- pdsch-PowerControlInfo PDSCH-PowerControlInfo OPTIONAL } }, dl_TxPower DL_TxPower } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PICHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { pichinfo PICH_Info, dl_TxPower PICH_PowerOffset, sccpchId_associated INTEGER (0..31) } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PRACHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { fdd_tdd CHOICE { fdd SEQUENCE { preambleSignature AvailableSignatures, spreadingFactorForDataPart SF_PRACH, preambleScramblingCode PreambleScramblingCodeWordNumber, puncturingLimit PuncturingLimit, accessSlot AvailableSubChannelNumbers }, tdd SEQUENCE { -- timeSlot TimeSlot, -- spreadingCode SpreadingCode, -- midambleCode MidambleCode, } } } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PayloadSize
Encoding Variation	:
Comments	:
Type Definition	
<pre> INTEGER (0..4992) </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PhysicalChannelIdentity
Encoding Variation	:
Comments	:
Type Definition	
INTEGER (0..31)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PrimaryCCPCHInfo
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { sttd_Indicator BOOLEAN, dl_TxPower DL_TxPower -- timeSlot TimeSlot OPTIONAL, -- burstType BurstType OPTIONAL, -- offset Offset OPTIONAL, -- repetitionPeriod RepetitionPeriod OPTIONAL, -- repetitionLength RepetitionLength OPTIONAL, }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PrimaryCPICHInfo
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { dl_TxPower_PCPICH DL_TxPower_PCPICH, txdiversityIndicator BOOLEAN }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: PrimarySCHInfo
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { tstdIndicator BOOLEAN, dl_TxPower DL_TxPower }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RBInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { sS_rlc_Info SS_RLC_Info OPTIONAL, rB_LogCH_Mapping RB_LogCH_Mapping }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RB_LogCH_Mapping
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { uLogicalChannelIdentity LogicalChannelIdentity OPTIONAL, dLogicalChannelIdentity LogicalChannelIdentity OPTIONAL, logicalChannelType LogicalChannelType OPTIONAL, cn_DomainIdentity CN_DomainIdentity OPTIONAL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RLC_IncMode
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED {notInc(0), inc(1)}	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RRC_Rel_Status
Encoding Variation	:
Comments	:
Type Definition	
<pre>ENUMERATED { cell_Dch(0), cell_Fach_Dcch(1), cell_Fach_Ccch(2) }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RRC_SequenceNumber
Encoding Variation	:
Comments	: 4 bits long
Type Definition	
INTEGER (0..15)	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: RabTxInfo
Encoding Variation	:
Comments	: Provide test data, number of RBs, and RB Tx info of each RB (RB id, SDU size and number of SDUs) to be transmitted in consecutive TTIs
Type Definition	
SEQUENCE { testData BIT STRING (SIZE (8..163840)), rbTxInfoList SEQUENCE (SIZE (1..6)) OF RbTxInfo } 	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: RatType
Encoding Variation	:
Comments	: To select route between each channels
Type Definition	
ENUMERATED { fdd(0), tdd(1) } 	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: RbTxInfo
Encoding Variation	:
Comments	: Info on RB id and the actual DL test data size (SDU_Size * number of SDUs). The actual test data is extracted from the first (SDU_Size * number of SDUs) bits in the raw testData buffer. SS shall transmit the actual test data in every TTI. The value nomOfSdu = T / TTI, whereby T=1200 is the duration of the data transmitting in the RAB test, taking into account the test tolerance (+50%) of the UE loop back delay (<800 ms).
Type Definition	
SEQUENCE { rB_Identity INTEGER(−31..32), sduSize INTEGER(1..163840), nomOfSdu INTEGER(0..255) — 0 is set for no data on this RB } 	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RegOr_MO
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED { est_Reg(0), est_MO(1), est_MT(2) } 	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: RoutingInfo
Encoding Variation	:
Comments	:
Type Definition	
CHOICE { physicalChannelIdentity INTEGER (0..31), transportChannelIdentity TransportChannelIdentity, logicalChannelIdentity LogicalChannelIdentity, rB_Identity INTEGER (−31..32), cn_DomainIdentity CN_DomainIdentity } 	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SCCPCHSlotFormat
Encoding Variation	:
Comments	: Reference to TS25.211
Type Definition	
INTEGER (0..17)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SIB
Encoding Variation	:
Comments	: Union of all system information blocks
Type Definition	
<pre> CHOICE { sIB1 SysInfoType1, sIB2 SysInfoType2, sIB3 SysInfoType3, sIB4 SysInfoType4, sIB5 SysInfoType5, sIB6 SysInfoType6, sIB7 SysInfoType7, sIB8 SysInfoType8, sIB9 SysInfoType9, sIB10 SysInfoType10, sIB11 SysInfoType11, sIB12 SysInfoType12, sIB13 SysInfoType13, sIB13_1 SysInfoType13_1, sIB13_2 SysInfoType13_2, sIB13_3 SysInfoType13_3, sIB13_4 SysInfoType13_4, sIB14 SysInfoType14, sIB15 SysInfoType15, sIB15_1 SysInfoType15_1, sIB15_2 SysInfoType15_2, sIB15_3 SysInfoType15_3, sIB16 SysInfoType16, sIB17 SysInfoType17, sIB18 SysInfoType18, mIB MasterInformationBlock, sB1 SysInfoTypeSB1, sB2 SysInfoTypeSB2 } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_ActivationTime
Encoding Variation	:
Comments	:
Type Definition	
<pre>CHOICE { activationCFN ActivationTime, activateNow NULL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_AddOrReconfMAC_dFlow
Encoding Variation	:
Comments	: Applicable Rel-5 or later.
Type Definition	
<pre>SEQUENCE { mac_hs_AddReconfQueue_List SEQUENCE (SIZE(1..maxQueueIDs)) OF SEQUENCE { mac_hs_AddReconfQueue SS_MAC_hs_AddReconfQueue } OPTIONAL, mac_hs_DelQueue_List SEQUENCE (SIZE(1..maxQueueIDs)) OF SEQUENCE { mac_hsQueueId INTEGER(0..7) } OPTIONAL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_DL_LogicalChannelMapping
Encoding Variation	:
Comments	<p>If the macHeaderManipulation field is 'normalMacHeader', then data transmitted on this logical channel shall have an appropriate MAC header added before it is sent to lower layers for transmission.</p> <p>If the macHeaderManipulation field is 'OmitMacHeader', then data transmitted on this logical channel shall not have any MAC header information added, even if the logical channel type and mapping indicates that there should be a MAC header present. This allows the entire MAC PDU to be specified in the TTCN, so individual fields in the MAC header can be modified.</p> <p>When used for logical channel to MAC_d flow mapping rlc_SizeList should choose RLC_SizeExplicitList.</p>
Type Definition	
<pre>SEQUENCE { macHeaderManipulation MAC_HeaderManipulation, dl_TransportChannelType SS_DL_TransportChannelType, logicalChannelIdentity LogicalChannelIdentity, logicalChannelType LogicalChannelType , rlc_SizeList CHOICE { allSizes NULL, configured NULL, explicitList RLC_SizeExplicitList }, mac_LogicalChannelPriority MAC_LogicalChannelPriority OPTIONAL }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_DL_RLC_Mode
Encoding Variation	:
Comments	:
Type Definition	
SEQUENCE { dl_PayloadSize PayloadSize OPTIONAL, dl_RLCModeInfo UL_RLC_Mode }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_DL_TransportChannelType
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED { dch(0), fach(1), bch(2), pch(3), dsch(4), hsdsch(5) --- Rel-5 or later }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_IntegrityProtActivationTimeInfo
Encoding Variation	:
Comments	: Omitting rrc_MessageSequenceNumber means activation time set to "now".
Type Definition	
SEQUENCE { rb_Identity INTEGER (-31..32), rrc_MessageSequenceNumber RRC_MessageSequenceNumber OPTIONAL }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_MAC_hs_AddReconfQueue
Encoding Variation	:
Comments	: Applicable Rel-5 or later. When writing TTCN constraints the TTCN writer shall set the priority of PriorityQueue in this object correctly according to the priority of logical channels which is mapped on to this priority queue. And please note that : the range of priority of PriorityQueue is from 0 to 7 and 0 is the lowest priority.
Type Definition	
<pre> SEQUENCE { mAChsAddReconfQueue MAC_hs_AddReconfQueue, logicalChannelList SEQUENCE OF LogicalChannelIdentity, -- logical channels mapping onto the priority queue -- which is specified in mAChsAddReconfQueue priority INTEGER(0..7), discardTimer ENUMERATED { v20(0),v40(1),v60(2),v80(3),v100(4), v120(5),v140(6),v160(7),v180(8),v200(9), v250(10),v300(11),v400(12),v500(13), v750(14),v1000(15),v1250(16),v1500(17), v1750(18),v2000(19),v2500(20),v3000(21), v3500(22),v4000(23),v4500(24),v5000(25), v7500(26) } OPTIONAL } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_RLC_Info
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { sS_ul_RLC_Mode DL_RLC_Mode OPTIONAL, sS_dl_RLC_Mode SS_DL_RLC_Mode OPTIONAL, sS_ul_RLC_Mode_r5 DL_RLC_Mode_r5 OPTIONAL -- Rel-5 or later } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_UL_LogicalChannelMapping
Encoding Variation	:
Comments	<p>: If the macHeaderManipulation field is 'normalMacHeader', then data received on the transport channel supporting this logical channel shall have it's MAC header inspected to determine the appropriate routing, and removed as normal. The MAC SDU shall be passed to the appropriate logical channel.</p> <p>If the macHeaderManipulation field field is 'OmitMacHeader', then data received on the transport channel supporting this logical channel shall have it's MAC header inspected to determine the appropriate routing, but the MAC layer shall not remove the MAC header. Thus the entire MAC PDU shall be passed to the appropriate logical channel, and the MAC header can be checked by the TTCN.</p>
Type Definition	
<pre>SEQUENCE { macHeaderManipulation MAC_HeaderManipulation, ul_TransportChannelType SS_UL_TransportChannelType, logicalChannelIdentity LogicalChannelIdentity, logicalChannelType LogicalChannelType }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SS_UL_TransportChannelType
Encoding Variation	:
Comments	:
Type Definition	
<pre>ENUMERATED { dch(0), rach(1), cpch(2), usch(3) }</pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SecondaryCCPCHInfo
Encoding Variation	:
Comments	: The range for powerOffsetOfTFCI_PO1 and powerOffsetOfPILOT_PO3 is 0–6 dB, 0.25 dB per step.
Type Definition	
<pre> SEQUENCE { scramblingCode INTEGER(0..15), dl_ChannelizationCode SF256_AndCodeNumber, sCCPCHSlotFormat SCCPCHSlotFormat, timingOffset INTEGER (0..149), positionFixedOrFlexible PositionFixedOrFlexible, sttd_Indicator BOOLEAN, dl_TxPower DL_TxPower, powerOffsetOfTFCI_PO1 INTEGER (0..24), powerOffsetOfPILOT_PO3 INTEGER (0..24) -- timeSlot TimeSlot OPTIONAL, -- burstType BurstType OPTIONAL, -- midambleShift MidambleShift OPTIONAL, -- offset Offset OPTIONAL, -- repetitionPeriod RepetitionPeriod OPTIONAL, -- repetitionLength RepetitionLength OPTIONAL, -- tFCIPresence TFCIPresence OPTIONAL, } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SecondaryCPICHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { scramblingCode INTEGER(0..63), dl_ChannelizationCode SF512_AndCodeNumber, dl_TxPower DL_TxPower } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SecondarySCHInfo
Encoding Variation	:
Comments	:
Type Definition	
<pre> SEQUENCE { tstdIndicator BOOLEAN, dl_TxPower DL_TxPower } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: SecurityInfo
Encoding Variation	:
Comments	: The integrityKey is not applicable to MAC
Type Definition	
<pre> SEQUENCE { cn_DomainIdentity CN_DomainIdentity, startValue START_Value OPTIONAL, cipheringKey BIT STRING (SIZE (128)) OPTIONAL, integrityKey BIT STRING (SIZE (128)) OPTIONAL, gsmCipheringKey BIT STRING (SIZE (64)) OPTIONAL } </pre>	
Detailed Comments : When the SS receives SecurityInfo, the SS first stores the contents. The SecurityInfo contents is not activated until the contents follows until receiving the subsequent ASP, CRLC_Ciphering_Activate_REQ, CMAC_Ciphering_Activate_REQ or CRLC_Integrity_Activate_REQ. Omitted fields of SecurityInfo shall not be affected by the subsequent ASP at the activation time. EXAMPLE: Omitting of startValue indicates not to re-initialize the relevant COUNT?C or COUNT-I, omitting of cipheringKey indicates that the current ciphering key is valid..	

ASN.1 Type Definition	
Type Name	: SegmentsOfSysInfoBlock
Encoding Variation	:
Comments	: The structure holding the segments of a MIB/SIB/SB, maximum number of segments is 16.
Type Definition	
<pre> SEQUENCE { segCount INTEGER (1..16), seg1 BIT STRING (SIZE (1..226)), seg2 BIT STRING (SIZE (1..222)) OPTIONAL, seg3 BIT STRING (SIZE (1..222)) OPTIONAL, seg4 BIT STRING (SIZE (1..222)) OPTIONAL, seg5 BIT STRING (SIZE (1..222)) OPTIONAL, seg6 BIT STRING (SIZE (1..222)) OPTIONAL, seg7 BIT STRING (SIZE (1..222)) OPTIONAL, seg8 BIT STRING (SIZE (1..222)) OPTIONAL, seg9 BIT STRING (SIZE (1..222)) OPTIONAL, seg10 BIT STRING (SIZE (1..222)) OPTIONAL, seg11 BIT STRING (SIZE (1..222)) OPTIONAL, seg12 BIT STRING (SIZE (1..222)) OPTIONAL, seg13 BIT STRING (SIZE (1..222)) OPTIONAL, seg14 BIT STRING (SIZE (1..222)) OPTIONAL, seg15 BIT STRING (SIZE (1..222)) OPTIONAL, seg16 BIT STRING (SIZE (1..222)) OPTIONAL } </pre>	
Detailed Comments :	

ASN.1 Type Definition	
Type Name	: TFC_Restriction
Encoding Variation	:
Comments	: This type is used to specify the allowed TFCs within the current TFCS. A TFC restriction is applicable until a subsequent TFC restriction is applied. TFC restrictions are not cumulative, so each TFC restriction completely replaces the previous TFC restriction. The downlink restriction can be used to ensure that the SS uses a specific TFC for transmission of data, by only allowing the 'No data' TFC, and the 'desired' TFC. It may also be necessary to include one or more 'signalling only' TFCs to allow signalling to occur. The uplink restriction can be used to verify that the UE has used a specific TFC. Any data received by the SS using a forbidden TFCI shall be discarded.
Type Definition	
<pre>SEQUENCE { ulTFCI_Restriction TFC_Subset OPTIONAL, dlTFCI_Restriction TFC_Subset OPTIONAL }</pre>	
Detailed Comments	: SS requirements for downlink: 1. The SS MAC layer shall not use a restricted non-allowed TFC for DL. 2. The SS MAC layer shall not use a TFC that requires the SS RLC layer to provide padding PDUs (Ref 3G TS 25.322) 3. In the case that there is data pending on one or more RLC entities, but not enough to use one of the allowed TFCs: a. The SS MAC layer shall use the 'No data' TFC until there is enough data in the RLC to use another allowed TFC. b. The SS RLC layer shall buffer the data until there is enough data in the RLC entities for the MAC layer to use an allowed TFC other than the 'No data' TFC for transmission of the data. NB: The TTCN author is responsible for ensuring: 1. The SDU discard function is not configured for TM and UM entities in the UE, and is configured to no_discard for AM entities in the UE. 2. That RLC SDUs that are expected to be sent in the same TTI (due to a TFC restriction) are sent as quickly as possible to minimise the number of 'no data' TFCs used by the MAC layer, and the amount of buffering that must be performed by the RLC layer. SS requirements for uplink: The SS shall discard all data received using a restricted non-allowed TFC.

ASN.1 Type Definition	
Type Name	: Tcell
Encoding Variation	:
Comments	: Timing offset between reference channel and this channel (unit : 256 chips)
Type Definition	
INTEGER (0..38399)	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: TrCHInfo
Encoding Variation	:
Comments	: The same TFCS information should be provided to the PHY and MAC layers at all times. When a CMAC_Config_REQ is used to configure the MAC layer, a corresponding CPHY_TrCH_Config_REQ should be sent to the PHY layer to ensure that the configuration is consistent. For MAC-hs configuration: When ulconnectedTrCHList, ulTFCS, dlconnectedTrCHList and dlTFCS are omitted this ASP configures an MAC-hs entity.
Type Definition	
<pre> SEQUENCE { ulconnectedTrCHList SEQUENCE (SIZE (1..maxulTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, transportChannelInfo CommonOrDedicatedTFS } OPTIONAL, ulTFCS TFCS OPTIONAL, dlconnectedTrCHList SEQUENCE (SIZE (1..maxdlTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, transportChannelInfo CommonOrDedicatedTFS } OPTIONAL, dlTFCS TFCS OPTIONAL, hsDSCHMacdFlows HS_DSCHMACdFlows OPTIONAL -- Rel-5 or later } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: TrCH_LogCHMappingList
Encoding Variation	:
Comments	: maxLogCHperTrCH = 8
Type Definition	
SEQUENCE (SIZE (1..maxLogCHperTrCH)) OF TrCH_LogicalChannelMapping	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: TrCH_LogCHMappingList1
Encoding Variation	:
Comments	: maxulTrCH = maxdlTrCH = 16
Type Definition	
<pre> SEQUENCE { ulconnectedTrCHList SEQUENCE (SIZE (1..maxulTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, trCH_LogCHMappingList TrCH_LogCHMappingList} OPTIONAL, dlconnectedTrCHList SEQUENCE (SIZE (1..maxdlTrCH)) OF SEQUENCE { trchid TransportChannelIdentity, trCH_LogCHMappingList TrCH_LogCHMappingList} OPTIONAL, dlconnectedMACdFlows SEQUENCE (SIZE (1..8)) OF SEQUENCE { mac_dFlowId MAC_d_FlowIdentity, trCH_LogCHMappingList TrCH_LogCHMappingList } OPTIONAL -- Rel-5 or later } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: TrCH_LogicalChannelMapping
Encoding Variation	:
Comments	: When used for logical channel to MAC_d flow mapping dl_LogicalChannelMapping shall be chosen,
Type Definition	
<pre> SEQUENCE { logicalChannel_Mapping CHOICE { ul_LogicalChannelMapping SS_UL_LogicalChannelMapping, dl_LogicalChannelMapping SS_DL_LogicalChannelMapping }, rB_Identity INTEGER (-31..32) OPTIONAL, cn_DomainIdentity CN_DomainIdentity OPTIONAL } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: TrChConfigType
Encoding Variation	:
Comments	:
Type Definition	
<pre> CHOICE { nonDch NULL, dch ENUMERATED {normal(0), softHO(1)} } </pre>	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: UE_Info
Encoding Variation	:
Comments	: The value of c_RNTI_DSCH_RNTI is 16 bits, used either for C-RNTI or DSCH-RNTI. DSCH is configured if the physical channel in CMAC_config_REQ is a PDSCH. Otherwise, C-RNTI is applied. For MAC-hs configuration both u_RNTI and c_RNTI_DSCH_RNTI are omitted.
Type Definition	
SEQUENCE { u_RNTI U_RNTI OPTIONAL, c_RNTI C_RNTI OPTIONAL }	
Detailed Comments	:

ASN.1 Type Definition	
Type Name	: UE_OperationMode
Encoding Variation	:
Comments	:
Type Definition	
ENUMERATED { opModeA (0), opModeC (1) }	
Detailed Comments	:

ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
DL_DCCH_MessageType	DL-DCCH-MessageType	Class-definitions		
UL_DCCH_MessageType	UL-DCCH-MessageType	Class-definitions		
DL_CCCH_MessageType	DL-CCCH-MessageType	Class-definitions		
UL_CCCH_MessageType	UL-CCCH-MessageType	Class-definitions		
PCCH_MessageType	PCCH-MessageType	Class-definitions		
DL_SHCCH_MessageType	DL-SHCCH-MessageType	Class-definitions		
UL_SHCCH_MessageType	UL-SHCCH-MessageType	Class-definitions		
BCCH_FACH_MessageType	BCCH-FACH-MessageType	Class-definitions		
ActiveSetUpdate	ActiveSetUpdate	Class-definitions		
ActiveSetUpdate_r3_IEs	ActiveSetUpdate-r3-IEs	Class-definitions		
ActiveSetUpdate_v4b0ext_IEs	ActiveSetUpdate-v4b0ext-IEs	Class-definitions		
ActiveSetUpdate_v590ext_IEs	ActiveSetUpdate-v590ext-IEs	Class-definitions		
ActiveSetUpdateComplete	ActiveSetUpdateComplete	Class-definitions		
ActiveSetUpdateFailure	ActiveSetUpdateFailure	Class-definitions		
AssistanceDataDelivery	AssistanceDataDelivery	Class-definitions		
AssistanceDataDelivery_r3_IEs	AssistanceDataDelivery-r3-IEs	Class-definitions		
AssistanceDataDelivery_v3a0ext	AssistanceDataDelivery-v3a0ext	Class-definitions		
AssistanceDataDelivery_v4b0ext_IEs	AssistanceDataDelivery-v4b0ext-IEs	Class-definitions		
CellChangeOrderFromUTRAN	CellChangeOrderFromUTRAN	Class-definitions		
CellChangeOrderFromUTRAN_r3_IEs	CellChangeOrderFromUTRAN-r3-IEs	Class-definitions		
CellChangeOrderFromUTRAN_v590ext_IEs	CellChangeOrderFromUTRAN-v590ext-IEs	Class-definitions		
CellChangeOrderFromUTRANFailure	CellChangeOrderFromUTRANFailure	Class-definitions		
CellChangeOrderFromUTRANFailure_r3_IEs	CellChangeOrderFromUTRANFailure-r3-IEs	Class-definitions		
CellUpdate	CellUpdate	Class-definitions		
CellUpdate_v590ext	CellUpdate-v590ext	Class-definitions		
CellUpdateConfirm	CellUpdateConfirm	Class-definitions		
CellUpdateConfirm_r3_IEs	CellUpdateConfirm-r3-IEs	Class-definitions		
CellUpdateConfirm_v3a0ext	CellUpdateConfirm-v3a0ext	Class-definitions		
CellUpdateConfirm_v4b0ext_IEs	CellUpdateConfirm-v4b0ext-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
CellUpdateConfirm_v590ext_IEs	CellUpdateConfirm-v590ext-IEs	Class-definitions		
CellUpdateConfirm_r4_IEs	CellUpdateConfirm-r4-IEs	Class-definitions		
CellUpdateConfirm_r5_IEs	CellUpdateConfirm-r5-IEs	Class-definitions		
CellUpdateConfirm_CCH	CellUpdateConfirm-CCH	Class-definitions		
CounterCheck	CounterCheck	Class-definitions		
CounterCheck_r3_IEs	CounterCheck-r3-IEs	Class-definitions		
CounterCheckResponse	CounterCheckResponse	Class-definitions		
DownlinkDirectTransfer	DownlinkDirectTransfer	Class-definitions		
DownlinkDirectTransfer_r3_IEs	DownlinkDirectTransfer-r3-IEs	Class-definitions		
HandoverToUTRANComplete	HandoverToUTRANComplete	Class-definitions		
InitialDirectTransfer	InitialDirectTransfer	Class-definitions		
InitialDirectTransfer_v3a0ext	InitialDirectTransfer-v3a0ext	Class-definitions		
InitialDirectTransfer_v590ext	InitialDirectTransfer-v590ext	Class-definitions		
HandoverFromUTRANCommand_GSM	HandoverFromUTRANCommand-GSM	Class-definitions		
HandoverFromUTRANCommand_GSM_r3_IEs	HandoverFromUTRANCommand-GSM-r3-IEs	Class-definitions		
HandoverFromUTRANCommand_GERANlu	HandoverFromUTRANCommand-GERANlu	Class-definitions		
HandoverFromUTRANCommand_GERANlu_r5_IEs	HandoverFromUTRANCommand-GERANlu-r5-IEs	Class-definitions		
HandoverFromUTRANCommand_CDMA2000	HandoverFromUTRANCommand-CDMA2000	Class-definitions		
HandoverFromUTRANCommand_CDMA2000_r3_IEs	HandoverFromUTRANCommand-CDMA2000-r3-IEs	Class-definitions		
HandoverFromUTRANFailure	HandoverFromUTRANFailure	Class-definitions		
HandoverFromUtranFailure_v590ext_IEs	HandoverFromUtranFailure-v590ext-IEs	Class-definitions		
MeasurementControl	MeasurementControl	Class-definitions		
MeasurementControl_r3_IEs	MeasurementControl-r3-IEs	Class-definitions		
MeasurementControl_v390ext	MeasurementControl-v390ext	Class-definitions		
MeasurementControl_v3a0ext	MeasurementControl-v3a0ext	Class-definitions		
MeasurementControl_r4_IEs	MeasurementControl-r4-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
MeasurementControl_v590ext_IEs	MeasurementControl_v590ext-IEs	Class-definitions		
MeasurementControl_v5b0ext_IEs	MeasurementControl_v5b0ext-IEs	Class-definitions		
MeasurementControlFailure	MeasurementControlFailure	Class-definitions		
MeasurementControlFailure_v590ext_IEs	MeasurementControlFailure_v590ext-IEs	Class-definitions		
MeasurementReport	MeasurementReport	Class-definitions		
MeasurementReport_v390ext	MeasurementReport_v390ext	Class-definitions		
MeasurementReport_v4b0ext_IEs	MeasurementReport_v4b0ext-IEs	Class-definitions		
MeasurementReport_v590ext_IEs	MeasurementReport_v590ext-IEs	Class-definitions		
MeasurementReport_v5b0ext_IEs	MeasurementReport_v5b0ext-IEs	Class-definitions		
PagingType1	PagingType1	Class-definitions		
PagingType1_v590ext_IEs	PagingType1_v590ext-IEs	Class-definitions		
PagingType2	PagingType2	Class-definitions		
PhysicalChannelReconfiguration	PhysicalChannelReconfiguration	Class-definitions		
PhysicalChannelReconfiguration_r3_IEs	PhysicalChannelReconfiguration_r3-IEs	Class-definitions		
PhysicalChannelReconfiguration_v3a0ext	PhysicalChannelReconfiguration_v3a0ext	Class-definitions		
PhysicalChannelReconfiguration_v4b0ext_IEs	PhysicalChannelReconfiguration_v4b0ext-IEs	Class-definitions		
PhysicalChannelReconfiguration_v590ext_IEs	PhysicalChannelReconfiguration_v590ext-IEs	Class-definitions		
PhysicalChannelReconfiguration_r4_IEs	PhysicalChannelReconfiguration_r4-IEs	Class-definitions		
PhysicalChannelReconfiguration_r5_IEs	PhysicalChannelReconfiguration_r5-IEs	Class-definitions		
PhysicalChannelReconfigurationComplete	PhysicalChannelReconfigurationComplete	Class-definitions		
PhysicalChannelReconfigurationFailure	PhysicalChannelReconfigurationFailure	Class-definitions		
PhysicalSharedChannelAllocation	PhysicalSharedChannelAllocation	Class-definitions		
PhysicalSharedChannelAllocation_r3_IEs	PhysicalSharedChannelAllocation_r3-IEs	Class-definitions		
PhysicalSharedChannelAllocation_r4_IEs	PhysicalSharedChannelAllocation_r4-IEs	Class-definitions		
PUSCHCapacityRequest	PUSCHCapacityRequest	Class-definitions		
PUSCHCapacityRequest_v590ext	PUSCHCapacityRequest_v590ext	Class-definitions		
RadioBearerReconfiguration	RadioBearerReconfiguration	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
RadioBearerReconfiguration_r3_IEs	RadioBearerReconfiguration-r3-IEs	Class-definitions		
RadioBearerReconfiguration_v3a0ext	RadioBearerReconfiguration-v3a0ext	Class-definitions		
RadioBearerReconfiguration_v4b0ext_IEs	RadioBearerReconfiguration-v4b0ext-IEs	Class-definitions		
RadioBearerReconfiguration_v590ext_IEs	RadioBearerReconfiguration-v590ext-IEs	Class-definitions		
RadioBearerReconfiguration_r4_IEs	RadioBearerReconfiguration-r4-IEs	Class-definitions		
RadioBearerReconfiguration_r5_IEs	RadioBearerReconfiguration-r5-IEs	Class-definitions		
RadioBearerReconfigurationComplete	RadioBearerReconfigurationComplete	Class-definitions		
RadioBearerReconfigurationFailure	RadioBearerReconfigurationFailure	Class-definitions		
RadioBearerRelease	RadioBearerRelease	Class-definitions		
RadioBearerRelease_r3_IEs	RadioBearerRelease-r3-IEs	Class-definitions		
RadioBearerRelease_v3a0ext	RadioBearerRelease-v3a0ext	Class-definitions		
RadioBearerRelease_v4b0ext_IEs	RadioBearerRelease-v4b0ext-IEs	Class-definitions		
RadioBearerRelease_v590ext_IEs	RadioBearerRelease-v590ext-IEs	Class-definitions		
RadioBearerRelease_r4_IEs	RadioBearerRelease-r4-IEs	Class-definitions		
RadioBearerRelease_r5_IEs	RadioBearerRelease-r5-IEs	Class-definitions		
RadioBearerReleaseComplete	RadioBearerReleaseComplete	Class-definitions		
RadioBearerReleaseFailure	RadioBearerReleaseFailure	Class-definitions		
RadioBearerSetup	RadioBearerSetup	Class-definitions		
RadioBearerSetup_r3_IEs	RadioBearerSetup-r3-IEs	Class-definitions		
RadioBearerSetup_v3a0ext	RadioBearerSetup-v3a0ext	Class-definitions		
RadioBearerSetup_v4b0ext_IEs	RadioBearerSetup-v4b0ext-IEs	Class-definitions		
RadioBearerSetup_v590ext_IEs	RadioBearerSetup-v590ext-IEs	Class-definitions		
RadioBearerSetup_r4_IEs	RadioBearerSetup-r4-IEs	Class-definitions		
RadioBearerSetup_r5_IEs	RadioBearerSetup-r5-IEs	Class-definitions		
RadioBearerSetupComplete	RadioBearerSetupComplete	Class-definitions		
RadioBearerSetupFailure	RadioBearerSetupFailure	Class-definitions		
RRCCConnectionReject	RRCCConnectionReject	Class-definitions		
RRCCConnectionReject_r3_IEs	RRCCConnectionReject-r3-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
RRCCConnectionRelease	RRCCConnectionRelease	Class-definitions		
RRCCConnectionRelease_r3_IEs	RRCCConnectionRelease-r3-IEs	Class-definitions		
RRCCConnectionRelease_r4_IEs	RRCCConnectionRelease-r4-IEs	Class-definitions		
RRCCConnectionRelease_CCCH	RRCCConnectionRelease-CCCH	Class-definitions		
RRCCConnectionRelease_CCCH_r3_IEs	RRCCConnectionRelease-CCCH-r3-IEs	Class-definitions		
RRCCConnectionRelease_CCCH_r4_IEs	RRCCConnectionRelease-CCCH-r4-IEs	Class-definitions		
RRCCConnectionRelease_CCCH_r5_IEs	RRCCConnectionRelease-CCCH-r5-IEs	Class-definitions		
RRCCConnectionReleaseComplete	RRCCConnectionReleaseComplete	Class-definitions		
RRCCConnectionRequest	RRCCConnectionRequest	Class-definitions		
RRCCConnectionRequest_v3d0ext_IEs	RRCCConnectionRequest-v3d0ext-IEs	Class-definitions		
RRCCConnectionRequest_v4b0ext_IEs	RRCCConnectionRequest-v4b0ext-IEs	Class-definitions		
RRCCConnectionRequest_v590ext_IEs	RRCCConnectionRequest-v590ext-IEs	Class-definitions		
RRCCConnectionSetup	RRCCConnectionSetup	Class-definitions		
RRCCConnectionSetup_r3_IEs	RRCCConnectionSetup-r3-IEs	Class-definitions		
RRCCConnectionSetup_v4b0ext_IEs	RRCCConnectionSetup-v4b0ext-IEs	Class-definitions		
RRCCConnectionSetup_v590ext_IEs	RRCCConnectionSetup-v590ext-IEs	Class-definitions		
RRCCConnectionSetup_r4_IEs	RRCCConnectionSetup-r4-IEs	Class-definitions		
RRCCConnectionSetup_r5_IEs	RRCCConnectionSetup-r5-IEs	Class-definitions		
RRCCConnectionSetupComplete	RRCCConnectionSetupComplete	Class-definitions		
RRCCConnectionSetupComplete_v370ext	RRCCConnectionSetupComplete-v370ext	Class-definitions		
RRCCConnectionSetupComplete_v380ext_IEs	RRCCConnectionSetupComplete-v380ext-IEs	Class-definitions		
RRCCConnectionSetupComplete_v3a0ext_IEs	RRCCConnectionSetupComplete-v3a0ext-IEs	Class-definitions		
RRCCConnectionSetupComplete_v3g0ext_IEs	RRCCConnectionSetupComplete-v3g0ext-IEs	Class-definitions		
RRCCConnectionSetupComplete_v4b0ext_IEs	RRCCConnectionSetupComplete-v4b0ext-IEs	Class-definitions		
RRCCConnectionSetupComplete_v590ext_IEs	RRCCConnectionSetupComplete-v590ext-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
RRCStatus	RRCStatus	Class-definitions		
SecurityModeCommand	SecurityModeCommand	Class-definitions		
SecurityModeCommand_r3_IEs	SecurityModeCommand-r3-IEs	Class-definitions		
SecurityModeComplete	SecurityModeComplete	Class-definitions		
SecurityModeFailure	SecurityModeFailure	Class-definitions		
SignallingConnectionRelease	SignallingConnectionRelease	Class-definitions		
SignallingConnectionRelease_r3_IEs	SignallingConnectionRelease-r3-IEs	Class-definitions		
SignallingConnectionReleaseIndication	SignallingConnectionReleaseIndication	Class-definitions		
SystemInformation_BCH	SystemInformation-BCH	Class-definitions		
SystemInformation_FACH	SystemInformation-FACH	Class-definitions		
FirstSegment	FirstSegment	Class-definitions		
FirstSegmentShort	FirstSegmentShort	Class-definitions		
SubsequentSegment	SubsequentSegment	Class-definitions		
LastSegment	LastSegment	Class-definitions		
LastSegmentShort	LastSegmentShort	Class-definitions		
CompleteSIB_List	CompleteSIB-List	Class-definitions		
CompleteSIB	CompleteSIB	Class-definitions		
CompleteSIBshort	CompleteSIBshort	Class-definitions		
SystemInformationChangeIndication	SystemInformationChangeIndication	Class-definitions		
TransportChannelReconfiguration	TransportChannelReconfiguration	Class-definitions		
TransportChannelReconfiguration_r3_IEs	TransportChannelReconfiguration-r3-IEs	Class-definitions		
TransportChannelReconfiguration_v3a0ext	TransportChannelReconfiguration-v3a0ext	Class-definitions		
TransportChannelReconfiguration_v4b0ext_IEs	TransportChannelReconfiguration-v4b0ext-IEs	Class-definitions		
TransportChannelReconfiguration_v590ext_IEs	TransportChannelReconfiguration-v590ext-IEs	Class-definitions		
TransportChannelReconfiguration_r4_IEs	TransportChannelReconfiguration-r4-IEs	Class-definitions		
TransportChannelReconfiguration_r5_IEs	TransportChannelReconfiguration-r5-IEs	Class-definitions		
TransportChannelReconfigurationComplete	TransportChannelReconfigurationComplete	Class-definitions		
TransportChannelReconfigurationFailure	TransportChannelReconfigurationFailure	Class-definitions		
TransportFormatCombinationControl	TransportFormatCombinationControl	Class-definitions		
TransportFormatCombinationControlFailure	TransportFormatCombinationControlFailure	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UECapabilityEnquiry	UECapabilityEnquiry	Class-definitions		
UECapabilityEnquiry_r3_IEs	UECapabilityEnquiry-r3-IEs	Class-definitions		
UECapabilityEnquiry_v4b0ext_IEs	UECapabilityEnquiry-v4b0ext-IEs	Class-definitions		
UECapabilityEnquiry_v590ext_IEs	UECapabilityEnquiry-v590ext-IEs	Class-definitions		
UECapabilityInformation	UECapabilityInformation	Class-definitions		
UECapabilityInformation_v370ext	UECapabilityInformation-v370ext	Class-definitions		
UECapabilityInformation_v380ext_IEs	UECapabilityInformation-v380ext-IEs	Class-definitions		
UECapabilityInformation_v3a0ext_IEs	UECapabilityInformation-v3a0ext-IEs	Class-definitions		
UECapabilityInformation_v4b0ext	UECapabilityInformation-v4b0ext	Class-definitions		
UECapabilityInformation_v590ext	UECapabilityInformation-v590ext	Class-definitions		
UECapabilityInformationConfirm	UECapabilityInformationConfirm	Class-definitions		
UECapabilityInformationConfirm_r3_IEs	UECapabilityInformationConfirm-r3-IEs	Class-definitions		
UplinkDirectTransfer	UplinkDirectTransfer	Class-definitions		
UplinkPhysicalChannelControl	UplinkPhysicalChannelControl	Class-definitions		
UplinkPhysicalChannelControl_r3_IEs	UplinkPhysicalChannelControl-r3-IEs	Class-definitions		
UplinkPhysicalChannelControl_v4b0ext_IEs	UplinkPhysicalChannelControl-v4b0ext-IEs	Class-definitions		
UplinkPhysicalChannelControl_r4_IEs	UplinkPhysicalChannelControl-r4-IEs	Class-definitions		
UplinkPhysicalChannelControl_r5_IEs	UplinkPhysicalChannelControl-r5-IEs	Class-definitions		
URAUpdate	URAUpdate	Class-definitions		
URAUpdateConfirm	URAUpdateConfirm	Class-definitions		
URAUpdateConfirm_r3_IEs	URAUpdateConfirm-r3-IEs	Class-definitions		
URAUpdateConfirm_r5_IEs	URAUpdateConfirm-r5-IEs	Class-definitions		
URAUpdateConfirm_CCCH	URAUpdateConfirm-CCCH	Class-definitions		
URAUpdateConfirm_CCCH_r3_IEs	URAUpdateConfirm-CCCH-r3-IEs	Class-definitions		
UTRANMobilityInformation	UTRANMobilityInformation	Class-definitions		
UTRANMobilityInformation_r3_IEs	UTRANMobilityInformation-r3-IEs	Class-definitions		
UTRANMobilityInformation_v3a0ext_IEs	UTRANMobilityInformation-v3a0ext-IEs	Class-definitions		
UTRANMobilityInformation_r5_IEs	UTRANMobilityInformation-r5-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UTRANMobilityInformationConfirm	UTRANMobilityInformationConfirm	Class-definitions		
UTRANMobilityInformationFailure	UTRANMobilityInformationFailure	Class-definitions		
Ansi_41_IDNNS	Ansi-41-IDNNS	Class-definitions		
CN_DomainIdentity	CN-DomainIdentity	Class-definitions		
CN_DomainInformation	CN-DomainInformation	Class-definitions		
CN_DomainInformationFull	CN-DomainInformationFull	Class-definitions		
CN_DomainInformationList	CN-DomainInformationList	Class-definitions		
CN_DomainInformationListFull	CN-DomainInformationListFull	Class-definitions		
CN_DomainSysInfo	CN-DomainSysInfo	Class-definitions		
CN_DomainSysInfoList	CN-DomainSysInfoList	Class-definitions		
CN_InformationInfo	CN-InformationInfo	Class-definitions		
CN_InformationInfoFull	CN-InformationInfoFull	Class-definitions		
Digit	Digit	Class-definitions		
Gsm_map_IDNNS	Gsm-map-IDNNS	Class-definitions		
IMEI	IMEI	Class-definitions		
IMEI_Digit	IMEI-Digit	Class-definitions		
IMSI_GSM_MAP	IMSI-GSM-MAP	Class-definitions		
IntraDomainNasNodeSelector	IntraDomainNasNodeSelector	Class-definitions		
LAI	LAI	Class-definitions		
MCC	MCC	Class-definitions		
MNC	MNC	Class-definitions		
NAS_Message	NAS-Message	Class-definitions		
NAS_Synchronisation_Indicator	NAS-Synchronisation-Indicator	Class-definitions		
NAS_SystemInformationGSM_MAP	NAS-SystemInformationGSM-MAP	Class-definitions		
P_TMSI_GSM_MAP	P-TMSI-GSM-MAP	Class-definitions		
PagingRecordTypeID	PagingRecordTypeID	Class-definitions		
PLMN_Identity	PLMN-Identity	Class-definitions		
PLMN_Type	PLMN-Type	Class-definitions		
RAB_Identity	RAB-Identity	Class-definitions		
RAI	RAI	Class-definitions		
RoutingAreaCode	RoutingAreaCode	Class-definitions		
RoutingParameter	RoutingParameter	Class-definitions		
TMSI_GSM_MAP	TMSI-GSM-MAP	Class-definitions		
AccessClassBarred	AccessClassBarred	Class-definitions		
AccessClassBarredList	AccessClassBarredList	Class-definitions		
AllowedIndicator	AllowedIndicator	Class-definitions		
CellAccessRestriction	CellAccessRestriction	Class-definitions		
CellBarred	CellBarred	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
CellIdentity	CellIdentity	Class-definitions		
CellIdentity_PerRL_List	CellIdentity-PerRL-List	Class-definitions		
CellSelectReselectInfoSIB_3_4	CellSelectReselectInfoSIB-3-4	Class-definitions		
MapParameter	MapParameter	Class-definitions		
Mapping	Mapping	Class-definitions		
Mapping_LCR_r4	Mapping-LCR-r4	Class-definitions		
MappingFunctionParameter	MappingFunctionParameter	Class-definitions		
MappingFunctionParameterList	MappingFunctionParameterList	Class-definitions		
MappingFunctionType	MappingFunctionType	Class-definitions		
MappingInfo	MappingInfo	Class-definitions		
Q_Hyst_S	Q-Hyst-S	Class-definitions		
Q_Hyst_S_Fine	Q-Hyst-S-Fine	Class-definitions		
RAT	RAT	Class-definitions		
RAT_FDD_Info	RAT-FDD-Info	Class-definitions		
RAT_FDD_InfoList	RAT-FDD-InfoList	Class-definitions		
RAT_Identifier	RAT-Identifier	Class-definitions		
RAT_TDD_Info	RAT-TDD-Info	Class-definitions		
RAT_TDD_InfoList	RAT-TDD-InfoList	Class-definitions		
ReservedIndicator	ReservedIndicator	Class-definitions		
S_SearchQual	S-SearchQual	Class-definitions		
S_SearchRXLEV	S-SearchRXLEV	Class-definitions		
T_Barred	T-Barred	Class-definitions		
T_Reselection_S	T-Reselection-S	Class-definitions		
T_Reselection_S_Fine	T-Reselection-S-Fine	Class-definitions		
UpperLimit	UpperLimit	Class-definitions		
URA_Identity	URA-Identity	Class-definitions		
URA_IdentityList	URA-IdentityList	Class-definitions		
AccessStratumReleaseIndicator	AccessStratumReleaseIndicator	Class-definitions		
ActivationTime	ActivationTime	Class-definitions		
BackoffControlParameters	BackoffControlParameters	Class-definitions		
C_RNTI	C-RNTI	Class-definitions		
CapabilityUpdateRequirement	CapabilityUpdateRequirement	Class-definitions		
CapabilityUpdateRequirement_r4_ext	CapabilityUpdateRequirement-r4-ext	Class-definitions		
CapabilityUpdateRequirement_r4	CapabilityUpdateRequirement-r4	Class-definitions		
CellUpdateCause	CellUpdateCause	Class-definitions		
ChipRateCapability	ChipRateCapability	Class-definitions		
CipheringAlgorithm	CipheringAlgorithm	Class-definitions		
CipheringModeCommand	CipheringModeCommand	Class-definitions		
CipheringModelInfo	CipheringModelInfo	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
CN_DRX_CycleLengthCoefficient	CN-DRX-CycleLengthCoefficient	Class-definitions		
CN_PagedUE_Identity	CN-PagedUE-Identity	Class-definitions		
CompressedModeMeasureCapability	CompressedModeMeasureCapability	Class-definitions		
CompressedModeMeasureCapability_LCR_r4	CompressedModeMeasureCapability-LCR-r4	Class-definitions		
CompressedModeMeasureCapabilityFDDList	CompressedModeMeasureCapabilityFDDList	Class-definitions		
CompressedModeMeasureCapabilityFDD	CompressedModeMeasureCapabilityFDD	Class-definitions		
CompressedModeMeasureCapabilityTDDList	CompressedModeMeasureCapabilityTDDList	Class-definitions		
CompressedModeMeasureCapabilityTDD	CompressedModeMeasureCapabilityTDD	Class-definitions		
CompressedModeMeasureCapabilityGSMList	CompressedModeMeasureCapabilityGSMList	Class-definitions		
CompressedModeMeasureCapabilityGSM	CompressedModeMeasureCapabilityGSM	Class-definitions		
CompressedModeMeasureCapabilityMC	CompressedModeMeasureCapabilityMC	Class-definitions		
CPCH_Parameters	CPCH-Parameters	Class-definitions		
DL_CapabilityWithSimultaneousHS_DSCHConfig	DL-CapabilityWithSimultaneousHS-DSCHConfig	Class-definitions		
DL_DPCCH_BER	DL-DPCCH-BER	Class-definitions		
DL_PhysChCapabilityFDD	DL-PhysChCapabilityFDD	Class-definitions		
DL_PhysChCapabilityFDD_v380ext	DL-PhysChCapabilityFDD-v380ext	Class-definitions		
SupportOfDedicatedPilotForChEstimation	SupportOfDedicatedPilotForChEstimation	Class-definitions		
DL_PhysChCapabilityTDD	DL-PhysChCapabilityTDD	Class-definitions		
DL_PhysChCapabilityTDD_LCR_r4	DL-PhysChCapabilityTDD-LCR-r4	Class-definitions		
DL_TransChCapability	DL-TransChCapability	Class-definitions		
DRAC_SysInfo	DRAC-SysInfo	Class-definitions		
DRAC_SysInfoList	DRAC-SysInfoList	Class-definitions		
DSCH_RNTI	DSCH-RNTI	Class-definitions		
ESN_DS_41	ESN-DS-41	Class-definitions		
EstablishmentCause	EstablishmentCause	Class-definitions		
FailureCauseWithProtErr	FailureCauseWithProtErr	Class-definitions		
FailureCauseWithProtErrTrId	FailureCauseWithProtErrTrId	Class-definitions		
GroupIdentityWithReleaseInformation	GroupIdentityWithReleaseInformation	Class-definitions		
GroupReleaseInformation	GroupReleaseInformation	Class-definitions		
GSM_Measurements	GSM-Measurements	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
H_RNTI	H-RNTI	Class-definitions		
HSDSCH_physical_layer_category	HSDSCH-physical-layer-category	Class-definitions		
UESpecificBehaviourInformation1idle	UESpecificBehaviourInformation1idle	Class-definitions		
IMSI_and_ESN_DS_41	IMSI-and-ESN-DS-41	Class-definitions		
IMSI_DS_41	IMSI-DS-41	Class-definitions		
InitialPriorityDelayList	InitialPriorityDelayList	Class-definitions		
InitialUE_Identity	InitialUE-Identity	Class-definitions		
IntegrityCheckInfo	IntegrityCheckInfo	Class-definitions		
IntegrityProtActivationInfo	IntegrityProtActivationInfo	Class-definitions		
IntegrityProtectionAlgorithm	IntegrityProtectionAlgorithm	Class-definitions		
IntegrityProtectionModeCommand	IntegrityProtectionModeCommand	Class-definitions		
IntegrityProtectionModeInfo	IntegrityProtectionModeInfo	Class-definitions		
IntegrityProtInitNumber	IntegrityProtInitNumber	Class-definitions		
MaxHcContextSpace	MaxHcContextSpace	Class-definitions		
MaxHcContextSpace_r5_ext	MaxHcContextSpace-r5-ext	Class-definitions		
MaxROHC_ContextSessions_r4	MaxROHC-ContextSessions-r4	Class-definitions		
MaximumAM_EntityNumberRLC_Cap	MaximumAM-EntityNumberRLC-Cap	Class-definitions		
MaximumBitRate	MaximumBitRate	Class-definitions		
MaximumRLC_WindowSize	MaximumRLC-WindowSize	Class-definitions		
MaxNoDPDCH_BitsTransmitted	MaxNoDPDCH-BitsTransmitted	Class-definitions		
MaxNoBits	MaxNoBits	Class-definitions		
MaxNoPhysChBitsReceived	MaxNoPhysChBitsReceived	Class-definitions		
MaxNoSCCPCH_RL	MaxNoSCCPCH-RL	Class-definitions		
MaxNumberOfTF	MaxNumberOfTF	Class-definitions		
MaxNumberOfTFC_DL	MaxNumberOfTFC-DL	Class-definitions		
MaxNumberOfTFC_UL	MaxNumberOfTFC-UL	Class-definitions		
MaxPhysChPerFrame	MaxPhysChPerFrame	Class-definitions		
MaxPhysChPerSubFrame_r4	MaxPhysChPerSubFrame-r4	Class-definitions		
MaxPhysChPerTimeslot	MaxPhysChPerTimeslot	Class-definitions		
MaxPhysChPerTS	MaxPhysChPerTS	Class-definitions		
MaxSimultaneousCCTrCH_Count	MaxSimultaneousCCTrCH-Count	Class-definitions		
MaxSimultaneousTransChsDL	MaxSimultaneousTransChsDL	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
MaxSimultaneousTransChsUL	MaxSimultaneousTransChsUL	Class-definitions		
MaxTransportBlocksDL	MaxTransportBlocksDL	Class-definitions		
MaxTransportBlocksUL	MaxTransportBlocksUL	Class-definitions		
MaxTS_PerFrame	MaxTS-PerFrame	Class-definitions		
MaxTS_PerSubFrame_r4	MaxTS-PerSubFrame-r4	Class-definitions		
MeasurementCapability	MeasurementCapability	Class-definitions		
MeasurementCapabilityExt	MeasurementCapabilityExt	Class-definitions		
MeasurementCapability_r4_ext	MeasurementCapability-r4-ext	Class-definitions		
MessageAuthenticationCode	MessageAuthenticationCode	Class-definitions		
MinimumSF_DL	MinimumSF-DL	Class-definitions		
MinimumSF_UL	MinimumSF-UL	Class-definitions		
MultiModeCapability	MultiModeCapability	Class-definitions		
MultiRAT_Capability	MultiRAT-Capability	Class-definitions		
MultiModeRAT_Capability_v590ext	MultiModeRAT-Capability-v590ext	Class-definitions		
N_300	N-300	Class-definitions		
N_301	N-301	Class-definitions		
N_302	N-302	Class-definitions		
N_304	N-304	Class-definitions		
N_308	N-308	Class-definitions		
N_310	N-310	Class-definitions		
N_312	N-312	Class-definitions		
N_312ext	N-312ext	Class-definitions		
N_312_r5	N-312-r5	Class-definitions		
N_313	N-313	Class-definitions		
N_315	N-315	Class-definitions		
N_315ext	N-315ext	Class-definitions		
N_315_r5	N-315-r5	Class-definitions		
N_AccessFails	N-AccessFails	Class-definitions		
N_AP_RetransMax	N-AP-RetransMax	Class-definitions		
NetworkAssistedGPS_Supported	NetworkAssistedGPS-Supported	Class-definitions		
NF_BO_AllBusy	NF-BO-AllBusy	Class-definitions		
NF_BO_NoAICH	NF-BO-NoAICH	Class-definitions		
NF_BO_Mismatch	NF-BO-Mismatch	Class-definitions		
NS_BO_Busy	NS-BO-Busy	Class-definitions		
NS_IP	NS-IP	Class-definitions		
P_TMSI_and_RAI_GSM_MAP	P-TMSI-and-RAI-GSM-MAP	Class-definitions		
PagingCause	PagingCause	Class-definitions		
PagingRecord	PagingRecord	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
PagingRecord2_r5	PagingRecord2-r5	Class-definitions		
PagingRecordList	PagingRecordList	Class-definitions		
PagingRecord2List_r5	PagingRecord2List-r5	Class-definitions		
PDCP_Capability	PDCP-Capability	Class-definitions		
PDCP_Capability_r4_ext	PDCP-Capability-r4-ext	Class-definitions		
PDCP_Capability_r5_ext	PDCP-Capability-r5-ext	Class-definitions		
PhysicalChannelCapability	PhysicalChannelCapability	Class-definitions		
PhysicalChannelCapability_LCR_r4	PhysicalChannelCapability-LCR-r4	Class-definitions		
PhysicalChannelCapability_hspdsch_r5	PhysicalChannelCapability-hspdsch-r5	Class-definitions		
PNBSCH_Allocation_r4	PNBSCH-Allocation-r4	Class-definitions		
ProtocolErrorCause	ProtocolErrorCause	Class-definitions		
ProtocolErrorIndicator	ProtocolErrorIndicator	Class-definitions		
ProtocolErrorIndicatorWithMoreInfo	ProtocolErrorIndicatorWithMoreInfo	Class-definitions		
ProtocolErrorMoreInformation	ProtocolErrorMoreInformation	Class-definitions		
RadioFrequencyBandFDD	RadioFrequencyBandFDD	Class-definitions		
RadioFrequencyBandTDDList	RadioFrequencyBandTDDList	Class-definitions		
RadioFrequencyBandTDD	RadioFrequencyBandTDD	Class-definitions		
RadioFrequencyBandGSM	RadioFrequencyBandGSM	Class-definitions		
Rb_timer_indicator	Rb-timer-indicator	Class-definitions		
Re_EstablishmentTimer	Re-EstablishmentTimer	Class-definitions		
RedirectionInfo	RedirectionInfo	Class-definitions		
RejectionCause	RejectionCause	Class-definitions		
ReleaseCause	ReleaseCause	Class-definitions		
RF_Capability	RF-Capability	Class-definitions		
RF_Capability_r4_ext	RF-Capability-r4-ext	Class-definitions		
RLC_Capability	RLC-Capability	Class-definitions		
RLC_Capability_r5_ext	RLC-Capability-r5-ext	Class-definitions		
RRC_ConnectionReleaseInformation	RRC-ConnectionReleaseInformation	Class-definitions		
RRC_MessageSequenceNumber	RRC-MessageSequenceNumber	Class-definitions		
RRC_MessageSequenceNumberList	RRC-MessageSequenceNumberList	Class-definitions		
RRC_StateIndicator	RRC-StateIndicator	Class-definitions		
RRC_TransactionIdentifier	RRC-TransactionIdentifier	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
S_RNTI	S-RNTI	Class-definitions		
SecurityCapability	SecurityCapability	Class-definitions		
SimultaneousSCCPC H_DPCH_Reception	SimultaneousSCCPC H-DPCH-Reception	Class-definitions		
SRNC_Identity	SRNC-Identity	Class-definitions		
START_Value	START-Value	Class-definitions		
STARTList	STARTList	Class-definitions		
STARTSingle	STARTSingle	Class-definitions		
CapabilityUpdateRequi rement_r5	CapabilityUpdateRequi rement-r5	Class-definitions		
SystemSpecificCapUp dateReq	SystemSpecificCapUp dateReq	Class-definitions		
SystemSpecificCapUp dateReq_v590ext	SystemSpecificCapUp dateReq-v590ext	Class-definitions		
SystemSpecificCapUp dateReq_r5	SystemSpecificCapUp dateReq-r5	Class-definitions		
SystemSpecificCapUp dateReqList	SystemSpecificCapUp dateReqList	Class-definitions		
SystemSpecificCapUp dateReqList_r5	SystemSpecificCapUp dateReqList-r5	Class-definitions		
T_300	T-300	Class-definitions		
T_301	T-301	Class-definitions		
T_302	T-302	Class-definitions		
T_304	T-304	Class-definitions		
T_305	T-305	Class-definitions		
T_307	T-307	Class-definitions		
T_308	T-308	Class-definitions		
T_309	T-309	Class-definitions		
T_310	T-310	Class-definitions		
T_311	T-311	Class-definitions		
T_312	T-312	Class-definitions		
T_313	T-313	Class-definitions		
T_314	T-314	Class-definitions		
T_315	T-315	Class-definitions		
T_316	T-316	Class-definitions		
T_317	T-317	Class-definitions		
T_CPCH	T-CPCH	Class-definitions		
TMSI_and_LAI_GSM_ MAP	TMSI-and-LAI-GSM -MAP	Class-definitions		
TMSI_DS_41	TMSI-DS-41	Class-definitions		
TotalRLC_AM_Buffer Size	TotalRLC-AM-Buffer Size	Class-definitions		
TotalRLC_AM_Buffer Size_r5_ext	TotalRLC-AM-Buffer Size-r5-ext	Class-definitions		
TransmissionProbabilit y	TransmissionProbabilit y	Class-definitions		
TransportChannelCap ability	TransportChannelCap ability	Class-definitions		
TurboSupport	TurboSupport	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
TxRxFrequencySeparation	TxRxFrequencySeparation	Class-definitions		
U_RNTI	U-RNTI	Class-definitions		
U_RNTI_Group	U-RNTI-Group	Class-definitions		
UE_ConnTimersAndConstants	UE-ConnTimersAndConstants	Class-definitions		
UE_ConnTimersAndConstants_v3a0ext	UE-ConnTimersAndConstants-v3a0ext	Class-definitions		
UE_ConnTimersAndConstants_r5	UE-ConnTimersAndConstants-r5	Class-definitions		
UE_IdleTimersAndConstants	UE-IdleTimersAndConstants	Class-definitions		
UE_IdleTimersAndConstants_v3a0ext	UE-IdleTimersAndConstants-v3a0ext	Class-definitions		
UE_MultiModeRAT_Capability	UE-MultiModeRAT-Capability	Class-definitions		
UE_PowerClass	UE-PowerClass	Class-definitions		
UE_PowerClassExt	UE-PowerClassExt	Class-definitions		
UE_RadioAccessCapability	UE-RadioAccessCapability	Class-definitions		
UE_RadioAccessCapability_v370ext	UE-RadioAccessCapability-v370ext	Class-definitions		
UE_RadioAccessCapability_v380ext	UE-RadioAccessCapability-v380ext	Class-definitions		
UE_RadioAccessCapability_v3a0ext	UE-RadioAccessCapability-v3a0ext	Class-definitions		
UE_RadioAccessCapability_v3g0ext	UE-RadioAccessCapability-v3g0ext	Class-definitions		
UE_PositioningCapabilityExt_v380	UE-PositioningCapabilityExt-v380	Class-definitions		
UE_PositioningCapabilityExt_v3a0	UE-PositioningCapabilityExt-v3a0	Class-definitions		
UE_PositioningCapabilityExt_v3g0	UE-PositioningCapabilityExt-v3g0	Class-definitions		
UE_RadioAccessCapabilityBandFDDList	UE-RadioAccessCapabilityBandFDDList	Class-definitions		
UE_RadioAccessCapabilityBandFDD	UE-RadioAccessCapabilityBandFDD	Class-definitions		
UE_RadioAccessCapability_v4b0ext	UE-RadioAccessCapability-v4b0ext	Class-definitions		
UE_RadioAccessCapability_v590ext	UE-RadioAccessCapability-v590ext	Class-definitions		
UL_PhysChCapabilityFDD	UL-PhysChCapabilityFDD	Class-definitions		
UL_PhysChCapabilityTDD	UL-PhysChCapabilityTDD	Class-definitions		
UL_PhysChCapabilityTDD_LCR_r4	UL-PhysChCapabilityTDD-LCR-r4	Class-definitions		
UL_TransChCapability	UL-TransChCapability	Class-definitions		
UE_Positioning_Capability	UE-Positioning-Capability	Class-definitions		
URA_UpdateCause	URA-UpdateCause	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UTRAN_DRX_CycleLengthCoefficient	UTRAN-DRX-CycleLengthCoefficient	Class-definitions		
WaitTime	WaitTime	Class-definitions		
AlgorithmSpecificInfo	AlgorithmSpecificInfo	Class-definitions		
AlgorithmSpecificInfo_r4	AlgorithmSpecificInfo-r4	Class-definitions		
CID_InclusionInfo_r4	CID-InclusionInfo-r4	Class-definitions		
COUNT_C	COUNT-C	Class-definitions		
COUNT_C_MSB	COUNT-C-MSB	Class-definitions		
DefaultConfigIdentity_r5	DefaultConfigIdentity-r5	Class-definitions		
DefaultConfigMode	DefaultConfigMode	Class-definitions		
DL_AM_RLC_Mode	DL-AM-RLC-Mode	Class-definitions		
DL_AM_RLC_Mode_r5	DL-AM-RLC-Mode-r5	Class-definitions		
DL_CounterSynchronisationInfo	DL-CounterSynchronisationInfo	Class-definitions		
DL_CounterSynchronisationInfo_r5	DL-CounterSynchronisationInfo-r5	Class-definitions		
DL_LogicalChannelMapping	DL-LogicalChannelMapping	Class-definitions		
DL_LogicalChannelMapping_r5	DL-LogicalChannelMapping-r5	Class-definitions		
DL_LogicalChannelMappingList	DL-LogicalChannelMappingList	Class-definitions		
DL_LogicalChannelMappingList_r5	DL-LogicalChannelMappingList-r5	Class-definitions		
DL_RFC3095_r4	DL-RFC3095-r4	Class-definitions		
DL_RLC_Mode	DL-RLC-Mode	Class-definitions		
DL_RLC_Mode_r5	DL-RLC-Mode-r5	Class-definitions		
DL_RLC_StatusInfo	DL-RLC-StatusInfo	Class-definitions		
DL_TM_RLC_Mode	DL-TM-RLC-Mode	Class-definitions		
DL_TransportChannelType	DL-TransportChannelType	Class-definitions		
DL_TransportChannelType_r5	DL-TransportChannelType-r5	Class-definitions		
DL_UM_RLC_LI_size	DL-UM-RLC-LI-size	Class-definitions		
DL_UM_RLC_Mode_r5	DL-UM-RLC-Mode-r5	Class-definitions		
ExpectReordering	ExpectReordering	Class-definitions		
ExplicitDiscard	ExplicitDiscard	Class-definitions		
HeaderCompressionInfo	HeaderCompressionInfo	Class-definitions		
HeaderCompressionInfoList	HeaderCompressionInfoList	Class-definitions		
HeaderCompressionInfo_r4	HeaderCompressionInfo-r4	Class-definitions		
HeaderCompressionInfoList_r4	HeaderCompressionInfoList-r4	Class-definitions		
LogicalChannelIdentity	LogicalChannelIdentity	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
LosslessSRNS_Reloc Support	LosslessSRNS-Reloc Support	Class-definitions		
MAC_d_HFN_initial_value	MAC-d-HFN-initial-value	Class-definitions		
MAC_LogicalChannel Priority	MAC-LogicalChannel Priority	Class-definitions		
MaxDAT	MaxDAT	Class-definitions		
MaxDAT_Retransmissions	MaxDAT-Retransmissions	Class-definitions		
MaxMRW	MaxMRW	Class-definitions		
MaxPDCP_SN_WindowSize	MaxPDCP-SN-WindowSize	Class-definitions		
MaxRST	MaxRST	Class-definitions		
NoExplicitDiscard	NoExplicitDiscard	Class-definitions		
PDCP_Info	PDCP-Info	Class-definitions		
PDCP_Info_r4	PDCP-Info-r4	Class-definitions		
PDCP_InfoReconfig	PDCP-InfoReconfig	Class-definitions		
PDCP_InfoReconfig_r4	PDCP-InfoReconfig-r4	Class-definitions		
PDCP_PDU_Header	PDCP-PDU-Header	Class-definitions		
PDCP_SN_Info	PDCP-SN-Info	Class-definitions		
Poll_PDU	Poll-PDU	Class-definitions		
Poll_SDU	Poll-SDU	Class-definitions		
PollingInfo	PollingInfo	Class-definitions		
PollWindow	PollWindow	Class-definitions		
PredefinedConfigIdentity	PredefinedConfigIdentity	Class-definitions		
PredefinedConfigValueTag	PredefinedConfigValueTag	Class-definitions		
PredefinedRB_Configuration	PredefinedRB-Configuration	Class-definitions		
PreDefRadioConfiguration	PreDefRadioConfiguration	Class-definitions		
RAB_Info	RAB-Info	Class-definitions		
RAB_InformationList	RAB-InformationList	Class-definitions		
RAB_InformationReconfigList	RAB-InformationReconfigList	Class-definitions		
RAB_InformationReconfig	RAB-InformationReconfig	Class-definitions		
RAB_InformationSetup	RAB-InformationSetup	Class-definitions		
RAB_InformationSetup_r4	RAB-InformationSetup-r4	Class-definitions		
RAB_InformationSetup_r5	RAB-InformationSetup-r5	Class-definitions		
RAB_InformationSetupList	RAB-InformationSetupList	Class-definitions		
RAB_InformationSetupList_r4	RAB-InformationSetupList-r4	Class-definitions		
RAB_InformationSetupList_r5	RAB-InformationSetupList-r5	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
RB_ActivationTimeInfo	RB-ActivationTimeInfo	Class-definitions		
RB_ActivationTimeInfoList	RB-ActivationTimeInfoList	Class-definitions		
RB_COUNT_C_Information	RB-COUNT-C-Information	Class-definitions		
RB_COUNT_C_InformationList	RB-COUNT-C-InformationList	Class-definitions		
RB_COUNT_C_MSB_Information	RB-COUNT-C-MSB-Information	Class-definitions		
RB_COUNT_C_MSB_InformationList	RB-COUNT-C-MSB-InformationList	Class-definitions		
RB_Identity	RB-Identity	Class-definitions		
RB_IdentityList	RB-IdentityList	Class-definitions		
RB_InformationAffected	RB-InformationAffected	Class-definitions		
RB_InformationAffected_r5	RB-InformationAffected-r5	Class-definitions		
RB_InformationAffectedList	RB-InformationAffectedList	Class-definitions		
RB_InformationAffectedList_r5	RB-InformationAffectedList-r5	Class-definitions		
RB_InformationReconfig	RB-InformationReconfig	Class-definitions		
RB_InformationReconfig_r4	RB-InformationReconfig-r4	Class-definitions		
RB_InformationReconfig_r5	RB-InformationReconfig-r5	Class-definitions		
RB_InformationReconfigList	RB-InformationReconfigList	Class-definitions		
RB_InformationReconfigList_r4	RB-InformationReconfigList-r4	Class-definitions		
RB_InformationReconfigList_r5	RB-InformationReconfigList-r5	Class-definitions		
RB_InformationReleaseList	RB-InformationReleaseList	Class-definitions		
RB_InformationSetup	RB-InformationSetup	Class-definitions		
RB_InformationSetup_r4	RB-InformationSetup-r4	Class-definitions		
RB_InformationSetup_r5	RB-InformationSetup-r5	Class-definitions		
RB_InformationSetupList	RB-InformationSetupList	Class-definitions		
RB_InformationSetupList_r4	RB-InformationSetupList-r4	Class-definitions		
RB_InformationSetupList_r5	RB-InformationSetupList-r5	Class-definitions		
RB_MappingInfo	RB-MappingInfo	Class-definitions		
RB_MappingInfo_r5	RB-MappingInfo-r5	Class-definitions		
RB_MappingOption	RB-MappingOption	Class-definitions		
RB_MappingOption_r5	RB-MappingOption-r5	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
RB_PDCPContextRelocation	RB-PDCPContextRelocation	Class-definitions		
RB_PDCPContextRelocationList	RB-PDCPContextRelocationList	Class-definitions		
RB_StopContinue	RB-StopContinue	Class-definitions		
RB_WithPDCP_Info	RB-WithPDCP-Info	Class-definitions		
RB_WithPDCP_InfoList	RB-WithPDCP-InfoList	Class-definitions		
ReceivingWindowSize	ReceivingWindowSize	Class-definitions		
RFC2507_Info	RFC2507-Info	Class-definitions		
RFC3095_Info_r4	RFC3095-Info-r4	Class-definitions		
RLC_Info	RLC-Info	Class-definitions		
RLC_Info_r5	RLC-Info-r5	Class-definitions		
RLC_InfoChoice	RLC-InfoChoice	Class-definitions		
RLC_InfoChoice_r5	RLC-InfoChoice-r5	Class-definitions		
RLC_SequenceNumber	RLC-SequenceNumber	Class-definitions		
RLC_SizeInfo	RLC-SizeInfo	Class-definitions		
RLC_SizeExplicitList	RLC-SizeExplicitList	Class-definitions		
ROHC_Profile_r4	ROHC-Profile-r4	Class-definitions		
ROHC_ProfileList_r4	ROHC-ProfileList-r4	Class-definitions		
ROHC_PacketSize_r4	ROHC-PacketSize-r4	Class-definitions		
ROHC_PacketSizeList_r4	ROHC-PacketSizeList-r4	Class-definitions		
SRB_InformationSetup	SRB-InformationSetup	Class-definitions		
SRB_InformationSetup_r5	SRB-InformationSetup-r5	Class-definitions		
SRB_InformationSetupList	SRB-InformationSetupList	Class-definitions		
SRB_InformationSetupList_r5	SRB-InformationSetupList-r5	Class-definitions		
SRB_InformationSetupList2	SRB-InformationSetupList2	Class-definitions		
TimerDiscard	TimerDiscard	Class-definitions		
TimerEPC	TimerEPC	Class-definitions		
TimerMRW	TimerMRW	Class-definitions		
TimerPoll	TimerPoll	Class-definitions		
TimerPollPeriodic	TimerPollPeriodic	Class-definitions		
TimerPollProhibit	TimerPollProhibit	Class-definitions		
TimerRST	TimerRST	Class-definitions		
TimerStatusPeriodic	TimerStatusPeriodic	Class-definitions		
TimerStatusProhibit	TimerStatusProhibit	Class-definitions		
TransmissionRLC_Discard	TransmissionRLC-Discard	Class-definitions		
TransmissionWindowSize	TransmissionWindowSize	Class-definitions		
UL_AM_RLC_Mode	UL-AM-RLC-Mode	Class-definitions		
UL_CounterSynchronisationInfo	UL-CounterSynchronisationInfo	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UL_LogicalChannelMapping	UL-LogicalChannelMapping	Class-definitions		
UL_LogicalChannelMappingList	UL-LogicalChannelMappingList	Class-definitions		
UL_LogicalChannelMappings	UL-LogicalChannelMappings	Class-definitions		
UL_RFC3095_r4	UL-RFC3095-r4	Class-definitions		
UL_RLC_Mode	UL-RLC-Mode	Class-definitions		
UL_TM_RLC_Mode	UL-TM-RLC-Mode	Class-definitions		
UL_UM_RLC_Mode	UL-UM-RLC-Mode	Class-definitions		
UL_TransportChannelType	UL-TransportChannelType	Class-definitions		
AddOrReconfMAC_dFlow	AddOrReconfMAC-dFlow	Class-definitions		
AllowedTFC_List	AllowedTFC-List	Class-definitions		
AllowedTFI_List	AllowedTFI-List	Class-definitions		
BitModeRLC_SizeInfo	BitModeRLC-SizeInfo	Class-definitions		
BLER_QualityValue	BLER-QualityValue	Class-definitions		
ChannelCodingType	ChannelCodingType	Class-definitions		
CodingRate	CodingRate	Class-definitions		
CommonDynamicTF_Info	CommonDynamicTF-Info	Class-definitions		
CommonDynamicTF_Info_DynamicTTI	CommonDynamicTF-Info-DynamicTTI	Class-definitions		
CommonDynamicTF_InfoList	CommonDynamicTF-InfoList	Class-definitions		
CommonDynamicTF_InfoList_DynamicTTI	CommonDynamicTF-InfoList-DynamicTTI	Class-definitions		
CommonTransChTFS	CommonTransChTFS	Class-definitions		
CommonTransChTFS_LCR	CommonTransChTFS-LCR	Class-definitions		
CPCH_SetID	CPCH-SetID	Class-definitions		
CRC_Size	CRC-Size	Class-definitions		
DedicatedDynamicTF_Info	DedicatedDynamicTF-Info	Class-definitions		
DedicatedDynamicTF_Info_DynamicTTI	DedicatedDynamicTF-Info-DynamicTTI	Class-definitions		
DedicatedDynamicTF_InfoList	DedicatedDynamicTF-InfoList	Class-definitions		
DedicatedDynamicTF_InfoList_DynamicTTI	DedicatedDynamicTF-InfoList-DynamicTTI	Class-definitions		
DedicatedTransChTFS	DedicatedTransChTFS	Class-definitions		
DL_AddReconfTransChInfo2List	DL-AddReconfTransChInfo2List	Class-definitions		
DL_AddReconfTransChInfoList	DL-AddReconfTransChInfoList	Class-definitions		
DL_AddReconfTransChInfoList_r4	DL-AddReconfTransChInfoList-r4	Class-definitions		
DL_AddReconfTransChInfoList_r5	DL-AddReconfTransChInfoList-r5	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
DL_AddReconfTransChInformation	DL-AddReconfTransChInformation	Class-definitions		
DL_AddReconfTransChInformation_r4	DL-AddReconfTransChInformation-r4	Class-definitions		
DL_AddReconfTransChInformation_r5	DL-AddReconfTransChInformation-r5	Class-definitions		
DL_AddReconfTransChInformation2	DL-AddReconfTransChInformation2	Class-definitions		
DL_CommonTransChInfo	DL-CommonTransChInfo	Class-definitions		
DL_CommonTransChInfo_r4	DL-CommonTransChInfo-r4	Class-definitions		
DL_DeletedTransChInfoList	DL-DeletedTransChInfoList	Class-definitions		
DL_DeletedTransChInfoList_r5	DL-DeletedTransChInfoList-r5	Class-definitions		
DL_TransportChannelIdentity	DL-TransportChannelIdentity	Class-definitions		
DL_TransportChannelIdentity_r5	DL-TransportChannelIdentity-r5	Class-definitions		
DL_TrCH_Type	DL-TrCH-Type	Class-definitions		
DL_TrCH_TypeId1_r5	DL-TrCH-TypeId1-r5	Class-definitions		
DL_TrCH_TypeId2_r5	DL-TrCH-TypeId2-r5	Class-definitions		
DRAC_ClassIdentity	DRAC-ClassIdentity	Class-definitions		
DRAC_StaticInformation	DRAC-StaticInformation	Class-definitions		
DRAC_StaticInformationList	DRAC-StaticInformationList	Class-definitions		
ExplicitTFCS_Configuration	ExplicitTFCS-Configuration	Class-definitions		
GainFactor	GainFactor	Class-definitions		
GainFactorInformation	GainFactorInformation	Class-definitions		
HSDSCH_Info	HSDSCH-Info	Class-definitions		
HARQ_Info	HARQ-Info	Class-definitions		
HARQMemorySize	HARQMemorySize	Class-definitions		
IndividualDL_CCTrCH_Info	IndividualDL-CCTrCH-Info	Class-definitions		
IndividualDL_CCTrCH_InfoList	IndividualDL-CCTrCH-InfoList	Class-definitions		
IndividualUL_CCTrCH_Info	IndividualUL-CCTrCH-Info	Class-definitions		
IndividualUL_CCTrCH_InfoList	IndividualUL-CCTrCH-InfoList	Class-definitions		
LogicalChannelByRB	LogicalChannelByRB	Class-definitions		
LogicalChannelList	LogicalChannelList	Class-definitions		
MAC_d_FlowIdentityDCHandHSDSCH	MAC-d-FlowIdentityDCHandHSDSCH	Class-definitions		
MAC_d_FlowIdentity	MAC-d-FlowIdentity	Class-definitions		
MAC_d_PDU_SizeInfo_List	MAC-d-PDU-SizeInfo-List	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
MAC_d_PDUsSizeInfo	MAC-d-PDUsSizeInfo	Class-definitions		
MAC_hs_AddReconfQueue_List	MAC-hs-AddReconfQueue-List	Class-definitions		
MAC_hs_AddReconfQueue	MAC-hs-AddReconfQueue	Class-definitions		
MAC_hs_DelQueue_List	MAC-hs-DelQueue-List	Class-definitions		
MAC_hs_DelQueue	MAC-hs-DelQueue	Class-definitions		
MAC_hs_WindowSize	MAC-hs-WindowSize	Class-definitions		
NumberOfTbSizeAndTList	NumberOfTbSizeAndTList	Class-definitions		
MessType	MessType	Class-definitions		
Non_allowedTFC_List	Non-allowedTFC-List	Class-definitions		
NumberOfTransportBlocks	NumberOfTransportBlocks	Class-definitions		
OctetModeRLC_SizeInfoType1	OctetModeRLC-SizeInfoType1	Class-definitions		
OctetModeRLC_SizeInfoType2	OctetModeRLC-SizeInfoType2	Class-definitions		
PowerOffsetInformation	PowerOffsetInformation	Class-definitions		
PowerOffsetPp_m	PowerOffsetPp-m	Class-definitions		
PreDefTransChConfiguration	PreDefTransChConfiguration	Class-definitions		
QualityTarget	QualityTarget	Class-definitions		
RateMatchingAttribute	RateMatchingAttribute	Class-definitions		
ReferenceTFC_ID	ReferenceTFC-ID	Class-definitions		
RestrictedTrChInfo	RestrictedTrChInfo	Class-definitions		
RestrictedTrChInfoList	RestrictedTrChInfoList	Class-definitions		
SemistaticTF_Information	SemistaticTF-Information	Class-definitions		
SignalledGainFactors	SignalledGainFactors	Class-definitions		
SplitTFCI_Signalling	SplitTFCI-Signalling	Class-definitions		
SplitType	SplitType	Class-definitions		
T1_ReleaseTimer	T1-ReleaseTimer	Class-definitions		
TFC_Subset	TFC-Subset	Class-definitions		
TFC_SubsetList	TFC-SubsetList	Class-definitions		
TFC_Value	TFC-Value	Class-definitions		
TFCI_Field2_Information	TFCI-Field2-Information	Class-definitions		
TFCI_Range	TFCI-Range	Class-definitions		
TFCI_RangeList	TFCI-RangeList	Class-definitions		
TFCS	TFCS	Class-definitions		
TFCS_Identity	TFCS-Identity	Class-definitions		
TFCS_IdentityPlain	TFCS-IdentityPlain	Class-definitions		
TFCS_InfoForDSCH	TFCS-InfoForDSCH	Class-definitions		
TFCS_ReconfAdd	TFCS-ReconfAdd	Class-definitions		
TFCS_Removal	TFCS-Removal	Class-definitions		
TFCS_RemovalList	TFCS-RemovalList	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
TimeDurationBeforeRetry	TimeDurationBeforeRetry	Class-definitions		
TM_SignallingInfo	TM-SignallingInfo	Class-definitions		
TransmissionTimeInterval	TransmissionTimeInterval	Class-definitions		
TransmissionTimeValidity	TransmissionTimeValidity	Class-definitions		
TransportChannelIdentity	TransportChannelIdentity	Class-definitions		
TransportChannelIdentityDCHandDSCH	TransportChannelIdentityDCHandDSCH	Class-definitions		
TransportFormatSet	TransportFormatSet	Class-definitions		
TransportFormatSet_LCR	TransportFormatSet-LCR	Class-definitions		
UL_AddReconfTransChInfoList	UL-AddReconfTransChInfoList	Class-definitions		
UL_AddReconfTransChInformation	UL-AddReconfTransChInformation	Class-definitions		
UL_CommonTransChInfo	UL-CommonTransChInfo	Class-definitions		
UL_CommonTransChInfo_r4	UL-CommonTransChInfo-r4	Class-definitions		
UL_ControlledTrChList	UL-ControlledTrChList	Class-definitions		
UL_DeletedTransChInfoList	UL-DeletedTransChInfoList	Class-definitions		
UL_TransportChannelIdentity	UL-TransportChannelIdentity	Class-definitions		
UL_TrCH_Type	UL-TrCH-Type	Class-definitions		
USCH_TransportChannelsInfo	USCH-TransportChannelsInfo	Class-definitions		
ACK_NACK_repetitionFactor	ACK-NACK-repetitionFactor	Class-definitions		
AC_To_ASC_Mapping	AC-To-ASC-Mapping	Class-definitions		
AC_To_ASC_MappingTable	AC-To-ASC-MappingTable	Class-definitions		
AccessServiceClass_FDD	AccessServiceClass-FDD	Class-definitions		
AccessServiceClass_TDD	AccessServiceClass-TDD	Class-definitions		
AccessServiceClass_TDD_LCR_r4	AccessServiceClass-TDD-LCR-r4	Class-definitions		
AICH_Info	AICH-Info	Class-definitions		
AICH_PowerOffset	AICH-PowerOffset	Class-definitions		
AICH_TransmissionTiming	AICH-TransmissionTiming	Class-definitions		
AllocationPeriodInfo	AllocationPeriodInfo	Class-definitions		
Alpha	Alpha	Class-definitions		
AP_AICH_ChannelisationCode	AP-AICH-ChannelisationCode	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
AP_PreambleScramblingCode	AP-PreambleScramblingCode	Class-definitions		
AP_Signature	AP-Signature	Class-definitions		
AP_Signature_VCAM	AP-Signature-VCAM	Class-definitions		
AP_Subchannel	AP-Subchannel	Class-definitions		
ASCSetting_FDD	ASCSetting-FDD	Class-definitions		
ASCSetting_TDD	ASCSetting-TDD	Class-definitions		
ASCSetting_TDD_LC_R_r4	ASCSetting-TDD-LC-R-r4	Class-definitions		
AvailableAP_Signature_VCAMList	AvailableAP-Signature-VCAMList	Class-definitions		
AvailableAP_SignatureList	AvailableAP-SignatureList	Class-definitions		
AvailableAP_SubchannelList	AvailableAP-SubchannelList	Class-definitions		
AvailableMinimumSF_ListVCAM	AvailableMinimumSF-ListVCAM	Class-definitions		
AvailableMinimumSF_VCAM	AvailableMinimumSF-VCAM	Class-definitions		
AvailableSignatures	AvailableSignatures	Class-definitions		
AvailableSubChannelNumbers	AvailableSubChannelNumbers	Class-definitions		
BurstType	BurstType	Class-definitions		
Bler_Target	Bler-Target	Class-definitions		
CCTrCH_PowerControlInfo	CCTrCH-PowerControlInfo	Class-definitions		
CCTrCH_PowerControlInfo_r4	CCTrCH-PowerControlInfo-r4	Class-definitions		
CCTrCH_PowerControlInfo_r5	CCTrCH-PowerControlInfo-r5	Class-definitions		
CD_AccessSlotSubchannel	CD-AccessSlotSubchannel	Class-definitions		
CD_AccessSlotSubchannelList	CD-AccessSlotSubchannelList	Class-definitions		
CD_CA_ICH_ChannelisationCode	CD-CA-ICH-ChannelisationCode	Class-definitions		
CD_PreambleScramblingCode	CD-PreambleScramblingCode	Class-definitions		
CD_SignatureCode	CD-SignatureCode	Class-definitions		
CD_SignatureCodeList	CD-SignatureCodeList	Class-definitions		
CellAndChannelIdentity	CellAndChannelIdentity	Class-definitions		
CellParametersID	CellParametersID	Class-definitions		
Cfntargetsfnsframeoffset	Cfntargetsfnsframeoffset	Class-definitions		
ChannelAssignmentActive	ChannelAssignmentActive	Class-definitions		
ChannelisationCode256	ChannelisationCode256	Class-definitions		
ChannelReqParamsForUCSM	ChannelReqParamsForUCSM	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
ClosedLoopTimingAdj Mode	ClosedLoopTimingAdj Mode	Class-definitions		
CodeNumberDSCH	CodeNumberDSCH	Class-definitions		
CodeRange	CodeRange	Class-definitions		
CodeWordSet	CodeWordSet	Class-definitions		
CommonTimeslotInfo	CommonTimeslotInfo	Class-definitions		
CommonTimeslotInfoS CCPCH	CommonTimeslotInfoS CCPCH	Class-definitions		
ConstantValue	ConstantValue	Class-definitions		
ConstantValueTdd	ConstantValueTdd	Class-definitions		
CPCH_PersistenceLevels	CPCH-PersistenceLevels	Class-definitions		
CPCH_PersistenceLevelsList	CPCH-PersistenceLevelsList	Class-definitions		
CPCH_SetInfo	CPCH-SetInfo	Class-definitions		
CPCH_SetInfoList	CPCH-SetInfoList	Class-definitions		
CPCH_StatusIndicationMode	CPCH-StatusIndicationMode	Class-definitions		
CQI_RepetitionFactor	CQI-RepetitionFactor	Class-definitions		
CSICH_PowerOffset	CSICH-PowerOffset	Class-definitions		
DefaultDPCH_Offset ValueFDD	DefaultDPCH-Offset ValueFDD	Class-definitions		
DefaultDPCH_Offset ValueTDD	DefaultDPCH-Offset ValueTDD	Class-definitions		
DeltaPp_m	DeltaPp-m	Class-definitions		
DeltaCQI	DeltaCQI	Class-definitions		
DeltaNACK	DeltaNACK	Class-definitions		
DeltaACK	DeltaACK	Class-definitions		
DeltaSIR	DeltaSIR	Class-definitions		
DL_CCTrCh	DL-CCTrCh	Class-definitions		
DL_CCTrCh_r4	DL-CCTrCh-r4	Class-definitions		
DL_CCTrChList	DL-CCTrChList	Class-definitions		
DL_CCTrChList_r4	DL-CCTrChList-r4	Class-definitions		
DL_CCTrChListToRemove	DL-CCTrChListToRemove	Class-definitions		
DL_ChannelisationCode	DL-ChannelisationCode	Class-definitions		
DL_ChannelisationCodeList	DL-ChannelisationCodeList	Class-definitions		
DL_CommonInformation	DL-CommonInformation	Class-definitions		
DL_CommonInformation_r4	DL-CommonInformation-r4	Class-definitions		
DL_CommonInformation_r5	DL-CommonInformation-r5	Class-definitions		
DL_CommonInformationPredef	DL-CommonInformationPredef	Class-definitions		
DL_CompressedMode Method	DL-CompressedMode Method	Class-definitions		
DL_DPCH_InfoCommon	DL-DPCH-InfoCommon	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
DL_DPCH_InfoCom mon_r4	DL-DPCH-InfoCom mon-r4	Class-definitions		
DL_DPCH_InfoCom monPredef	DL-DPCH-InfoCom monPredef	Class-definitions		
DL_DPCH_InfoPerRL	DL-DPCH-InfoPerR L	Class-definitions		
DL_DPCH_InfoPerRL _r4	DL-DPCH-InfoPerR L-r4	Class-definitions		
DL_DPCH_InfoPerRL _r5	DL-DPCH-InfoPerR L-r5	Class-definitions		
DL_DPCH_PowerCo ntrollInfo	DL-DPCH-PowerCo ntrollInfo	Class-definitions		
DL_FrameType	DL-FrameType	Class-definitions		
DL_HSPDSCH_Infor mation	DL-HSPDSCH-Infor mation	Class-definitions		
DL_HSPDSCH_TS_C onfiguration	DL-HSPDSCH-TS- Configuration	Class-definitions		
DL_InformationPerRL	DL-InformationPerRL	Class-definitions		
DL_InformationPerRL _r4	DL-InformationPerRL -r4	Class-definitions		
DL_InformationPerRL _r5	DL-InformationPerRL -r5	Class-definitions		
DL_InformationPerRL _r5bis	DL-InformationPerRL -r5bis	Class-definitions		
DL_InformationPerRL _List	DL-InformationPerRL -List	Class-definitions		
DL_InformationPerRL _List_r4	DL-InformationPerRL -List-r4	Class-definitions		
DL_InformationPerRL _List_r5	DL-InformationPerRL -List-r5	Class-definitions		
DL_InformationPerRL _List_r5bis	DL-InformationPerRL -List-r5bis	Class-definitions		
DL_PDSCH_Informat ion	DL-PDSCH-Informa tion	Class-definitions		
DL_rate_matching_res triction	DL-rate-matching-res triction	Class-definitions		
DL_TPC_PowerOffse tPerRL	DL-TPC-PowerOffse tPerRL	Class-definitions		
DL_TPC_PowerOffse tPerRL_List	DL-TPC-PowerOffse tPerRL-List	Class-definitions		
DL_TS_Channelisatio nCode	DL-TS-Channelisatio nCode	Class-definitions		
DL_TS_Channelisatio nCodesShort	DL-TS-Channelisatio nCodesShort	Class-definitions		
DownlinkAdditionalTim eslots	DownlinkAdditionalTim eslots	Class-definitions		
DownlinkAdditionalTim eslots_LCR_r4	DownlinkAdditionalTim eslots-LCR-r4	Class-definitions		
DownlinkTimeslotsCod es	DownlinkTimeslotsCod es	Class-definitions		
DownlinkTimeslotsCod es_LCR_r4	DownlinkTimeslotsCod es-LCR-r4	Class-definitions		
DPC_Mode	DPC-Mode	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
DPCCH_PowerOffset	DPCCH-PowerOffset	Class-definitions		
DPCH_CompressedModelInfo	DPCH-CompressedModelInfo	Class-definitions		
DPCH_CompressedModelStatusInfo	DPCH-CompressedModelStatusInfo	Class-definitions		
DPCH_FrameOffset	DPCH-FrameOffset	Class-definitions		
DSCH_Mapping	DSCH-Mapping	Class-definitions		
DSCH_MappingList	DSCH-MappingList	Class-definitions		
DSCH_RadioLinkIdentifier	DSCH-RadioLinkIdentifier	Class-definitions		
DSCH_TransportChannelsInfo	DSCH-TransportChannelsInfo	Class-definitions		
DurationTimeInfo	DurationTimeInfo	Class-definitions		
DynamicPersistenceLevel	DynamicPersistenceLevel	Class-definitions		
DynamicPersistenceLevelList	DynamicPersistenceLevelList	Class-definitions		
DynamicPersistenceLevelTF_List	DynamicPersistenceLevelTF-List	Class-definitions		
FACH_PCH_Information	FACH-PCH-Information	Class-definitions		
FACH_PCH_InformationList	FACH-PCH-InformationList	Class-definitions		
Feedback_cycle	Feedback-cycle	Class-definitions		
FPACH_Info_r4	FPACH-Info-r4	Class-definitions		
FrequencyInfo	FrequencyInfo	Class-definitions		
FrequencyInfoFDD	FrequencyInfoFDD	Class-definitions		
FrequencyInfoTDD	FrequencyInfoTDD	Class-definitions		
HS_ChannelisationCode_LCR	HS-ChannelisationCode-LCR	Class-definitions		
HS_PDSCH_Midamble_Configuration_TDD128	HS-PDSCH-Midamble-Configuration-TDD128	Class-definitions		
HS_SCCH_Info	HS-SCCH-Info	Class-definitions		
HS_SCCH_Codes	HS-SCCH-Codes	Class-definitions		
HS_SCCH_TDD128	HS-SCCH-TDD128	Class-definitions		
HS_SICH_Configuration_TDD128	HS-SICH-Configuration-TDD128	Class-definitions		
HS_SCCH_TDD384	HS-SCCH-TDD384	Class-definitions		
HS_SICH_Configuration_TDD384	HS-SICH-Configuration-TDD384	Class-definitions		
HS_SICH_Power_Control_Info_TDD384	HS-SICH-Power-Control-Info-TDD384	Class-definitions		
IndividualTimeslotInfo	IndividualTimeslotInfo	Class-definitions		
IndividualTimeslotInfo_LCR_r4	IndividualTimeslotInfo-LCR-r4	Class-definitions		
IndividualTimeslotInfo_LCR_r4_ext	IndividualTimeslotInfo-LCR-r4-ext	Class-definitions		
IndividualTS_Interference	IndividualTS-Interference	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
IndividualTS_InterferenceList	IndividualTS-InterferenceList	Class-definitions		
ITP	ITP	Class-definitions		
NidentifyAbort	NidentifyAbort	Class-definitions		
MaxAllowedUL_TX_Power	MaxAllowedUL-TX-Power	Class-definitions		
MaxAvailablePCPCH_Number	MaxAvailablePCPCH-Number	Class-definitions		
MaxPowerIncrease_r4	MaxPowerIncrease-r4	Class-definitions		
MaxTFCI_Field2Value	MaxTFCI-Field2Value	Class-definitions		
Measurement_Feedback_Info	Measurement-Feedback-Info	Class-definitions		
MidambleConfigurationBurstType1and3	MidambleConfigurationBurstType1and3	Class-definitions		
MidambleConfigurationBurstType2	MidambleConfigurationBurstType2	Class-definitions		
MidambleShiftAndBurstType	MidambleShiftAndBurstType	Class-definitions		
MidambleShiftAndBurstType_LCR_r4	MidambleShiftAndBurstType-LCR-r4	Class-definitions		
MidambleShiftAndBurstType_DL	MidambleShiftAndBurstType-DL	Class-definitions		
MidambleShiftLong	MidambleShiftLong	Class-definitions		
MidambleShiftShort	MidambleShiftShort	Class-definitions		
MinimumSpreadingFactor	MinimumSpreadingFactor	Class-definitions		
MultiCodeInfo	MultiCodeInfo	Class-definitions		
N_EOT	N-EOT	Class-definitions		
N_GAP	N-GAP	Class-definitions		
N_PCH	N-PCH	Class-definitions		
N_StartMessage	N-StartMessage	Class-definitions		
NB01	NB01	Class-definitions		
NF_Max	NF-Max	Class-definitions		
NumberOfDPDCH	NumberOfDPDCH	Class-definitions		
NumberOfFBI_Bits	NumberOfFBI-Bits	Class-definitions		
OpenLoopPowerControl_TDD	OpenLoopPowerControl-TDD	Class-definitions		
OpenLoopPowerControl_IPDL_TDD_r4	OpenLoopPowerControl-IPDL-TDD-r4	Class-definitions		
PagingIndicatorLength	PagingIndicatorLength	Class-definitions		
PC_Preamble	PC-Preamble	Class-definitions		
PCP_Length	PCP-Length	Class-definitions		
PCPCH_ChannelInfo	PCPCH-ChannelInfo	Class-definitions		
PCPCH_ChannelInfoList	PCPCH-ChannelInfoList	Class-definitions		
PCPICH_UsageForChannelEst	PCPICH-UsageForChannelEst	Class-definitions		
PDSCH_CapacityAllocationInfo	PDSCH-CapacityAllocationInfo	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
PDSCH_CapacityAllocationInfo_r4	PDSCH-CapacityAllocationInfo-r4	Class-definitions		
PDSCH_CodeInfo	PDSCH-CodeInfo	Class-definitions		
PDSCH_CodeInfoList	PDSCH-CodeInfoList	Class-definitions		
PDSCH_CodeMap	PDSCH-CodeMap	Class-definitions		
PDSCH_CodeMapList	PDSCH-CodeMapList	Class-definitions		
PDSCH_CodeMapping	PDSCH-CodeMapping	Class-definitions		
PDSCH_Identity	PDSCH-Identity	Class-definitions		
PDSCH_Info	PDSCH-Info	Class-definitions		
PDSCH_Info_r4	PDSCH-Info-r4	Class-definitions		
PDSCH_Info_LCR_r4	PDSCH-Info-LCR-r4	Class-definitions		
PDSCH_PowerControlInfo	PDSCH-PowerControlInfo	Class-definitions		
PDSCH_SHO_DCH_Info	PDSCH-SHO-DCH-Info	Class-definitions		
PDSCH_SysInfo	PDSCH-SysInfo	Class-definitions		
PDSCH_SysInfo_HCR_r5	PDSCH-SysInfo-HCR-r5	Class-definitions		
PDSCH_SysInfo_LCR_r4	PDSCH-SysInfo-LCR-r4	Class-definitions		
PDSCH_SysInfoList	PDSCH-SysInfoList	Class-definitions		
PDSCH_SysInfoList_HCR_r5	PDSCH-SysInfoList-HCR-r5	Class-definitions		
PDSCH_SysInfoList_LCR_r4	PDSCH-SysInfoList-LCR-r4	Class-definitions		
PDSCH_SysInfoList_SFN	PDSCH-SysInfoList-SFN	Class-definitions		
PDSCH_SysInfoList_SFN_HCR_r5	PDSCH-SysInfoList-SFN-HCR-r5	Class-definitions		
PDSCH_SysInfoList_SFN_LCR_r4	PDSCH-SysInfoList-SFN-LCR-r4	Class-definitions		
PersistenceScalingFactor	PersistenceScalingFactor	Class-definitions		
PersistenceScalingFactorList	PersistenceScalingFactorList	Class-definitions		
PI_CountPerFrame	PI-CountPerFrame	Class-definitions		
PichChannelisationCodeList_LCR_r4	PichChannelisationCodeList-LCR-r4	Class-definitions		
PICH_Info	PICH-Info	Class-definitions		
PICH_Info_LCR_r4	PICH-Info-LCR-r4	Class-definitions		
PICH_PowerOffset	PICH-PowerOffset	Class-definitions		
PilotBits128	PilotBits128	Class-definitions		
PilotBits256	PilotBits256	Class-definitions		
MeasurementPowerOffset	MeasurementPowerOffset	Class-definitions		
PositionFixedOrFlexible	PositionFixedOrFlexible	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
PowerControlAlgorithm	PowerControlAlgorithm	Class-definitions		
PowerOffsetPilot_pdpdch	PowerOffsetPilot-pdpdch	Class-definitions		
PowerOffsetTPC_pdpdch	PowerOffsetTPC-pdpdch	Class-definitions		
PowerRampStep	PowerRampStep	Class-definitions		
PRACH_ChانCodes_LCR_r4	PRACH-ChانCodes-LCR-r4	Class-definitions		
PRACH_Definition_LCR_r4	PRACH-Definition-LCR-r4	Class-definitions		
PRACH_Midamble	PRACH-Midamble	Class-definitions		
PRACH_Partitioning	PRACH-Partitioning	Class-definitions		
PRACH_Partitioning_LCR_r4	PRACH-Partitioning-LCR-r4	Class-definitions		
PRACH_PowerOffset	PRACH-PowerOffset	Class-definitions		
PRACH_RACH_Info	PRACH-RACH-Info	Class-definitions		
PRACH_RACH_Info_LCR_r4	PRACH-RACH-Info-LCR-r4	Class-definitions		
PRACH_SystemInformation	PRACH-SystemInformation	Class-definitions		
PRACH_SystemInformation_LCR_r4	PRACH-SystemInformation-LCR-r4	Class-definitions		
PRACH_SystemInformationList	PRACH-SystemInformationList	Class-definitions		
PRACH_SystemInformationList_LCR_r4	PRACH-SystemInformationList-LCR-r4	Class-definitions		
PreambleRetransMax	PreambleRetransMax	Class-definitions		
PreambleScramblingCodeWordNumber	PreambleScramblingCodeWordNumber	Class-definitions		
PreDefPhyChConfiguration	PreDefPhyChConfiguration	Class-definitions		
PrimaryCCPCH_Info	PrimaryCCPCH-Info	Class-definitions		
PrimaryCCPCH_Info_r4	PrimaryCCPCH-Info-r4	Class-definitions		
PrimaryCCPCH_Info_LCR_r4	PrimaryCCPCH-Info-LCR-r4	Class-definitions		
PrimaryCCPCH_Info_LCR_r4_ext	PrimaryCCPCH-Info-LCR-r4-ext	Class-definitions		
PrimaryCCPCH_TX_Power	PrimaryCCPCH-TX-Power	Class-definitions		
PrimaryCPICH_Info	PrimaryCPICH-Info	Class-definitions		
PrimaryCPICH_TX_Power	PrimaryCPICH-TX-Power	Class-definitions		
PrimaryScramblingCode	PrimaryScramblingCode	Class-definitions		
PuncturingLimit	PuncturingLimit	Class-definitions		
PUSCH_CapacityAllocationInfo	PUSCH-CapacityAllocationInfo	Class-definitions		
PUSCH_CapacityAllocationInfo_r4	PUSCH-CapacityAllocationInfo-r4	Class-definitions		
PUSCH_Identity	PUSCH-Identity	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
PUSCH_Info	PUSCH-Info	Class-definitions		
PUSCH_Info_r4	PUSCH-Info-r4	Class-definitions		
PUSCH_Info_LCR_r4	PUSCH-Info-LCR-r4	Class-definitions		
PUSCH_PowerControlInfo_r4	PUSCH-PowerControlInfo-r4	Class-definitions		
PUSCH_SysInfo	PUSCH-SysInfo	Class-definitions		
PUSCH_SysInfo_HCR_r5	PUSCH-SysInfo-HCR-r5	Class-definitions		
PUSCH_SysInfo_LCR_r4	PUSCH-SysInfo-LCR-r4	Class-definitions		
PUSCH_SysInfoList	PUSCH-SysInfoList	Class-definitions		
PUSCH_SysInfoList_HCR_r5	PUSCH-SysInfoList-HCR-r5	Class-definitions		
PUSCH_SysInfoList_LCR_r4	PUSCH-SysInfoList-LCR-r4	Class-definitions		
PUSCH_SysInfoList_SFN	PUSCH-SysInfoList-SFN	Class-definitions		
PUSCH_SysInfoList_SFN_HCR_r5	PUSCH-SysInfoList-SFN-HCR-r5	Class-definitions		
PUSCH_SysInfoList_SFN_LCR_r4	PUSCH-SysInfoList-SFN-LCR-r4	Class-definitions		
RACH_TransmissionParameters	RACH-TransmissionParameters	Class-definitions		
RepetitionPeriodAndLength	RepetitionPeriodAndLength	Class-definitions		
RepetitionPeriodLengthAndOffset	RepetitionPeriodLengthAndOffset	Class-definitions		
ReplacedPDSCH_CodeInfo	ReplacedPDSCH-CodeInfo	Class-definitions		
ReplacedPDSCH_CodeInfoList	ReplacedPDSCH-CodeInfoList	Class-definitions		
RepPerLengthOffset_PICH	RepPerLengthOffset-PICH	Class-definitions		
RestrictedTrCH	RestrictedTrCH	Class-definitions		
RestrictedTrCH_InfoList	RestrictedTrCH-InfoList	Class-definitions		
RL_AdditionInformation	RL-AdditionInformation	Class-definitions		
RL_AdditionInformationList	RL-AdditionInformationList	Class-definitions		
RL_IdentifierList	RL-IdentifierList	Class-definitions		
RL_RemovalInformationList	RL-RemovalInformationList	Class-definitions		
RPP	RPP	Class-definitions		
S_Field	S-Field	Class-definitions		
SCCPCH_ChannelisationCode	SCCPCH-ChannelisationCode	Class-definitions		
SCCPCH_ChannelisationCodeList	SCCPCH-ChannelisationCodeList	Class-definitions		
SCCPCH_InfoForFACH	SCCPCH-InfoForFACH	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
SCCPCH_InfoForFA CH_r4	SCCPCH-InfoForFA CH-r4	Class-definitions		
SCCPCH_SystemInfo r4	SCCPCH-SystemInf ormation	Class-definitions		
SCCPCH_SystemInfo r4_ext	SCCPCH-SystemInf ormation-LCR-r4-ex t	Class-definitions		
SCCPCH_SystemInfo r4_List	SCCPCH-SystemInf ormationList	Class-definitions		
SCCPCH_SystemInfo r4_List_LCR_r4_ext	SCCPCH-SystemInf ormationList-LCR-r4 -ext	Class-definitions		
ScramblingCodeChan ge	ScramblingCodeChan ge	Class-definitions		
ScramblingCodeType	ScramblingCodeType	Class-definitions		
SecondaryCCPCH_In fo	SecondaryCCPCH-In fo	Class-definitions		
SecondaryCCPCH_In fo_r4	SecondaryCCPCH-In fo-r4	Class-definitions		
SecondaryCCPCH_In fo_LCR_r4_ext	SecondaryCCPCH-In fo-LCR-r4-ext	Class-definitions		
SecondaryCPICH_In fo	SecondaryCPICH-In fo	Class-definitions		
SecondaryScrambling Code	SecondaryScrambling Code	Class-definitions		
SecondInterleavingMo de	SecondInterleavingMo de	Class-definitions		
SF256_AndCodeNum ber	SF256-AndCodeNum ber	Class-definitions		
SF512_AndCodeNum ber	SF512-AndCodeNum ber	Class-definitions		
SF512_AndPilot	SF512-AndPilot	Class-definitions		
SF_PDSCH	SF-PDSCH	Class-definitions		
SF_PRACH	SF-PRACH	Class-definitions		
SFN_TimeInfo	SFN-TimeInfo	Class-definitions		
SpecialBurstSchedulin g	SpecialBurstSchedulin g	Class-definitions		
SpreadingFactor	SpreadingFactor	Class-definitions		
SRB_delay	SRB-delay	Class-definitions		
SSDT_CellIdentity	SSDT-CellIdentity	Class-definitions		
SSDT_Information	SSDT-Information	Class-definitions		
SSDT_Information_r4	SSDT-Information-r4	Class-definitions		
SSDT_UL	SSDT-UL	Class-definitions		
SynchronisationParam eters_r4	SynchronisationParam eters-r4	Class-definitions		
SYNC_UL_Procedure _r4	SYNC-UL-Procedure -r4	Class-definitions		
SYNC_UL_Info_r4	SYNC-UL-Info-r4	Class-definitions		
TDD_FPACH_CCode 16_r4	TDD-FPACH-CCode 16-r4	Class-definitions		
TDD_UL_Interference	TDD-UL-Interference	Class-definitions		
TDD_PICH_CCode	TDD-PICH-CCode	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
TDD_PRACH_CCode8	TDD-PRACH-CCode8	Class-definitions		
TDD_PRACH_CCode16	TDD-PRACH-CCode16	Class-definitions		
TDD_PRACH_CCode_LCR_r4	TDD-PRACH-CCode-LCR-r4	Class-definitions		
TDD_PRACH_CCodeList	TDD-PRACH-CCodeList	Class-definitions		
TFC_ControlDuration	TFC-ControlDuration	Class-definitions		
TFCI_Coding	TFCI-Coding	Class-definitions		
TGCFN	TGCFN	Class-definitions		
TGD	TGD	Class-definitions		
TGL	TGL	Class-definitions		
TGMP	TGMP	Class-definitions		
TGP_Sequence	TGP-Sequence	Class-definitions		
TGPS_Reconfiguration_CFN	TGPS-Reconfiguration-CFN	Class-definitions		
TGP_SequenceList	TGP-SequenceList	Class-definitions		
TGP_SequenceShort	TGP-SequenceShort	Class-definitions		
TGPL	TGPL	Class-definitions		
TGPRC	TGPRC	Class-definitions		
TGPS_ConfigurationParameters	TGPS-ConfigurationParameters	Class-definitions		
TGPSI	TGPSI	Class-definitions		
TGSN	TGSN	Class-definitions		
TimeInfo	TimeInfo	Class-definitions		
TimeslotList	TimeslotList	Class-definitions		
TimeslotList_r4	TimeslotList-r4	Class-definitions		
TimeslotNumber	TimeslotNumber	Class-definitions		
TimeslotNumber_LCR_r4	TimeslotNumber-LCR-r4	Class-definitions		
TimeslotNumber_PRA_CH_LCR_r4	TimeslotNumber-PRA-CH-LCR-r4	Class-definitions		
TimeslotSync2	TimeslotSync2	Class-definitions		
TimingOffset	TimingOffset	Class-definitions		
TPC_CombinationIndex	TPC-CombinationIndex	Class-definitions		
TPC_StepSizeFDD	TPC-StepSizeFDD	Class-definitions		
TPC_StepSizeTDD	TPC-StepSizeTDD	Class-definitions		
TreconfirmAbort	TreconfirmAbort	Class-definitions		
TX_DiversityMode	TX-DiversityMode	Class-definitions		
UARFCN	UARFCN	Class-definitions		
UCSM_Info	UCSM-Info	Class-definitions		
UL_CCTrCH	UL-CCTrCH	Class-definitions		
UL_CCTrCH_r4	UL-CCTrCH-r4	Class-definitions		
UL_CCTrCHList	UL-CCTrCHList	Class-definitions		
UL_CCTrCHList_r4	UL-CCTrCHList-r4	Class-definitions		
UL_CCTrCHListToRe-move	UL-CCTrCHListToRe-move	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UL_CCTrChTPCList	UL-CCTrChTPCList	Class-definitions		
UL_ChannelRequirement	UL-ChannelRequirement	Class-definitions		
UL_ChannelRequirement_r4	UL-ChannelRequirement-r4	Class-definitions		
UL_ChannelRequirement_r5	UL-ChannelRequirement-r5	Class-definitions		
UL_ChannelRequirementWithCPCH_SetID	UL-ChannelRequirementWithCPCH-SetID	Class-definitions		
UL_ChannelRequirementWithCPCH_SetID_r4	UL-ChannelRequirementWithCPCH-SetID-r4	Class-definitions		
UL_ChannelRequirementWithCPCH_SetID_r5	UL-ChannelRequirementWithCPCH-SetID-r5	Class-definitions		
UL_CompressedModeMethod	UL-CompressedModeMethod	Class-definitions		
UL_DL_Mode	UL-DL-Mode	Class-definitions		
UL_DPCCH_SlotFormat	UL-DPCCH-SlotFormat	Class-definitions		
UL_DPCH_Info	UL-DPCH-Info	Class-definitions		
UL_DPCH_Info_r4	UL-DPCH-Info-r4	Class-definitions		
UL_DPCH_Info_r5	UL-DPCH-Info-r5	Class-definitions		
UL_DPCH_InfoPredef	UL-DPCH-InfoPredef	Class-definitions		
UL_DPCH_PowerControlInfo	UL-DPCH-PowerControlInfo	Class-definitions		
UL_DPCH_PowerControlInfo_r4	UL-DPCH-PowerControlInfo-r4	Class-definitions		
UL_DPCH_PowerControlInfo_r5	UL-DPCH-PowerControlInfo-r5	Class-definitions		
UL_DPCH_PowerControlInfoPredef	UL-DPCH-PowerControlInfoPredef	Class-definitions		
UL_Interference	UL-Interference	Class-definitions		
UL_ScramblingCode	UL-ScramblingCode	Class-definitions		
UL_SynchronisationParameters_r4	UL-SynchronisationParameters-r4	Class-definitions		
UL_TargetSIR	UL-TargetSIR	Class-definitions		
UL_TimingAdvance	UL-TimingAdvance	Class-definitions		
UL_TimingAdvanceControl	UL-TimingAdvanceControl	Class-definitions		
UL_TimingAdvanceControl_r4	UL-TimingAdvanceControl-r4	Class-definitions		
UL_TS_ChannelisationCode	UL-TS-ChannelisationCode	Class-definitions		
UL_TS_ChannelisationCodeList	UL-TS-ChannelisationCodeList	Class-definitions		
UplinkAdditionalTimeslots	UplinkAdditionalTimeslots	Class-definitions		
UplinkAdditionalTimeslots_LCR_r4	UplinkAdditionalTimeslots-LCR-r4	Class-definitions		
UplinkTimeslotsCodes	UplinkTimeslotsCodes	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UplinkTimeslotsCodes_LCR_r4	UplinkTimeslotsCodes-LCR-r4	Class-definitions		
Wi_LCR	Wi-LCR	Class-definitions		
AcquisitionSatInfo	AcquisitionSatInfo	Class-definitions		
AcquisitionSatInfoList	AcquisitionSatInfoList	Class-definitions		
AdditionalMeasuremen tID_List	AdditionalMeasuremen tID-List	Class-definitions		
AlmanacSatInfo	AlmanacSatInfo	Class-definitions		
AlmanacSatInfoList	AlmanacSatInfoList	Class-definitions		
AverageRLC_BufferP ayload	AverageRLC-BufferP ayload	Class-definitions		
AzimuthAndElevation	AzimuthAndElevation	Class-definitions		
BadSatList	BadSatList	Class-definitions		
Frequency_Band	Frequency-Band	Class-definitions		
BCCH_ARFCN	BCCH-ARFCN	Class-definitions		
BLER_MeasurementR esults	BLER-MeasurementR esults	Class-definitions		
BLER_MeasurementR esultsList	BLER-MeasurementR esultsList	Class-definitions		
BLER_TransChIdList	BLER-TransChIdList	Class-definitions		
BSIC_VerificationReq uired	BSIC-VerificationReq uired	Class-definitions		
BSICReported	BSICReported	Class-definitions		
BurstModeParameters	BurstModeParameters	Class-definitions		
CellDCH_ReportCrite ria	CellDCH-ReportCrite ria	Class-definitions		
CellDCH_ReportCrite ria_LCR_r4	CellDCH-ReportCrite ria-LCR-r4	Class-definitions		
CellIndividualOffset	CellIndividualOffset	Class-definitions		
CellInfo	CellInfo	Class-definitions		
CellInfo_r4	CellInfo-r4	Class-definitions		
CellInfoSI_RSCP	CellInfoSI-RSCP	Class-definitions		
CellInfoSI_RSCP_LC R_r4	CellInfoSI-RSCP-LC R-r4	Class-definitions		
CellInfoSI_ECN0	CellInfoSI-ECN0	Class-definitions		
CellInfoSI_ECN0_LC R_r4	CellInfoSI-ECN0-LC R-r4	Class-definitions		
CellInfoSI_HCS_RSC P	CellInfoSI-HCS-RSC P	Class-definitions		
CellInfoSI_HCS_RSC P_LCR_r4	CellInfoSI-HCS-RSC P-LCR-r4	Class-definitions		
CellInfoSI_HCS_ECN 0	CellInfoSI-HCS-EC N0	Class-definitions		
CellInfoSI_HCS_ECN 0_LCR_r4	CellInfoSI-HCS-EC N0-LCR-r4	Class-definitions		
CellMeasuredResults	CellMeasuredResults	Class-definitions		
CellMeasurementEven tResults	CellMeasurementEven tResults	Class-definitions		
CellMeasurementEven tResults_LCR_r4	CellMeasurementEven tResults-LCR-r4	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
CellReportingQuantities	CellReportingQuantities	Class-definitions		
CellSelectReselectInfoSIB_11_12	CellSelectReselectInfoSIB-11-12	Class-definitions		
CellSelectReselectInfoSIB_11_12_RSCP	CellSelectReselectInfoSIB-11-12-RSCP	Class-definitions		
CellSelectReselectInfoSIB_11_12_ECN0	CellSelectReselectInfoSIB-11-12-ECN0	Class-definitions		
CellSelectReselectInfoSIB_11_12_HCS_RSCP	CellSelectReselectInfoSIB-11-12-HCS-RSCP	Class-definitions		
CellSelectReselectInfoSIB_11_12_HCS_ECN0	CellSelectReselectInfoSIB-11-12-HCS-ECN0	Class-definitions		
CellSelectReselectInfo_v590ext	CellSelectReselectInfo_v590ext	Class-definitions		
CellSelectReselectInfoPCHFACH_v5b0ext	CellSelectReselectInfoPCHFACH-v5b0ext	Class-definitions		
CellsForInterFreqMeasList	CellsForInterFreqMeasList	Class-definitions		
CellsForInterRATMeasList	CellsForInterRATMeasList	Class-definitions		
CellsForIntraFreqMeasList	CellsForIntraFreqMeasList	Class-definitions		
CellSynchronisationInfo	CellSynchronisationInfo	Class-definitions		
CellToReport	CellToReport	Class-definitions		
CellToReportList	CellToReportList	Class-definitions		
CodePhaseSearchWindow	CodePhaseSearchWindow	Class-definitions		
CountC_SFN_Frame_difference	CountC-SFN-Frame-difference	Class-definitions		
CPICH_Ec_N0	CPICH-Ec-N0	Class-definitions		
CPICH_RSCP	CPICH-RSCP	Class-definitions		
DeltaPRC	DeltaPRC	Class-definitions		
DeltaQrxlevmin	DeltaQrxlevmin	Class-definitions		
DeltaRSCP	DeltaRSCP	Class-definitions		
DeltaRSCPPerCell	DeltaRSCPPerCell	Class-definitions		
DeltaRRC	DeltaRRC	Class-definitions		
DGPS_CorrectionSatInfo	DGPS-CorrectionSatInfo	Class-definitions		
DGPS_CorrectionSatInfoList	DGPS-CorrectionSatInfoList	Class-definitions		
DiffCorrectionStatus	DiffCorrectionStatus	Class-definitions		
DL_TransportChannelBLER	DL-TransportChannelBLER	Class-definitions		
DopplerUncertainty	DopplerUncertainty	Class-definitions		
EllipsoidPoint	EllipsoidPoint	Class-definitions		
EllipsoidPointAltitude	EllipsoidPointAltitude	Class-definitions		
EllipsoidPointAltitudeEllipsoide	EllipsoidPointAltitudeEllipsoide	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
EllipsoidPointUncertCircle	EllipsoidPointUncertCircle	Class-definitions		
EllipsoidPointUncertEllipse	EllipsoidPointUncertEllipse	Class-definitions		
EnvironmentCharacterisation	EnvironmentCharacterisation	Class-definitions		
Event1a	Event1a	Class-definitions		
Event1a_r4	Event1a-r4	Class-definitions		
Event1a_LCR_r4	Event1a-LCR-r4	Class-definitions		
Event1b	Event1b	Class-definitions		
Event1b_r4	Event1b-r4	Class-definitions		
Event1b_LCR_r4	Event1b-LCR-r4	Class-definitions		
Event1c	Event1c	Class-definitions		
Event1e	Event1e	Class-definitions		
Event1f	Event1f	Class-definitions		
Event2a	Event2a	Class-definitions		
Event2b	Event2b	Class-definitions		
Event2c	Event2c	Class-definitions		
Event2d	Event2d	Class-definitions		
Event2e	Event2e	Class-definitions		
Event2f	Event2f	Class-definitions		
Event3a	Event3a	Class-definitions		
Event3b	Event3b	Class-definitions		
Event3c	Event3c	Class-definitions		
Event3d	Event3d	Class-definitions		
EventIDInterFreq	EventIDInterFreq	Class-definitions		
EventIDInterRAT	EventIDInterRAT	Class-definitions		
EventIDIntraFreq	EventIDIntraFreq	Class-definitions		
EventResults	EventResults	Class-definitions		
ExtraDopplerInfo	ExtraDopplerInfo	Class-definitions		
FACH_MeasurementOccasionInfo	FACH-MeasurementOccasionInfo	Class-definitions		
FACH_MeasurementOccasionInfo_LCR_r4_ext	FACH-MeasurementOccasionInfo-LCR-r4-ext	Class-definitions		
FilterCoefficient	FilterCoefficient	Class-definitions		
FineSFN_SFN	FineSFN-SFN	Class-definitions		
ForbiddenAffectCell	ForbiddenAffectCell	Class-definitions		
ForbiddenAffectCell_r4	ForbiddenAffectCell-r4	Class-definitions		
ForbiddenAffectCell_LCR_r4	ForbiddenAffectCell-LCR-r4	Class-definitions		
ForbiddenAffectCellList	ForbiddenAffectCellList	Class-definitions		
ForbiddenAffectCellList_r4	ForbiddenAffectCellList-r4	Class-definitions		
ForbiddenAffectCellList_LCR_r4	ForbiddenAffectCellList-LCR-r4	Class-definitions		
FreqQualityEstimateQuantity_FDD	FreqQualityEstimateQuantity-FDD	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
FreqQualityEstimateQuantity_TDD	FreqQualityEstimateQuantity-TDD	Class-definitions		
GPS_MeasurementParam	GPS-MeasurementParam	Class-definitions		
GPS_MeasurementParamList	GPS-MeasurementParamList	Class-definitions		
GSM_CarrierRSSI	GSM-CarrierRSSI	Class-definitions		
GSM_MeasuredResults	GSM-MeasuredResults	Class-definitions		
GSM_MeasuredResultsList	GSM-MeasuredResultsList	Class-definitions		
GPS_TOW_1msec	GPS-TOW-1msec	Class-definitions		
GPS_TOW_Assist	GPS-TOW-Assist	Class-definitions		
GPS_TOW_AssistList	GPS-TOW-AssistList	Class-definitions		
HCS_CellReselectInformation_RSCP	HCS-CellReselectInformation-RSCP	Class-definitions		
HCS_CellReselectInformation_ECN0	HCS-CellReselectInformation-ECN0	Class-definitions		
HCS_NeighbouringCellInformation_RSCP	HCS-NeighbouringCellInformation-RSCP	Class-definitions		
HCS_NeighbouringCellInformation_ECN0	HCS-NeighbouringCellInformation-ECN0	Class-definitions		
HCS_PRIO	HCS-PRIO	Class-definitions		
HCS_ServingCellInformation	HCS-ServingCellInformation	Class-definitions		
Hysteresis	Hysteresis	Class-definitions		
HysteresisInterFreq	HysteresisInterFreq	Class-definitions		
InterFreqCell	InterFreqCell	Class-definitions		
InterFreqCell_LCR_r4	InterFreqCell-LCR-r4	Class-definitions		
InterFreqCellID	InterFreqCellID	Class-definitions		
InterFreqCellInfoList	InterFreqCellInfoList	Class-definitions		
InterFreqCellInfoList_r4	InterFreqCellInfoList-r4	Class-definitions		
InterFreqCellInfoSI_List_RSCP	InterFreqCellInfoSI-List-RSCP	Class-definitions		
InterFreqCellInfoSI_List_ECN0	InterFreqCellInfoSI-List-ECN0	Class-definitions		
InterFreqCellInfoSI_List_HCS_RSCP	InterFreqCellInfoSI-List-HCS-RSCP	Class-definitions		
InterFreqCellInfoSI_List_HCS_ECN0	InterFreqCellInfoSI-List-HCS-ECN0	Class-definitions		
InterFreqCellInfoSI_List_RSCP_LCR	InterFreqCellInfoSI-List-RSCP-LCR	Class-definitions		
InterFreqCellInfoSI_List_ECN0_LCR	InterFreqCellInfoSI-List-ECN0-LCR	Class-definitions		
InterFreqCellInfoSI_List_HCS_RSCP_LCR	InterFreqCellInfoSI-List-HCS-RSCP-LCR	Class-definitions		
InterFreqCellInfoSI_List_HCS_ECN0_LCR	InterFreqCellInfoSI-List-HCS-ECN0-LCR	Class-definitions		
InterFreqCellList	InterFreqCellList	Class-definitions		
InterFreqCellList_LCR_r4_ext	InterFreqCellList-LCR-r4-ext	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
InterFreqCellMeasuredResultsList	InterFreqCellMeasuredResultsList	Class-definitions		
InterFreqEvent	InterFreqEvent	Class-definitions		
InterFreqEventList	InterFreqEventList	Class-definitions		
InterFrequencyMeasuredResultsList_v590ext	InterFrequencyMeasuredResultsList-v590ext	Class-definitions		
Inter_FreqEventCriteria_v590ext	Inter-FreqEventCriteria-v590ext	Class-definitions		
Inter_FreqEventCriteriaList_v590ext	Inter-FreqEventCriteriaList-v590ext	Class-definitions		
Intra_FreqEventCriteriaList_v590ext	Intra-FreqEventCriteriaList-v590ext	Class-definitions		
IntraFrequencyMeasuredResultsList_v590ext	IntraFrequencyMeasuredResultsList-v590ext	Class-definitions		
IntraFreqReportingCriteria_1b_r5	IntraFreqReportingCriteria-1b-r5	Class-definitions		
PeriodicReportingInfo_1b	PeriodicReportingInfo-1b	Class-definitions		
InterFreqEventResults	InterFreqEventResults	Class-definitions		
InterFreqEventResults_LCR_r4_ext	InterFreqEventResults-LCR-r4-ext	Class-definitions		
InterFreqMeasQuantity	InterFreqMeasQuantity	Class-definitions		
InterFreqMeasuredResults	InterFreqMeasuredResults	Class-definitions		
InterFreqMeasuredResultsList	InterFreqMeasuredResultsList	Class-definitions		
InterFreqMeasurementSysInfo_RSCP	InterFreqMeasurementSysInfo-RSCP	Class-definitions		
InterFreqMeasurementSysInfo_ECN0	InterFreqMeasurementSysInfo-ECN0	Class-definitions		
InterFreqMeasurementSysInfo_HCS_RSCP	InterFreqMeasurementSysInfo-HCS-RSCP	Class-definitions		
InterFreqMeasurementSysInfo_HCS_ECN0	InterFreqMeasurementSysInfo-HCS-ECN0	Class-definitions		
InterFreqMeasurementSysInfo_RSCP_LCR_r4	InterFreqMeasurementSysInfo-RSCP-LCR-r4	Class-definitions		
InterFreqMeasurementSysInfo_ECN0_LCR_r4	InterFreqMeasurementSysInfo-ECN0-LCR-r4	Class-definitions		
InterFreqMeasurementSysInfo_HCS_RSCP_LCR_r4	InterFreqMeasurementSysInfo-HCS-RSCP-LCR-r4	Class-definitions		
InterFreqMeasurementSysInfo_HCS_ECN0_LCR_r4	InterFreqMeasurementSysInfo-HCS-ECN0-LCR-r4	Class-definitions		
InterFreqReportCriteria	InterFreqReportCriteria	Class-definitions		
InterFreqReportCriteria_r4	InterFreqReportCriteria-r4	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
InterFreqReportingCriteria	InterFreqReportingCriteria	Class-definitions		
InterFreqReportingQuantity	InterFreqReportingQuantity	Class-definitions		
InterFrequencyMeasurement	InterFrequencyMeasurement	Class-definitions		
InterFrequencyMeasurement_r4	InterFrequencyMeasurement-r4	Class-definitions		
InterRAT_TargetCellDescription	InterRAT-TargetCellDescription	Class-definitions		
InterRATCellID	InterRATCellID	Class-definitions		
InterRATCellInfoIndication	InterRATCellInfoIndication	Class-definitions		
InterRATCellInfoList	InterRATCellInfoList	Class-definitions		
InterRATCellInfoList_B	InterRATCellInfoList-B	Class-definitions		
InterRATCellInfoList_r4	InterRATCellInfoList-r4	Class-definitions		
InterRATCellIndividualOffset	InterRATCellIndividualOffset	Class-definitions		
InterRATEvent	InterRATEvent	Class-definitions		
InterRATEventList	InterRATEventList	Class-definitions		
InterRATEventResults	InterRATEventResults	Class-definitions		
InterRATInfo	InterRATInfo	Class-definitions		
InterRATMeasQuantity	InterRATMeasQuantity	Class-definitions		
InterRATMeasuredResults	InterRATMeasuredResults	Class-definitions		
InterRATMeasuredResultsList	InterRATMeasuredResultsList	Class-definitions		
InterRATMeasurement	InterRATMeasurement	Class-definitions		
InterRATMeasurement_r4	InterRATMeasurement-r4	Class-definitions		
InterRATMeasurementSysInfo	InterRATMeasurementSysInfo	Class-definitions		
InterRATMeasurementSysInfo_B	InterRATMeasurementSysInfo-B	Class-definitions		
InterRATReportCriteria	InterRATReportCriteria	Class-definitions		
InterRATReportingCriteria	InterRATReportingCriteria	Class-definitions		
InterRATReportingQuantity	InterRATReportingQuantity	Class-definitions		
IntraFreqCellID	IntraFreqCellID	Class-definitions		
IntraFreqCellInfoList	IntraFreqCellInfoList	Class-definitions		
IntraFreqCellInfoList_r4	IntraFreqCellInfoList-r4	Class-definitions		
IntraFreqCellInfoSI_List_RSCP	IntraFreqCellInfoSI-List-RSCP	Class-definitions		
IntraFreqCellInfoSI_List_ECN0	IntraFreqCellInfoSI-List-ECN0	Class-definitions		
IntraFreqCellInfoSI_List_HCS_RSCP	IntraFreqCellInfoSI-List-HCS-RSCP	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
IntraFreqCellInfoSI_List_HCS_ECN0	IntraFreqCellInfoSI_List-HCS-ECN0	Class-definitions		
IntraFreqCellInfoSI_List_RSCP_LCR_r4	IntraFreqCellInfoSI_List-RSCP-LCR-r4	Class-definitions		
IntraFreqCellInfoSI_List_ECN0_LCR_r4	IntraFreqCellInfoSI_List-ECN0-LCR-r4	Class-definitions		
IntraFreqCellInfoSI_List_HCS_RSCP_LCR_r4	IntraFreqCellInfoSI_List-HCS-RSCP-LCR-r4	Class-definitions		
IntraFreqCellInfoSI_List_HCS_ECN0_LCR_r4	IntraFreqCellInfoSI_List-HCS-ECN0-LCR-r4	Class-definitions		
IntraFreqEvent	IntraFreqEvent	Class-definitions		
IntraFreqEvent_r4	IntraFreqEvent-r4	Class-definitions		
IntraFreqEvent_LCR_r4	IntraFreqEvent-LCR-r4	Class-definitions		
IntraFreqEvent_1d_r5	IntraFreqEvent-1d-r5	Class-definitions		
IntraFreqEventCriteria	IntraFreqEventCriteria	Class-definitions		
IntraFreqEventCriteria_r4	IntraFreqEventCriteria-r4	Class-definitions		
IntraFreqEventCriteria_LCR_r4	IntraFreqEventCriteria-LCR-r4	Class-definitions		
IntraFreqEventCriteriaList	IntraFreqEventCriteriaList	Class-definitions		
IntraFreqEventCriteriaList_r4	IntraFreqEventCriteriaList-r4	Class-definitions		
IntraFreqEventCriteriaList_LCR_r4	IntraFreqEventCriteriaList-LCR-r4	Class-definitions		
IntraFreqEventResults	IntraFreqEventResults	Class-definitions		
IntraFreqMeasQuantity	IntraFreqMeasQuantity	Class-definitions		
IntraFreqMeasQuantity_FDD	IntraFreqMeasQuantity-FDD	Class-definitions		
IntraFreqMeasQuantity_TDD	IntraFreqMeasQuantity-TDD	Class-definitions		
IntraFreqMeasQuantity_TDDList	IntraFreqMeasQuantity-TDDList	Class-definitions		
IntraFreqMeasuredResultsList	IntraFreqMeasuredResultsList	Class-definitions		
IntraFreqMeasurementSysInfo_RSCP	IntraFreqMeasurementSysInfo-RSCP	Class-definitions		
IntraFreqMeasurementSysInfo_ECN0	IntraFreqMeasurementSysInfo-ECN0	Class-definitions		
IntraFreqMeasurementSysInfo_HCS_RSCP	IntraFreqMeasurementSysInfo-HCS-RSCP	Class-definitions		
IntraFreqMeasurementSysInfo_HCS_ECN0	IntraFreqMeasurementSysInfo-HCS-ECN0	Class-definitions		
IntraFreqMeasurementSysInfo_RSCP_LCR_r4	IntraFreqMeasurementSysInfo-RSCP-LCR-r4	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
IntraFreqMeasurement SysInfo_ECN0_LCR_ r4	IntraFreqMeasurement SysInfo-ECN0-LCR- r4	Class-definitions		
IntraFreqMeasurement SysInfo_HCS_RSCP_ _LCR_r4	IntraFreqMeasurement SysInfo-HCS-RSCP- LCR-r4	Class-definitions		
IntraFreqMeasurement SysInfo_HCS_ECN0_ _LCR_r4	IntraFreqMeasurement SysInfo-HCS-ECN0- LCR-r4	Class-definitions		
IntraFreqReportCriteri a	IntraFreqReportCriteri a	Class-definitions		
IntraFreqReportCriteri a_r4	IntraFreqReportCriteri a-r4	Class-definitions		
IntraFreqReportingCri teria	IntraFreqReportingCri teria	Class-definitions		
IntraFreqReportingCri teria_r4	IntraFreqReportingCri teria-r4	Class-definitions		
IntraFreqReportingCri teria_LCR_r4	IntraFreqReportingCri teria-LCR-r4	Class-definitions		
IntraFreqReportingQu antity	IntraFreqReportingQu antity	Class-definitions		
IntraFreqReportingQu antityForRACH	IntraFreqReportingQu antityForRACH	Class-definitions		
IntraFreqRepQuantity RACH_FDD	IntraFreqRepQuantity RACH-FDD	Class-definitions		
IntraFreqRepQuantity RACH_TDD	IntraFreqRepQuantity RACH-TDD	Class-definitions		
IntraFreqRepQuantity RACH_TDDList	IntraFreqRepQuantity RACH-TDDList	Class-definitions		
IntraFrequencyMeasur ement	IntraFrequencyMeasur ement	Class-definitions		
IntraFrequencyMeasur ement_r4	IntraFrequencyMeasur ement-r4	Class-definitions		
IODE	IODE	Class-definitions		
IP_Length	IP-Length	Class-definitions		
IP_PCCPCH_r4	IP-PCCPCH-r4	Class-definitions		
IP_Spacing	IP-Spacing	Class-definitions		
IP_Spacing_TDD	IP-Spacing-TDD	Class-definitions		
IS_2000SpecificMeasI nfo	IS-2000SpecificMeasI nfo	Class-definitions		
MaxNumberOfReporti ngCellsType1	MaxNumberOfReporti ngCellsType1	Class-definitions		
MaxNumberOfReporti ngCellsType2	MaxNumberOfReporti ngCellsType2	Class-definitions		
MaxNumberOfReporti ngCellsType3	MaxNumberOfReporti ngCellsType3	Class-definitions		
MaxReportedCellsOn RACH	MaxReportedCellsOn RACH	Class-definitions		
MeasuredResults	MeasuredResults	Class-definitions		
MeasuredResults_v39 0ext	MeasuredResults-v39 0ext	Class-definitions		
MeasuredResults_v59 0ext	MeasuredResults-v59 0ext	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
MeasuredResults_LCR_r4	MeasuredResults-LCR-r4	Class-definitions		
MeasuredResultsList	MeasuredResultsList	Class-definitions		
MeasuredResultsList_LCR_r4_ext	MeasuredResultsList-LCR-r4-ext	Class-definitions		
MeasuredResultsOnRACH	MeasuredResultsOnRACH	Class-definitions		
MeasurementCommand	MeasurementCommand	Class-definitions		
MeasurementCommand_r4	MeasurementCommand-r4	Class-definitions		
MeasurementControlSysInfo	MeasurementControlSysInfo	Class-definitions		
MeasurementControlSysInfo_LCR_r4_ext	MeasurementControlSysInfo-LCR-r4-ext	Class-definitions		
MeasurementIdentity	MeasurementIdentity	Class-definitions		
MeasurementQuantityGSM	MeasurementQuantityGSM	Class-definitions		
MeasurementReportingMode	MeasurementReportingMode	Class-definitions		
MeasurementType	MeasurementType	Class-definitions		
MeasurementType_r4	MeasurementType-r4	Class-definitions		
MeasurementValidity	MeasurementValidity	Class-definitions		
MonitoredCellRACH_List	MonitoredCellRACH-List	Class-definitions		
MonitoredCellRACH_Result	MonitoredCellRACH-Result	Class-definitions		
MultipathIndicator	MultipathIndicator	Class-definitions		
N_CR_T_CRMMaxHyst	N-CR-T-CRMMaxHyst	Class-definitions		
NavigationModelSatInfo	NavigationModelSatInfo	Class-definitions		
NavigationModelSatInfoList	NavigationModelSatInfoList	Class-definitions		
EphemerisParameterNC_Mode	EphemerisParameterNC-Mode	Class-definitions		
Neighbour	Neighbour	Class-definitions		
Neighbour_v390ext	Neighbour-v390ext	Class-definitions		
NeighbourList	NeighbourList	Class-definitions		
NeighbourList_v390ext	NeighbourList-v390ext	Class-definitions		
NeighbourQuality	NeighbourQuality	Class-definitions		
NewInterFreqCell	NewInterFreqCell	Class-definitions		
NewInterFreqCell_r4	NewInterFreqCell-r4	Class-definitions		
NewInterFreqCellList	NewInterFreqCellList	Class-definitions		
NewInterFreqCellList_r4	NewInterFreqCellList-r4	Class-definitions		
NewInterFreqCellSI_RSCP	NewInterFreqCellSI-RSCP	Class-definitions		
NewInterFreqCellSI_ECNO	NewInterFreqCellSI-ECNO	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
NewInterFreqCellSI_HCS_RSCP	NewInterFreqCellSI-HCS-RSCP	Class-definitions		
NewInterFreqCellSI_HCS_ECN0	NewInterFreqCellSI-HCS-ECN0	Class-definitions		
NewInterFreqCellSI_RSCP_LCR_r4	NewInterFreqCellSI-RSCP-LCR-r4	Class-definitions		
NewInterFreqCellSI_ECN0_LCR_r4	NewInterFreqCellSI-ECN0-LCR-r4	Class-definitions		
NewInterFreqCellSI_HCS_RSCP_LCR_r4	NewInterFreqCellSI-HCS-RSCP-LCR-r4	Class-definitions		
NewInterFreqCellSI_HCS_ECN0_LCR_r4	NewInterFreqCellSI-HCS-ECN0-LCR-r4	Class-definitions		
NewInterFreqCellSI_List_ECN0	NewInterFreqCellSI-List-ECN0	Class-definitions		
NewInterFreqCellSI_List_HCS_RSCP	NewInterFreqCellSI-List-HCS-RSCP	Class-definitions		
NewInterFreqCellSI_List_HCS_ECN0	NewInterFreqCellSI-List-HCS-ECN0	Class-definitions		
NewInterFreqCellSI_List_RSCP	NewInterFreqCellSI-List-RSCP	Class-definitions		
NewInterFreqCellSI_List_ECN0_LCR_r4	NewInterFreqCellSI-List-ECN0-LCR-r4	Class-definitions		
NewInterFreqCellSI_List_HCS_RSCP_LCR_r4	NewInterFreqCellSI-List-HCS-RSCP-LCR-r4	Class-definitions		
NewInterFreqCellSI_List_HCS_ECN0_LCR_r4	NewInterFreqCellSI-List-HCS-ECN0-LCR-r4	Class-definitions		
NewInterFreqCellSI_List_RSCP_LCR_r4	NewInterFreqCellSI-List-RSCP-LCR-r4	Class-definitions		
NewInterRATCell	NewInterRATCell	Class-definitions		
NewInterRATCell_B	NewInterRATCell-B	Class-definitions		
NewInterRATCellList	NewInterRATCellList	Class-definitions		
NewInterRATCellList_B	NewInterRATCellList-B	Class-definitions		
NewIntraFreqCell	NewIntraFreqCell	Class-definitions		
NewIntraFreqCell_r4	NewIntraFreqCell-r4	Class-definitions		
NewIntraFreqCellList	NewIntraFreqCellList	Class-definitions		
NewIntraFreqCellList_r4	NewIntraFreqCellList-r4	Class-definitions		
NewIntraFreqCellSI_RSCP	NewIntraFreqCellSI-RSCP	Class-definitions		
NewIntraFreqCellSI_ECN0	NewIntraFreqCellSI-ECN0	Class-definitions		
NewIntraFreqCellSI_HCS_RSCP	NewIntraFreqCellSI-HCS-RSCP	Class-definitions		
NewIntraFreqCellSI_HCS_ECN0	NewIntraFreqCellSI-HCS-ECN0	Class-definitions		
NewIntraFreqCellSI_RSCP_LCR_r4	NewIntraFreqCellSI-RSCP-LCR-r4	Class-definitions		
NewIntraFreqCellSI_ECN0_LCR_r4	NewIntraFreqCellSI-ECN0-LCR-r4	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
NewIntraFreqCellSI_HCS_RSCP_LCR_r4	NewIntraFreqCellSI-HCS-RSCP-LCR-r4	Class-definitions		
NewIntraFreqCellSI_HCS_ECN0_LCR_r4	NewIntraFreqCellSI-HCS-ECN0-LCR-r4	Class-definitions		
NewIntraFreqCellSI_List_RSCP	NewIntraFreqCellSI-List-RSCP	Class-definitions		
NewIntraFreqCellSI_List_ECN0	NewIntraFreqCellSI-List-ECN0	Class-definitions		
NewIntraFreqCellSI_List_HCS_RSCP	NewIntraFreqCellSI-List-HCS-RSCP	Class-definitions		
NewIntraFreqCellSI_List_HCS_ECN0	NewIntraFreqCellSI-List-HCS-ECN0	Class-definitions		
NewIntraFreqCellSI_List_RSCP_LCR_r4	NewIntraFreqCellSI-List-RSCP-LCR-r4	Class-definitions		
NewIntraFreqCellSI_List_ECN0_LCR_r4	NewIntraFreqCellSI-List-ECN0-LCR-r4	Class-definitions		
NewIntraFreqCellSI_List_HCS_RSCP_LCR_r4	NewIntraFreqCellSI-List-HCS-RSCP-LCR-r4	Class-definitions		
NewIntraFreqCellSI_List_HCS_ECN0_LCR_r4	NewIntraFreqCellSI-List-HCS-ECN0-LCR-r4	Class-definitions		
NonUsedFreqParameter	NonUsedFreqParameter	Class-definitions		
NonUsedFreqParameterList	NonUsedFreqParameterList	Class-definitions		
ObservedTimeDifferenceToGSM	ObservedTimeDifferenceToGSM	Class-definitions		
OTDOA_SearchWindowSize	OTDOA-SearchWindowSize	Class-definitions		
Pathloss	Pathloss	Class-definitions		
PenaltyTime_RSCP	PenaltyTime-RSCP	Class-definitions		
PenaltyTime_ECN0	PenaltyTime-ECN0	Class-definitions		
PendingTimeAfterTrigger	PendingTimeAfterTrigger	Class-definitions		
PeriodicalOrEventTrigger	PeriodicalOrEventTrigger	Class-definitions		
PeriodicalReportingCriteria	PeriodicalReportingCriteria	Class-definitions		
PeriodicalWithReportingCellStatus	PeriodicalWithReportingCellStatus	Class-definitions		
PLMNIdentitiesOfNeighbourCells	PLMNIdentitiesOfNeighbourCells	Class-definitions		
PLMNsOfInterFreqCellsList	PLMNsOfInterFreqCellsList	Class-definitions		
PLMNsOfIntraFreqCellsList	PLMNsOfIntraFreqCellsList	Class-definitions		
PLMNsOfInterRATCellsList	PLMNsOfInterRATCellsList	Class-definitions		
PositionEstimate	PositionEstimate	Class-definitions		
PositioningMethod	PositioningMethod	Class-definitions		
PRC	PRC	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
PrimaryCCPCH_RSCP	PrimaryCCPCH-RSCP	Class-definitions		
Q_HCS	Q-HCS	Class-definitions		
Q_OffsetS_N	Q-OffsetS-N	Class-definitions		
Q_QualMin	Q-QualMin	Class-definitions		
Q_RxlevMin	Q-RxlevMin	Class-definitions		
QualityEventResults	QualityEventResults	Class-definitions		
QualityMeasuredResults	QualityMeasuredResults	Class-definitions		
QualityMeasurement	QualityMeasurement	Class-definitions		
QualityReportCriteria	QualityReportCriteria	Class-definitions		
QualityReportingCriteria	QualityReportingCriteria	Class-definitions		
QualityReportingCriteriaSingle	QualityReportingCriteriaSingle	Class-definitions		
QualityReportingQuantity	QualityReportingQuantity	Class-definitions		
RAT_Type	RAT-Type	Class-definitions		
ReferenceCellPosition	ReferenceCellPosition	Class-definitions		
ReferenceLocation	ReferenceLocation	Class-definitions		
ReferenceTimeDifferenceToCell	ReferenceTimeDifferenceToCell	Class-definitions		
RemovedInterFreqCellList	RemovedInterFreqCellList	Class-definitions		
RemovedInterRATCellList	RemovedInterRATCellList	Class-definitions		
RemovedIntraFreqCellList	RemovedIntraFreqCellList	Class-definitions		
ReplacementActivationThreshold	ReplacementActivationThreshold	Class-definitions		
ReportDeactivationThreshold	ReportDeactivationThreshold	Class-definitions		
ReportingAmount	ReportingAmount	Class-definitions		
ReportingCellStatus	ReportingCellStatus	Class-definitions		
ReportingCellStatusOpt	ReportingCellStatusOpt	Class-definitions		
ReportingInfoForCellDCH	ReportingInfoForCellDCH	Class-definitions		
ReportingInfoForCellDCH_LCR_r4	ReportingInfoForCellDCH-LCR-r4	Class-definitions		
ReportingInterval	ReportingInterval	Class-definitions		
ReportingIntervalLong	ReportingIntervalLong	Class-definitions		
ReportingRange	ReportingRange	Class-definitions		
RL_AdditionInfoList	RL-AdditionInfoList	Class-definitions		
RL_InformationLists	RL-InformationLists	Class-definitions		
RLC_BuffersPayload	RLC-BuffersPayload	Class-definitions		
RRC	RRC	Class-definitions		
SatData	SatData	Class-definitions		
SatDataList	SatDataList	Class-definitions		
SatelliteStatus	SatelliteStatus	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
SatID	SatID	Class-definitions		
SFN_Offset_VValidity	SFN-Offset-Validity	Class-definitions		
SFN_SFNDrift	SFN-SFN-Drift	Class-definitions		
SFN_SFNObsTimeDifference	SFN-SFN-ObsTimeDifference	Class-definitions		
SFN_SFNObsTimeDifference1	SFN-SFN-ObsTimeDifference1	Class-definitions		
SFN_SFNObsTimeDifference2	SFN-SFN-ObsTimeDifference2	Class-definitions		
SFN_SFNOTD_Type	SFN-SFN-OTD-Type	Class-definitions		
SFN_SFNRelTimeDifference1	SFN-SFN-RelTimeDifference1	Class-definitions		
SFN_TOW_Uncertainty	SFN-TOW-Uncertainty	Class-definitions		
SIR	SIR	Class-definitions		
SIR_MeasurementList	SIR-MeasurementList	Class-definitions		
SIR_MeasurementResults	SIR-MeasurementResults	Class-definitions		
SIR_TFCS	SIR-TFCS	Class-definitions		
SIR_TFCS_List	SIR-TFCS-List	Class-definitions		
SIR_TimeslotList	SIR-TimeslotList	Class-definitions		
SubFrame1Reserved	SubFrame1Reserved	Class-definitions		
T_ADVinfo	T-ADVinfo	Class-definitions		
T_CRMax	T-CRMax	Class-definitions		
T_CRMaxHyst	T-CRMaxHyst	Class-definitions		
TemporaryOffset1	TemporaryOffset1	Class-definitions		
TemporaryOffset2	TemporaryOffset2	Class-definitions		
TemporaryOffsetList	TemporaryOffsetList	Class-definitions		
Threshold	Threshold	Class-definitions		
ThresholdNonUsedFrequency_deltaList	ThresholdNonUsedFrequency_deltaList	Class-definitions		
ThresholdPositionChange	ThresholdPositionChange	Class-definitions		
ThresholdSFN_GPS_TOW	ThresholdSFN-GPS-TOW	Class-definitions		
ThresholdSFN_SFNCChange	ThresholdSFN-SFN-Change	Class-definitions		
ThresholdUsedFrequency	ThresholdUsedFrequency	Class-definitions		
TimeInterval	TimeInterval	Class-definitions		
TimeslotInfo	TimeslotInfo	Class-definitions		
TimeslotInfo_LCR_r4	TimeslotInfo-LCR-r4	Class-definitions		
TimeslotInfoList	TimeslotInfoList	Class-definitions		
TimeslotInfoList_LCR_r4	TimeslotInfoList-LCR-r4	Class-definitions		
TimeslotInfoList_r4	TimeslotInfoList-r4	Class-definitions		
TimeslotISCP	TimeslotISCP	Class-definitions		
TimeslotISCP_List	TimeslotISCP-List	Class-definitions		
TimeslotListWithISCP	TimeslotListWithISCP	Class-definitions		
TimeslotWithISCP	TimeslotWithISCP	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
TimeToTrigger	TimeToTrigger	Class-definitions		
TrafficVolumeEventParam	TrafficVolumeEventParam	Class-definitions		
TrafficVolumeEventResults	TrafficVolumeEventResults	Class-definitions		
TrafficVolumeEventType	TrafficVolumeEventType	Class-definitions		
TrafficVolumeMeasQuantity	TrafficVolumeMeasQuantity	Class-definitions		
TrafficVolumeMeasSysInfo	TrafficVolumeMeasSysInfo	Class-definitions		
TrafficVolumeMeasureResults	TrafficVolumeMeasureResults	Class-definitions		
TrafficVolumeMeasureResultsList	TrafficVolumeMeasureResultsList	Class-definitions		
TrafficVolumeMeasurement	TrafficVolumeMeasurement	Class-definitions		
TrafficVolumeMeasurementObjectList	TrafficVolumeMeasurementObjectList	Class-definitions		
TrafficVolumeReportCriteria	TrafficVolumeReportCriteria	Class-definitions		
TrafficVolumeReportCriteriaSysInfo	TrafficVolumeReportCriteriaSysInfo	Class-definitions		
TrafficVolumeReportingCriteria	TrafficVolumeReportingCriteria	Class-definitions		
TrafficVolumeReportingQuantity	TrafficVolumeReportingQuantity	Class-definitions		
TrafficVolumeThreshold	TrafficVolumeThreshold	Class-definitions		
TransChCriteria	TransChCriteria	Class-definitions		
TransChCriteriaList	TransChCriteriaList	Class-definitions		
TransferMode	TransferMode	Class-definitions		
TransmittedPowerThreshold	TransmittedPowerThreshold	Class-definitions		
TriggeringCondition1	TriggeringCondition1	Class-definitions		
TriggeringCondition2	TriggeringCondition2	Class-definitions		
TX_InterruptionAfterTrigger	TX-InterruptionAfterTrigger	Class-definitions		
UDRE	UDRE	Class-definitions		
UE_6AB_Event	UE-6AB-Event	Class-definitions		
UE_6FG_Event	UE-6FG-Event	Class-definitions		
UE_AutonomousUpdateMode	UE-AutonomousUpdateMode	Class-definitions		
UE_InternalEventParam	UE-InternalEventParam	Class-definitions		
UE_InternalEventParamList	UE-InternalEventParamList	Class-definitions		
UE_InternalEventResults	UE-InternalEventResults	Class-definitions		
UE_InternalMeasQuantity	UE-InternalMeasQuantity	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UE_InternalMeasuredResults	UE-InternalMeasuredResults	Class-definitions		
UE_InternalMeasuredResults_LCR_r4	UE-InternalMeasuredResults-LCR-r4	Class-definitions		
UE_InternalMeasurement	UE-InternalMeasurement	Class-definitions		
UE_InternalMeasurement_r4	UE-InternalMeasurement-r4	Class-definitions		
UE_InternalMeasurementSysInfo	UE-InternalMeasurementSysInfo	Class-definitions		
UE_InternalReportCriteria	UE-InternalReportCriteria	Class-definitions		
UE_InternalReportingCriteria	UE-InternalReportingCriteria	Class-definitions		
UE_InternalReportingQuantity	UE-InternalReportingQuantity	Class-definitions		
UE_InternalReportingQuantity_r4	UE-InternalReportingQuantity-r4	Class-definitions		
UE_MeasurementQuantity	UE-MeasurementQuantity	Class-definitions		
UE_RX_TX_ReportEntry	UE-RX-TX-ReportEntry	Class-definitions		
UE_RX_TX_ReportEntryList	UE-RX-TX-ReportEntryList	Class-definitions		
UE_RX_TX_TimeDifferenceType1	UE-RX-TX-TimeDifferenceType1	Class-definitions		
UE_RX_TX_TimeDifferenceType2	UE-RX-TX-TimeDifferenceType2	Class-definitions		
UE_RX_TX_TimeDifferenceType2Info	UE-RX-TX-TimeDifferenceType2Info	Class-definitions		
UE_RX_TX_TimeDifferenceThreshold	UE-RX-TX-TimeDifferenceThreshold	Class-definitions		
UE_TransmittedPower	UE-TransmittedPower	Class-definitions		
UE_TransmittedPowerTDD_List	UE-TransmittedPowerTDD-List	Class-definitions		
UL_TrCH_Identity	UL-TrCH-Identity	Class-definitions		
UE_Positioning_Accuracy	UE-Positioning-Accuracy	Class-definitions		
UE_Positioning_CipherParameters	UE-Positioning-CipherParameters	Class-definitions		
UE_Positioning_Error	UE-Positioning-Error	Class-definitions		
UE_Positioning_ErrorCause	UE-Positioning-ErrorCause	Class-definitions		
UE_Positioning_EventParam	UE-Positioning-EventParam	Class-definitions		
UE_Positioning_EventParamList	UE-Positioning-EventParamList	Class-definitions		
UE_Positioning_EventSpecificInfo	UE-Positioning-EventSpecificInfo	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UE_Positioning_GPS_AcquisitionAssistance	UE-Positioning-GPS-AcquisitionAssistance	Class-definitions		
UE_Positioning_GPS_AdditionalAssistanceDataRequest	UE-Positioning-GPS-AdditionalAssistanceDataRequest	Class-definitions		
UE_Positioning_GPS_Almanac	UE-Positioning-GPS-Almanac	Class-definitions		
UE_Positioning_GPS_AssistanceData	UE-Positioning-GPS-AssistanceData	Class-definitions		
UE_Positioning_GPS_DGPS_Corrections	UE-Positioning-GPS-DGPS-Corrections	Class-definitions		
UE_Positioning_GPS_IonosphericModel	UE-Positioning-GPS-IonosphericModel	Class-definitions		
UE_Positioning_GPS_MeasurementResults	UE-Positioning-GPS-MeasurementResults	Class-definitions		
UE_Positioning_GPS_NavigationModel	UE-Positioning-GPS-NavigationModel	Class-definitions		
UE_Positioning_GPS_NavModelAddDataReq	UE-Positioning-GPS-NavModelAddDataReq	Class-definitions		
UE_Positioning_GPS_ReferenceCellInfo	UE-Positioning-GPS-ReferenceCellInfo	Class-definitions		
UE_Positioning_GPS_ReferenceTime	UE-Positioning-GPS-ReferenceTime	Class-definitions		
UE_Positioning_GPS_UTC_Model	UE-Positioning-GPS-UTC-Model	Class-definitions		
UE_Positioning_IPDL_Parameters	UE-Positioning-IPDL-Parameters	Class-definitions		
UE_Positioning_IPDL_Parameters_r4	UE-Positioning-IPDL-Parameters-r4	Class-definitions		
UE_Positioning_IPDL_Parameters_TDD_r4_ext	UE-Positioning-IPDL-Parameters-TDD-r4-ext	Class-definitions		
UE_Positioning_MeasuredResults	UE-Positioning-MeasuredResults	Class-definitions		
UE_Positioning_MeasuredResults_v390ext	UE-Positioning-MeasuredResults-v390ext	Class-definitions		
UE_Positioning_Measurement	UE-Positioning-Measurement	Class-definitions		
UE_Positioning_Measurement_v390ext	UE-Positioning-Measurement-v390ext	Class-definitions		
UE_Positioning_Measurement_r4	UE-Positioning-Measurement-r4	Class-definitions		
UE_Positioning_MeasurementEventResults	UE-Positioning-MeasurementEventResults	Class-definitions		
UE_Positioning_MeasurementInterval	UE-Positioning-MeasurementInterval	Class-definitions		
UE_Positioning_MethodType	UE-Positioning-MethodType	Class-definitions		
UE_Positioning_OTD_OA_AssistanceData	UE-Positioning-OTD-OA-AssistanceData	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UE_Positioning_OTD OA_AssistanceData_r4	UE-Positioning-OTD OA-AssistanceData-r4	Class-definitions		
UE_Positioning_OTD OA_AssistanceData_r4ext	UE-Positioning-OTD OA-AssistanceData-r4ext	Class-definitions		
UE_Positioning_OTD OA_AssistanceData_UEB	UE-Positioning-OTD OA-AssistanceData-UEB	Class-definitions		
UE_Positioning_IPDL _Parameters_TDDList_r4_ext	UE-Positioning-IPDL -Parameters-TDDList-r4-ext	Class-definitions		
UE_Positioning_OTD OA_Measurement	UE-Positioning-OTD OA-Measurement	Class-definitions		
UE_Positioning_OTD OA_Measurement_v390ext	UE-Positioning-OTD OA-Measurement-v390ext	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellInfo	UE-Positioning-OTD OA-NeighbourCellInfo	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellInfo_r4	UE-Positioning-OTD OA-NeighbourCellInfo-r4	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellInfo_UEB	UE-Positioning-OTD OA-NeighbourCellInfo-UEB	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellList	UE-Positioning-OTD OA-NeighbourCellList	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellList_r4	UE-Positioning-OTD OA-NeighbourCellList-r4	Class-definitions		
UE_Positioning_OTD OA_NeighbourCellList_UEB	UE-Positioning-OTD OA-NeighbourCellList-UEB	Class-definitions		
UE_Positioning_OTD OA_Quality	UE-Positioning-OTD OA-Quality	Class-definitions		
UE_Positioning_OTD OA_ReferenceCellInfo	UE-Positioning-OTD OA-ReferenceCellInfo	Class-definitions		
UE_Positioning_OTD OA_ReferenceCellInfo_r4	UE-Positioning-OTD OA-ReferenceCellInfo-r4	Class-definitions		
UE_Positioning_OTD OA_ReferenceCellInfo_UEB	UE-Positioning-OTD OA-ReferenceCellInfo-UEB	Class-definitions		
UE_Positioning_PositionEstimateInfo	UE-Positioning-PositionEstimateInfo	Class-definitions		
UE_Positioning_ReportCriteria	UE-Positioning-ReportCriteria	Class-definitions		
UE_Positioning_ReportingQuantity	UE-Positioning-ReportingQuantity	Class-definitions		
UE_Positioning_ReportingQuantity_v390ext	UE-Positioning-ReportingQuantity-v390ext	Class-definitions		
UE_Positioning_ReportingQuantity_r4	UE-Positioning-ReportingQuantity-r4	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
UE_Positioning_ResponseTime	UE-Positioning-ResponseTime	Class-definitions		
UTRA_CarrierRSSI	UTRA-CarrierRSSI	Class-definitions		
UTRAN_GPS_DriftRate	UTRAN-GPS-DriftRate	Class-definitions		
UTRAN_GPSReferenceTime	UTRAN-GPSReferenceTime	Class-definitions		
UTRAN_GPSReferenceTimeResult	UTRAN-GPSReferenceTimeResult	Class-definitions		
VarianceOfRLC_BufferPayload	VarianceOfRLC-BufferPayload	Class-definitions		
W	W	Class-definitions		
BCC	BCC	Class-definitions		
BCCH_ModificationInfo	BCCH-ModificationInfo	Class-definitions		
BCCH_ModificationTime	BCCH-ModificationTime	Class-definitions		
BSIC	BSIC	Class-definitions		
CBS_DRX_Level1Information	CBS-DRX-Level1Information	Class-definitions		
CDMA2000_Message	CDMA2000-Message	Class-definitions		
CDMA2000_MessageList	CDMA2000-MessageList	Class-definitions		
CDMA2000_UMTS_Frequency_List	CDMA2000-UMTS-Frequency-List	Class-definitions		
CellValueTag	CellValueTag	Class-definitions		
ExpirationTimeFactor	ExpirationTimeFactor	Class-definitions		
FDD_UMTS_Frequency_List	FDD-UMTS-Frequency-List	Class-definitions		
FrequencyInfoCDMA2000	FrequencyInfoCDMA2000	Class-definitions		
GERAN_SystemInfoBlock	GERAN-SystemInfoBlock	Class-definitions		
GERAN_SystemInformation	GERAN-SystemInformation	Class-definitions		
GSM_BA_Range	GSM-BA-Range	Class-definitions		
GSM_BA_Range_List	GSM-BA-Range-List	Class-definitions		
GSM_Classmark2	GSM-Classmark2	Class-definitions		
GSM_Classmark3	GSM-Classmark3	Class-definitions		
GSM_MessageList	GSM-MessageList	Class-definitions		
GsmSecurityCapability	GsmSecurityCapability	Class-definitions		
IdentificationOfReceivedMessage	IdentificationOfReceivedMessage	Class-definitions		
InterRAT_ChangeFailureCause	InterRAT-ChangeFailureCause	Class-definitions		
GERANlu_MessageList	GERANlu-MessageList	Class-definitions		
GERANlu_RadioAccessCapability	GERANlu-RadioAccessCapability	Class-definitions		
InterRAT_UE_RadioAccessCapability	InterRAT-UE-RadioAccessCapability	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
InterRAT_UE_RadioAccessCapabilityList	InterRAT-UE-RadioAccessCapabilityList	Class-definitions		
InterRAT_UE_RadioAccessCapability_v590ext	InterRAT-UE-RadioAccessCapability-v590ext	Class-definitions		
InterRAT_UE_SecurityCapability	InterRAT-UE-SecurityCapability	Class-definitions		
InterRAT_UE_SecurityCapList	InterRAT-UE-SecurityCapList	Class-definitions		
InterRAT_HO_FailureCause	InterRAT-HO-FailureCause	Class-definitions		
MasterInformationBlock	MasterInformationBlock	Class-definitions		
MIB_ValueTag	MIB-ValueTag	Class-definitions		
NCC	NCC	Class-definitions		
PLMN_ValueTag	PLMN-ValueTag	Class-definitions		
PredefinedConfigIdentityAndValueTag	PredefinedConfigIdentityAndValueTag	Class-definitions		
ProtocolErrorInformation	ProtocolErrorInformation	Class-definitions		
ReceivedMessageType	ReceivedMessageType	Class-definitions		
Rplmn_Information	Rplmn-Information	Class-definitions		
Rplmn_Information_r4	Rplmn-Information-r4	Class-definitions		
SchedulingInformation	SchedulingInformation	Class-definitions		
SchedulingInformationSIB	SchedulingInformationSIB	Class-definitions		
SchedulingInformationSIBSb	SchedulingInformationSIBSb	Class-definitions		
SegCount	SegCount	Class-definitions		
SegmentIndex	SegmentIndex	Class-definitions		
SFN_Prime	SFN-Prime	Class-definitions		
SIB_Data_fixed	SIB-Data-fixed	Class-definitions		
SIB_Data_variable	SIB-Data-variable	Class-definitions		
SIBOccurIdentity	SIBOccurIdentity	Class-definitions		
SIBOccurrenceIdentityAndValueTag	SIBOccurrenceIdentityAndValueTag	Class-definitions		
SIBOccurValueTag	SIBOccurValueTag	Class-definitions		
SIB_ReferenceList	SIB-ReferenceList	Class-definitions		
SIBSb_ReferenceList	SIBSb-ReferenceList	Class-definitions		
SIB_ReferenceListFACH	SIB-ReferenceListFACH	Class-definitions		
SIB_Type	SIB-Type	Class-definitions		
SIB_TypeAndTag	SIB-TypeAndTag	Class-definitions		
SIBSb_TypeAndTag	SIBSb-TypeAndTag	Class-definitions		
SibOFF	SibOFF	Class-definitions		
SibOFF_List	SibOFF-List	Class-definitions		
SysInfoType1	SysInfoType1	Class-definitions		
SysInfoType1_v3a0ext-IEs	SysInfoType1-v3a0ext-IEs	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
SysInfoType2	SysInfoType2	Class-definitions		
SysInfoType3	SysInfoType3	Class-definitions		
SysInfoType3_v4b0ext_IEs	SysInfoType3-v4b0ext-IEs	Class-definitions		
SysInfoType3_v590ext	SysInfoType3-v590ext	Class-definitions		
SysInfoType4	SysInfoType4	Class-definitions		
SysInfoType4_v4b0ext_IEs	SysInfoType4-v4b0ext-IEs	Class-definitions		
SysInfoType4_v590ext	SysInfoType4-v590ext	Class-definitions		
SysInfoType4_v5b0ext_IEs	SysInfoType4-v5b0ext-IEs	Class-definitions		
SysInfoType5	SysInfoType5	Class-definitions		
SysInfoType5_v4b0ext_IEs	SysInfoType5-v4b0ext-IEs	Class-definitions		
SysInfoType5_v590ext_IEs	SysInfoType5-v590ext-IEs	Class-definitions		
SysInfoType6	SysInfoType6	Class-definitions		
SysInfoType6_v4b0ext_IEs	SysInfoType6-v4b0ext-IEs	Class-definitions		
SysInfoType6_v590ext_IEs	SysInfoType6-v590ext-IEs	Class-definitions		
SysInfoType7	SysInfoType7	Class-definitions		
SysInfoType8	SysInfoType8	Class-definitions		
SysInfoType9	SysInfoType9	Class-definitions		
SysInfoType10	SysInfoType10	Class-definitions		
SysInfoType11	SysInfoType11	Class-definitions		
SysInfoType11_v4b0ext_IEs	SysInfoType11-v4b0ext-IEs	Class-definitions		
SysInfoType11_v590ext_IEs	SysInfoType11-v590ext-IEs	Class-definitions		
SysInfoType12	SysInfoType12	Class-definitions		
SysInfoType12_v4b0ext_IEs	SysInfoType12-v4b0ext-IEs	Class-definitions		
SysInfoType12_v590ext_IEs	SysInfoType12-v590ext-IEs	Class-definitions		
SysInfoType13	SysInfoType13	Class-definitions		
SysInfoType13_v3a0ext_IEs	SysInfoType13-v3a0ext-IEs	Class-definitions		
SysInfoType13_v4b0ext_IEs	SysInfoType13-v4b0ext-IEs	Class-definitions		
SysInfoType13_1	SysInfoType13-1	Class-definitions		
SysInfoType13_2	SysInfoType13-2	Class-definitions		
SysInfoType13_3	SysInfoType13-3	Class-definitions		
SysInfoType13_4	SysInfoType13-4	Class-definitions		
SysInfoType14	SysInfoType14	Class-definitions		
SysInfoType15	SysInfoType15	Class-definitions		
SysInfoType15_v4b0ext_IEs	SysInfoType15-v4b0ext-IEs	Class-definitions		
SysInfoType15_1	SysInfoType15-1	Class-definitions		

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ASN.1 Type Definitions By Reference				
Type Name	Type Reference	Module Identifier	Encoding Variation	Comments
SysInfoType15_2	SysInfoType15-2	Class-definitions		
SysInfoType15_3	SysInfoType15-3	Class-definitions		
SysInfoType16	SysInfoType16	Class-definitions		
SysInfoType17	SysInfoType17	Class-definitions		
SysInfoType17_v4b0ext_IEs	SysInfoType17-v4b0ext-IEs	Class-definitions		
SysInfoType17_v590ext_IEs	SysInfoType17-v590ext-IEs	Class-definitions		
SysInfoType18	SysInfoType18	Class-definitions		
SysInfoTypeSB1	SysInfoTypeSB1	Class-definitions		
SysInfoTypeSB2	SysInfoTypeSB2	Class-definitions		
TDD_UMTS_Frequency_List	TDD-UMTS-Frequency-List	Class-definitions		
ANSI_41_GlobalServiceRedirectInfo	ANSI-41-GlobalServiceRedirectInfo	Class-definitions		
ANSI_41_PrivateNeighbourListInfo	ANSI-41-PrivateNeighbourListInfo	Class-definitions		
ANSI_41_RAND_Information	ANSI-41-RAND-Information	Class-definitions		
ANSI_41_UserZoneID_Information	ANSI-41-UserZoneID-Information	Class-definitions		
ANSI_41_NAS_Parameter	ANSI-41-NAS-Parameter	Class-definitions		
Min_P_REV	Min-P-REV	Class-definitions		
NAS_SystemInformationANSI_41	NAS-SystemInformationANSI-41	Class-definitions		
NID	NID	Class-definitions		
P_REV	P-REV	Class-definitions		
SID	SID	Class-definitions		
Detailed Comments :				

Encoding Definitions			
Encoding Rule Name	Reference	Default	Comments
OctetUnaligned	TS 25.322 clause 9.2.1.2 – TMD PDU		<p>To be used for the encoding of RLC TMD PDU.</p> <p>An RLC PDU is a bit string. An RLC SDU is included into an RLC PDU from first bit onward. The TMD PDU is used to transfer user data when RLC is operating in transparent mode. No overhead is added to the SDU by RLC. The data length is not constrained to be a multiple of 8 bits.</p> <p>The TTCN writer shall take care of the length of the bitstring to be sent according to the allowed RLC size in the Transport Format.</p>
PER_Unaligned	X.691		<p>Packet encoding rules (X.691) unaligned and with adapted padding</p>
Detailed Comments :			

Test Suite Operation Definition	
Operation Name	: o_GetLeastSignificantBits(bstring : BITSTRING; lg : INTEGER)
Result Type	: BITSTRING
Comments	:
Description	
<p>o_GetLeastSignificantBits operation returns the 'lg' least significant bits of the original 'bstring'.</p> <p>for example:</p> <p>o_GetLeastSignificantBits('110011000101010'B, 3) = '010'B, o_GetLeastSignificantBits('110011000101010'B, 6) = '101010'B.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_GetMostSignificantBits(bstring : BITSTRING; lg :INTEGER)
Result Type	: BITSTRING
Comments	:
Description	
<p>o_GetMostSignificantBits operation returns the 'lg' most significant bits of the original 'bstring'.</p> <p>for example:</p> <p>o_GetMostSignificantBits('110011000101010'B, 3) = '110'B, o_GetMostSignificantBits('110011000101010'B, 6) = '110011'B.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_IntToOct(p_N : INTEGER; p_L: INTEGER)
Result Type	: OCTETSTRING
Comments	:
Description	
<p>o_IntToOct converts the INTEGER p_N into OCTETSTRING with length = p_L.</p> <p>for example:</p> <p>o_IntToOct(14,1) = '0E'O; o_IntToOct(18,1) = '12'O; o_IntToOct(18,2) = '0012'O.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name : o_AuthRspChk(p_AuthRsp: AuthRsp; p_AuthRspExt: AuthRspExt; p_K:BITSTRING; p_RAND: BITSTRING; p_Ext: BOOLEAN)	
Result Type : BOOLEAN	
Comments :	
Description	
<p>Checks the input parameter p_AuthRsp and p_AuthRspExt, both received in an Authentication Response, according to the authentication algorithm defined in the following procedure.</p> <p>The extension, p_AuthRspExt, is optional. Its presence is indicated by p_Ext.</p> <p>Returns TRUE if the Authentication Response contained in parameters p_AuthRsp and eventually p_AuthRspExt is correct, FALSE otherwise.</p> <p>The value of tcv_AuthN indicates whether the AuthRspExt has been provided by the UE or not (n=31, or 31 < n < 128). See 3G TS 34.108 cl. 8.1.2.</p> <p>If not the parameter p_AuthRspExt is not to be used.</p> <p>Algorithm (without the knowledge of tcv_AuthN):</p> <pre>=====</pre> <p>if NOT p_Ext EvaluateAuthRsp else EvaluateAuthRspAndAuthRspExt</p> <p>EvaluateAuthRsp:</p> <pre>=====</pre> <p>resultbitstring = o_BitstringXOR(XRES, AuthRsp)</p> <p>if resultbitstring is all 0s then there is a match.</p> <p>EvaluateAuthRspAndAuthRspExt:</p> <pre>=====</pre> <p>XRESHigh = o_BitstringXtract(XRES, 32, 32, 0)</p> <p>/* XRES divides into 2 parts: the higher part of 32 bits related to AuthRsp and the lower part related to AuthRspExt */</p> <p>/* SourceLength of 32 is only to ensure usage of the procedure */</p> <p>resultbitstring = o_BitstringXOR(XRESHigh, AuthRsp)</p> <p>if resultbitstring is all 0s then there is a match for the first 32 bits:EvaluateAuthRspExt else Authentication failed.</p> <p>EvaluateAuthRspExt:</p> <pre>=====</pre> <p>/* As AuthRspExt may not be octet aligned the last octet indicated in AuthRspExt is not used for checking */</p> <p>if (AuthRspExt.iel = 1) then Authentication passed</p> <p>/* there was only 1 possibly incomplete octet which is not used */</p> <p>else</p> <pre>{</pre> <p>AuthRspExthigh = o_BitstringXtract(AuthRspExt.authRsp, ((AuthRspExt.iel -1)* 8), (AuthRspExt.iel -1)* 8, 0)</p> <p>/* extract (AuthRspExt.iel -1)* 8 bits starting from bit 0 */</p> <p>XRESLow = o_BitstringXtract(XRES, ((AuthRspExt.iel -1)* 8 + 32), (AuthRspExt.iel -1)* 8, 32)</p> <p>/* extract (AuthRspExt.iel -1)* 8 bits starting from bit 32 */</p> <p>resultbitstring = o_BitstringXOR(XRESLow, AuthRspExthigh, (AuthRspExt.iel -1)* 8)</p> <p>if resultbitstring is all 0s then there is a match for the bits following the first 32 bits else Authentication failed</p> <pre>}</pre>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name :	o_BitstringConcat(p_Str1, p_Str2: BITSTRING; p_Len1, p_Len2: INTEGER)
Result Type :	BITSTRING
Comments :	
Description	
<p>Performs the concatenation of 2 bitstrings of possibly different lengths. The bit significance is from left to right, ie the MSB is at the lefthand side.</p> <p>Returns a resulting bitstring p_Str1 p_Str2 of length p_Len1 + p_Len.</p> <p>Example: o_BitstringConcat('010101'B, '11'B) produces '01010111'B of length 6 + 2 = 8..</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name :	o_BitstringXOR(p_Str1, p_Str2: BITSTRING; p_Len: INTEGER)
Result Type :	BITSTRING
Comments :	
Description	
<p>Performs an XOR operation using 2 bitstrings of the same length (p_Len).</p> <p>Returns a resulting Bitstring of length p_Len.</p> <p>Example: o_BitstringXOR('0011'B, '0101'B, 4) produces '0110'B</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_BitstringXtract(p_Str: BITSTRING; p_SrcLen, p_TargetLen, p_Offset: INTEGER)
Result Type	: BITSTRING
Comments	:
Description	
<p>Performs the wrap around extract of a bitstring. The length of the string from which extraction is to be made is specified in p_SrcLen. The length of the bitstring to be extracted is indicated as p_TargetLen, the offset in the original string is indicated in p_Offset.</p> <p>The bit position 0 is at the left.</p> <p>Returns a resulting bitstring of length p_TargetLen.</p> <p>Examples:</p> <p>o_BitstringXtract('101010'B, 6, 2, 1) produces '01'B.</p> <p>o_BitstringXtract('101010'B, 6, 4, 3) produces '0101'B, wrapping around.</p> <p>o_BitstringXtract('111000'B, 6, 4, 5) produces '0111'B, wrapping around.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_ConvertIMSI(p_Imsi : HEXSTRING)
Result Type	: IMSI_GSM_MAP
Comments	: The input parameter 'imsi' is a BCD string (subset of HEXSTRING), the result is of type IMSI_GSM_MAP.
Description	
The input parameter p_Imsi is a BCD string (subset of HEXSTRING), the result is of type IMSI_GSM_MAP.	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_ConvertPTMSI(p_PTMSI : OCTETSTRING)
Result Type	: P_TMSI_GSM_MAP
Comments	: The input parameter 'PTMSI' is a OCTETSTRING, the result is of type TMSI_GSM_MAP.
Description	
The input parameter 'PTMSI' is a OCTETSTRING, the result is of type P_TMSI_GSM_MAP.	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_ConvertTMSI(p_Tmsi : OCTETSTRING)
Result Type	: TMSI_GSM_MAP
Comments	: The input parameter 'tmsi' is a OCTETSTRING, the result is of type TMSI_GSM_MAP.
Description	
The input parameter p_Tmsi is a OCTETSTRING, the result is of type TMSI_GSM_MAP.	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_ConvtPLMN(p_MCC, p_MNC : HEXSTRING)
Result Type	: OCTETSTRING
Comments	:
Description	
<p>the functions of o_ConvtPLMN are as following:</p> <ol style="list-style-type: none"> 1. The least significant HEX of p_MNC is removed from p_MNC and inserted into p_MCC in the position left to the third HEX to form a new p_MCC of 4 HEXs, then swap the first HEX (left most, most significant Hex) with the second HEX of the new p_MCC. 2. Swap the first Hex with the second HEX of the remaining part of p_MNC and append it to the new p_MCC formed in Step1 above. <p>For example: o_ConvtPLMN('123'H, '456'H) = '216354'O o_ConvtPLMN ('234'H, '01F'H) = '32F410'O</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name : o_GetN_OctetsFromPRBS(p_Start, p_N: INTEGER) Result Type : OCTETSTRING Comments :	
Description	
<p>This operation returns N octets from a repeated pseudo random bit sequence, starting with octet position p_Start. The PRBS is the 2047 bit pseudo random test pattern defined in ITU-T Recommendation O.153 for measurements at 64 kbit/s and N x 64 kbit/s</p> <p>o_GetN_OctetsFromPRBS(p_Start, p_N) generates an OCTETSTRING containing p_N octets starting from octet number p_Start in the PRBS.</p> <p>Requirements p_Start >= 0 p_N >= 1</p> <p>Definition Define the 2047 bit PRBS sequence b(i) as an m-sequence produced by using the following primitive (over GF(2)) generator polynomial of degree 11:</p> $X^{11} + X^9 + 1$ <p>This sequence is defined recursively as:</p> $b(i) = 1, i = 0, 1, \dots, 10$ $b(i) = b(i - 2) + b(i - 11) \text{ modulo } 2, i = 11, 16, \dots, 2046$ <p>The OCTETSTRING, o(j) generated by the present TSO is produced by extracting p_N octets from the repeated sequence b(i) as follows:</p> $o(j,k) = b(((n_Start + j) * 8 + k) \text{ modulo } 2047)$ <p>where:</p> $j = 0, 1, \dots, p_N - 1$ $k = 0, 1, \dots, 7$ <p>o(j,k) is the kth bit of the jth octet in o(j), o(j,0) is the MSB of the jth octet in o(j), o(j,7) is the LSB of the jth octet in o(j),</p> <p>Example results: o_GetN_OctetsFromPRBS(0, 25) and o_GetN_OctetsFromPRBS(2047, 25) both return:</p> <p>'FFE665A5C5CA3452085408ABEECE4B0B813FD337873F2CD1E2'O</p> <p>o_GetN_OctetsFromPRBS(255, 25) and o_GetN_OctetsFromPRBS(255 + 2047, 25) both return</p> <p>'01FFCCCB4B8B9468A410A81157DD9C9617027FA66F0E7E59A3'O</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_GetPI (p_Imsi : HEXSTRING; p_Np: INTEGER)
Result Type	: BITSTRING
Comments	: The operation is used to calculate the PI (Page Indicator) from the given input parameters.
Description	
<p>The PI is calculated as following: $PI = drx_index \bmod np$</p> <p>The drx_index is calculated as described hereafter: $drx_index = (p_Imsi / 8192)$</p> <p>This calculation is defined in TS 25.304 clause 8.3.</p> <p>NOTE: the IMSI is passed as HEXSTRING, the relevant conversion shall be done.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_HexToDigitsMCC(p_BCDdigits : HEXSTRING)
Result Type	: MCC
Comments	:
Description	
<p>The input parameter p_BCDdigits shall be a BCD string (subset of HEXSTRING), the result is a SEQUENCE (SIZE(3)) OF digit (MCC).</p> <p>NOTE: The length of p_BCDdigits shall be 3. User shall take the responsibility of fulfilling this requirement.</p> <p>for example:</p> <p>$o_HexToDigitsMCC('111'H) = \{1, 1, 1\}$ $o_HexToDigitsMCC('123'H) = \{1, 2, 3\}$.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name :	o_HexToDigitsMNC(p_BCDdigits : HEXSTRING)
Result Type :	MNC
Comments :	
Description	
<p>The length of the input parameter p_BCDdigits shall be 3 HEX DIGITS, the first two shall be BCDdigit (subset of HEXSTRING) the result is a SEQUENCE (SIZE(2..3)) OF Digit (MNC).</p> <p>The function of this operation is:</p> <ol style="list-style-type: none"> 1. the least significant HEX is removed if it is 'F' and the operation returns SEQUENCE (SIZE(2)) OF Digit. 2. the operation returns SEQUENCE (SIZE(3)) OF Digit if all 3 HEX digits in p_BCDdigits are BCD Digit. <p>for example:</p> <p>o_HexToDigitsMNC('123'H) = {1, 2, 3}</p> <p>o_HexToDigitsMNC('13F'H) = {1, 3}.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name :	o_OctetstringConcat(p_Str1, p_Str2: OCTETSTRING)
Result Type :	OCTETSTRING
Comments :	
Description	
<p>o_OctetstringConcat Performs the concatenation of 2 octetstrings of possibly different lengths. The octet significance is from left to right, i.e. the MSB is at the lefthand side.</p> <p>Returns a resulting octetstring p_Str1 p_Str2 .</p> <p>Example:</p> <p>o_OctetstringConcat('135'O, '9A38'O) = '1359A38'O.</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name :	o_OctToBit (p_OctetStr: OCTETSTRING)
Result Type :	BITSTRING
Comments :	Converts an OCTETSTRING into a BITSTRING
Description	
<p>The size of the resulting BITSTRING is 8 times the size of the input OCTETSTRING</p>	
Detailed Comments :	

Test Suite Operation Definition	
Operation Name	: o_SIB_PER_Encoding(p_SIB : SIB)
Result Type	: BITSTRING
Comments	: generate the unaligned PER codes without "Encoder added (0–7) bits padding" from the input system information block
Description	
<p>The function of the o_SIB_PER_Encoding is as the follows:</p> <p>it returns the unaligned PER encoding (BIT STRING) of the input system information block p_SIB (without "Encoder added (1–7) bits padding"). The bits corresponding to the encoding of the CHOICE of the SIB type shall be removed.</p> <p>Example: for the following SIBType1 value:</p> <pre> SysInfoType1 ::= { cn_CommonGSM_MAP_NAS_SysInfo '0080'O, cn_DomainSysInfoList {{cn_DomainIdentity ps_domain, cn_Type gsm_MAP: '0000'O, cn_DRX_CycleLengthCoeff p_CellInfo.dRX_CycleLength }, {cn_DomainIdentity cs_domain, cn_Type gsm_MAP: o_OctetstringConcat(p_T3212, cn_DRX_CycleLengthCoeff p_CellInfo.dRX_CycleLength } }, o_IntToOct(p_ATTFlag, 1)), cn_DRX_CycleLengthCoeff p_CellInfo.dRX_CycleLength } }, ue_ConnTimersAndConstants { t_301 ms2000, n_301 2, t_302 ms4000, n_302 3, t_304 ms1000, n_304 3, t_305 m60, t_307 s50, t_308 ms320, t_309 8, t_310 ms320, n_310 5, t_311 ms500, t_312 5, n_312 s200, t_313 10, n_313 s20, t_314 s20, t_315 s30, n_315 s200, t_316 s50, t_317 s1800 }, ue_IdleTimersAndConstants { t_300 ms400, n_300 7, t_312 10, n_312 s200 }, nonCriticalExtensions } </pre> <p>The operation returns BITSTRING:"1110010000000010000000011000100000000000000010000100000000000000011111111111111111110100101100011011011100110111111110100101011101010011101001110111100101111010011"</p>	

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Test Suite Operation Definition
Detailed Comments : 3GPP TS 25.331 clause 12.1

Test Suite Operation Definition
Operation Name : o_SIB_Segmentation(p_SIBBitString : BITSTRING) Result Type : SegmentsOfSysInfoBlock Comments : The returned result is a structured type.
Description
<p>The function of the o_SIB_Segmentation is as following:</p> <ol style="list-style-type: none">1. If the p_SIBBitString is less than or equal to 226 bits, the bit string is fit into one segment. If the bit string is less than 226 bits but more than 214 bits, the segment shall be padded to 226 bits long with padding bits set to '0'B.2. If the input operand p_SIBBitString is longer than 226 bits it is segmented from left to right into segments, each segment except the last one is 222 bits . The last segment may be 222 bits or shorter. If the length of last segment is greater than 214 bits pad it to 222 bits with padding bits set to '0'B.3. The number of segments is assigned to segCount field of the result.4. The first segment is assigned to seg1 field of the result, the second segment is assigned to the seg2 field of the result, the third segment is assigned to the seg3 field of the result, and so on till the last segment.
Detailed Comments :

Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
pc_RAB_A_18c_26	BOOLEAN	PICS, Table A.18c/26	RAB for TC_14_2_26
pc_RAB_A_18c_12	BOOLEAN	PICS, Table A.18c/12	RAB for TC_14_2_12
pc_RAB_A_18c_13_1	BOOLEAN	PICS, Table A.18c/13.1	RAB for TC_14_2_13_1
pc_RAB_A_18c_13_2	BOOLEAN	PICS, Table A.18c/13.2	RAB for TC_14_2_13_2
pc_RAB_A_18c_14_1	BOOLEAN	PICS, Table A.18c/14.1	RAB for TC_14_2_14_1
pc_RAB_A_18c_14_2	BOOLEAN	PICS, Table A.18c/14.2	RAB for TC_14_2_14_2
pc_RAB_A_18c_23a_1	BOOLEAN	PICS, Table A.18c/23a.1	RAB for TC_14_2_23a_1
pc_RAB_A_18c_23_1	BOOLEAN	PICS, Table A.18c/23.1	RAB for TC_14_2_23_1
pc_RAB_A_18c_27	BOOLEAN	PICS, Table A.18c/27	RAB for TC_14_2_27
pc_RAB_A_18c_28	BOOLEAN	PICS, Table A.18c/28	RAB for TC_14_2_28
pc_RAB_A_18c_29	BOOLEAN	PICS, Table A.18c/29	RAB for TC_14_2_29
pc_RAB_A_18c_31_1	BOOLEAN	PICS, Table A.18c/31.1	RAB for TC_14_2_31_1
pc_RAB_A_18c_32_1	BOOLEAN	PICS, Table A.18c/32.1	RAB for TC_14_2_32_1
pc_RAB_A_18c_32_2	BOOLEAN	PICS, Table A.18c/32.2	RAB for TC_14_2_32_2
pc_RAB_A_18c_34_1	BOOLEAN	PICS, Table A.18c/34.1	RAB for TC_14_2_34_1
pc_RAB_A_18c_41	BOOLEAN	PICS, Table A.18c/41	RAB for TC_14_2_41
pc_RAB_A_18c_43_1	BOOLEAN	PICS, Table A.18c/43.1	RAB for TC_14_2_43_1
pc_RAB_A_18c_43_2	BOOLEAN	PICS, Table A.18c/43.2	RAB for TC_14_2_43_2
pc_RAB_A_18c_49_1	BOOLEAN	PICS, Table A.18c/49.1	RAB for TC_14_2_49_1
pc_RAB_A_18c_5a	BOOLEAN	PICS, Table A.18c/5a	RAB for TC_14_2_5a
pc_RAB_A_18c_51_1	BOOLEAN	PICS, Table A.18c/51.1	RAB for TC_14_2_51_1
pc_RAB_A_18c_7a	BOOLEAN	PICS, Table A.18c/7a	RAB for TC_14_2_7
pc_RAB_A_18c_9	BOOLEAN	PICS, Table A.18c/9	RAB for TC_14_2_9
pc_RAB_A_18e_2	BOOLEAN	PICS, Table A.18e/2	RAB for TC_14_4_2
pc_RAB_A_18e_3	BOOLEAN	PICS, Table A.18e/3	RAB for TC_14_4_3
px_DSCH_RNTI	DSCH_RNTI	PIXIT Table B.1	DSCH RNTI. (Copied from C-RNTI) Default value: '0000 0000 0000 0010'B
pc_RAB_A_18e_5	BOOLEAN	PICS, Table A.18e/5	RAB for TC_14_4_2a
pc_FDD	BOOLEAN	PICS, Table A.1/1	FDD(DS)
pc_RAB_A_18c_15	BOOLEAN	PICS, Table A.18c/15	UE supports UTRAN Streaming/unknown/uplink:1 4.4 DL:14.4 kbps/CS RAB + uplink:3.4 DL:3.4 kbps SRBs
pc_RAB_A_18c_16	BOOLEAN	PICS, Table A.18c/16	UE supports UTRAN Streaming/unknown/uplink:2 8.8 DL:28.8 kbps/CS RAB + uplink:3.4 DL:3.4 kbps SRBs
pc_RAB_A_18c_17	BOOLEAN	PICS, Table A.18c/17	UE supports UTRAN Streaming/unknown/uplink:5 7.6 DL:57.6 kbps/CS RAB + uplink:3.4 DL:3.4 kbps SRBs
pc_RAB_A_18c_4	BOOLEAN	PICS, Table A.18c/4	RAB for TC_14_2_4
pc_RAB_A_18c_40	BOOLEAN		RAB for TC_14_2_40
pc_AutomaticAttachSwitchON	BOOLEAN	PICS, Table A	Parameter is TRUE if UE supports automatic triggering of GMM Attach procedure when switched on
pc_Background	BOOLEAN	PICS, Table A.6/4	Background QoS/traffic class supported

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Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
pc_CS	BOOLEAN	PICS, Table A.3/1	Circuit Switched
pc_Interactive	BOOLEAN	PICS, Table A.6/3	Interactive QoS/traffic class supported
pc_MS_ClsmkA5_1	B1	PICS Table A.20/44	default Algorithm A5/1 supported. Default value: '0'B(i.e. A5/1 is supported)
pc_MS_ClsmkA5_2	B1	PICS Table A.20/46	default Algorithm A5/2 supported. Default value: '0'B (encryption algorithm A5/2 not available)
pc_MS_ClsmkA5_3	B1	PICS Table A.20/47	default Algorithm A5/3 supported. Default value: '0'B(encryption algorithm A5/3 not available)
pc_MS_ClsmkA5_4	B1	PICS Table A.20/48	default Algorithm A5/3 supported. Default value: '0'B(encryption algorithm A5/4 not available)
pc_MS_ClsmkA5_5	B1	PICS Table A.20/49	default Algorithm A5/3 supported. Default value: '0'B(encryption algorithm A5/5 not available)
pc_MS_ClsmkA5_6	B1	PICS Table A.20/50	default Algorithm A5/3 supported. Default value: '0'B(encryption algorithm A5/6 not available)
pc_MS_ClsmkA5_7	B1	PICS Table A.20/51	default Algorithm A5/7 supported. Default value: '0'B(encryption algorithm A5/7 not available)
pc_PS	BOOLEAN	PICS, Table A.3/2	Packet Switched
pc_SwitchOnOff	BOOLEAN	PICS, Table A.20/35	switch on/off supported
pc_UEA1_Supp	BOOLEAN	PICS, Table A.20/27	Support of UMTS encryption algorithm UEA1
pc_UMTS_GSM	BOOLEAN	PICS, Table A.1/4	To check if the UE supports both UMTS as well as GSM. If the variable is set as TRUE, means that both UMTS and GSM is supported. If the variable is set as FALSE, means that UE supports only UMTS
pc_USIM_Rmv	BOOLEAN	PICS, Table A.20/36	USIM removable without power down supported

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Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
px_AuthAMF	BITSTRING	PIXIT Table B.1	Authentication Management Field (16 bits). The value shall be different from '1111 1111 1111 111'B (AMFresynch). Default value: no default value can be proposed
px_AuthK	BITSTRING	PIXIT Table B.1	Authentication Key (128 bits). Default value: '0000000000000000100000 0100000001100000100000 0010100000110000001110 0001000000010010000101 0000010110000110000001 1010000111000001111'B
px_AuthN	INTEGER	PIXIT Table B.1	value of n to initialize tcv_AuthN (length of extended response) min 31, max 127 (TS 34.108 cl. 8.1.2). Default value: 127
px_AuthRAND	BITSTRING	PIXIT Table B.1	Random Challenge (128 bits). Default value: '010101..01'B The value shall not be repeatable with 3 bits i.e. something like '001001001001...001'B not allowed.
px_CipherAlg	B3	PIXIT Table B.1	Cipher algorithm. Default value: (A5/1) '000'B
px_CipheringOnOff	BOOLEAN	PIXIT Table B.1	Security mode – TRUE if ciphering is applicable. Default value: TRUE
px_FDD_OperationBand	INTEGER	PIXIT Table B.1	The operation band under test, as defined in 34.108 clause 5.1.1. Value 1 means Band 1, 2 means Band 2, 3 means Band 3, 6 means Band 6. Rest of the values are not defined. This pixit shall be set in synchronisation with the values that are being set to 6 other Pixits viz: px_UARFCN_D_High,px_UARFCN_U_High, px_UARFCN_D_Mid,px_UARFCN_L_Mid, px_UARFCN_D_Low, px_UARFCN_U_Low
px_FRESH	Fresh	PIXIT Table B.1	FRESH. Default value: no default value can be proposed
px_IMSI_Def	HEXSTRING	PIXIT Table B.1	default IMSI. Default value: '001010123456063'H

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Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
px_MS_ClsmkESIND	B1	PICS Table B.1	default Early Sending Indication. Default value: '0'B("Controlled Early Classmark Sending" option is not implemented in the MS")
px_MS_ClsmkRF_PwrCap	B3	PIXIT Table B.1	default RF Power Capability. Default value: '000'B
px_MS_ClsmkRevLvl	B2	PIXIT Table B.1	default Revision Level. Default value: '10'B
px_PTMSI_Def	OCTETSTRING	PIXIT Table B.1	default PTMSI . Default value: '12345678'O
px_PTMSI_SigDef	OCTETSTRING	PIXIT Table B.1	default PTMSI signature (3 octets, 3GPP 24.008 / 10.5.5.8). Default value: 'AB1234'O
px_PriScrmCode	PrimaryScramblingCode	PIXIT Table B.1	Primary scrambling code. Default value: 100
px_RAT	RatType	PIXIT Table B.1	This parameter is used to specify which radio access technology is being used for the current test execution. Valid values: fdd and tdd. Default value: fdd
px_SRNC_Id	SRNC_Identity	PIXIT Table B.1	SRNC Id. Default value: '000000000001'B
px_SRNTI	S_RNTI	PIXIT Table B.1	S RNTI. Default value: '0000000000000000001'B
px_TCellA	Tcell	PIXIT Table B.1	TCell value for cell A. Default value: 0
px_TCellB	Tcell	PIXIT Table B.1	TCell value for cell B. Default value: 512
px_TCellC	Tcell	PIXIT Table B.1	TCell value for cell C. Default value: 1536
px_TCellD	Tcell	PIXIT Table B.1	TCell value for cell D. Default value: 321
px_TCellE	Tcell	PIXIT Table B.1	TCell value for cell E. Default value: 833
px_TCellF	Tcell	PIXIT Table B.1	TCell value for cell F. Default value: 6577
px_TCellG	Tcell	PIXIT Table B.1	TCell value for cell G. Default value: 7253
px_TCellH	Tcell	PIXIT Table B.1	TCell value for cell H. Default value: 4351
px_TMSI_Def	OCTETSTRING	PIXIT Table B.1	default TMSI. Default value: '12345678'O
px_UARFCN_D_High	INTEGER	PIXIT Table B.1	High Range downlink UARFCN value. Default value: 10837
px_UARFCN_D_Low	INTEGER	PIXIT Table B.1	Low Range downlink UARFCN value Default value: 10563

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Test Suite Parameter Declarations			
Parameter Name	Type	PICS/PIXIT Ref	Comments
px_UARFCN_D_Mid	INTEGER	PIXIT Table B.1	Mid Range downlink UARFCN value Default value: 10700
px_UARFCN_U_High	INTEGER	PIXIT Table B.1	High Range uplink UARFCN value. This value shall be set based on the operation band supported. Default value: 9887
px_UARFCN_U_Low	INTEGER	PIXIT Table B.1	Low Range uplink UARFCN value. This value shall be set based on the operation band supported. Default value: 9613
px_UARFCN_U_Mid	INTEGER	PIXIT Table B.1	Mid Range uplink UARFCN value. This value shall be set based on the operation band supported. Default value: 9750
px_UE_OpModeDef	UE_OperationMode	PIXIT Table B.1	Default UE operation mode (either opModeA or opModeC). (For most UEs this corresponds class-A or class-C, and can not be changed by the user). Default value: opModeA
px_UL_ScramblingCode	UL_ScramblingCode	PIXIT Table B.1	UL scrambling code value to be used by UE. Default value : 0
px_UTRAN_GERAN	UTRAN_GERAN	PIXIT Table B.1	This parameter is used to specify for which region the system information blocks are broadcast in the test execution. Valid values: "UTRAN only" and "UTRAN and GERAN". Default value: "UTRAN and GERAN"
Detailed Comments :			

Test Case Selection Expression Definitions		
Expression Name	Selection Expression	Comments
RB_Intract_Backgr_C397	(pc_FDD) AND pc_RAB_A_18c_23a_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/23a.1
RB_Conv_IB_C183	(pc_FDD) AND pc_RAB_A_18c_51_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/51.1
RB_Conversational_C118	(pc_FDD) AND pc_RAB_A_18c_12	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/12
RB_Conversational_C119	(pc_FDD) AND pc_RAB_A_18c_13_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/13.1
RB_Conversational_C120	(pc_FDD) AND pc_RAB_A_18c_13_2	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/13.2
RB_Conversational_C121	(pc_FDD) AND pc_RAB_A_18c_14_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/14.1
RB_Conversational_C122	(pc_FDD) AND pc_RAB_A_18c_14_2	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/14.2
RB_Intract_Backgr_C131	(pc_FDD) AND pc_RAB_A_18c_23_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/23.1
RB_Intract_Backgr_C140	(pc_FDD) AND pc_RAB_A_18c_26	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/26
RB_Intract_Backgr_C141	(pc_FDD) AND pc_RAB_A_18c_27	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/27
RB_Intract_Backgr_C142	(pc_FDD) AND pc_RAB_A_18c_28	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/28
RB_Intract_Backgr_C143	(pc_FDD) AND pc_RAB_A_18c_29	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/29
RB_Intract_Backgr_C145	(pc_FDD) AND pc_RAB_A_18c_31_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/31.1
RB_Intract_Backgr_C147	(pc_FDD) AND pc_RAB_A_18c_32_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/32.1
RB_Intract_Backgr_C148	(pc_FDD) AND pc_RAB_A_18c_32_2	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/32.2
RB_Intract_Backgr_C151	(pc_FDD) AND pc_RAB_A_18c_34_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/34.1
RB_Intract_Backgr_C204	(pc_FDD) AND pc_RAB_A_18e_2	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18e/2
RB_Intract_Backgr_C205	(pc_FDD) AND pc_RAB_A_18e_3	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18e/3
RB_Speech_C110	(pc_FDD) AND pc_RAB_A_18c_4	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/4
RB_Speech_C115	(pc_FDD) AND pc_RAB_A_18c_9	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/9
RB_Speech_Conv_C179	(pc_FDD) AND pc_RAB_A_18c_49_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/49.1
RB_Speech_IB_C167	(pc_FDD) AND pc_RAB_A_18c_40	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/40
RB_Speech_IB_C168	(pc_FDD) AND pc_RAB_A_18c_41	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/41
RB_Speech_IB_C171	(pc_FDD) AND pc_RAB_A_18c_43_1	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/43.1
RB_Speech_IB_C172	(pc_FDD) AND pc_RAB_A_18c_43_2	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/43.2
RB_Streaming_C123	(pc_FDD) AND pc_RAB_A_18c_15	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/15
RB_Streaming_C124	(pc_FDD) AND pc_RAB_A_18c_16	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/16

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Test Case Selection Expression Definitions		
Expression Name	Selection Expression	Comments
RB_Streaming_C125	(pc_FDD) AND pc_RAB_A_18c_17	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/17
RB_Speech_C57	(pc_FDD) AND pc_RAB_A_18c_5a	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/5a
RB_Speech_C58	(pc_FDD) AND pc_RAB_A_18c_7a	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18c/5a
RB_Intract_Backgr_C64	(pc_FDD) AND pc_RAB_A_18e_5	(A.1/1 OR A.1/3 OR A.1/4 OR A.1/6) AND A.18e/5
Detailed Comments :		

Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_Sfc4	SF512_AndCodeNumber	sf4: 3	Default RAB Id for the 2nd CS domain
tsc_Sfc8	SF512_AndCodeNumber	sf8: 7	
tsc_Sfd128	SF512_AndPilot	sfd128:pb4	
tsc_Sfd4	SF512_AndPilot	sfd4:NULL	
tsc_Sfd8	SF512_AndPilot	sfd8:NULL	
tsc_Sf4	SpreadingFactor	sf4	
tsc_RAB2_DefCS	B8	'00000010'B	
tsc_RAB2_DefPS	B8	'00000110'B	
tsc_S_CCPCH1_DL_ChC	SF256_AndCodeNumber	sf128:6	
tsc_S_CCPCH2_DL_ChC	SF256_AndCodeNumber	sf64:1	
tsc_S_CCPCH3_ChC	SF256_AndCodeNumber	sf64:2	Spreading factor and pilot bits for tsc_S_CCPCH3
tsc_BCCH7	INTEGER	7	
tsc_DL_CCCH6	INTEGER	6	
tsc_DL_DTCH5	INTEGER	5	
tsc_UL_DTCH5	INTEGER	5	
tsc_DSCH1	INTEGER	19	
tsc_DL_DPCH1_SFP_256k_PS	SF512_AndPilot	sfd256 : pb4	
tsc_RB_TestData_103	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 13)),103)	
tsc_RB_TestData_672	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0,84)),672)	
tsc_RB_TestData_1360	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 170)),1360)	
tsc_RB_TestData_2688	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 336)),2688)	Test Data 103 bits
tsc_RB_TestData_3024	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0,378)),3024)	
tsc_RB_TestData_4032	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 504)),4032)	
tsc_RB_TestData_5376	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0,672)),5376)	
tsc_RB_TestData_8064	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0,1008)),8064)	
tsc_DL_DCH4	INTEGER	9	
tsc_DL_DTCH4	INTEGER	10	

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_DL_TxPower_PDSCH	DL_TxPower	-5	down link transmit power level of PDPCH Default value is -5 dBm
tsc_FACH3	INTEGER	16	transport channel identity for second FACH when it is connected to a third CCPCH together with PCH (transport channel identity = 16)
tsc_FACH4	INTEGER	17	transport channel identity for second FACH when it is connected to a third CCPCH together with PCH (transport channel identity = 17)
tsc_New_CRNTI2	C_RNTI	'1010101010101010'B	A different value for new crnti
tsc_PIO_96	PuncturingLimit	pl0_96	
tsc_RB_DataSpeech_12_2	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 13)),103)	
tsc_RB_Interactive_64	BITSTRING	o_GetLeastSignificantBits(o_OctToBit(o_GetN_OctetsFromPRBS(0, 168)),1344)	
tsc_Sf128	SpreadingFactor	sf128	
tsc_Sf16	SpreadingFactor	sf16	
tsc_Sf64	SpreadingFactor	sf64	
tsc_Sf8	SpreadingFactor	sf8	
tsc_Sfc128	SF512_AndCodeNumber	sf128: 127	
tsc_Sfc16	SF512_AndCodeNumber	sf16: 15	
tsc_Sfc256	SF512_AndCodeNumber	sf256: 255	
tsc_Sfc32	SF512_AndCodeNumber	sf32: 31	
tsc_Sfd128_4	SF512_AndPilot	sfd128: pb4	
tsc_Sfd128_8	SF512_AndPilot	sfd128: pb8	
tsc_Sfd16	SF512_AndPilot	sfd16:NULL	
tsc_Sfd256_4	SF512_AndPilot	sfd256: pb4	
tsc_Sfd32	SF512_AndPilot	sfd32:NULL	
tsc_UL_DCH4	INTEGER	4	identity for transport channel DCH4 (upink), in AMR speech
tsc_UL_DTCH4	INTEGER	10	Logical channel identity for DTCH4 (uplink)
tsc_CTCH	INTEGER	11	Logical channel identity for CTCH (downlink)
tsc_DL_DCH2	INTEGER	7	identity for transport channel DCH2 (downlink), in AMR speech this transport channel is used for RAB subflow#2
tsc_DL_DCH3	INTEGER	8	identity for transport channel DCH4 (downlink), in AMR speech this transport channel is used for RAB subflow#3

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_DL_DPCH1_ChC_Speech	SF512_AndCodeNumber	sf128:0	Channelization code for tsc_DL_DPCH1 for a speech configuration
tsc_DL_DPCH1_SFP_64k_CS	SF512_AndPilot	sfd32 : NULL	Spreading factor and pilot bits for tsc_DL_DPCH1 for a 64 kbps CS configuration
tsc_DL_DPCH1_SFP_64k_PS	SF512_AndPilot	sfd32 : NULL	Spreading factor and pilot bits for tsc_DL_DPCH1 for a 64 kbps PS configuration
tsc_DL_DPCH1_SFP_Speech	SF512_AndPilot	sfd128 : pb4	Spreading factor and pilot bits for tsc_DL_DPCH1 for a speech configuration
tsc_DL_DPCH1_SFP_Streaming	SF512_AndPilot	sfd32 : NULL	Spreading factor and pilot bits for tsc_DL_DPCH1 for a Streaming 57.6 kbps configuration
tsc_DL_DTCH2	INTEGER	8	Logical channel identity for DTCH2 (downlink)
tsc_DL_DTCH3	INTEGER	9	Logical channel identity for DTCH3 (downlink)
tsc_Sf32	SpreadingFactor	sf32	identity for transport channel DCH3 (uplink), in AMR speech this transport channel is used for RAB subflow#3
tsc_Sfc64	SF512_AndCodeNumber	sf64: 63	
tsc_Sfd64	SF512_AndPilot	sfd64:NULL	
tsc_UL_DCH3	INTEGER	3	
tsc_UL_DPDCH_SF_64k_CS	SpreadingFactor	sf16	Channelization code for UL DPDCH for a 64 kbps CS configuration
tsc_UL_DPDCH_SF_64k_PS	SpreadingFactor	sf16	Channelization code for UL DPDCH for a 64 kbps PS configuration
tsc_UL_DPDCH_SF_Speech	SpreadingFactor	sf64	Channelization code for UL DPDCH for a speech configuration
tsc_UL_DPDCH_SF_Streaming	SpreadingFactor	sf16	Channelization code for UL DPDCH for a Streaming 57.6 kbps configuration
tsc_UL_DTCH2	INTEGER	8	Logical channel identity for DTCH2 (uplink)
tsc_UL_DTCH3	INTEGER	9	Logical channel identity for DTCH3 (uplink)
maxLogCHperTrCH	INTEGER	15	This constant is from MCI ASP proposal, but there is no value for it in the proposal. value 8 is for temporary use
maxdlTrCH	INTEGER	16	Physical channel identity for AICH channel associated with first PRACH
maxulTrCH	INTEGER	16	
tsc_AICH1	INTEGER	7	

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_AICH1_ChC	ChannelisationCode256	3	Channelization code for tsc_AICH1
tsc_AICH2	INTEGER	12	Physical channel identity for AICH2 channel associated with second PRACH
tsc_AT_ResultOK	IA5String	"<CR><LF>OK<CR><LF>"	
tsc_AttOn	INTEGER	1	Attach flag value On
tsc_AttenuationServingCell	INTEGER	0	Value of attenuator to be used when setting a Serving Cell.
tsc_BCCH1	INTEGER	1	Logical channel identity for logical channel BCCH mapped on BCH
tsc_BCCH6	INTEGER	6	Logical channel identity for logical channel BCCH mapped on FACH
tsc_BCH1	INTEGER	11	Identity for transport channel BCH
tsc_CRNTI	C_RNTI	'0000000000000001'B	C RNTI. Default value: '0000 0000 0000 0001'B
tsc_CS_Domain	CN_DomainIdentity	cs_domain	
tsc_CellA	INTEGER	0	Identity of Cell A
tsc_CellB	INTEGER	1	Identity of Cell B
tsc_CellC	INTEGER	2	Identity of Cell C
tsc_CellD	INTEGER	3	Identity of Cell D
tsc_CellDedicated	INTEGER	-1	Identity of the dedicated cell.
tsc_CellE	INTEGER	4	Identity of Cell E
tsc_CellF	INTEGER	5	Identity of Cell F
tsc_CellG	INTEGER	6	Identity of Cell G
tsc_CellH	INTEGER	7	Identity of Cell H
tsc_CellIdCellA	BITSTRING	'00000000000000000000000000000000'B	
tsc_CellIdCellB	BITSTRING	'00000000000000000000000000000001'B	
tsc_CellIdCellC	BITSTRING	'00000000000000000000000000000010'B	
tsc_CellIdCellD	BITSTRING	'00000000000000000000000000000011'B	
tsc_CellIdCellE	BITSTRING	'00000000000000000000000000000100'B	
tsc_CellIdCellF	BITSTRING	'00000000000000000000000000000101'B	
tsc_CellIdCellG	BITSTRING	'00000000000000000000000000000110'B	
tsc_CellIdCellH	BITSTRING	'00000000000000000000000000000111'B	
tsc_DC_ControlPDU	DC_Field	'0'B	Value for D/C field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.1

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_DL_CCCH5	INTEGER	5	Logical channel identity for logical channel CCCH mapped on FACH (downlink)
tsc_DL_DCCH1	INTEGER	1	Logical channel identity for DCCH1 (downlink), used by signalling radio bearer 1
tsc_DL_DCCH2	INTEGER	2	Logical channel identity for DCCH2 (downlink), used by signalling radio bearer 2
tsc_DL_DCCH3	INTEGER	3	Logical channel identity for DCCH3 (downlink), used by signalling radio bearer 3
tsc_DL_DCCH4	INTEGER	4	Logical channel identity for DCCH4 (downlink), used by signalling radio bearer 4
tsc_DL_DCH1	INTEGER	6	identity for transport channel DCH1 (downlink), in AMR speech this transport channel is used for RAB subflow#1
tsc_DL_DCH5	INTEGER	10	identity for transport channel DCH5 (downlink), in most case this transport channel is used for signalling bearers.
tsc_DL_DPCH1	INTEGER	26	physical channel identity for DPCH1(downlink)
tsc_DL_DPCH1_2ndScrC	SecondaryScramblingCode	1	secondary scrambling code for DL DPCH1
tsc_DL_DPCH1_ChC_64k_CS	SF512_AndCodeNumber	sf32:0	Channelization code for tsc_DL_DPCH1 for a 64 kbps CS configuration
tsc_DL_DPCH1_ChC_64k_PS	SF512_AndCodeNumber	sf32:0	Channelization code for tsc_DL_DPCH1 for a 64 kbps PS configuration
tsc_DL_DPCH1_ChC_SRB	SF512_AndCodeNumber	sf128:0	Channelization code for tsc_DL_DPCH1 for a SRB connection with a RAB established
tsc_DL_DPCH1_SFP_SRB	SF512_AndPilot	sfd128:pb4	Spreading factor and pilot bits for tsc_DL_DPCH1 for an SRB connection with a RAB established
tsc_DL_DTCH1	INTEGER	7	Logical channel identity for DTCH1 (downlink)
tsc_DL_PDSCH1	INTEGER	16	Physical channel identity for primaryPDSCH channel
tsc_DL_TxPower_DPCH	DL_TxPower	-5	down link transmit power level of DPCH used for CS RABS. Default value is -5 dBm
tsc_DL_TxPower_DPCH_64k	DL_TxPower	-2	down link transmit power level of DPCH used 64 KBPS PS RAB's Default value is -2 dBm

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_DPCCH_PowerOffset	DPCCH_PowerOffset	–40	DPCCH power offset value. Defalut value: –80 (IE Value *2),
tsc_DPCH_PowerOffsetPILOT	INTEGER	0	Power offsett value of PILOT on DPCH
tsc_DPCH_PowerOffsetTFCl	INTEGER	0	Power offsett value of TFCl on DPCH
tsc_DPCH_PowerOffsetTPC	INTEGER	0	Power offsett value of TPC on DPCH
tsc_DefaultDPCH_OffsetValue	DefaultDPCH_OffsetValueFDD	459	Default DPCH offset value. Actual value DefaultDPCH–OffsetValueFDD = IE value * 512 Default value : 459
tsc_DelayAfterRRC_ConnRel	INTEGER	1000	Delay before sending after receiving RRC CONNECTION RELEASE complete in AM Mode (value in ms), so as to allow SS RLC layer to send ACK to UE.
tsc_DelayBeforeRRC_ConnRel	INTEGER	1000	Delay before sending RRC CONNECTION RELEASE (value in ms)
tsc_FACH1	INTEGER	13	transport channel identity for FACH
tsc_FACH2	INTEGER	14	transport channel identity for second FACH when it is connected to a secondary CCPCH together with PCH (transport channel identity = 14)
tsc_GMM_PD	ProtocolDiscriminator	'1000'B	GMM protocol discriminator 3GPP 24.008 clause 10.4
tsc_GainFactorBetaC_Below64k	INTEGER	11	Gain factor Beta C value to be used for RAB UL below 64kbps
tsc_GainFactorBetaC_Higher64k	INTEGER	9	Gain factor Beta C value to be used for RAB UL higher than 64kbps
tsc_GainFactorBetaD	INTEGER	15	
tsc_IntegrProtAlgCap	B16	'00000000000000010'B	Integrity Protection Algorithm Capability
tsc_KeySeqDef	KeySeq	'111'B	default Key Sequence. Default value: '111'B
tsc_LAC_Def	OCTETSTRING	'0001'O	
tsc_MCC_Def	HEXSTRING	'001'H	
tsc_MNC_Def	HEXSTRING	'01F'H	
tsc_MSN	INTEGER	0	
tsc_MaxAllowPwr	MaxAllowedUL_TX_Power	33	
tsc_MessAuthCode	BITSTRING	'00000000000000000000000000000000'B	
tsc_Mui	INTEGER	0	
tsc_N300	INTEGER	3	

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_NMO_I	OCTETSTRING	'00'O	Network Mode of Operation I (3GPP 24.008 / 10.5.1.12.3)
tsc_NMO_II	OCTETSTRING	'01'O	Network Mode of Operation II (3GPP 24.008 / 10.5.1.12.3)
tsc_Now	INTEGER	512	To indicate the system information change starts immediately.
tsc_PCCH1	INTEGER	1	Logical channel identity for logical channel PCCH
tsc_PCH1	INTEGER	12	identity for transport channel PCH1
tsc_PDU_TypeStatus	CtrlPDU_Type	'000'B	Value for PDU type field within a STATUS PDU. Ref 3G TS 25.322 clause 9.2.2.2
tsc_PICH1	INTEGER	6	Physical channel identity for PICH channel associated with first secondary CCPCH
tsc_PICH1_ChC	ChannelisationCode256	2	Channelization code for tsc_PICH1
tsc_PICH2	INTEGER	11	Physical channel identity for PICH2 channel associated with second secondary CCPCH
tsc_PRACH1	INTEGER	8	Physical channel identity for first PRACH channel
tsc_PRACH1_SF	SF_PRACH	sfpr64	Channelization code for UL DPDCH for PRACH1
tsc_PRACH1_ScrC	PreambleScramblingCodeWordNumber	0	Scrambling code for preamble of PRACH1
tsc_PRACH1_Signatures	AvailableSignatures	'0000000011111111'B	available signatures for PRACH. (from 34.108 cl. 6.1 (SIB5))
tsc_PRACH2	INTEGER	9	Physical channel identity for second PRACH channel
tsc_PS_Domain	CN_DomainIdentity	ps_domain	
tsc_P_CCPCH	INTEGER	4	Physical channel identity for primary CCPCH channel
tsc_P_CPICH	INTEGER	0	Physical channel identity for primary CPICH channel
tsc_P_SCH	INTEGER	1	Physical channel identity for primary SCH channel
tsc_PowerAICH	AICH_PowerOffset	-5	relative transmission power level of AICH. The power level is specified relatively to power level of CPICH in terms of AICH_Ec. Default is -5 dBm/3.84MHz
tsc_PowerPICH	PICH_PowerOffset	-5	relative transmission power level of PICH. The power level is specified relatively to power level of CPICH in terms of PICH_Ec. Default is -5 dBm/3.84MHz

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_PowerpCCPCH	DL_TxPower	-2	transmission power level of primary CCPCH relative to CPICH. Default is -2 dBm.
tsc_PowerpCPICH	DL_TxPower_PCPICH	-60	transmission power level of primary CPICH. The power level is specified in terms of CPICH_Ec. Default is -60 dBm.
tsc_PowerpSCH	DL_TxPower	-5	transmission power level of primary SCH relative to CPICH. Default is -5 dBm.
tsc_PowersCCPCH1	DL_TxPower	-2	transmission power level of secondary CCPCH1 relative to CPICH. Default is -2 dBm
tsc_PowersSCH	DL_TxPower	-5	transmission power level of secondary SCH relative to CPICH. Default is -5 dBm
tsc_PuncLimit	PuncturingLimit	pl1	puncturing limit for PRACH. default value is 1. Default value: Pl1
tsc_RAB_DefCS	B8	'00000001'B	Default AB Id for the CS domain
tsc_RAB_DefPS	B8	'00000101'B	Default AB Id for the PS domain
tsc_RACH1	INTEGER	15	transport channel identity for RACH
tsc_RAC_Def	OCTETSTRING	'05'O	Routing Area Code, 1 octet, 3GPP 24.008 clause 10.5.1.12.3
tsc_RB0	INTEGER	0	signalling radio bearer on TM + CCCH
tsc_RB1	INTEGER	1	signalling radio bearer on UM + DCCH
tsc_RB10	INTEGER	10	radio access bearer identity, in AMR speech this RB is RAB subflow#1, in other cases this is a radio access bearer
tsc_RB11	INTEGER	11	radio access bearer identity, in AMR speech this RB is RAB subflow#2, in some other cases this is the signalling radio bearer for TM + BCCH_FACH.
tsc_RB12	INTEGER	12	radio access bearer, in AMR speech this RB is RAB subflow#3, in some other cases this is the bearer for TM PCCH

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_RB13	INTEGER	13	radio access bearer, in AMR speech this RB is RAB subflow#3, in some other cases this is the bearer for TM PCCH
tsc_RB2	INTEGER	2	signalling radio bearer on AM + DCCH
tsc_RB20	INTEGER	20	radio access bearer for PS RAB
tsc_RB21	INTEGER	21	radio access bearer for PS RAB
tsc_RB22	INTEGER	22	Second AM radio access bearer for PS
tsc_RB23	INTEGER	23	Second AM radio access bearer for PS
tsc_RB24	INTEGER	24	Second AM radio access bearer for PS
tsc_RB25	INTEGER	25	PS Radio Bearer associated with a DTCH on HS-DSCH
tsc_RB29	INTEGER	29	RB Id for Radio bearer that carries the 2nd CCCH in the DL
tsc_RB3	INTEGER	3	signalling radio bearer on AM + DCCH for high priority NAS_DT
tsc_RB30	INTEGER	30	radio access bearer for CBS RAB
tsc_RB31	INTEGER	31	
tsc_RB4	INTEGER	4	signalling radio bearer on AM + DCCH for low priority NAS_DT
tsc_RB_2ndCCCH	INTEGER	-5	uplink signalling radio bearer on TM + second CCCH
tsc_RB_2ndPCCH	INTEGER	-4	radio bearer identity for bearer on TM + PCCH + PCH used for sending paging message when there is no RAB subflows for speech.
tsc_RB_AM_15_RLC	SS_RB_Identity	-13	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate an AM RLC entity using 15 bit length indicators. The RB identity can be used by the SS decoder to determine which RLC mode is being simulated. A negative value is used to indicate that the SS configuration is not identical to the UE configuration.

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_RB_AM_7_RLC	SS_RB_Identity	-12	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate an AM RLC entity using 7 bit length indicators. The RB identity can be used by the SS decoder to determine which RLC mode is being simulated. A negative value is used to indicate that the SS configuration is not identical to the UE configuration.
tsc_RB_BCCH	INTEGER	-1	radio bearer identity for bearer on TM + BCCH + BCH used for broadcasting system information blocks
tsc_RB_BCCH_FACH	INTEGER	-3	signalling radio bearer on TM + BCCH + FACH + sCCPCH1
tsc_RB_BCCH_FACH_RAB	INTEGER	-19	RB Id for Radio bearer that carries the 2nd BCCH
tsc_RB_CCCH_FACH_MAC	SS_RB_Identity	-18	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate the CCCH mapped to FACH. The RB identity can be used by the SS decoder to determine which MAC configuration is being simulated. A negative value is used to indicate that the SS configuration is not identical to the UE configuration.
tsc_RB_DCCH_DCH_MAC	SS_RB_Identity	-15	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate the high priority NAS SRB mapped to DCH. The RB identity can be used by the SS decoder to determine which MAC configuration is being simulated. A negative value is used to indicate that the SS configuration is not identical to the UE configuration.

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_RB_DCCH_FACH_MAC	SS_RB_Identity	-14	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate the high priority NAS SRB mapped to FACH. The RB identity can be used by the SS decoder to determine which MAC configuration is being simulated.
tsc_RB_PCCH	INTEGER	-2	A negative value is used to indicate that the SS configuration is not identical to the UE configuration. radio bearer identity for bearer on TM + PCCH + PCH used for sending paging message when there is no RAB subflows for speech.
tsc_RB_PCCH2	INTEGER	-4	
tsc_RB_UM_15_RLC	SS_RB_Identity	-11	RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate a UM RLC entity using 15 bit length indicators. The RB identity can be used by the SS decoder to determine which RLC mode is being simulated.
tsc_RB_UM_7_RLC	SS_RB_Identity	-10	A negative value is used to indicate that the SS configuration is not identical to the UE configuration. RB Identity used for configuration of a TM RLC entity in the SS that will be used to simulate a UM RLC entity using 7 bit length indicators. The RB identity can be used by the SS decoder to determine which RLC mode is being simulated.
tsc_SFN_OffsetA	INTEGER	0	A negative value is used to indicate that the SS configuration is not identical to the UE configuration. SFN offset values for cell A Default value: 0
tsc_SFN_OffsetB	INTEGER	0	SFN offset values for cell B Default value: 0
tsc_SFN_OffsetC	INTEGER	0	SFN offset values for cell C Default value: 0

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_SFN_OffsetD	INTEGER	3000	SFN offset values for cell D Default value: 3000
tsc_SFN_OffsetE	INTEGER	3000	SFN offset values for cell E Default value: 3000
tsc_SFN_OffsetF	INTEGER	678	SFN offset values for cell F Default value: 678
tsc_SFN_OffsetG	INTEGER	1356	SFN offset values for cell G Default value: 1356
tsc_SFN_OffsetH	INTEGER	2034	SFN offset values for cell H Default value: 2034
tsc_SMPD	ProtocolDiscriminator	'1010'B	SM protocol discriminator
tsc_SS_CS_Domain	SS_CN_DomainIdentity	0	
tsc_SS_PS_Domain	SS_CN_DomainIdentity	1	
tsc_S_CCPCH1	INTEGER	5	Physical channel identity for first secondary CCPCH channel
tsc_S_CCPCH1_ChC	SF256_AndCodeNumber	sf64:1	Channelization code for tsc_S_CCPCH1
tsc_S_CCPCH2	INTEGER	10	Physical channel identity for second secondary CCPCH channel
tsc_S_CCPCH3	INTEGER	13	Physical channel identity forThird secondary CCPCH channel
tsc_S_CCPCH_2ndScrCode	INTEGER	0	Secondary scrambling code for S-CCPCH channels
tsc_S_SCH	INTEGER	2	Physical channel identity for secondary SCH channel
tsc_SlotFormatsCCPCH1	SCCPCHSlotFormat	8	channelization code for secondary CCPCH1 when spreading factor = 64, default value is 8. Default value: 8
tsc_T3212_Def	OCTETSTRING	'1E'O	infinite
tsc_TPC_CombinationIndex	TPC_CombinationIndex	0	
tsc_TT01	INTEGER	5000	TC protocol Timer: 5 sec (shall be 2,5 sec)
tsc_TWaitSysInfo	INTEGER	5000	Wait for the completion of SysInfo sending
tsc_TimingsCCPCH1	INTEGER	0	timing offset for secondary CCPCH1. default is 0. Default value: 0
tsc_TpcStepSize	TPC_StepSizeFDD	0	Value 0 corresponds to 1 dB
tsc_UE_TestLoopMode1	UE_TestLoopMode	'00'O	mode 1 with DCCH dummy transmission disabled
tsc_UL_CCCH5	INTEGER	5	Logical channel identity for logical channel CCCH mapped RACH (uplink)
tsc_UL_DCCH1	INTEGER	1	Logical channel identity for DCCH1 (uplink), used by signalling radio bearer 1

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_UL_DCCH2	INTEGER	2	Logical channel identity for DCCH2 (uplink), used by signalling radio bearer 2
tsc_UL_DCCH3	INTEGER	3	Logical channel identity for DCCH3 (uplink), used by signalling radio bearer 3
tsc_UL_DCCH4	INTEGER	4	Logical channel identity for DCCH4 (uplink), used by signalling radio bearer 4
tsc_UL_DCH1	INTEGER	1	identity for transport channel DCH1 (uplink), in AMR speech this transport channel is used for RAB subflow#1
tsc_UL_DCH2	INTEGER	2	identity for transport channel DCH2 (uplink), in AMR speech this transport channel is used for RAB subflow#2
tsc_UL_DCH5	INTEGER	5	identity for transport channel DCH5 (uplink), in most case this transport channel is used for signalling bearers.
tsc_UL_DPCH1	INTEGER	20	physical channel identity for DPCH1(uplink)
tsc_UL_DPDCH_SF_SRB	SpreadingFactor	sf64	Channelization code for UL DPDCH for an SRB connection with a RAB established
tsc_UL_DTCH1	INTEGER	7	Logical channel identity for DTCH1 (uplink)
tsc_UL_MAC_Prt1	MAC_LogicalChannelPriority	1	
tsc_UL_MAC_Prt2	MAC_LogicalChannelPriority	2	
tsc_UL_MAC_Prt3	MAC_LogicalChannelPriority	3	
tsc_UL_MAC_Prt4	MAC_LogicalChannelPriority	4	
tsc_URA_IdCellA	BITSTRING	'0000000000000001'B	
tsc_URA_IdCellB	BITSTRING	'0000000000000001'B	
tsc_URA_IdCellC	BITSTRING	'0000000000000010'B	
tsc_URA_IdCellD	BITSTRING	'0000000000000010'B	
tsc_URA_IdCellE	BITSTRING	'0000000000000011'B	
tsc_URA_IdCellF	BITSTRING	'0000000000000011'B	
tsc_URA_IdCellG	BITSTRING	'0000000000000100'B	
tsc_URA_IdCellH	BITSTRING	'0000000000000100'B	
tsc_USIM_NeedRmv	BOOLEAN	TRUE	Removal of USIM is needed in a test case (USIM is not always inserted)
tsc_WaitBeforePaging	INTEGER	5000	Waiting time before PAGING (ms)

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Test Suite Constant Declarations			
Constant Name	Type	Value	Comments
tsc_sCCPCH_PowerOffset PILOT	INTEGER	6	Power offsett value of PILOT on sCCPCH
tsc_sCCPCH_PowerOffset TFCI	INTEGER	6	Power offsett value of TFCI on sCCPCH
Detailed Comments :			

Test Suite Constant Declarations By Reference				
Constant Name	Type	Value Reference	Module Identifier	Comments
hiPD SCHidentities	INTEGER	hiPD SCHidentities	Class-definitions	
hiPUSCHidentities	INTEGER	hiPUSCHidentities	Class-definitions	
hiRM	INTEGER	hiRM	Class-definitions	
maxAC	INTEGER	maxAC	Class-definitions	
maxAdditionalMeas	INTEGER	maxAdditionalMeas	Class-definitions	
maxASC	INTEGER	maxASC	Class-definitions	
maxASCmap	INTEGER	maxASCmap	Class-definitions	
maxASCpersist	INTEGER	maxASCpersist	Class-definitions	
maxCCTrCH	INTEGER	maxCCTrCH	Class-definitions	
maxCellMeas	INTEGER	maxCellMeas	Class-definitions	
maxCellMeas_1	INTEGER	maxCellMeas-1	Class-definitions	
maxCNdomains	INTEGER	maxCNdomains	Class-definitions	
maxCPC Hsets	INTEGER	maxCPC Hsets	Class-definitions	
maxDPCH_DLchan	INTEGER	maxDPCH-DLchan	Class-definitions	
maxDPDCH_UL	INTEGER	maxDPDCH-UL	Class-definitions	
maxDRACclasses	INTEGER	maxDRACclasses	Class-definitions	
maxFACHPCH	INTEGER	maxFACHPCH	Class-definitions	
maxFreq	INTEGER	maxFreq	Class-definitions	
maxFreqBandsFDD	INTEGER	maxFreqBandsFDD	Class-definitions	
maxFreqBandsTDD	INTEGER	maxFreqBandsTDD	Class-definitions	
maxFreqBandsGSM	INTEGER	maxFreqBandsGSM	Class-definitions	
maxGERAN_SI	INTEGER	maxGERAN-SI	Class-definitions	
maxHProcesses	INTEGER	maxHProcesses	Class-definitions	
maxHSSCCHs	INTEGER	maxHSSCCHs	Class-definitions	
maxInterSysMessages	INTEGER	maxInterSysMessages	Class-definitions	
maxLoCHperRLC	INTEGER	maxLoCHperRLC	Class-definitions	
maxMAC_d_PDUsize s	INTEGER	maxMAC-d-PDUsize s	Class-definitions	
maxMeasEvent	INTEGER	maxMeasEvent	Class-definitions	
maxMeasIntervals	INTEGER	maxMeasIntervals	Class-definitions	
maxMeasParEvent	INTEGER	maxMeasParEvent	Class-definitions	
maxNumCDMA2000F reqs	INTEGER	maxNumCDMA2000F reqs	Class-definitions	
maxNumGSMFreqRan ges	INTEGER	maxNumGSMFreqRan ges	Class-definitions	
maxNumFDDFreqs	INTEGER	maxNumFDDFreqs	Class-definitions	
maxNumTDDFreqs	INTEGER	maxNumTDDFreqs	Class-definitions	
maxOtherRAT	INTEGER	maxOtherRAT	Class-definitions	
maxOtherRAT_16	INTEGER	maxOtherRAT-16	Class-definitions	
maxPage1	INTEGER	maxPage1	Class-definitions	
maxPCPCH_APsig	INTEGER	maxPCPCH-APsig	Class-definitions	
maxPCPCH_APsubC h	INTEGER	maxPCPCH-APsubC h	Class-definitions	
maxPCPCH_CDsig	INTEGER	maxPCPCH-CDsig	Class-definitions	
maxPCPCH_CDsubC h	INTEGER	maxPCPCH-CDsubC h	Class-definitions	
maxPCPCH_SF	INTEGER	maxPCPCH-SF	Class-definitions	
maxPCPCHs	INTEGER	maxPCPCHs	Class-definitions	
maxPDCPA lgoType	INTEGER	maxPDCPA lgoType	Class-definitions	

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Test Suite Constant Declarations By Reference				
Constant Name	Type	Value Reference	Module Identifier	Comments
maxPDSCH	INTEGER	maxPDSCH	Class-definitions	
maxPDSCH_TFCIgroups	INTEGER	maxPDSCH-TFCIgroups	Class-definitions	
maxPRACH	INTEGER	maxPRACH	Class-definitions	
maxPRACH_FPACH	INTEGER	maxPRACH-FPACH	Class-definitions	
maxPUSCH	INTEGER	maxPUSCH	Class-definitions	
maxQueueIDs	INTEGER	maxQueueIDs	Class-definitions	
maxRABsetup	INTEGER	maxRABsetup	Class-definitions	
maxRAT	INTEGER	maxRAT	Class-definitions	
maxRB	INTEGER	maxRB	Class-definitions	
maxRBallRABs	INTEGER	maxRBallRABs	Class-definitions	
maxRBMuxOptions	INTEGER	maxRBMuxOptions	Class-definitions	
maxRBperRAB	INTEGER	maxRBperRAB	Class-definitions	
maxReportedGSMCells	INTEGER	maxReportedGSMCells	Class-definitions	
maxRL	INTEGER	maxRL	Class-definitions	
maxRL_1	INTEGER	maxRL-1	Class-definitions	
maxROHC_PacketSizes_r4	INTEGER	maxROHC-PacketSizes-r4	Class-definitions	
maxROHC_Profile_r4	INTEGER	maxROHC-Profile-r4	Class-definitions	
maxSat	INTEGER	maxSat	Class-definitions	
maxSCCPCH	INTEGER	maxSCCPCH	Class-definitions	
maxSIB	INTEGER	maxSIB	Class-definitions	
maxSIB_FACH	INTEGER	maxSIB-FACH	Class-definitions	
maxSIBperMsg	INTEGER	maxSIBperMsg	Class-definitions	
maxSRBsetup	INTEGER	maxSRBsetup	Class-definitions	
maxSystemCapability	INTEGER	maxSystemCapability	Class-definitions	
maxTF	INTEGER	maxTF	Class-definitions	
maxTF_CPCH	INTEGER	maxTF-CPCH	Class-definitions	
maxTFC	INTEGER	maxTFC	Class-definitions	
maxTFCsub	INTEGER	maxTFCsub	Class-definitions	
maxTFCI_2_Combs	INTEGER	maxTFCI-2-Combs	Class-definitions	
maxTGPS	INTEGER	maxTGPS	Class-definitions	
maxTrCH	INTEGER	maxTrCH	Class-definitions	
maxTrCHpreconf	INTEGER	maxTrCHpreconf	Class-definitions	
maxTS	INTEGER	maxTS	Class-definitions	
maxTS_1	INTEGER	maxTS-1	Class-definitions	
maxTS_2	INTEGER	maxTS-2	Class-definitions	
maxTS_LCR	INTEGER	maxTS-LCR	Class-definitions	
maxTS_LCR_1	INTEGER	maxTS-LCR-1	Class-definitions	
maxURA	INTEGER	maxURA	Class-definitions	
maxURNTI_Group	INTEGER	maxURNTI-Group	Class-definitions	
Detailed Comments :				

Test Suite Variable Declarations			
Variable Name	Type	Value	Comments
tcv_count_RB22	INTEGER	0	
tsv_AuthSQN	BITSTRING	'00000000000000000000000000000000 00000000000000000000000000000000 00000'B	48 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2) used and updated whenever an authentication is performed
Detailed Comments :			

Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_ReceiveFourthSDU	BOOLEAN	FALSE	Indicates wether Fourth SDU has been received on RB10
tcv_ReceiveThirdSDU	BOOLEAN	FALSE	Indicates wether Third SDU has been received on RB10
tcv_ReceiveSecondSDU	BOOLEAN	FALSE	Indicates wether Second SDU has been received on RB10
tcv_ReceiveFirstSDU	BOOLEAN	FALSE	Indicates wether First SDU has been received on RB10
tcv_RB_Data4	BITSTRING	'0'B	
tcv_RB_testdata1	BITSTRING	"B	Changed from '0'B to "B
tcv_RB_testdata2	BITSTRING	"B	Changed from '0'B to "B
tcv_RB_testdata3	BITSTRING	"B	Changed from '0'B to "B
tcv_ReceiveRB10	BOOLEAN	FALSE	Indicates wether data has been received on RB10
tcv_ReceiveRB11	BOOLEAN	FALSE	Indicates wether data has been received on RB11
tcv_ReceiveRB12	BOOLEAN	FALSE	Indicates wether data has been received on RB12
tcv_ReceiveRB13	BOOLEAN	FALSE	Indicates wether data has been received on RB13
tcv_ReceiveRB20	BOOLEAN	FALSE	Indicates wether data has been received on RB20
tcv_Receive_RB10	INTEGER	0	
tcv_count_RB10	INTEGER	0	
tcv_count_RB11	INTEGER	0	
tcv_count_RB20	INTEGER	0	
tcv_count_RB12	INTEGER	0	
tcv_result	BOOLEAN	FALSE	
tcv_testdata2_len	INTEGER	0	
tcv_max_Timer	INTEGER	0	
tcv_count_RB13	INTEGER	0	
tcv_Receive_RB13	INTEGER	0	
tcv_CellCnfg	INTEGER	0	tcv to represent the type of the configured cell.
tcv_ReceiveRB22	BOOLEAN	FALSE	Indicates wether data has been received on RB22
tcv_Bind_TestData	BOOLEAN	FALSE	Indicates wether SS has been configured correctly for the sending the data in all TM RBS in one TTI
tcv_RB_Data1	BITSTRING	'0'B	
tcv_RB_Data2	BITSTRING	'0'B	
tcv_RB_Data3	BITSTRING	'0'B	
tcv_Len	INTEGER	0	
tcv_ActTime	ActivationTime	0	Activation Time
tcv_Assigned_PTMSI_Sig	O3	px_PTMSI_SigDef	Current assigned PTMSI signature
tcv_AssignedPTMSI	OCTETSTRING	px_PTMSI_Def	Current assigned PTMSI
tcv_AssignedTMSI	OCTETSTRING	px_TMSI_Def	Current assigned TMSI

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_AuthAK	BITSTRING	INT_TO_BIT (0, 48)	Anonymity Key 48 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthAMF	BITSTRING	px_AuthAMF	Authentication Management Field 16 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthAUTN	B128	INT_TO_BIT (0, 128)	to hold complete calculated AUTN 128 bits (TS24.008 cl 10.5.3.1.1)
tcv_AuthAUTN_1	B48	INT_TO_BIT (0, 48)	to hold first part of calculated AUTN 64 bits
tcv_AuthAUTN_2	B80	INT_TO_BIT (0, 80)	to hold second part of calculated AUTN 64 bits
tcv_AuthCDOUT	BITSTRING	INT_TO_BIT (0, 64)	CDOUT 64 bits (TS 34.108 cl. 8.1.2)
tcv_AuthCK	BITSTRING	INT_TO_BIT (0, 128)	Ciphering Key 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthCK_1	BITSTRING	INT_TO_BIT (0, 64)	Ciphering Key 1st part (bits 0–63) 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthCK_2	BITSTRING	INT_TO_BIT (0, 64)	Ciphering Key 2nd part (bits 64–127) 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthCK_XOR	BITSTRING	INT_TO_BIT (0, 64)	Ciphering Key 1st and 2nd part, XORed 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthIK	BITSTRING	INT_TO_BIT (0, 128)	Integrity Key 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthIK_1	BITSTRING	INT_TO_BIT (0, 64)	Integrity Key 1st part (bits 0–63) 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthIK_2	BITSTRING	INT_TO_BIT (0, 64)	Integrity Key 2nd part (bits 64–127) 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthIK_XOR	BITSTRING	INT_TO_BIT (0, 64)	Integrity Key 1st and 2nd part, XORed 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthK	BITSTRING	px_AuthK	Authentication Key 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthKcGSM	BITSTRING	INT_TO_BIT (0, 64)	GSM Cipher Key 64 bits (TS 33.102 cl. 6.8.1.2)
tcv_AuthMAC	BITSTRING	INT_TO_BIT (0, 64)	Message Authentication Code 64 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_AuthN	INTEGER	px_AuthN	min 31, max 127 (TS 34.108 cl. 8.1.2)
tcv_AuthRAND	BITSTRING	px_AuthRAND	Random Challenge 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
tcv_AuthRsp	AuthRsp	INT_TO_BIT (0,32)	to hold a Authentication Response parameter value received from the UE 32 bits (TS 24.008 cl 10.5.3.2)
tcv_AuthRspExt	AuthRspExt		to hold a Authentication Response Extension parameter value received from the UE 96 bits (TS24.008 cl 10.5.3.2.1)
tcv_AuthRspPDU	AUTHENTICATIONRESPONSE		to hold a Authentication Response PDU
tcv_AuthXDOUT	BITSTRING	INT_TO_BIT (0, 128)	XDOUT 128 bits (TS 34.108 cl. 8.1.2)
tcv_AuthXDOUT_Half	BITSTRING	INT_TO_BIT (0, 64)	lower half of XDOUT 64 bits (TS 34.108 cl. 8.1.2)
tcv_AuthXRES	BITSTRING	INT_TO_BIT (0, 32)	XRES min 32, max 128 bits (TS 34.108 cl. 8.1.2)
tcv_BCCH_ModifyTime	INTEGER	512	To hold the BCCH modification time to be used by PAGING TYPE1 or SYSTEM INFORMATION CHANGE INDICATION. The initial value indicates changing now.
tcv_CellIndInfo	CellIndependantInfo	c_CellIndInfoDef	
tcv_CellInfoA	CellInfoCfg	c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode)	
tcv_CellInfoB	CellInfoCfg	c_CellInfoDef (tsc_CellB, px_PriScrmCode, tsc_URA_IdCellB, px_TCellB, tsc_SFN_OffsetB, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 1000) MOD 16777216))	

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_CellInfoC	CellInfoCfg	c_CellInfoDef (tsc_CellC, px_PriScrmCode, tsc_URA_IdCellC, px_TCellC, tsc_SFN_OffsetC, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 2000) MOD 16777216))	
tcv_CellInfoD	CellInfoCfg	c_CellInfoDef (tsc_CellD, px_PriScrmCode, tsc_URA_IdCellD, px_TCellD, tsc_SFN_OffsetD, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 3000) MOD 16777216))	
tcv_CellInfoE	CellInfoCfg	c_CellInfoDef (tsc_CellE, px_PriScrmCode, tsc_URA_IdCellE, px_TCellE, tsc_SFN_OffsetE, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 4000) MOD 16777216))	
tcv_CellInfoF	CellInfoCfg	c_CellInfoDef (tsc_CellF, px_PriScrmCode, tsc_URA_IdCellF, px_TCellF, tsc_SFN_OffsetF, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 5000) MOD 16777216))	
tcv_CellInfoG	CellInfoCfg	c_CellInfoDef (tsc_CellG, px_PriScrmCode, tsc_URA_IdCellG, px_TCellG, tsc_SFN_OffsetG, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 6000) MOD 16777216))	

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_CellInfoH	CellInfoCfg	c_CellInfoDef (tsc_CellH, px_PriScrmCode, tsc_URA_IdCellH, px_TCellH, tsc_SFN_OffsetH, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), ((px_UL_ScramblingCode + 7000) MOD 1677216))	
tcv_CipherActTime	ActivationTime	0	Activation Time
tcv_CN_Domain	CN_DomainIdentity	cs_domain	
tcv_Count	INTEGER	0	To hold a temporary counter value.
tcv_CS_KeySeq	KeySeq	tsc_KeySeqDef	
tcv_DefaultRadioCnf	BOOLEAN	TRUE	To be used in test cases that require a non default radio configuration. TRUE: a default radio configuration is to be used FALSE: a non default radio configuration is to be used.
tcv_dRX_CycleLengthPaging	UTRAN_DRX_CycleLength Coefficient		DRX cycleLength value to be used for PAGING
tcv_E_PLMN	PLMN_List	c_PLMN_List1 (o_ConvtPLMN(tsc_MCC_Def, tsc_MNC_Def))	
tcv_FrameNumber	INTEGER	0	Values 0..4095
tcv_FreqInfoHigh	FrequencyInfo	c_FreqInfo (px_UARFCN_D_High – 950 , px_UARFCN_D_High)	
tcv_FreqInfoLow	FrequencyInfo	c_FreqInfo (px_UARFCN_D_Low – 950 , px_UARFCN_D_Low)	
tcv_FreqInfoMid	FrequencyInfo	c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid)	
tcv_GMM_AttachExpect	BOOLEAN	FALSE	This TCV is to be used in Idle update step in NMO2 for a class A Mobile
tcv_GMM_AttachRec	BOOLEAN	FALSE	This TCV is to be used in Idle update step in NMO2 for a class A Mobile
tcv_GMM_DetachExpect	BOOLEAN	FALSE	This TCV is to be used in the Detach Handling
tcv_GMM_RAU_Expect	BOOLEAN	FALSE	This TCV is to be used in the Routing Area Update Handler
tcv_GMM_RAU_Rec	BOOLEAN	FALSE	This TCV is to be used in the Routing Area Update Handler
tcv_HFN	B20	'00000000000000000000'B	Hyper Frame Number for CS or PS domain – to be used in security steps

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_InitialUE_Id	InitialUE_Identity	c_UE_IdDefIMSI	Used to store the UE Identity
tcv_Int_ModifyFlag	BOOLEAN	FALSE	This Flag will be used in Security related steps. If it is set to True, it means that A Integrity Modification is being done at that stage.
tcv_K	INTEGER	0	the initial value c_MIB_DefUTRAN GERAN is for UTRAN/GERAN, for UTRAN only tcv_MIB shall be re-initialized to c_MIB_Def_UTRAN.
tcv_MIB	MasterInformationBlock	c_MIB_Def (c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode))	
tcv_MIB_ValueTagChanged	BOOLEAN	TRUE	initial value = TRUE, set to TRUE after MIBValueTag changed, set to FALSE after MIB delivered to SS.
tcv_MM_TestExecution	BOOLEAN	FALSE	Indication of MM test execution, set to TRUE once NMO II is set, used to properly handle ATTACH and DETACH procedures in MM tests
tcv_N308	INTEGER	2	Number of cells configured
tcv_NumCfgCell	INTEGER	0	
tcv_NumOfPLMN	INTEGER	1	
tcv_PollSDU	Poll_SDU	sdu1	This TCV is used to decide if the test case is a Single PLMN or Multi PLMN test case and based on it initialise SIB 11, 12 & 18 accordingly. Values 1>> 1 PLMN, 2 >> 2 PLMN, 3 >> 3 PLMN Rest Not defined
tcv_PollWindow	PollWindow	pw99	number of SDU's between pollings, this initial value is for ts_SS_CreateCellFACH. (from 34.123–1). used in RB2, RB3, RB4.
tcv_PS_AuthCK	BITSTRING	INT_TO_BIT (0, 128)	percentage of transmission window, threshold for polling, this initial value is for ts_SS_CreateCellFACH. (from 34.123–1). used in RB2, RB3, RB4.
tcv_PS_AuthIK	BITSTRING	INT_TO_BIT (0, 128)	Ciphering Key for PS domain 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)
			Integrity Key for PS domain 128 bits (TS 33.102 cl. 6.3.7, TS 34.108 cl. 8.1.2)

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_PS_KeySeq	KeySeq	tsc_KeySeqDef	Ciphering key sequence number for PS domain
tcv_RB_ActivationTimeInfoList	RB_ActivationTimeInfoList	cs_RB_ActTimeInfoListSRBs (0,0,0,0)	
tcv_RB_SigResumed	BOOLEAN	FALSE	
tcv_RB_TestModeActivated	BOOLEAN	FALSE	
tcv_ReceivePS_ServiceReq	BOOLEAN	FALSE	Set to TRUE if RB test mode is activated. To be used in the postamble: when RB test mode has been used then NAS do not need to be released.
tcv_Res	BOOLEAN	FALSE	This Type tcv by default will be set to False. It will be set to TRUE, when MO Service request has been rejected, and hence Ue can retransmit Service Request which should be handled in Default handlers.
tcv_RLC_IgnoreStatus	BOOLEAN	FALSE	to hold the BOOLEAN result of a test suite operation or a test Step
tcv_RLC_SeqNumDL_RB1	RLC_SequenceNumber	0	This variable is used to indicate that an RLC STATUS PDU may be received and should be ignored .
tcv_RLC_SeqNumDL_RB2	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB1
tcv_RLC_SeqNumDL_RB20	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB2
tcv_RLC_SeqNumDL_RB21	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB20
tcv_RLC_SeqNumDL_RB22	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB21
tcv_RLC_SeqNumDL_RB24	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB22
tcv_RLC_SeqNumDL_RB3	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB24
tcv_RLC_SeqNumDL_RB4	RLC_SequenceNumber	0	Downlink RLC Sequence Number for RB3
tcv_RRC_MSN_RB0	RRC_SequenceNumber	0	Downlink RLC Sequence Number for RB4
tcv_RRC_MSN_RB1	RRC_SequenceNumber	0	Used to store the MSN of RB0
tcv_RRC_MSN_RB2	RRC_SequenceNumber	0	Used to store the MSN of RB1
tcv_RRC_MSN_RB2_UL	RRC_SequenceNumber	0	Used to store the MSN of RB2
tcv_RRC_MSN_RB3	RRC_SequenceNumber	0	Used to store the UL MSN of RB2
tcv_RRC_MSN_RB4	RRC_SequenceNumber	0	Used to store the MSN of RB3
			Used to store the MSN of RB4

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_RRC_RelStatus	RRC_Rel_Status	cell_Dch	To hold the RRC Transaction Identifier.
tcv_RRC_Ti	RRC_TransactionIdentifier	0	
tcv_SB1	SysInfoTypeSB1	c_SB1_Def	
tcv_SB1_ValueTag	INTEGER	1	the initial value c_SB1_DefUTRAN_GERAN is for UTRAN/GERAN, for UTRAN only tcv_SB1 shall be re-initialized to c_SB1_DefUTRAN.
tcv_SB1_ValueTagChanged	BOOLEAN	FALSE	To hold current SB1_ValueTag, value range is 1 to 4.
tcv_Segs	SegmentsOfSysInfoBlock		initial value = FALSE, set to TRUE after SB1ValueTag changed, set to FALSE after SB1 delivered to SS.
tcv_SIB1	SysInfoType1	cb_SIB1_Def (c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFNOffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode))	Variable buffering the result of SIBSegmentation
tcv_SIB11	SysInfoType11	cb_SIB11_Def(c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFNOffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode), c_CellInfoDef (tsc_CellB, px_PriScrmCode, tsc_URA_IdCellB, px_TCellB, tsc_SFNOffsetB, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode), c_CellInfoDef (tsc_CellC, px_PriScrmCode, tsc_URA_IdCellC, px_TCellC, tsc_SFNOffsetC, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode), c_CellInfoDef (

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tsc_CellD,		tsc_CellD,	
px_PriScrmCode,		px_PriScrmCode,	
tsc_URA_IdCellD,		tsc_URA_IdCellD,	
px_TCellD,		px_TCellD,	
tsc_SFN_OffsetD,		tsc_SFN_OffsetD,	
c_FreqInfo (c_FreqInfo (
px_UARFCN_D_Mid – 950		px_UARFCN_D_Mid – 950	
, px_UARFCN_D_Mid),		, px_UARFCN_D_Mid),	
px_UL_ScramblingCode),		px_UL_ScramblingCode),	
c_CellInfoDef (c_CellInfoDef (
tsc_CellE,		tsc_CellE,	
px_PriScrmCode,		px_PriScrmCode,	
tsc_URA_IdCellE,		tsc_URA_IdCellE,	
px_TCellE,		px_TCellE,	
tsc_SFN_OffsetE,		tsc_SFN_OffsetE,	
c_FreqInfo (c_FreqInfo (
px_UARFCN_D_Mid – 950		px_UARFCN_D_Mid – 950	
, px_UARFCN_D_Mid),		, px_UARFCN_D_Mid),	
px_UL_ScramblingCode),		px_UL_ScramblingCode),	
c_CellInfoDef (c_CellInfoDef (
tsc_CellF,		tsc_CellF,	
px_PriScrmCode,		px_PriScrmCode,	
tsc_URA_IdCellF,		tsc_URA_IdCellF,	
px_TCellF,		px_TCellF,	
tsc_SFN_OffsetF,		tsc_SFN_OffsetF,	
c_FreqInfo (c_FreqInfo (
px_UARFCN_D_Mid – 950		px_UARFCN_D_Mid – 950	
, px_UARFCN_D_Mid),		, px_UARFCN_D_Mid),	
px_UL_ScramblingCode),		px_UL_ScramblingCode),	
c_CellInfoDef (c_CellInfoDef (
tsc_CellG,		tsc_CellG,	
px_PriScrmCode,		px_PriScrmCode,	
tsc_URA_IdCellG,		tsc_URA_IdCellG,	
px_TCellG,		px_TCellG,	
tsc_SFN_OffsetG,		tsc_SFN_OffsetG,	
c_FreqInfo (c_FreqInfo (
px_UARFCN_D_Mid – 950		px_UARFCN_D_Mid – 950	
, px_UARFCN_D_Mid),		, px_UARFCN_D_Mid),	
px_UL_ScramblingCode),		px_UL_ScramblingCode),	
c_CellInfoDef (c_CellInfoDef (
tsc_CellH,		tsc_CellH,	
px_PriScrmCode,		px_PriScrmCode,	
tsc_URA_IdCellH,		tsc_URA_IdCellH,	
px_TCellH,		px_TCellH,	
tsc_SFN_OffsetH,		tsc_SFN_OffsetH,	
c_FreqInfo (c_FreqInfo (
px_UARFCN_D_Mid – 950		px_UARFCN_D_Mid – 950	
, px_UARFCN_D_Mid),		, px_UARFCN_D_Mid),	
px_UL_ScramblingCode))		px_UL_ScramblingCode))	
cb_SIB12_Def		cb_SIB12_Def	
tcv_SIB12	SysInfoType12		
tcv_SIB18	SysInfoType18		no initial value
tcv_SIB2	SysInfoType2		no initial value

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_SIB3	SysInfoType3	cb_SIB3_DefUTRAN_GERAN (c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode))	the initial value c_SB3_DefUTRAN_GERAN is for UTRAN/GERAN, for UTRAN only tcv_SIB3 shall be re-initialized to c_SB3_DefUTRAN.
tcv_SIB4	SysInfoType4	cb_SIB4_DefUTRAN_GERAN (c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode))	the initial value c_SB4_DefUTRAN_GERAN is for UTRAN/GERAN, for UTRAN only tcv_SIB4 shall be re-initialized to c_SB4_DefUTRAN.
tcv_SIB7	SysInfoType7	c_SIB7_Def	
tcv_Start	START_Value	'00000000000000000000'B	To hold the START value received in the INITIAL DIRECT TRANSFER message
tcv_StartList	STARTList	c_StartListCS	To hold the START list sent by UE
tcv_SubChNum	AvailableSubChannelNumbers	'111111111111'B	Available subchannel numbers for PRACH, this initial value is for ts_SS_CreateCellFACH. (from 34.108 cl. 6.1 (SIB5))
tcv_TestBody	BOOLEAN	FALSE	to indicate if the test body is currently being executed
tcv_TGCFN	TGCFN	0	
tcv_TimeoutInDefault	BOOLEAN	FALSE	Indication of a TimeoutInDefault having occurred
tcv_TimerPoll	TimerPoll	tp400	value for polling timer, this initial value is for ts_SS_CreateCellFACH. (from 34.123–1). used in RB2, RB3, RB4
tcv_TimerPollProhibit	TimerPollProhibit	tpp200	minimum time between polls, this initial value is for ts_SS_CreateCellFACH. (from 34.123–1). used in RB2, RB3, RB4
tcv_TmpAttachReqPDU	ATTACHREQUEST		Temporary variable
tcv_TmpAuthAndCiphRspPDU	AUTHENTICATIONANDCIPHERINGRESPONSE		Temporary variable
tcv_TmpB3	B3	'000'B	Temporary variable

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Test Case Variable Declarations			
Variable Name	Type	Value	Comments
tcv_TmpCellInfo	CellInfoCfg	c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, c_FreqInfo (px_UARFCN_D_Mid – 950 , px_UARFCN_D_Mid), px_UL_ScramblingCode)	To temporary store CellInfo data
tcv_TmpRAU_ReqPDU	ROUTINGAREAUPDATEREQUEST		Temporary variable
tcv_UE_OpMode	UE_OperationMode	px_UE_OpModeDef	Indicates the current UE operation mode (either A or C).
tcv_UE_SwitchedOn	BOOLEAN	TRUE	This value is to represent the state of the UE. TRUE means UE is Switched/Powered On, and hence only Switch/Power OFF operation can be done, and no Switch/Power On operation. False Means, UE is off, and only Switch/Power On Operation can be done. This Variable is introduced to help automation of test cases. The default value of ON, is consistent with present Test Sequence, where MMI command to switch/Power off is called before creation of the first cell on SS Side.
tcv_UE_SystemSpecificCap	INTEGER	0	use to represent the integer equivalent of 7 bit UE system specific Capability. The Valid range is 0 to 127
tcv_Use_E_PLMN	BOOLEAN	FALSE	The user of ts_IdleUpdated needs to set tcv_Use_E_PLMN to 'True' if he wants to send 'equivalent PLMN list' as set in tcv_E_PLMN during CS or PS registration
Detailed Comments :			

PCO Type Declarations		
PCO Type	Role	Comments
DSAP	LT	
CSAP	LT	
Dc_SAP	LT	
MMI	UT	
Detailed Comments :		

PCO Declarations			
PCO Name	PCO Type	Role	Comments
AM	DSAP	LT	PCO above AM SAP of RLC
CMAC	CSAP	LT	Control and observation point between RRC and MAC
CPHY	CSAP	LT	Control and observation point between RRC and PHY
CRLC	CSAP	LT	Control and observation point between RRC and RLC
Dc	Dc_SAP	LT	Carry transmission and reception of NAS messages
TM	DSAP	LT	PCO above TM SAP of RLC
UM	DSAP	LT	PCO above UM SAP of RLC
Ut	MMI	UT	The PCO used for the upper tester
Detailed Comments :			

Timer Declarations			
Timer Name	Duration	Unit	Comments
t_TimeoutInDefault	300	ms	Used to catch timeouts in the Defaults
t_Dly	5000	ms	general purpose delay timer
t_Guard	300	s	test case guard timer
t_T312	1	s	Timer to check physical channel establishment criteria
t_WaitMS	13500	ms	general wait timer
t_WaitS	15	s	general watch timer
Detailed Comments :			

ASP Type Definition		
ASP Name : AT_CmdCnf PCO Type : MMI Comments : The ASP is used get the result of a requested AT command previously sent to the UT (UT ->LT).		
Parameter Name	Parameter Type	Comments
result	BOOLEAN	OPTIONAL
resultString	IA5String	OPTIONAL
sMS_BlockMode	HEXSTRING	to control and observe the Block mode procedure for SMS
Detailed Comments :		

ASP Type Definition		
ASP Name : AT_CmdReq PCO Type : MMI Comments : The ASP is used to request a AT command to the UT (LT ->UT).		
Parameter Name	Parameter Type	Comments
cmd	IA5String	command line
sMS_BlockMode	HEXSTRING	to control and observe the Block mode procedure for SMS
Detailed Comments :		

ASP Type Definition		
ASP Name : MMI_CmdCnf		
PCO Type : MMI		
Comments : The ASP is used get the result of a requested command previously sent to the UT (UT ->LT).		
Parameter Name	Parameter Type	Comments
result	BOOLEAN	OPTIONAL
resultString	IA5String	
Detailed Comments :		

ASP Type Definition		
ASP Name : MMI_CmdReq PCO Type : MMI Comments : The ASP is used to request an MMI command to the UT (LT ->UT).		
Parameter Name	Parameter Type	Comments
cmd	IA5String	command line
Detailed Comments :		

ASP Type Definition		
ASP Name : RLC_AM_TestDataInd		
PCO Type : DSAP		
Comments : To indicate the reception of unstructured data using acknowledged mode in the uplink direction		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	RB identity (RB3 or RB4)
rB_Id	SS_RB_Identity	
data	PDU	
Detailed Comments :		

ASP Type Definition		
ASP Name : RLC_AM_TestDataReq		
PCO Type : DSAP		
Comments : To request the transmission of unstructured data using acknowledged mode in the downlink direction		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	RB identity (RB3 or RB4)
rB_Id	SS_RB_Identity	
data	PDU	
Detailed Comments :		

ASP Type Definition		
ASP Name : RLC_TR_TestDataInd		
PCO Type : DSAP		
Comments : To indicate the reception of unstructured data using transparent mode in the uplink direction		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	RB identity (RB3 or RB4)
rB_Id	SS_RB_Identity	
data	PDU	
Detailed Comments :		

ASP Type Definition		
ASP Name : RLC_TR_TestDataReq		
PCO Type : DSAP		
Comments : To request the transmission of unstructured data using transparent mode in the downlink direction. TTCN writer is requires to send data according to the transport block size allowed.		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	RB identity (RB3 or RB4)
rB_Id	SS_RB_Identity	
data	PDU	
Detailed Comments :		

ASP Type Definition		
ASP Name : RRC_DataInd (RRC-DATA-IND) PCO Type : Dc_SAP Comments : The ASP is used to indicate the receipt of the NAS PDU message using acknowledged operation (NAS <- RRC).		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	Cell Id
rB_Id	SS_RB_Identity	RB identity (RB3 or RB4)
ch	LogicChGERAN	Logical channel (used for interworking with GERAN)
sapId	SapId	RRC SAP identifier (SAP0)
cN_Domain	SS_CN_DomainIdentity	CN domain identity
start	START_Value	Mandatory in INITIAL DIRECT TRANSFER
msg	PDU	NAS PDU
Detailed Comments :		

ASP Type Definition		
ASP Name : RRC_DataReq PCO Type : Dc_SAP Comments : The ASP is used to request the transmission of the NAS PDU message using acknowledged operation (NAS -> RRC).		
Parameter Name	Parameter Type	Comments
cellId	INTEGER	
rB_Id	SS_RB_Identity	RB identity (RB3 or RB4)
ch	LogicChGERAN	Logical channel (used for interworking with GERAN)
sapId	SapId	RRC SAP identifier (SAP0)
cN_Domain	SS_CN_DomainIdentity	CN domain identity
msg	PDU	NAS PDU
Detailed Comments :		

ASN.1 ASP Type Definition	
ASP Name : CRLC_SendContinuousData_REQ PCO Type : CSAP Comments : To request sending data in every TTI on each RB identified. After the CMAC_Restriction_REQ, the TFC under test will be the one corresponding to the maximum CTFC value in the Restricted list, so that SS can select the number of Transport blocks and the size of Transport blocks on individual Transport channels derived from this CTFC. SS shall take care about all kind of discard info in all RLC modes and the final goal is that the DL TFCs under test shall be selected in downlink for sending data on the request RBs in each TTI.	
Type Definition	
SEQUENCE { cellId INTEGER(-1..63), rabTxInfo RabTxInfo } 	
Detailed Comments :	

ASN.1 ASP Type Definition
ASP Name : CRLC_SendContinuousData_CNF PCO Type : CSAP Comments : Confirm sending data in every TTI on each requested RB
Type Definition
SEQUENCE { cellId INTEGER(−1..63), result ENUMERATED{failure(0), success(1)} }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Bind_TestData_TTI_REQ PCO Type : CSAP Comments : To request binding subsequent data sending RLC_TR_TestDataReq on the different DL RBs in the same TTI. On the request, the transmission of the test data is temporarily suppressed on those radio bearers which follow subsequently this CRLC_Bind_TestData_TTI_REQ and have 'numOfDiffRb' different RB IDs. Having received the number 'numOfDiffRb' of RLC_TR_TestDataReq, the SS RLC sends the test data on those RBs in the same TTI according to the allowed DL TFCS.
Type Definition
SEQUENCE { cellId INTEGER(−1..63), numOfDiffRb INTEGER(2..6) -- Number of different RB IDs }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Bind_TestData_TTI_CNF PCO Type : CSAP Comments : To confirm the request of binding subsequent data sending RLC_TR_TestDataReq on the different DL RBs in the same TTI.
Type Definition
SEQUENCE { cellId INTEGER(−1..63), result ENUMERATED{failure(0), success(1)} }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SetRRC_MessageSN_REQ PCO Type : CSAP Comments : To request the SS to set the RRC message sequence number in COUNT-I to the value specified in this ASP. The ASP is used to initialise SS RRC
Type Definition
SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, count_I_LSB_UL RRC_SequenceNumber OPTIONAL, count_I_LSB_DL RRC_SequenceNumber OPTIONAL }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SetRRC_MessageSN_CNF PCO Type : CSAP Comments : To confirm the RRC message sequence number setting request
Type Definition
SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_RRC_MessageSN_REQ PCO Type : CSAP Comments : To request the SS to return the values in COUNT-I for sending the next DL RRC message or for receiving the next UL RRC message on the concerned SRB.
Type Definition
SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_RRC_MessageSN_CNF PCO Type : CSAP Comments : To return the counter I value (HFN and RRC message sequence number) for sending the next DL RRC message or for receiving the next UL RRC message on the concerned SRB. COUNT_I_MSB is the 28 MSB of the COUNT-I (HFN)
Type Definition
SEQUENCE { cellId INTEGER(−1..63), routingInfo RoutingInfo, count_I_MSB_UL COUNT_I_MSB, count_I_LSB_UL RRC_SequenceNumber, count_I_MSB_DL COUNT_I_MSB, count_I_LSB_DL RRC_SequenceNumber }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Ciphering_Activate_CNF PCO Type : CSAP Comments : To confirm to activate or inactivate the ciphering
Type Definition
SEQUENCE { cellId INTEGER (−1..63), routingInfo RoutingInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Ciphering_Activate_REQ PCO Type : CSAP Comments : To request to start or, restart or stop downlink ciphering or uplink deciphering. The physicalChannelIdentity of DPCH applies to routingInfo.Do not increment HFN part of COUNT-C if the value of incrementCOUNT_C_Ind is "NotIncr".
Type Definition
SEQUENCE { cellId INTEGER(−1..63), routingInfo RoutingInfo, ratType RatType, cn_DomainIdentity CN_DomainIdentity, cipheringModelInfo CipheringModelInfo, incHFN Increment_Mode }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Config_CNF PCO Type : CSAP Comments : For MAC emulator to report that a previous attempt to setup, reconfigure or release a logical channel is successful.
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Config_REQ PCO Type : CSAP Comments : To request to configure MAC entity. Setup is used for creation of the MAC instances or the MAC resources. Release is used for free the all MAC resources. The reconfiguration is to change the MAC parameters, it is not the MAC modification.
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, ratType RatType, configMessage CHOICE { setup CmacConfigReq, reconfigure CmacConfigReq, release NULL } }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_PAGING_Config_CNF PCO Type : CSAP Comments : To confirm to setup the Paging message
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_PAGING_Config_REQ PCO Type : CSAP Comments : To request MAC layer to send the Paging message on the specified configuration.
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo, ratType RatType, configMessage CmacPagingConfigReq }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Restriction_CNF PCO Type : CSAP Comments : For MAC emulator to report that a previous attempt of restricting TFCs have been successful.
Type Definition
<pre>SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_Restriction_REQ PCO Type : CSAP Comments : To request to configure MAC entity. The field restrictAllowedTFCs is provided to allow the UL and/or DL SS TFCs to be restricted for a specific transport channel. This information only needs to be sent to the MAC layer, since it is the MAC layer's responsibility to determine the set of valid TFCs each TTI.
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, ratType RatType, restrictAllowedTFCs TFC_Restriction }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_SYSINFO_Config_CNF PCO Type : CSAP Comments : To confirm to setup the BCCH message to MAC layer
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_SYSINFO_Config_REQ PCO Type : CSAP Comments : To request MAC layer to send the BCCH message on the specified configuration.
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo, ratType RatType, configMessage CmacSysinfoConfigReq }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_SecurityMode_Config_CNF PCO Type : CSAP Comments : To confirm to configure the MAC security mode
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63) }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CMAC_SecurityMode_Config_REQ PCO Type : CSAP Comments : To request to configure the MAC security mode If there are several CMAC_Ciphering_Activate_REQ follow this ASP, the SS shall take a serial of specified actions on the same contents in this ASP at the activation time indicated in each CMAC_Ciphering_Activate_REQ.
Type Definition
SEQUENCE { cellId INTEGER (–1..63), macCipheringInfo SecurityInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Cell_Config_CNF PCO Type : CSAP Comments : To confirm to setup the cell parameter
Type Definition
SEQUENCE { cellId INTEGER (0..63) }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Cell_Config_REQ PCO Type : CSAP Comments : To request to setup the cell parameter. The unit of tcell is chip; the unit of sfnOffset is frame number; the primary scrambling code number of the cell is 16*primaryScramblingCode_SS; the unit of dLTxAttenuationLevel is dB.
Type Definition
SEQUENCE { cellId INTEGER (0..63), tcell INTEGER(0..38399), sfnOffset INTEGER (0 .. 4095), frequencyInfo FrequencyInfo, primaryScramblingCode_SS INTEGER (0..511), cellTxPowerLevel CellTxPowerLevel, dLTxAttenuationLevel INTEGER(0..30) }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Cell_Release_CNF PCO Type : CSAP Comments : The confirmation to the CPHY_Cell_Release_Req
Type Definition
<pre>SEQUENCE { soft_Reset BOOLEAN, cell_ID_List SEQUENCE (SIZE (1..8)) OF INTEGER(0..63) -- cell IDs }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Cell_Release_REQ PCO Type : CSAP Comments : 1. This Primitive with "Soft_Reset" flag ON gives a common known starting point/state of SS for a test case. The SS performs the following whenever it receives this primitive with "Soft_Reset" flag ON:Releases all configured Channels and cells (if any) irrespective of Cell ID list IE. 2. Releases the associated Memory Buffers (if any). 3. Cancels all active timers (if any) With "Soft_Reset" flag OFF: 1. Releases cells listed in IE Cell_ID_List and associated configured Channels (if any) 2. Releases the Memory Buffers(if any) associated with Cells listed in IE Cell_ID_List 3. Cancels all active timers (if any) associated with Cells listed in IE Cell_ID_List.
Type Definition
<pre>SEQUENCE { soft_Reset BOOLEAN, cell_ID_List SEQUENCE (SIZE (1..8)) OF INTEGER(0..63) -- cell IDs }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Frame_Number_CNF PCO Type : CSAP Comments : To return the requested connection frame number. The routingInfo indicates a physical channel.
Type Definition
<pre>SEQUENCE{ cellId INTEGER(0..63), routingInfo RoutingInfo, frameNumber INTEGER (0..255) }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Frame_Number_REQ PCO Type : CSAP Comments : To request the physical layer to return a connection frame number on which the next message can be sent at the specified PCO on the specified logical channel. The return frame number shall leave time from current frame number in order to leave some execution time for TTCN preparing next message. The routingInfo indicates a physical channel
Type Definition
<pre>SEQUENCE{ cellId INTEGER(0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Ini_CNF PCO Type : CSAP Comments : Confirm the test initialisation
Type Definition
<pre>SEQUENCE { confirmation NULL }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Ini_REQ PCO Type : CSAP Comments : Request to initialise the test
Type Definition
<pre>ENUMERATED { defaultRadioEnvironment(0), nonDefaultMultiCell(1) }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Out_of_Sync_IND PCO Type : CSAP Comments : To report that the physical channel synchronization (in FDD mode, sync with uplink DPCCH) was lost as detected by the SS receiver.
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Modify_CNF PCO Type : CSAP Comments : To confirm to modify the Radio Link
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Modify_REQ PCO Type : CSAP Comments : To request to modify the Radio Link HardHandover (PhysicalChannelReconfig) ChannelisationCodeChange FrequencyChange PhysicalChannelModifyForTrCHReconfig CompressedMode(PhysicalChannelReconfig) Re_Synchronized HardHandover SoftHandover
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo, ratType RatType, modifyMessage CphyRIModifyReq }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Release_CNF PCO Type : CSAP Comments : PHY emulator confirms that a specified physical channel has been released.
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Release_REQ PCO Type : CSAP Comments : To request to release the Radio Link
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Setup_CNF PCO Type : CSAP Comments : To confirm to setup the Radio Link
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_RL_Setup_REQ PCO Type : CSAP Comments : To request to setup the associated transport channels and the Radio Link itself.
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo, ratType RatType, setupMessage CphyRISetupReq }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_Sync_IND PCO Type : CSAP Comments : To indicate that physical channel synchronization (in FDD mode, sync with DPCCH) has been achieved.
Type Definition
<pre>SEQUENCE{ cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_TrCH_Config_CNF PCO Type : CSAP Comments : To confirm to configure the transport channel
Type Definition
<pre>SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_TrCH_Config_REQ PCO Type : CSAP Comments : To request to configure the transport channel
Type Definition
<pre>SEQUENCE { cellId INTEGER(0..63), routingInfo RoutingInfo, ratType RatType, trchConfigType TrChConfigType, configMessage CphyTrchConfigReq }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_TrCH_Release_CNF PCO Type : CSAP Comments : To confirm to release tthe Radio Link
Type Definition
SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CPHY_TrCH_Release_REQ PCO Type : CSAP Comments : To request to release tthe Radio Link
Type Definition
SEQUENCE { cellId INTEGER (0..63), routingInfo RoutingInfo, trchConfigType TrChConfigType }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Ciphering_Activate_CNF PCO Type : CSAP Comments : To confirm to activate or inactivate the ciphering
Type Definition
SEQUENCE { cellId INTEGER (-1..63)}
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Ciphering_Activate_REQ PCO Type : CSAP Comments : To request to start, restart or stop downlink ciphering or uplink deciphering. The RB-identity applied to routingInfo indicates the SRB being not suspended during the execution of the ciphering mode command. Each call of the ASP includes one RLC SN in rb_DL_CiphActivationTimeInfo for the corresponding rb-identity.
Type Definition
SEQUENCE { cellId INTEGER (–1..63), ratType RatType, cn_DomainIdentity CN_DomainIdentity, ciphActivationInfo CiphActivationInfo, incHFN RLC_IncMode }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Config_CNF PCO Type : CSAP Comments : For RLC emulator to confirm that a previous attempt to establish, reconfigure or release a radio bearer has been successful
Type Definition
SEQUENCE { cellId INTEGER (–1..63), routingInfo RoutingInfo }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Config_REQ PCO Type : CSAP Comments : To request to setup, reconfigure or release RLC entity
Type Definition
SEQUENCE { cellId INTEGER (–1..63), routingInfo RoutingInfo, ratType RatType, configMessage CrlcConfigReq }
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Integrity_Activate_CNF PCO Type : CSAP Comments : To confirm to activate or inactivate the integrity protection
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63) }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Integrity_Activate_REQ PCO Type : CSAP Comments : To request to start or to modify the downlink or uplink integrity protection. The ASP shall be called before the sending of SECURITY MODE COMMAND. It activates the integrity on all SRBs in DL. The ASP shall not be called if the integrity shall be switched off in the test case.
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), cn_DomainIdentity CN_DomainIdentity, integrityActivationInfo IntegrityActivationInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Integrity_Failure_IND PCO Type : CSAP Comments : RLC emulator reports the occurrences of a failure in integrity protection, i.e. reception of an integrity-protected RLC AM/UM SDU containing a non-matching X-MAC value compared to the desired.
Type Definition
<pre>SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, failureCause ENUMERATED { codeNotMatched(0) } }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Resume_CNF PCO Type : CSAP Comments : To confirm the resume request
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Resume_REQ PCO Type : CSAP Comments :
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SecurityMode_Config_CNF PCO Type : CSAP Comments : To confirm to configure the ciphering
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63) }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SecurityMode_Config_REQ PCO Type : CSAP Comments : To request to configure the RLC security mode If several subsequent CRLC_Integrity_Activate_REQ or CRLC_Ciphering_Activate_REQ follow this ASP, the SS shall take a serial of specified actions on the same contents in this ASP at the activation time indicated in each CRLC_ Integrity (or Ciphering)_Activate_REQ.
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), rlcSecurityInfo SecurityInfo }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SequenceNumber_CNF PCO Type : CSAP Comments : To return the requested counter sequence number to which the next DL PDU to be sent or the expected UL PDU to be received.
Type Definition
<pre> SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, count_C_MSB_UL COUNT_C_MSB , count_C_LSB_UL RLC_SequenceNumber, count_C_MSB_DL COUNT_C_MSB , count_C_LSB_DL RLC_SequenceNumber } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_SequenceNumber_REQ PCO Type : CSAP Comments : To request the RLC layer to return current counter sequence numbers to which the next DL PDU to be sent or the expected UL PDU to be received.
Type Definition
<pre> SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Suspend_CNF PCO Type : CSAP Comments : To confirm the suspension of data transmission. The parameter vt indicates either the value of the Send State Variable VT(S) for AM, or the value of Data State Variable VT(US) for UM.
Type Definition
<pre> SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, vt RLC_SequenceNumber } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : CRLC_Suspend_REQ PCO Type : CSAP Comments : To request the suspension of data transmission. The parameter n indicates that an RLC entity will not send a PDU with "Sequence Number" >= VT(S)+N for AM and "Sequence Number" >= VT(US)+N for UM, where N is a non-negative integer
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, n RLC_SequenceNumber }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_AM_DATA_CNF PCO Type : DSAP Comments :
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, mui Mui }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_AM_DATA_IND PCO Type : DSAP Comments : To indicate to receive DATA using acknowledged mode
Type Definition
<pre>SEQUENCE { cellId INTEGER (-1..63), routingInfo RoutingInfo, integrityResult IntegrityResult , aM_message CHOICE { uL_DCCH_Message UL_DCCH_Message, uL_CCCH_Message UL_CCCH_Message, uL_SHCCH_Message UL_SHCCH_Message } }</pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_AM_DATA_REQ PCO Type : DSAP Comments : To request to transmit DATA using acknowledged mode
Type Definition
<pre> SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, confirmationRequest AmConfirmationRequest, aM_message CHOICE { dL_DCCH_Message DL_DCCH_Message, dL_CCCH_Message DL_CCCH_Message, pCCH_Message PCCH_Message, dL_SHCCH_Message DL_SHCCH_Message, bCCH_FACH_Message BCCH_FACH_Message, bCCH_BCH_Message BCCH_BCH_Message, invalid_dL_DCCH_Message Invalid_DL_DCCH_Message, invalid_dL_CCCH_Message Invalid_DL_CCCH_Message, invalid_dL_SHCCH_Message Invalid_DL_SHCCH_Message } } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_TR_DATA_IND PCO Type : DSAP Comments : To indicate to receive DATA using transparent mode
Type Definition
<pre> SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, tM_message CHOICE { uL_DCCH_Message UL_DCCH_Message, uL_CCCH_Message UL_CCCH_Message, uL_SHCCH_Message UL_SHCCH_Message } } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_TR_DATA_REQ PCO Type : DSAP Comments : To request to transmit DATA using transparent mode
Type Definition
<pre> SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, tM_message CHOICE { dL_DCCH_Message DL_DCCH_Message, dL_CCCH_Message DL_CCCH_Message, pCCH_Message PCCH_Message, dL_SHCCH_Message DL_SHCCH_Message, bCCH_FACH_Message BCCH_FACH_Message, bCCH_BCH_Message BCCH_BCH_Message, invalid_dL_DCCH_Message Invalid_DL_DCCH_Message, invalid_dL_CCCH_Message Invalid_DL_CCCH_Message, invalid_dL_SHCCH_Message Invalid_DL_SHCCH_Message } } </pre>
Detailed Comments :

ASN.1 ASP Type Definition
ASP Name : RLC_UM_DATA_IND PCO Type : DSAP Comments : To indicate to receive DATA using unacknowledged mode
Type Definition
<pre> SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, integrityResult IntegrityResult, uM_message CHOICE { uL_DCCH_Message UL_DCCH_Message, uL_CCCH_Message UL_CCCH_Message, uL_SHCCH_Message UL_SHCCH_Message } } </pre>
Detailed Comments :

ASN.1 ASP Type Definition	
ASP Name : RLC_UM_DATA_REQ PCO Type : DSAP Comments : To request to transmit DATA using unacknowledged mode	
Type Definition	
<pre> SEQUENCE { cellId INTEGER(-1..63), routingInfo RoutingInfo, uM_message CHOICE { dL_DCCH_Message DL_DCCH_Message, dL_CCCH_Message DL_CCCH_Message, pCCH_Message PCCH_Message, dL_SHCCH_Message DL_SHCCH_Message, bCCH_FACH_Message BCCH_FACH_Message, bCCH_BCH_Message BCCH_BCH_Message, invalid_dL_DCCH_Message Invalid_DL_DCCH_Message, invalid_dL_CCCH_Message Invalid_DL_CCCH_Message, invalid_dL_SHCCH_Message Invalid_DL_SHCCH_Message }, specialLI BOOLEAN } </pre>	
Detailed Comments :	

PDU Type Definition			
PDU Name : AUTHENTICATION_AND_CIPHERING_FAILURE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.10a (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		'0001 1100'B M BITSTRING [8]
gmmCause	RejCau		M 1 octet
authFailurePar	AuthenticationFailureParameter		O TLV, 16 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : ACTIVATEPDPCONTEXTREQUESTul PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : Activate PDP Context Request ue -> n 3GPP 24.008 clause, 9.5.1			
Field Name	Field Type	Field Encoding	Comments
ti	TI		transaction identifier M BITSTRING [4]
sM_ProtocolDiscriminator	ProtocolDiscriminator		protocol discriminator M BITSTRING [4]
msgType	MsgType		message type M BITSTRING [8]
requestedNSAPI	NSAPI_v		Network service access point identifier M V BITSTRING [8]
requestedLLC_SAPI	LLC_SAPI_v		LLC service access point identifier M V BITSTRING [8]
requestedQoS	QualityOfService_lv		Quality of service M LV OCTETSTRING [13–15]
pDP_Address	PktDataProtoAddr_lv		Packet data protocol address M LV OCTETSTRING [3–19]
accessPtName	AccessPtName		Access point name O TLV OCTETSTRING [3–102]
protocolConfOpts	ProtoCfgOpt		Protocol configuration options O TLV OCTETSTRING [3–253]
Detailed Comments :			

PDU Type Definition			
PDU Name : ACTIVATERBTESTMODE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : ACTIVATE RB TEST MODE n -> ue 3G TS 34.109 V3.0.0 cl. 6.6			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : ACTIVATERBTESTMODECOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : ACTIVATE RB TEST MODE COMPLETE ue -> n 3G TS 34.109 V3.0.0 cl. 6.7			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : ATTACHACCEPT PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.2 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gmmProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
forceToStandby	ForceToStandby		M 1/2 octet
attachResult	AttachResult		M 1/2 octet
periodicRAupdateTimer	GPRS_Timer_v		M 1 octet
radioPrioTOM8	RadioPriority2_v		M 1/2 octet
radioPrioSMS	RadioPriority_v		M 1/2 octet
rai	RAI_v		Routing Area Identification M 6 octets
ptmsiSignature	PTMSI_Signature		O 4 octets
negReadyTimer	GPRS_Timer		O 2 octets
allocatedPTMSI	GMM_MS_IdentityPTMSI		O 7 octets
msIdentity	GMM_MS_Identity		O 7–10 octets
gmmCause	GMM_Cause		O 2 octets
t3302Value	GPRS_Timer2		O TLV 3 octets
cellNotification	CellNotification		O 1 octet
equivalentPLMN	PLMN_List		O 5–17 octets
ntwFeatureSupport	NtwFeatureSupport_tv		O TV 1 octet
emergNumList	EmergNumList		O TLV 5–50 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : ATTACHCOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.3 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		'0000 0011'B M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : ATTACHREJECT PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.4 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator '1000' M BITSTRING [4]
msgType	MsgType		Message Type '0000 0100' M BITSTRING [8]
gmmCause	RejCau		M 1 octet
t3302Value	GPRS_Timer2		O, TLV 3 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : ATTACHREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.1 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator '1000' M BITSTRING [4]
msgType	MsgType		Message Type '0000 0001' M BITSTRING [8]
msNetworkCap	MS_NetworkCap_lv		MS network capability M 3–9 octets
gprsCiphKeySeqNo	CiphKeySeqNum		Ciphering key sequence number M BITSTRING[4]
attachType	AttachType		Attach Type M BITSTRING [4]
drxParameter	DRXparamter		M 2 octets
ptmsiORimsi	MS_Identity_lv		Mobile Identity M 6–9 octets
oldRAI	RAI_v		Routing Area Identification M 6 octets
msRadioAccessCap	MSRadioAccessCap_lv		M 6–52 octets
oldPTMSI_Signature	PTMSI_Signature		O 4 octets
readyTimer	GPRS_Timer		O 2 octets
tmsiStatus	TMSI_Status		O 1 octet
pS_LCS_Capability	PS_LCS_Capability		O TLV 3 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : AUTHENTICATIONANDCIPHERINGREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.9 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		'0001 0010'B M BITSTRING [8]
imeisvReq	IMEISVRequest		M BITSTRING[4]
ciphAlgorithm	CiphAlgorithm		Ciphering algorithm M BITSTRING[4]
acRefNo	AC_ReferenceNumber		M BITSTRING [4]
forceToStandby	ForceToStandby		M BITSTRING [4]
authRand	AuthenticationParamterRAND		O, TV 17 octets
gprsCiphKeySeqNo	CiphKeySeqNum_tv		Ciphering key sequence number O, TV, 1
aUTN	GMM_AUTN		Auth. parameter AUTN O, TLV, 18 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : AUTHENTICATIONANDCIPHERINGRESPONSE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.10 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		'0001 0011'B M BITSTRING [8]
spare4	B4		
acRefNo	AC_ReferenceNumber		M BITSTRING [4]
authRsp	AuthRsp_tv		O, TV 5 octets
imei5v	GMM_MS_Identity		O TLV, 11 octets
authRspExt	AuthRspExt		O TLV, 3–14 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : AUTHENTICATIONFAILURE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM AUTHENTICATION FAILURE ue -> n 3G TS 24.008 V3.4.0 cl. 9.2.3a			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
rejCau	RejCau		Reject Cause M BITSTRING [8]
authFailParam	AuthenticationFailureParameter		Authentication Failure Paramter O AuthFail (128 bits)
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the MS, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequence number; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number.			

PDU Type Definition			
PDU Name : AUTHENTICATIONREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM AUTHENTICATION REQUEST n -> ms 3G TS 24.008 V3.4.0 cl. 9.2.2			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
spare4	B4		Spare half octet M BITSTRING [4]
ciphKeySeqNum	CiphKeySeqNum		Ciphering Key Sequence Number M BITSTRING [4]
rAND	MM_RAND		Auth. parameter RAND M BITSTRING [128]
aUTN	AUTN		Auth. parameter AUTN O AUTN
Detailed Comments : (1) In messages sent from the network bits 7 and 8 are "0" (see 3G TS 24.008 cl. 10.4) .			

PDU Type Definition			
PDU Name : AUTHENTICATIONRESPONSE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM AUTHENTICATION RESPONSE ue -> n 3G TS 24.008 V3.4.0 cl. 9.2.2			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
authRsp	AuthRsp		Authentication Response M OCTETSTRING [4]
authRspExt	AuthRspExt		Authentication Response Extension O AuthRspExt (3–14 octets)
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the MS, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequence number; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number.			

PDU Type Definition			
PDU Name : CLOSEUETESTLOOP PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : CLOSE UE TEST LOOP n -> ue 3G TS 34.109 V3.0.0 cl. 6.2			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
uE_TestLoopMode	UE_TestLoopMode		UE test loop mode M UE_TestLoopMode
uE_TestLoopMode1LB_Setup	UE_TestLoopMode1LB_Setup		UE test loop mode 1 LB setup C UE_TestLoopMode1LB_Setup
Detailed Comments :			

PDU Type Definition			
PDU Name : CLOSEUETESTLOOPCOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : CLOSE UE TEST LOOP ue -> n 3G TS 34.109 V3.0.0 cl. 6.3			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : DEACTIVATEPDPCONTEXTREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : Deactivate PDP Context Request n <=> ue 24.008, 9.5.14			
Field Name	Field Type	Field Encoding	Comments
ti	TI		transaction identifier M BITSTRING [4]
sM_ProtocolDiscriminator	ProtocolDiscriminator		protocol discriminator M BITSTRING [4]
msgType	MsgType		message type M BITSTRING [8]
sM_Cause	SM_Cause_v		SM cause M V OCTETSTRING [1]
tearDwnInd	TearDwnInd_tv		Tear down indicator O TV BITSTRING [8]
protocolConfOpts	ProtoCfgOpt		Protocol configuration options O TLV OCTETSTRING [3–253]
Detailed Comments :			

PDU Type Definition			
PDU Name : DEACTIVATERBTESTMODE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : DEACTIVATE RB TEST MODE n -> ue 3G TS 34.109 V3.0.0 cl. 6.8			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : DEACTIVATERBTESTMODECOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : DEACTIVATE RB TEST MODE COMPLETE ue -> n 3G TS 34.109 cl. 6.9			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : DETACHREQUESTMO PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.5.2 (Mobile originating detach, GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
spare4	B4		M 1/2 octet
detachType	DetachType		M 1/2 octet
ptmsi	GMM_MS_IdentityPTMSI		O TLV
ptmsiSignature	PTMSI_Signature_tlv		O
Detailed Comments :			

PDU Type Definition			
PDU Name : IMSIDETACHINDICATION PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM IMSI DETACH INDICATION ue -> n 3G TS 24.008 V3.4.0 cl. 9.2.12			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
mSClsmk1	MS_Clsmk1		Mobile Station Classmark 1 M MSClsmk1 (1 octets)
mobileId	MS_Identity_lv		Mobile Identity LV M MobileId (2-10 octets)
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the UE, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequence number; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number.			

PDU Type Definition			
PDU Name : LOCATIONUPDATINGACCEPT PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM LOCATION UPDATING ACCEPT n -> ms 3G TS 24.008 V3.4.0 cl. 9.2.13			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
locAreald	LocAreald_v		Location Area Id. V M LocAreald_v (5 octets)
mobileId	MM_MS_Identity		Mobile Identity O MobileId (3–11 octets)
followOnProceed	FollowOnProceed		Follow on proceed O BITSTRING [8]
cTSPerm	CTSPerm		CTS Permission O BITSTRING [8]
equivalentPLMN	PLMN_List		O 17 octets
emergNumList	EmergNumList		O TLV 5–50 octets
Detailed Comments : (1) In messages sent from the network bits 7 and 8 are "0" (see 3G TS 24.008 cl. 10.4) .			

PDU Type Definition			
PDU Name : LOCATIONUPDATINGREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM LOCATION UPDATING REQUEST ue -> n 3G TS 24.008 V3.4.0 cl. 9.2.15			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
ciphKeySeqNum	CiphKeySeqNum		Ciphering Key Sequence Number M BITSTRING [4]
locUpdType	LocUpdType		Location Updating Type M BITSTRING [4]
locAreald	LocAreald_v		Location Area Id. V M LocAreald (5 octets)
mSClsmk1	MS_Clsmk1		Mobile Station Classmark 1 M MSClsmk1 (1 octets)
mobileId	MS_Identity_Iv		Mobile Identity LV M MobileId (2-9 octets)
mSClsmk2	MS_Clsmk2		Mobile Station Classmark 2 TLV O MSClsmk2 (5 octets)
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the UE, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequence number; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number.			

PDU Type Definition			
PDU Name : OPENUETESTLOOP PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : OPEN UE TEST LOOP n -> ue 3G TS 34.109 V3.0.0 cl. 6.4			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : OPENUETESTLOOPCOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : OPEN UE TEST LOOP ue -> n 3G TS 34.109 V3.0.0 cl. 6.5			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
tCProtocolDiscriminator	ProtocolDiscriminator		TC Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
Detailed Comments :			

PDU Type Definition			
PDU Name : PAGINGRESPONSE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : PAGINGRESPONSE ue -> n GSM 04.18 cl. 9.1.25			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
rRProtocolDiscriminator	ProtocolDiscriminator		RR Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
spare4	B4		Spare half octet M BITSTRING [4]
ciphKeySeqNum	CiphKeySeqNum		Ciphering Key Sequence Number M BITSTRING [4]
mSCIsmk2	MS_Clsmk2_lv		Mobile Station Classmark 2 M MSCIsmk2 (4 octets)
mobileId	MS_Identity_lv		Mobile Identity LV M MobileId (2–10 octets)
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the UE, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequencenumber; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number. (2) (see 3G TS 24.008 cl. 4.5.1.3.3) The UE shall respond with the PAGING RESPONSE message defined in GSM 04.18, chapter 9.1.25. For reasons of backward compatibility the paging response shall use the RR protocol discriminator.			

PDU Type Definition			
PDU Name : ROUTINGAREAUPDATEREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.14 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator '1000' M BITSTRING [4]
msgType	MsgType		Message Type '0000 1000' M BITSTRING [8]
gprsCiphKeySeqNo	CiphKeySeqNum		Ciphering key sequence number M BITSTRING[4]
updateType	UpdateType_v		Update Type M BITSTRING [4]
oldRAI	RAI_v		Routing Area Identification M 6 octets
msRadioAccessCap	MSRadioAccessCap_lv		M 6–52 octets
oldPTMSI_Signature	PTMSI_Signature		O 4 octets
readyTimer	GPRS_Timer		O 2 octets
drxParameter	DRXparamter_tv		O, TV 3 octets
tmsiStatus	TMSI_Status		O 1 octet
ptmsi	GMM_MS_IdentityPTMSI		O, TLV 7 octets
msnetworkcap	MS_NetworkCap_tlv		MS network capability O, TLV 4–10 octets
pDP_ContextStatus	PDP_ContextStatus		O 4 octets
pS_LCS_Capability	PS_LCS_Capability		O TLV 3 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : SERVICEREQUEST PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.20 (GMM message)			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
gMMProtocolDiscriminator	ProtocolDiscriminator		GMM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type M BITSTRING [8]
serviceType	ServiceType_v		Service type M BITSTRING[4]
ciphKeySeqNo	CiphKeySeqNum		Ciphering key sequence number M BITSTRING[4]
ptmsi	MS_Identity_lv		Mobile Identity M 6 octets
pDP_ContextStatus	PDP_ContextStatus		O 4 octets
Detailed Comments :			

PDU Type Definition			
PDU Name : SETUPul PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : CC SETUP n <- ue 3G TS 24.008 cl. 9.3.23.2			
Field Name	Field Type	Field Encoding	Comments
ti	TI		transaction identifier M BITSTRING [4]
cC_ProtocolDiscriminator	ProtocolDiscriminator		CC protocol discriminator M BITSTRING [4]
msgType	MsgType		message type (1) M BITSTRING [8]
repeatInd	RepeatInd		repeat indicator C BITSTRING[8]
bcap1	Bcap		Bearer capability M OCTETSTRING [3..16]
bcap2	Bcap		Bearer capability O OCTETSTRING [3..16]
facility	Facility		facility O
cgps	CGPS		calling party subaddr. O OCTETSTRING [2..23]
cdpn	CDPN		called party number M OCTETSTRING[3..43]
cdps	CDPS		called party subaddr. O OCTETSTRING [2..23]
llcRepeatInd	RepeatInd		LLC repeat indicator O OCTETSTRING [1]
llc1	LLC		low layer compatib.1 O OCTETSTRING [2..18]
llc2	LLC		low layer compatib.2 O OCTETSTRING [2..18]
hlcRepeatInd	RepeatInd		HLC repeat indicator O OCTETSTRING [1]
hlc1	HLC		high layer compat.1 O OCTETSTRING [2..5]
hlc2	HLC		high layer compat. 2 O OCTETSTRING [2..5]
userUser	UserUser		user-user O OCTETSTRING [3..131]
sS_VersionInd	SS_VersionInd		SS version indicator ue -> n O OCTETSTRING [2..3]
cLIR_Suppression	CLIR_Suppression		CLIR suppression C OCTETSTRING[1]
cLIR_Invocation	CLIR_Invocation		CLIR invocation O OCTETSTRING[1]

Continued on next page

Continued from previous page

PDU Type Definition			
Field Name	Field Type	Field Encoding	Comments
cC_Capabilities	CC_Capabilities		cc capabilities O
facilityCCBS_AdvRecall	Facility		OCTETSTRING[3] facility for CCBS (advanced recall alignemet) O
facilityCCBS_RecallAlign	Facility		facility for CCBS (recall alignment not essential) O
streamId	StreamId		stream identifier O
supportedCodecs	CodecList		OCTETSTRING[3] supported codecs O OCTETSTRING[5–n]
Detailed Comments : (1) bits 7 and 8 are 0 and send sequence number for earlier than R99 resp. a 2bit send sequence number for R99 or later (see 3G TS 24.008 cl. 10.4)			

PDU Type Definition			
PDU Name : STATUS_PDU PCO Type : DSAP Encoding Rule Name : Encoding Variation : Comments : An AMD STATUS PDU. Ref 3G TS 25.322 clause 9.2.1.5			
Field Name	Field Type	Field Encoding	Comments
dC_Field	DC_Field		1
type	CtrlPDU_Type		2
superFieldsTx	SuperFields		3
superFieldsAndPadRx	HEXSTRING		4
paddingTx	Padding		5
Detailed Comments : 1. Always tsc_DC_ControlPDU for a STATUS PDU. 2. Always tsc_PDU_TypeStatus for a STATUS PDU. 3. The superfields transmitted in the STATUS PDU. 4. The superfields and padding to be received in the STATUS PDU. 5. The padding transmitted must be present to ensure that the total size of this PDU is exactly equal to the current PDU size. It is the callers responsibility to ensure that the superfields are either terminated with a NO_MORE SUFI, or an ACK SUFI.			

PDU Type Definition			
PDU Name : TMSIREALLOCATIONCOMPLETE PCO Type : Dc_SAP Encoding Rule Name : Encoding Variation : Comments : MM TMSI REALLOCATION COMPLETE ue -> n 3G TS 24.008 V3.4.0 cl. 9.2.18			
Field Name	Field Type	Field Encoding	Comments
skipIndicator	SkipIndicator		Skip Indicator M BITSTRING [4]
mMProtocolDiscriminator	ProtocolDiscriminator		MM Protocol Discriminator M BITSTRING [4]
msgType	MsgType		Message Type (1) M BITSTRING [8]
Detailed Comments : (1) (see 3G TS 24.008 cl. 10.4) In messages sent from the UE, for core network nodes earlier than R99: bit 8 shall be set to 0 and bit 7 is reserved for the send sequence number; for core network nodes of R99 or later: bits 7 and 8 are reserved for the send sequence number.			

PDU Type Definition			
PDU Name : TrD_PDU PCO Type : DSAP Encoding Rule Name : OctetUnaligned Encoding Variation : Comments : Transparent mode RLC PDU. Ref 3G TS 25.322 clause 9.2.1.2			
Field Name	Field Type	Field Encoding	Comments
data	BITSTRING		
Detailed Comments :			

ASN.1 PDU Type Definition	
PDU Name : Invalid_DL_CCCH_Message PCO Type : DSAP Encoding Rule Name : PER_Unaligned Encoding Variation : Comments :	
Type Definition	
SEQUENCE { integrityCheckInfo IntegrityCheckInfo OPTIONAL, message Invalid_DL_CCCH_MsgType }	
Detailed Comments :	

ASN.1 PDU Type Definition	
PDU Name	: Invalid_DL_DCCH_Message
PCO Type	: DSAP
Encoding Rule Name	: PER_Unaligned
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { integrityCheckInfo [0] IntegrityCheckInfo OPTIONAL, message [1] Invalid_DL_DCCH_MsgType }</pre>	
Detailed Comments	:

ASN.1 PDU Type Definition	
PDU Name	: Invalid_DL_SHCCH_Message
PCO Type	: DSAP
Encoding Rule Name	: PER_Unaligned
Encoding Variation	:
Comments	:
Type Definition	
<pre>SEQUENCE { message Invalid_DL_SHCCH_MsgType }</pre>	
Detailed Comments	:

ASN.1 PDU Type Definitions By Reference						
PDU Name	PCO Type	Type Reference	Module Identifie	Enc Rule	Enc Variation	Comments
BCCH_BCH_Message	DSAP	BCCH-BCH-Message	Class-definition	PER_Unaligned		
BCCH_FACH_Message	DSAP	BCCH-FACH-Message	Class-definition	PER_Unaligned		
DL_CCCH_Message	DSAP	DL-CCCH-Message	Class-definition	PER_Unaligned		
DL_DCCH_Message	DSAP	DL-DCCH-Message	Class-definition	PER_Unaligned		
DL_SHCCH_Message	DSAP	DL-SHCCH-Message	Class-definition	PER_Unaligned		
PCCH_Message	DSAP	PCCH-Messagge	Class-definition	PER_Unaligned		
UL_CCCH_Message	DSAP	UL-CCCH-Message	Class-definition	PER_Unaligned		
UL_DCCH_Message	DSAP	UL-DCCH-Message	Class-definition	PER_Unaligned		
UL_SHCCH_Message	DSAP	UL-SHCCH-Message	Class-definition	PER_Unaligned		
Detailed Comments :						

Alias Definitions		
Alias Name	Expansion	Comments
RxStatus	RLC_TR_TestDataInd	This alias is used to receive an AM STATUS PDU. Note that AM PDUs are received using the Tr PCO, so that the RLC PDU can be specified within the TTCN. This is described in detail in 34.123-3, clause 6.5 (RLC test method).
Detailed Comments :		

III

Constraints Part

Structured Type Constraint Declaration			
Constraint Name : cb_UE_TestLoopMode1LB_Setup4 (p_Size1, p_RB1, p_Size2, p_RB2, p_Size3, p_RB3, p_Size4, p_RB4 : INTEGER) Group : Structured Type : UE_TestLoopMode1LB_Setup Derivation Path : Encoding Variation : Comments : This constraint is used as a complete UE test loop mode 1 setup IE, with a 3 LB entity in the LB setup list.			
Element Name	Element Value	Element Encoding	Comments
iei	'0C'O		
IB_SetupRB_IE1	c_LB_SetupRB_IE(p_Size1, p_RB1)		
IB_SetupRB_IE2	c_LB_SetupRB_IE(p_Size2, p_RB2)		
IB_SetupRB_IE3	c_LB_SetupRB_IE(p_Size3, p_RB3)		
IB_SetupRB_IE4	c_LB_SetupRB_IE(p_Size4, p_RB4)		
IB_SetupRB_IE5	OMIT		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AuthFailParamGmmAny Group : Structured Type : AuthenticationFailureParameter Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00110000'B		
iei	'0E'O		
auts	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AC_RefNum3 Group : Structured Type : AC_ReferenceNumber Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
value	'0011'B		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AttachTypeAny Group : Structured Type : AttachType Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
for	?		Follow-on request
type	?		Type of attach
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AuthCiphRspExtAny Group : Structured Type : AuthRspExt Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00101001'B		IEI is 0x29 (see 24.008 / 9.4.10)
iel	?		
rES	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AuthFailParamAny Group : Structured Type : AuthenticationFailureParameter Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00100010'B		
iel	'0E'O		
auts	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AuthRspAny_tv Group : Structured Type : AuthRsp_tv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei value	'00100010'B ?		'00100010'B (22 hex) Authentication Parameter RES
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AuthRspExtAny Group : Structured Type : AuthRspExt Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei iel rES	'00100001'B ? ?		'00100001'B
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_AUTN(p_AUTN: BITSTRING) Group : Structured Type : AUTN Derivation Path : Encoding Variation : Comments : Authentication Parameter AUTN 3G TS 24.008 cl. 10.5.3.1.1			
Element Name	Element Value	Element Encoding	Comments
iei iel aUTN	'00100000'B '10'O p_AUTN		Length of 16 octets value of Authentication Parameter AUTN
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_CellIndInfoDef Group : Structured Type : CellIndependantInfo Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
cs_cipheringStarted	FALSE		the domain on which security was recently started, and hence the SRB are ciphered and Integrity protected with this domain.
ps_cipheringStarted	FALSE		
recentSecureDomain	cs_domain		
dL_CipherMode	cs_Null_CipheringModeCommand		
uL_CipherMode	–		
cipheringAlgorithmCapability	'000000000000000011'B		
integrityStarted	FALSE		
dL_Integrity	cs_IntegrityProtectStart (px_FRESH)		
uL_Integrity	–		
dL_IntegrityCheckInfo	–		
start_CS	'000000000000000000000000'B		Default new key
start_PS	'000000000000000000000000'B		Default new key
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_CellInfoDef (p_CellId : INTEGER; p_priScrmCode : PrimaryScramblingCode; p_URA_Id : BITSTRING; p_tCell : Tcell; p_sfnOffset : INTEGER; p_FreqInfo : FrequencyInfo; p_UL_ScramblingCode : UL_ScramblingCode) Group : Structured Type : CellInfoCfg Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
cellId	p_CellId		
frequencyInfo	p_FreqInfo		
attenuationLevel	tsc_AttenuationServingCell		
priScrmCode	p_priScrmCode		
powerpCPICH	tsc_PowerpCPICH		
powerpSCH	tsc_PowerpSCH		
powersSCH	tsc_PowersSCH		
powerpCCPCH	tsc_PowerpCCPCH		
powersCCPCH	tsc_PowersCCPCH1		
powersCCPCH1	tsc_PowersCCPCH1		
timingsCCPCH1	tsc_TimingsCCPCH1		
powerAICH	tsc_PowerAICH		
powerPICH	tsc_PowerPICH		
cellTxPowerLevel	defaultCellTxPowerLvl : NULL		
tCell	p_tCell		
sfnOffset	p_sfnOffset		
puncLimit	tsc_PuncLimit		
sf_PRACH	tsc_PRACH1_SF		
slotFormatsCCPCH1	tsc_SlotFormatsCCPCH1		
mcc	tsc_MCC_Def		
mnc	tsc_MNC_Def		
lac	tsc_LAC_Def		
rac	tsc_RAC_Def		
attFlag	tsc_AttOn		
nmo	tsc_NMO_I		
ura_Identity	p_URA_Id		
t3212	tsc_T3212_Def		
cRNTI	tsc_CRNTI		
uRNTI	c_U_RNTI_Def		
cellConfig	cell_NotConfigured		
dRX_CycleLength	c_DRX_CycleLengthStrucDef		
uL_ScramblingCode	p_UL_ScramblingCode		
DL_DPCH_SHO	FALSE		
UL_DPCH_SHO	FALSE		
dl_DPCH_2ndScrCode	tsc_DL_DPCH1_2ndScrC		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_CiphAlgorithm (p_alg : B3) Group : Structured Type : CiphAlgorithm Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1	'0'B		
algorithm	p_alg		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_CiphKeySeqNum(p_KeySeq: KeySeq) Group : Structured Type : CiphKeySeqNum Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1	'0'B		
keySeq	p_KeySeq		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_DetachType(p_pow : B1; p_type : B3) Group : Structured Type : DetachType Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
powOff	p_pow		
type	p_type		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_DetachTypeReAttNotRequiredGPRS Group : Structured Type : DetachType Derivation Path : Encoding Variation : Comments : 'normal detach, re-attach not required'			
Element Name	Element Value	Element Encoding	Comments
powOff	'0'B		
type	'001'B		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_DRX_CycleLengthStrucDef Group : Structured Type : DRX_CycleLengthStructure Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
cN_CS_DRX_CycleLength	7		
cN_PS_DRX_CycleLength	7		
uTRAN_DRX_CycleLength	9		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_DRX_ParamterAny Group : Structured Type : DRXparamter Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
splitPGcycleCode	?		Split PG cycle code
cnDRXcoef	?		CN specific DRX cycle length coefficient
splitOnCCCH	?		Split on CCCCH
nonDRXtimer	?		non-DRX timer
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_ForceToStandby(p_val : B3) Group : Structured Type : ForceToStandby Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare value	'0'B p_val		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_AttachResult(p_result : B3) Group : Structured Type : AttachResult Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare result	'0'B p_result		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_AuthAUTN (p_autn : B128) Group : Structured Type : GMM_AUTN Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei iel aUTN	'00101000'B '10'O p_autn		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_AuthRAND (p_rand : BITSTRING) Group : Structured Type : AuthenticationParamterRAND Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei randValue	'00100001'B p_rand		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_KeySeq_tv (p_key : KeySeq) Group : Structured Type : CiphKeySeqNum_tv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei spare1 keySeq	'1000'B '0'B p_key		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_MobileIdTMSI (p_tmsi : OCTETSTRING) Group : Structured Type : GMM_MS_Identity Derivation Path : Encoding Variation : Comments : Default TMSI			
Element Name	Element Value	Element Encoding	Comments
iei iel iDigit1 oddEvenInd typeOfId otherDigits	'00100011'B '05'O '1111'B '0'B '100'B p_tmsi		TMSI consists of 4 octets special coding for TMSI even TMSI
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GMM_UpdateType_v(p_for : B1; p_type : B3) Group : Structured Type : UpdateType_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
for value	p_for p_type		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GPRS_Timer_v(p_unit : B3; p_value : B5) Group : Structured Type : GPRS_Timer_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
unit value	p_unit p_value		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_GPRS_TimerAny Group : Structured Type : GPRS_Timer Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei unit value	'00010111'B ? ?		00010111'B (17 hex) Unit Timer value
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_LB_SetupRB_IE(p_LB_Size, p_RB_Identity: INTEGER) Group : Structured Type : LB_SetupRB_IE Derivation Path : Encoding Variation : Comments : This constraint is used as an LB entry in an LB setup list. Parameters: p_LB_Size: The uplink RLC SDU size in bits. This value will be represented as a 14 bit value in the LB Setup IE, so the valid range is from 0..16383. p_RB_Identity: The RB Id of the radio bearer that loopback is to be setup for. Valid range is 5..31, since RB 0–4 are used for signalling radio bearers.			
Element Name	Element Value	Element Encoding	Comments
rLC_SDU_Size	INT_TO_BIT(p_LB_Size, 16)		
spare_2	'000'B		
rB_Identity	INT_TO_BIT(p_RB_Identity, 5)		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_LocAreald_v(p_MCC: HEXSTRING; p_MNC: HEXSTRING; p_LAC: OCTETSTRING) Group : Structured Type : LocAreald_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
plmn	o_ConvtPLMN(p_MCC, p_MNC)		
lac	p_LAC		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_LocArealdAny_v Group : Structured Type : LocAreald_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
plmn	?		
lac	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_LocUpdTypeAny Group : Structured Type : LocUpdType Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
fOR	?		
spare1	'0'B		
IUT	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdAny_Iv Group : Structured Type : MS_Identity_Iv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iel	?		
iDigit1	?		
oddEvenInd	?		
typeOfId	?		
otherDigits	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdPTMSI (p_ptmsi : O0_8)			
Group :			
Structured Type : GMM_MS_IdentityPTMSI			
Derivation Path :			
Encoding Variation :			
Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00011000'B		TMSI consists of 4 octets special coding for TMSI even TMSI / P-TMSI
iel	'05'O		
iDigit1	'1111'B		
oddEvenInd	'0'B		
typeOfId	'100'B		
otherDigits	p_ptmsi		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdPTMSI_Any			
Group :			
Structured Type : GMM_MS_IdentityPTMSI			
Derivation Path :			
Encoding Variation :			
Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00011000'B		TMSI consists of 4 octets special coding for TMSI even TMSI / P-TMSI Any value
iel	'05'O		
iDigit1	'1111'B		
oddEvenInd	'0'B		
typeOfId	'100'B		
otherDigits	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdPTMSI_lv (p_ptmsi : O0_8)			
Group :			
Structured Type : MS_Identity_lv			
Derivation Path :			
Encoding Variation :			
Comments :			
Element Name	Element Value	Element Encoding	Comments
iel	'05'O		TMSI consists of 4 octets special coding for TMSI even TMSI / P-TMSI
iDigit1	'1111'B		
oddEvenInd	'0'B		
typeOfId	'100'B		
otherDigits	p_ptmsi		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdTMSI_Def			
Group :			
Structured Type : MM_MS_Identity			
Derivation Path :			
Encoding Variation :			
Comments : Default TMSI			
Element Name	Element Value	Element Encoding	Comments
iei	'00010111'B		TMSI consists of 4 octets special coding for TMSI even TMSI
iel	'05'O		
iDigit1	'1111'B		
oddEvenInd	'0'B		
typeOfId	'100'B		
otherDigits	px_TMSI_Def		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MobileIdTMSI_lv			
Group :			
Structured Type : MS_Identity_lv			
Derivation Path :			
Encoding Variation :			
Comments : Default TMSI			
Element Name	Element Value	Element Encoding	Comments
iel	'05'O		TMSI consists of 4 octets special coding for TMSI even TMSI
iDigit1	'1111'B		
oddEvenInd	'0'B		
typeOfId	'100'B		
otherDigits	px_TMSI_Def		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MS_Clsmk1_Any Group : Structured Type : MS_Clsmk1 Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1	'0'B		
revLvl	?		
eSIND	?		
a5_1	?		
rFPwrCap	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MS_Clsmk1_Def Group : Structured Type : MS_Clsmk1 Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1	'0'B		
revLvl	px_MS_ClsmkRevLvl		
eSIND	px_MS_ClsmkESIND		
a5_1	pc_MS_ClsmkA5_1		
rFPwrCap	px_MS_ClsmkRF_PwrCap		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MS_Clsmk2_Any Group : Structured Type : MS_Clsmk2 Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00110011'B		
iel	'03'O		
spare1_1	'0'B		
revLvl	?		
eSIND	?		
a5_1	?		
rFPwrCap	?		
spare1_2	'0'B		
pSCap	?		
sSSI	?		
sMCap	?		
vBS	?		
vGCS	?		
fC	?		
cM3	?		
spare1_3	'0'B		
ICSVA	?		
uCS2	?		
soLSA	?		
cMSP	?		
a5_3	?		
a5_2	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MS_Clsmk2_Any_Iv Group : Structured Type : MS_Clsmk2_Iv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iel	'03'O		
spare1_1	'0'B		
revLvl	?		
eSIND	?		
a5_1	?		
rFPwrCap	?		
spare1_2	'0'B		
pSCap	?		
sSSI	?		
sMCap	?		
vBS	?		
vGCS	?		
fC	?		
cM3	?		
spare1_3	'0'B		
ICSVA	?		
uCS2	?		
soLSA	?		
cMSP	?		
a5_3	?		
a5_2	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_MS_RadioAccessCapAny_Iv Group : Structured Type : MSRadioAccessCap_Iv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iel	?		
value	?		MS radio access capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_PLMN_List1 (p_Plmn1 : OCTETSTRING) Group : Structured Type : PLMN_List Derivation Path : Encoding Variation : Comments : equivalent PLMN list containing 1 PLMN (OCTETSTRING[3])			
Element Name	Element Value	Element Encoding	Comments
iei	'01001010'B		'01001010'B
iel	'03'O		
plmn1	p_Plmn1		PLMN 1
plmn2	–		PLMN 2
plmn3	–		PLMN 3
plmn4	–		PLMN 4
plmn5	–		PLMN 5
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_PTMSI_Signature (p_ptmsi : OCTETSTRING) Group : Structured Type : PTMSI_Signature Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00011001'B		
value	p_ptmsi		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_PTMSI_Signature_tlv (p_ptmsi : OCTETSTRING) Group : Structured Type : PTMSI_Signature_tlv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00011001'B		
iel	'03'O		
value	p_ptmsi		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_PTMSI_SignatureAny Group : Structured Type : PTMSI_Signature Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei value	'00011001'B ?		'00011001'B (19 hex) P-TMSI signature value
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_RadioPriority(p_val : B3) Group : Structured Type : RadioPriority_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare value	'0'B p_val		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_RadioPriority2(p_val : B3) Group : Structured Type : RadioPriority2_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare value	'0'B p_val		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_RAI_Any_v Group : Structured Type : RAI_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
plmn	?		MCC + MNC 3 digits each
lac	?		LAC
rac	?		RAC
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_RAI_v (p_mcc : HEXSTRING; p_mnc : HEXSTRING; p_lac : OCTETSTRING; p_rac : OCTETSTRING) Group : Structured Type : RAI_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
plmn	o_ConvtPLMN(p_mcc, p_mnc)		
lac	p_lac		
rac	p_rac		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_RepeatIndAny Group : Structured Type : RepeatInd Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'1101'B		'D'H
repeatInd	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_ServiceType_v(p_type : B3) Group : Structured Type : ServiceType_v Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1	'0'B		
type	p_type		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_TMSI_StatusAny Group : Structured Type : TMSI_Status Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'1001'B		'1001'B
spare3	'000'B		
flag	?		Flag
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_UE_TestLoopMode1_LB_Setup(p_LB_Size, p_RB_Identity: INTEGER) Group : Structured Type : UE_TestLoopMode1LB_Setup Derivation Path : Encoding Variation : Comments : This constraint is used as a complete UE test loop mode 1 setup IE, with a single LB entity in the LB setup list. Parameters: p_LB_Size: The uplink RLC SDU size in bits. This value will be represented as a 14 bit value in the LB Setup IE, so the valid range is from 0..16383. p_RB_Identity: The RB Id of the radio bearer that loopback is to be setup for. Valid range is 5..31, since RB 0-4 are used for signalling radio bearers.			
Element Name	Element Value	Element Encoding	Comments
iel	'03'O		
IB_SetupRB_IE1	c_LB_SetupRB_IE(p_LB_Size, p_RB_Identity)		
IB_SetupRB_IE2	–		
IB_SetupRB_IE3	–		
IB_SetupRB_IE4	–		
IB_SetupRB_IE5	–		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : c_UE_TestLoopMode1_LB_Setup2(p_Size1, p_RB1, p_Size2, p_RB2 : INTEGER) Group : Structured Type : UE_TestLoopMode1LB_Setup Derivation Path : Encoding Variation : Comments : This constraint is used as a complete UE test loop mode 1 setup IE, with a 2 LB entity in the LB setup list.			
Element Name	Element Value	Element Encoding	Comments
iel	'06'O		
IB_SetupRB_IE1	c_LB_SetupRB_IE(p_Size1, p_RB1)		
IB_SetupRB_IE2	c_LB_SetupRB_IE(p_Size2, p_RB2)		
IB_SetupRB_IE3	–		
IB_SetupRB_IE4	–		
IB_SetupRB_IE5	–		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cb_IMEISV_Request(p_value: B3) Group : Structured Type : IMEISVRequest Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
spare1 value	'0'B p_value		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cb_UE_TestLoopMode1LB_Setup3 (p_Size1, p_RB1, p_Size2, p_RB2, p_Size3, p_RB3 : INTEGER) Group : Structured Type : UE_TestLoopMode1LB_Setup Derivation Path : Encoding Variation : Comments : This constraint is used as a complete UE test loop mode 1 setup IE, with a 3 LB entity in the LB setup list.			
Element Name	Element Value	Element Encoding	Comments
iel IB_SetupRB_IE1 IB_SetupRB_IE2 IB_SetupRB_IE3 IB_SetupRB_IE4 IB_SetupRB_IE5	'09'O c_LB_SetupRB_IE(p_Size1, p_RB1) c_LB_SetupRB_IE(p_Size2, p_RB2) c_LB_SetupRB_IE(p_Size3, p_RB3) – –		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_AccessPtNameAny Group : Structured Type : AccessPtName Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iel length accessPtName	'00101000'B ? ?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_Bcap3aEtcAny Group : Structured Type : Bcap3aEtc Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
extBit	?		
coding	?		
spare2	?		
speechVersion	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_BcapAnyMO Group : Structured Type : Bcap Derivation Path : Encoding Variation : Comments : Any bearer capability for direction n <- ue			
Element Name	Element Value	Element Encoding	Comments
iei	'00000100'B		
iel	?		
extBit3	*		
radioChRequi	*		
codingStd	*		
transferMode	*		
itc	*		
bcap3aEtc1	cr_Bcap3aEtcAny IF_PRESENT		
bcap3aEtc2	cr_Bcap3aEtcAny IF_PRESENT		
bcap3aEtc3	cr_Bcap3aEtcAny IF_PRESENT		
bcap3aEtc4	cr_Bcap3aEtcAny IF_PRESENT		
bcap3aEtc5	cr_Bcap3aEtcAny IF_PRESENT		
bcap3aEtc6	cr_Bcap3aEtcAny IF_PRESENT		
extBit4	*		
compress	*		
structure	*		
duplexMode	*		
cfg	*		
nirr	*		
establish	*		
extBit5	*		
accessId	*		
rateAdapt	*		
sacp	*		
extBit5a	*		
OherItc	*		
OtherRateAdapt	*		
spare3	*		
extBit5b	*		
rateAdaptHeader	*		
multiFrame	*		
mode	*		
logLinkId	*		
assignorAssignee	*		
inBandOutBand	*		
spare1	*		
extBit6	*		

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Structured Type Constraint Declaration			
Element Name	Element Value	Element Encoding	Comments
layer1Id	*		
userInfoLayer1	*		
syncAsync	*		
extBit6a	*		
numStopBits	*		
nego	*		
numDataBits	*		
userRate	*		
extBit6b	*		
intermRate	*		
nicTx	*		
nicRx	*		
parity	*		
extBit6c	*		
connectElem	*		
modemType	*		
extBit6d	*		
OtherModemType	*		
FixedNtwUserRate	*		
extBit6e	*		
acceptChCoding	*		
maxNumTrafficCh	*		
extBit6f	*		
uIMI	*		
wAIUR	*		
extBit6g	*		
acceptChCodingExt	*		
asymInd	*		
spare2	*		
extBit7	*		
layer2Id	*		
userInfoLayer2	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CC_CapabilitiesAny Group : Structured Type : CC_Capabilities Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00010101'B		
iel	?		
maxNumBearer	?		
spare2	?		
pcp	?		
dtmf	?		
spare4	?		
maxNumSpeechBearer	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CDPN_Any Group : Structured Type : CDPN Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'01011110'B		information element identifier
iel	?		length
typeOfNumPlan	cr_TypeOfNumPlanAny		type of number and numbering plan identification
digits	*		BCD numbers
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CDPS_Any Group : Structured Type : CDPS Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'01101101'B		
iel	?		
subadrs	cr_SubadrsAny		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CGPS_Any Group : Structured Type : CGPS Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'01011101'B		information element identifier
iel	?		length
subadrs	cr_SubadrsAny		Subaddress
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CodecAny Group : Structured Type : Codec Derivation Path : Encoding Variation : Comments : Codec 3G TS 24.008 cl. 10.5.4.32			
Element Name	Element Value	Element Encoding	Comments
sysId	?		system identification
len	?		length
bitMap1to8	?		codec bitmap bits 1–8
bitMap9to16	*		codec bitmap bits 9–16
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_CodecListAny Group : Structured Type : CodecList Derivation Path : Encoding Variation : Comments : Supported Codec List 3G TS 24.008 cl. 10.5.3.32			
Element Name	Element Value	Element Encoding	Comments
iei	'01000000'B		'01000000'B (40 hex)
iel	?		length
codec1	cr_CodecAny IF_PRESENT		Codec
codec2	cr_CodecAny IF_PRESENT		Codec
codec3	cr_CodecAny IF_PRESENT		Codec
codec4	cr_CodecAny IF_PRESENT		Codec
codec5	cr_CodecAny IF_PRESENT		Codec
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_DRXparamter_tv_Any Group : Structured Type : DRXparamter_tv Derivation Path : Encoding Variation : Comments : to be used in ROUTINGAREAUPDATEREQUEST constraints			
Element Name	Element Value	Element Encoding	Comments
iei	'00100111'B		'00100111'B (hex 27)
splitPGcycleCode	?		Split PG cycle code
cnDRXcoef	?		CN specific DRX cycle length coefficient
splitOnCCCCH	?		Split on CCCCH
nonDRXtimer	?		non-DRX timer
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_Facility_Any Group : Structured Type : Facility Derivation Path : Encoding Variation : Comments : Facility information element 3G TS 24.008 cl. 10.5.4.15			
Element Name	Element Value	Element Encoding	Comments
iei	'00011100'B		information element identifier '00011100'B
iel	?		length
comps	*		Component
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_FacilityAdvRecall Group : Structured Type : Facility Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00011101'B		information element identifier
iel	?		length
comps	*		Component
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_GPRS_TimerAny Group : Structured Type : GPRS_Timer Derivation Path : Encoding Variation : Comments : to be used in ROUTINGAREAUPDATEREQUEST constraints			
Element Name	Element Value	Element Encoding	Comments
iei	'00010111'B		00010111'B (17 hex)
unit	?		Unit
value	?		Timer value
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_HLC_Any			
Group :			
Structured Type : HLC			
Derivation Path :			
Encoding Variation :			
Comments : High layer compatibility			
Element Name	Element Value	Element Encoding	Comments
iei	'01111101'B		information element identifier
iel	?		
extBit3	*		
codingStd	*		
interpretation	*		
presentModeProtocolProfile	*		
extBit4	*		
hlcd	*		
extBit4a	*		
exteHlcd	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_LLC_Any Group : Structured Type : LLC Derivation Path : Encoding Variation : Comments : Low layer compatibility (CC information element)			
Element Name	Element Value	Element Encoding	Comments
iei	'01111100'B		
iel	?		
extBit3	*		
codingStd	*		
itc	*		
extBit3a	*		
negoInd	*		
spare6	*		
extBit4	*		
transferMode	*		
infoTransferRate	*		
extBit4_1	*		
rateMultiplier	*		
extBit5	*		
layer1Id	*		
userInfoLayer1	*		
extBit5a	*		
syncAsync	*		
nego	*		
userRate	*		
extBit5b1	*		
intermRate	*		
nicTx	*		
nicRx	*		
flowCtrlTx	*		
flowCtrlRx	*		
spare1	*		
extBit5b2	*		
rateAdaptHeader	*		
multiFrame	*		
mode	*		
logLinkId	*		
assignorAssignee	*		
inBandOutBand	*		
spare1_5b2	*		
extBit5c	*		
numStopBits	*		
numDataBits	*		
parity	*		
extBit5d	*		
duplexMode	*		

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Structured Type Constraint Declaration			
Element Name	Element Value	Element Encoding	Comments
modemType	*		
extBit6	*		
layer2id	*		
userInfoLayer2	*		
extBit6a1	*		
modeLayer2	*		
spare3	*		
q933	*		
extBit6a2	*		
userSpecifLayer2	*		
extBit6b	*		
windowSize	*		
extBit7	*		
layer3id	*		
userInfoLayer3	*		
extBit7a1	*		
OptionUserSpecifLayer3	*		
extBit7a2	*		
modeLayer3	*		
spare5	*		
extb7b	*		
spare3_7b	*		
defaultPacketSize	*		
extBit7c	*		
packetWindowSize	*		
extBit7a3	*		
spare3_7a3	*		
addLayer3ProtocolInfo	*		
extBit7a4	*		
spare3_7a4	*		
addLayer3ProtocolInfoL	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_LLC_SAPI_v Group : Structured Type : LLC_SAPI_v Derivation Path : Encoding Variation : Comments : LLC SAPI value assigned as SAPI 3 in order to ensure that there are no problems at the time of handover from UMTS to GSM			
Element Name	Element Value	Element Encoding	Comments
spare	'0000'B		
ILC_SAPI_Value	('0000'B, '0011'B, '0101'B, '1001'B, '1011'B)		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_MS_NetworkCap_tlv_Any Group : Structured Type : MS_NetworkCap_tlv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00110001'B		'00110001'B (hex 31)
iel	?		
value	?		MS network capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_MS_NetworkCapAss_lv Group : Structured Type : MS_NetworkCap_lv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iel	?		
value	?		MS network capability value (CSN.1 coding)
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_NSAPI_v Group : Structured Type : NSAPI_v Derivation Path : Encoding Variation : Comments : Received value of NSAPI			
Element Name	Element Value	Element Encoding	Comments
spare nSAPI_Value	'0000'B ('0101'B, '0110'B, '0111'B, '1000'B, '1001'B, '1010'B, '1011'B, '1100'B, '1101'B, '1110'B, '1111'B)		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_PDP_ContextStatusAny Group : Structured Type : PDP_ContextStatus Derivation Path : Encoding Variation : Comments : to be used in ROUTINGAREAUPDATEREQUEST constraints			
Element Name	Element Value	Element Encoding	Comments
iei iel nSAPI	'00110010'B ? ?		'00110010'B Unit Timer value
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_ProtoCfgOptAny Group : Structured Type : ProtoCfgOpt Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei length ext spare configprotocol protocolIdContents	'00100111'B ? ? ? ? *		270
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_PS_LCS_CapabilityAny			
Group :			
Structured Type : PS_LCS_Capability			
Derivation Path :			
Encoding Variation :			
Comments : PS LCS Capability 3GPP 24.008 / 10.5.5.22			
Element Name	Element Value	Element Encoding	Comments
iei	'00110011'B		'00110011'B (33 hex)
iel	'01'O		
spare	'000'B		
oTD_A	?		
oTD_B	?		
gPS_A	?		
gPS_B	?		
gPS_C	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_QualityOfService_Iv_Any Group : Structured Type : QualityOfService_Iv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
length	?		
spare	?		
dlyClass	?		
reliabilityClass	?		
peakThroughput	?		
spare1	?		
precedenceClass	?		
spare2	?		
meanThroughput	?		
trafficClass	?		
deliveryOrder	?		
deliveryErrorSDU	?		
maxSDUSize	?		
maxBitRateUplink	?		
maxBitRateDnlink	?		
residualBER	?		
sduErrRatio	?		
transDly	?		
trafficHandpro	?		
bitRateUplink	?		
bitRateDnlink	?		
spare3	*		
signallingInd	*		
srcStatsDescr	*		
maxBitRateDnlinkExt	*		
bitRateDnlinkExt	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_SS_VersionIndAny Group : Structured Type : SS_VersionInd Derivation Path : Encoding Variation : Comments : SS version indicator IE with AnyOrOmit values			
Element Name	Element Value	Element Encoding	Comments
iei	'01111111'B		
iel	?		
sS_VersionInfo	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_StaticPDP_AddressAny Group : Structured Type : PktDataProtoAddr_Iv Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
length	?		
spare	'0000'B		
pDP_TypeOrg	?		
pDP_TypeNo	?		
addrInfo	*		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_StreamIdAny Group : Structured Type : StreamId Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'00101101'B		
iel	?		
val	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_SubadrsAny Group : Structured Type : Subadrs Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
extBit	?		extension bit
typrOfSubadrs	?		Type of subaddress
oddEven	?		odd/even indicator
spare3	?		3 spare bits
subadrsInfo	*		subaddress information
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_TearDwnInd_tv			
Group :			
Structured Type : TearDwnInd_tv			
Derivation Path :			
Encoding Variation :			
Comments : Tear down indicator used for the direction ue -> n			
Element Name	Element Value	Element Encoding	Comments
iei	'1001'B		TDI Flag = 0, Tear down of all PDP Context not requested. TDI Flag = 1, Tear down of all PDP Context requested.
spare	'000'B		
tdiflag	'?'B		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_TI_Any			
Group :			
Structured Type : TI			
Derivation Path :			
Encoding Variation :			
Comments : Transaction identifier – used for MO calls			
Element Name	Element Value	Element Encoding	Comments
tiFlag	?		from network to MS
tiVal	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_TI_MO			
Group :			
Structured Type : TI			
Derivation Path :			
Encoding Variation :			
Comments : Transaction identifier – used for MO calls			
Element Name	Element Value	Element Encoding	Comments
tiFlag	'0'B		from network to MS
tiVal	?		
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_TypeOfNumPlanAny Group : Structured Type : TypeOfNumPlan Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
extBit	?		extension bit
typeOfNum	?		Type of number
numbPlanId	?		Numbering plan id.
Detailed Comments :			

Structured Type Constraint Declaration			
Constraint Name : cr_UserUserAny Group : Structured Type : UserUser Derivation Path : Encoding Variation : Comments :			
Element Name	Element Value	Element Encoding	Comments
iei	'01111110'B		information element identifier
iel	?		length
userUserProtocolDiscr	?		user–user protocol discriminator
userUserInfo	*		user user information
Detailed Comments : In SETUP, ALERTING, CONNECT, DISCONNECT, RELEASE and RELEASE COMPLETE messages the userUserInfo length is of 0 – 32 bytes. In USER INFORMATION messages the userUserInfo length is of 1 – 128.			

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PowerOffsetInfoBelow64k
Group	:
ASN1 Type	: PowerOffsetInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ gainFactorInformation signalledGainFactors:{ modeSpecificInfo fdd:{ gainFactorBetaC tsc_GainFactorBetaC_Below64k }, gainFactorBetaD tsc_GainFactorBetaD, referenceTFC_ID 0 }, powerOffsetPp_m OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PowerOffsetInfoComputed
Group	:
ASN1 Type	: PowerOffsetInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ gainFactorInformation computedGainFactors : 0, powerOffsetPp_m OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PowerOffsetInfoHigher64k
Group	:
ASN1 Type	: PowerOffsetInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { gainFactorInformation signalledGainFactors:{ modeSpecificInfo fdd:{ gainFactorBetaC tsc_GainFactorBetaC_Higher64k }, gainFactorBetaD tsc_GainFactorBetaD, referenceTFC_ID 0 }, powerOffsetPp_m OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListAM2_No_Pdcp_sdu4 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList { --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_sdu4, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH1 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2} }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} } } } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB22, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_sdu4, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH2 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH2 }} } } }} } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_4_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2, p_DchTFS3 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_5_6_7_11_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_4_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2 , p_DchTFS3 :CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_5_6_7_11_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_60_TFS_0B
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 60, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subcl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 60 bits; TansportBlock = 1, Size = 60. coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_60_TFS_UE_0B
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1: 60, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subcl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 60 bits; TansportBlock = 1, Size = 60. rlc_Size = TB_Size (DCH, w/o MUX) coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_15_17_18_35
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,15,17, 18, 35}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_15_16_18_34
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1, 3,15,16, 18, 34}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_12_14_18_32
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,12,14, 18, 32}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_12_13_18_31
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1,3,12,13, 18, 31}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_9_11_18_29
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,9,11, 18, 29}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_9_10_18_28
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,1,3,9,10,18,28}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_6_8_18_26
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,2,3,6,8,18,26}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_6_7_18_25
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,1,3,6,7,18,25}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_5_18_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,2,3,5,18,23}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_4_18_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,1,3,4,18,22}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_AM_RLC_rw512
Group	:
ASN1 Type	: DL_AM_RLC_Mode
Derivation Path	: cb_DL_AM_RLC.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE receivingWindowSize BY rw512	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_19
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To19_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_DL128k_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.58.2.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 :{ {tb_Size 656, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 3,small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; nineteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2,TransportBlocks = 3, TB size = 656 bits., TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_DL128k_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.58.2.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 12, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 3,small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; nineteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 3, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits, coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5_12_14_15_17_27_29
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5, 12, 14, 15, 17, 27, 29 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_12_13_15_16_27_28
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 12, 13, 15, 16, 27, 28 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5_9_11_15_17_24_26
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5, 9, 11, 15, 17, 24, 26 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_9_10_15_16_24_25
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3,4, 9, 10, 15, 16, 24, 25}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5_6_8_15_17_21_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5,6, 8, 15, 17, 21, 23}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_6_7_15_16_21_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0, 1, 2, 3, 4,6, 7, 15, 16, 21, 22}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_AMR_NoSc (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_AMR.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList BY {{ secondaryScramblingCode OMIT, sf_AndCodeNumber p_sf }}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_122_AMR_ChC127 (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_122_AMR.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList.[0].sf_AndCodeNumber BY tsc_Sfc128	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_64K_CS_ChC31 (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_64K_CS.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList.[0].sf_AndCodeNumber BY tsc_Sfc32	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_64K_PS_ChC31 (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_64K_PS.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList.[0].sf_AndCodeNumber BY tsc_Sfc32	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_SIB5_NoSib6 (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType5
Derivation Path	: cb_SIB5_Def.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE sib6indicator BY FALSE	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_ReEstTimerT315
Group	:
ASN1 Type	: Re_EstablishmentTimer
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
useT315	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM4_DiffRM_DCH5 (p_DedTranChTFS1,p_DedTranChTFS2,p_DedTranChTFS3,p_DedTranChTFS4:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH3, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS3, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH4, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS4, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_7_4k_AMR_DiffRM_DCH5
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_61_TFS_6_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_87_TFS_5 }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM2_DiffRM_DCH5 (p_DedTranChTFS1,p_DedTranChTFS2 :DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_10_2k_AMR_AMR_DiffRM_DCH5
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_65_TFS_6_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_99_TFS_5 }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_40_TFS }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_12_2k_AMR_DiffRM_DCH5(p_TFCS:TFCS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_81_TFS_6_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_103_TFS_5 }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS p_TFCS } </pre>	
Detailed Comments	: c_TFCS_Cmpl0_To239_Tx (c_PowerOffsetInfoBelow64k)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingPCH_NoFACH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only. map PCCH to PCH Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH.
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType pch, logicalChannelIdentity tsc_PCCH1, logicalChannelType pCCH , rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_PCCH } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_4_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 2 ,3,4,7}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_9_10_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 ,5,9,10,19}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_8_10_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 ,5,8,10,18}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_7_10_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 ,5,7,10,17}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM1_AM1_0To19_Order1 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To19_Tx_Order1 (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM1_AM1_0To19_Order1
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To19_Rx_Order1 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM1_AM1_0_19_Order1
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To19_Rx_Order1 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_3_0To19_Order1 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To19_Tx_Order1 (p_PowerOffsetInformation) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To19_Rx_Order1
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter and receiver
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_3_0To19_Order1(p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: with CTFC 0 to 19 but in order for RAB test cases
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To19_Rx_Order1 } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To19_Tx_Order1 (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter and receiver
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation p_PowerOffsetInformation } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_103_TFS_5
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 103, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 84, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 103, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	<p>: TS 34.108 cl. 6.10.2.4.1.4a.2.1.1</p> <p>TTI = 20 ms;</p> <p>Five transport formats: TransportBlocks = 0, TB size = 103 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 84 bits; TansportBlock = 4, Size = 103.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = N/A;</p> <p>RateMatching = 190</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_103_TFS_UE_5
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 103, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 84, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 103, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4a: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 103 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 84 bits; TransportBlocks = 4, TB size = 103 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_DL_RM192
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti40 :{{ tb_Size 148, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 192, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.2: TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 192	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_17
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti80 : {{ tb_Size 148, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 170, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.1: TTI = 80 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_18_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ DL:.64kbps
Constraint Value	
<pre>{ tti tti40 :{ { tb_Size 320, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_18_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ DL:.64kbps
Constraint Value	
<pre>{ tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 1, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_19_UL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown UL:64 kbps This constraint refers to table 6.10.2.4.1.19.1.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti40 : { { tb_Size 320, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 320 bits; TransportBlock = 1, Size = 320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits, TransportBlocks = 8, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_19_UL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown UL:64 kbps This constraint refers to table 6.10.2.4.1.19.1.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 1, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 320 bits; TransportBlock = 1, Size = 320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits, TransportBlocks = 8, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_20_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ DL:.128kbps
Constraint Value	
<pre> { tti tti40 :{ { tb_Size 320, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8, small : 16}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; six transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits, TransportBlocks = 16, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_20_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ DL:.128kbps
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 1, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8, small : 16}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; six transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits, TransportBlocks = 16, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_21_UL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ UL:128 kbps This constraint refers to table 6.10.2.4.1.21.1.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti40 :{ {tb_Size 320, numberOfTbSizeList { zero : NULL,one : NULL, small : 2, small : 4, small : 8, small : 16}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; six transport formats: TransportBlocks = 0, TB size = 320 bits; TransportBlock = 1, Size = 320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits, TransportBlocks = 8, TB size = 320 bits, TransportBlocks = 16, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_21_UL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/ UL:128 kbps This constraint refers to table 6.10.2.4.1.21.1.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 1, part2 2}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2, small : 4, small : 8, small : 16}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; six transport formats: TransportBlocks = 0, TB size = 320 bits; TransportBlock = 1, Size = 320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits, TransportBlocks = 8, TB size = 320 bits, TransportBlocks = 16, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_22_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/DL:.384kbps
Constraint Value	
<pre> { tti40 :{ { tb_Size 320, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8, small : 16,large : 32,large : 48}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; eight transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits,,TransportBlocks = 16, TB size = 320 bits,,TransportBlocks = 32, TB size = 320 bits,TransportBlocks = 48, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_320_TFS_22_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/DL:.384kbps
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 1, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL,small : 2,small : 4,small : 8, small : 16,large : 32,large : 48}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; eight transport formats: TransportBlocks = 0, TB size = 320 bits; TansportBlock = 1, Size =320 bits; TransportBlocks = 2, TB size = 320 bits., TransportBlocks = 4, TB size = 320 bits,TransportBlocks = 8, TB size = 320 bits,,TransportBlocks = 16, TB size = 320 bits,,TransportBlocks = 32, TB size = 320 bits,TransportBlocks = 48, TB size = 320 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_8_148_DL_Info(p_ActTime : ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_336_8_TFS), { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL }}, dlTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoHigher64k) --- sent to SS } </pre>	
Detailed Comments : For DCH1 (34.108 cl. 6.10.2.4.25): TTI = 20 ms; 5 transport formats: TransportBlocks = 0,1,2,4 and 8; all TB sizes = 336 bits; coding = turbo; CRCsize = 16; RateMatching = 130 For DCH5 (34.108 cl. 6.10.2.4.26): TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 155.	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_8_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in cb_DL_AddReconfTransChInfoList_Dch_PS_64k_128k
Constraint Value	
<pre>{ tti tti20 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,4 and 8 ; coding = turbo; CRCsize = 16; RateMatching = 130 ---TBD(120-160)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_8_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in cb_DL_AddReconfTransChInfoList_Dch_PS_64k_128k
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,4 and 8 ; coding = turbo; CRCsize = 16; RateMatching = 130 ---TBD(120-160)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_20_CC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional: third , rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.16kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, TB Size =336 bits; TransportBlock = 2, TB Size =336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_DL_40_CC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional: third , rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_DL_40_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional: third , rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, TB Size =336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_10_CC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This constraint refers to table 6.10.2.4.1.23.1.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti10 : { {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2. coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_10_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This constraint refers to table 6.10.2.4.1.23.1.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti10 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, Size = 336 bits; TransportBlocks = 2. coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This constraint refers to table 6.10.2.4.1.23.1.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti10 : { {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, Size = 336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This constraint refers to table 6.10.2.4.1.23.1.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti10 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_20_CC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23. 1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 336 , numberOfTbSizeList { zero : NULL,one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175_

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_20_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23. 1.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = convolutional; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23.1.1.1 uplink of 34.108
Constraint Value	
<pre>{ tti tti20 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23.1.1.1 uplink of 34.108
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_40_1_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint referes to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, TB Size =336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_40_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional: third , rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23.1.1.1 uplink of 34.108
Constraint Value	
<pre> { tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_UL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, TB Size = 336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23b_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre>{ tti40 : { {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 40 ms; Three transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, TB Size = 336 bits; TransportBlock = 2, TB Size = 336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23c_DL_40
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23c.2.1.1 of 34.108
Constraint Value	
<pre>{ tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits; TansportBlock = 3, Size =336 bits;TansportBlock = 4, Size =336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23c_UL_40
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23c.2.1.1 of 34.108
Constraint Value	
<pre>{ tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits; TansportBlock = 3, Size =336 bits;TansportBlock = 4, Size =336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_24_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.64kbps This corresponds to 6.10.2.4.1.24.1.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_24_UL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.64kbps This corresponds to 6.10.2.4.1.24.1.1.1 of 34.108
Constraint Value	
<pre>{ tti tti10 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 10 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_24_UL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.64kbps This corresponds to 6.10.2.4.1.24.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 3,small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_27_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background DL/128Kbps This corresponds to 6.10.2.4.1.27.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(120–160)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_27_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background DL/128Kbps This corresponds to 6.10.2.4.1.27.2.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_28_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.128kbps This corresponds to 6.10.2.4.1.28.1.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 : { { tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(120–160)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_28_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.128kbps This corresponds to 6.10.2.4.1.28.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 140, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(120–160)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_29_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background DL/144Kbps This corresponds to 6.10.2.4.1.29.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 9}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 160, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TansportBlock = 9, Size =336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(140–180)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_29_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background DL/144Kbps This corresponds to 6.10.2.4.1.29.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 9}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 160, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TansportBlock = 9, Size =336 bits coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_30_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background UL/144Kbps This corresponds to 6.10.2.4.1.30.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 9}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 160, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TansportBlock = 9, Size =336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(140–180)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_30_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background UL/144Kbps This corresponds to 6.10.2.4.1.30.1.1.1 of 34.108
Constraint Value	
<pre>{ tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 9}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 160, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TansportBlock = 9, Size =336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(140–180)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_31_DL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.256kbps This corresponds to 6.10.2.4.1.31.2.1.1 of 34.108
Constraint Value	
<pre>{ tti10 : { {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 10 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_31_DL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.256kbps This corresponds to 6.10.2.4.1.31.2.1.1 of 34.108
Constraint Value	
<pre>{ tti10 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 10 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_31_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.256kbps , TTI=20ms This corresponds to 6.10.2.4.1.31.2.1.1 of 34.108
Constraint Value	
<pre>{ tti20 : { {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12, small : 16 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; seven transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_31_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.256kbps , TTI=20ms This corresponds to 6.10.2.4.1.31.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12,small : 16 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; seven transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_32_DL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.384kbps , TTI=10ms This corresponds to 6.10.2.4.1.32.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_32_DL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:384kbps , TTI=10ms This corresponds to 6.10.2.4.1.32.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_32_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.384kbps , TTI=20ms This corresponds to 6.10.2.4.1.32.2.1.1 of 34.108
Constraint Value	
<pre> { tti_tti20 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; Nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits, TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_32_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:384kbps , TTI=20ms This corresponds to 6.10.2.4.1.32.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; Nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits, TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_63_DL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.768kbps , TTI=10ms This corresponds to 6.10.2.4.1.32.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; Nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits, TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_63_DL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:384kbps , TTI=10ms This corresponds to 6.10.2.4.1.63 of 34.108
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; Nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits, TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_34_UL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.384kbps This corresponds to 6.10.2.4.1.34.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 : { { tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 16, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_34_UL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.384kbps This corresponds to 6.10.2.4.1.34.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; six transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 16, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_34_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:384kbps This corresponds to 6.10.2.4.1.34.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits,TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits,TransportBlocks = 16, TB size = 336 bits,TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_34_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:384kbps This corresponds to 6.10.2.4.1.34.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12, small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 20 ms; nine transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 4, TB size = 336 bits, TransportBlocks = 8, TB size = 336 bits, TransportBlocks = 12, TB size = 336 bits, TransportBlocks = 16, TB size = 336 bits, TransportBlocks = 20, TB size = 336 bits, TransportBlocks = 24, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(110–150)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_38a_DL_20_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:32kbps This corresponds to 6.10.2.4.1.23. 1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_38a_UL_20_CC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.23. 1.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional: third, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; coding = convolutional; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_38b_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.38b.1.1.2 uplink of 34.108
Constraint Value	
<pre>{ tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_38b_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.38b.2.1.2 uplink of 34.108
Constraint Value	
<pre>{ tti tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_38b_UL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.32kbps This corresponds to 6.10.2.4.1.38b.1.1.2 uplink of 34.108
Constraint Value	
<pre>{ tti tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_40_TC_2
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_16kPS_RAB_SRB
Constraint Value	
<pre>{ tti tti40 :{{ tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; 3 transport formats: TB size always=336 bits; TransportBlocks = 0,1and 2 ; coding = turbo; CRCsize = 16; RateMatching = 155	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_40_TC_4_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel
Constraint Value	
<pre>{ tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; 3 transport formats: TB size always=336 bits; TransportBlocks = 0,1, 2, 3 and 4 ; coding = turbo; CRCsize = 16; RateMatching = 155	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_40_TC_UE_23b
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel
Constraint Value	
<pre>{ tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; 3 transport formats: TB size always=336 bits; TransportBlocks = 0,1 and 2 ; coding = turbo; CRCsize = 16; RateMatching = 155	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_51b_UL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL: 16kbps This corresponds to 6.10.2.4.1.51b.1.1.2 uplink of 34.108
Constraint Value	
<pre>{ tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2 Size =336 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_51b_UL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.51b.1.1.2 of 34.108 V4.3
Constraint Value	
<pre>{ tti tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, TB Size = 336 bits; TransportBlock = 2, TB Size = 336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in PS 16k. TS34.108 cl. 6.10.2.4.1.58
Constraint Value	
<pre>{ tti tti20 : { { tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 20 ms; 2 transport formats: TB size always=336 bits; TransportBlocks = 0,1 ; coding = turbo; CRCsize = 16; RateMatching = 155

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_UL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in PS 16k. TS34.108 cl. 6.10.2.4.1.58
Constraint Value	
<pre> { tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; 2 transport formats: TB size always=336 bits; TransportBlocks = 0,1 ; coding = turbo; CRCsize = 16; RateMatching = 155

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_340_TFS_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel
Constraint Value	
<pre> { tti tti40 : { { rlc_Size bitMode : sizeType3 : {part1 5, part2 4}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; 2 transport formats: TB size always=340 bits; TransportBlocks = 0, 1 ; coding = turbo; CRCsize = 16; RateMatching = (135 – 175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_42_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList {zero:NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.11 TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 42. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200 (180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_42_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList {zero:NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.11 TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 42. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200 (180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_49_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 49, numberOfTbSizeList {zero:NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; three transport formats: TransportBlocks =1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 49 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_49_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 49, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; three transport formats: TransportBlocks =1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 49 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_53_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre>{ tti tti20 :{ {tb_Size 53, numberOfTbSizeList { zero : NULL , one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } }</pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.11: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 53 bits; TansportBlock = 1, Size = 53. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190 (170–210)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_54_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre>{ tti tti20 :{ { tb_Size 54, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } }</pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 54 bits; TansportBlock = 1, Size = 54. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190(170–210)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_55_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 55, numberOfTbSizeList {zero:NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList {one :NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 55. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_55_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 55. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_148_DL_Info_1(p_ActTime: ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_576_TFS}, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL} }, dlTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
<p>Detailed Comments : For DCH1 (34.108 cl. 6.10.2.4.17):</p> <p>TTI = 40 ms;</p> <p>five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits.</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 125</p> <p>For DCH5 (34.108 cl. 6.10.2.4.17):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155.</p>	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_1
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_57_6kCS_RAB_SRB
Constraint Value	
<pre> { tti tti40 :{{tb_Size 576, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 125, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = 125	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_UE_1
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_57_6kCS_RAB_SRB for message sent to UE
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 9, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : 3GPP TS 34.108 clause 6.10.2.4.17 TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits. rlc_Size = TB_Size (DCH, w/o MUX) coding = turbo; CRCsize = 16; RateMatching = 125	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_58_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 58, numberOfTbSizeList { zero:NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 58 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_58_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 58 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 61, numberOfTbSizeList {zero:NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 61 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_6
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 61, numberOfTbSizeList {zero : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 61, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlocks = 2, TB size = 42 bits; TransportBlocks = 3, TB size = 55 bits; TransportBlocks = 4, TB size = 58 bits; TansportBlock = 5, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_6_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList {one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 61, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	<p>TS 34.108 subclause 6.10.2.4.1.4:</p> <p>TTI = 20 ms;</p> <p>three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlocks = 2, TB size = 42 bits; TransportBlocks = 3, TB size = 55 bits; TransportBlocks = 4, TB size = 58 bits; TansportBlock = 5, Size = 65.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 12;</p> <p>RateMatching = 200</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList { one : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, {tb_Size 61, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 61 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_UE_6
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 61 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 58; TransportBlock = 5, Size = 61. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_UE_6_DL
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_63_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 63, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.9: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 63 bits; TransportBlock = 1, Size = 63. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 170 (170–210)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_20_2
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 640, numberOfTbSizeList { zero : NULL, small : 2 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 172, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.14: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 150(165–210)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_20_2_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 11, part2 2}, numberOfTbSizeList { zero : NULL,small : 2 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 172, crc_Size crc16 } }</pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.13: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 150 165–210	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_40_2
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre>{ tti tti40 :{ { tb_Size 640, numberOfTbSizeList { zero : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 187, crc_Size crc16 } }</pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.13: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 172(150–195)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_40_4
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti40 :{ { tb_Size 640, numberOfTbSizeList { zero : NULL, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 172, crc_Size crc16 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.13: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 172(150–195)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_35_DL_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.35.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 :{ {tb_Size 656, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large: 24, large:28, large:32}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; eleven transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits,TransportBlocks = 8, TB size = 656 bits, TransportBlocks = 12, TB size = 656 bits,TransportBlocks = 16, TB size = 656 bits,TransportBlocks = 20, TB size = 656 bits, TransportBlocks = 24, TB size = 656 bits, TransportBlocks = 28, TB size = 656 bits, TransportBlocks = 32, TB size = 656 bits coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_35_DL_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.35.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti10 : { {rlc_Size octetModeType1 : sizeType2 : {part1 12, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12 , small : 16, large : 20, large: 24, large:28, large:32}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; eleven transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits, TransportBlocks = 8, TB size = 656 bits, TransportBlocks = 12, TB size = 656 bits, TransportBlocks = 16, TB size = 656 bits, TransportBlocks = 20, TB size = 656 bits, TransportBlocks = 24, TB size = 656 bits, TransportBlocks = 28, TB size = 656 bits, TransportBlocks = 32, TB size = 656 bits coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_35_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.35.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 656, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large: 24, large:28, large:32, large:36 , large:40 , large: 44, large: 48,large: 52, large: 56, large: 60, large: 64}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; ninteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits,TransportBlocks = 8, TB size = 656 bits, TransportBlocks = 12, TB size = 656 bits,TransportBlocks = 16, TB size = 656 bits,TransportBlocks = 20, TB size = 656 bits, TransportBlocks = 24, TB size = 656 bits, TransportBlocks = 28, TB size = 656 bits, TransportBlocks = 32, TB size = 656 bits, TransportBlocks = 36, TB size = 656 bits, TransportBlocks = 40 , TB size = 656 bits, TransportBlocks = 44, TB size = 656 bits, TransportBlocks = 48, TB size = 656 bits, TransportBlocks = 52, TB size = 656 bits, TransportBlocks = 56, TB size = 656 bits, TransportBlocks = 60, TB size = 656 bits, TransportBlocks = 64, TB size = 656 bits, coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_35_DL_20_TC1
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.2048kbps This corresponds to 6.10.2.4.1.35.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 656, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 4,small : 8,small : 12 , small : 16, large : 20, large: 24, large:28, large:32, large:36 , large:40 , large: 44, large: 48, large: 52,large: 56, large: 60, large: 64}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	<p>: TTI = 20 ms;</p> <p>nineteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits,TransportBlocks = 8, TB size = 656 bits, TransportBlocks = 12, TB size = 656 bits,TransportBlocks = 16, TB size = 656 bits,TransportBlocks = 20, TB size = 656 bits, TransportBlocks = 24, TB size = 656 bits, TransportBlocks = 28, TB size = 656 bits, TransportBlocks = 32, TB size = 656 bits, TransportBlocks = 36, TB size = 656 bits, TransportBlocks = 40 , TB size = 656 bits, TransportBlocks = 44, TB size = 656 bits, TransportBlocks = 48, TB size = 656 bits, TransportBlocks = 52, TB size = 656 bits, TransportBlocks = 56, TB size = 656 bits, TransportBlocks = 60, TB size = 656 bits, TransportBlocks = 64, TB size = 656 bits,</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = parameter(130–170)</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_35_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:2048kbps This corresponds to 6.10.2.4.1.35.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 : { { rlc_Size octetModeType1 : sizeType2 : {part1 12, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12 , small : 16, large : 20, large: 24, large:28, large:32, large:36 , large:40 , large: 44, large: 48, large: 52, large: 56, large: 60, large: 64}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; ninteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits, TransportBlocks = 8, TB size = 656 bits, TransportBlocks = 12, TB size = 656 bits, TransportBlocks = 16, TB size = 656 bits, TransportBlocks = 20, TB size = 656 bits, TransportBlocks = 24, TB size = 656 bits, TransportBlocks = 28, TB size = 656 bits, TransportBlocks = 32, TB size = 656 bits, TransportBlocks = 36, TB size = 656 bits, TransportBlocks = 40 , TB size = 656 bits, TransportBlocks = 44, TB size = 656 bits, TransportBlocks = 48, TB size = 656 bits, TransportBlocks = 52, TB size = 656 bits, TransportBlocks = 56, TB size = 656 bits, TransportBlocks = 60, TB size = 656 bits, TransportBlocks = 64, TB size = 656 bits, coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.2048kbps This corresponds to 6.10.2.4.1.58.2.1.1 of 34.108
Constraint Value	
<pre>{ tti_tti40 : { {tb_Size 656, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 20 ms; nineteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits; CRCsize = 16; RateMatching = parameter(125–165)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_656_TFS_DL_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.2048kbps This corresponds to 6.10.2.4.1.58.2.1.1 of 34.108
Constraint Value	
<pre>{ tti_tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 12, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 20 ms; nineteen transport formats: TransportBlocks = 0, TB size = 656 bits; TansportBlock = 1, Size =656 bits; TransportBlocks = 2, TB size = 656 bits., TransportBlocks = 4, TB size = 656 bits, coding = turbo; CRCsize = 16; RateMatching = parameter(125–165)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_6
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 65, numberOfTbSizeList {zero : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlocks = 2, TB size = 42 bits; TransportBlocks = 3, TB size = 55 bits; TransportBlocks = 4, TB size = 58 bits; TansportBlock = 5, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_6_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList {one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlocks = 2, TB size = 42 bits; TransportBlocks = 3, TB size = 55 bits; TransportBlocks = 4, TB size = 58 bits; TansportBlock = 5, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList {one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_UE_6
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_UE_6_DL
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_75_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti20 :{{ tb_Size 75, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.6: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 75 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_75_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti20 :{{ tb_Size 0, numberOfTbSizeList { one : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 75, numberOfTbSizeList { one : NULL}, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.6: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 75 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200(180–220)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_76_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 76, numberOfTbSizeList { zero : NULL, one:NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 76 bits; TansportBlock = 1, Size = 76 coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190(170–210)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_6
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 81, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 75, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_6_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 75, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4a.2.1.1: TTI = 20 ms; Six transport formats: TransportBlock = 0, TB size = 0 bits; TransportBlock = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_UE_6
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 81, numberOfTbSizeList { zero : NULL}, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 75, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_UE_6_DL
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 75, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4a: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 2, Size = 42; TransportBlock = 3, Size = 55; TransportBlock = 4, Size = 75; TransportBlock = 5, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_84_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 84, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size =0 bits; TransportBlocks = 1, TB size = 84 bits. coding = convolutional; coding rate = 1/3; CRCsize = NA; RateMatching = 170–210	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_87_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 87, numberOfTbSizeList { zero : NULL, one:NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 87 bits; TansportBlock = 1, Size = 87. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190(170–210)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_87_TFS_5
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 87, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 76, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 87, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 99 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 76 bits; TransportBlocks = 4, TB size = 99 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_87_TFS_UE_5
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 87, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 76, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 87, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4a: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 103 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 76 bits; TransportBlocks = 4, TB size = 103 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_99_TFS_5
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 99, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 76, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 99, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 99 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 76 bits; TransportBlocks = 4, TB size = 99 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_99_TFS_UE_5
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 99, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 53, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 63, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 76, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 99, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4a: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 103 bits; TransportBlocks = 1, TB size = 53 bits; TransportBlocks = 2, TB size = 63 bits; TransportBlocks = 3, TB size = 76 bits; TransportBlocks = 4, TB size = 103 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_3_4k_RM192 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM2 (p_DedTranChTFS1,p_DedTranChTFS2 : DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_DCH1 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_DCH2 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_DCH4 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH4, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_PS (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH4, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListDCH_PS_64k_128k
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_336_8_TFS_UE, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, c_DL_AddReconfTransChInfo (tsc_DL_DCH5, tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM1 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM2 (p_DedTranChTFS1,p_DedTranChTFS2 :DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM3_RM192 (p_DedTranChTFS1,p_DedTranChTFS2,p_DedTranChTFS3 :DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH3, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS3, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM3 (p_DedTranChTFS1,p_DedTranChTFS2,p_DedTranChTFS3 :DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH3, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS3, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT }, c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM4 (p_DedTranChTFS1,p_DedTranChTFS2,p_DedTranChTFS3,p_DedTranChTFS4:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS1, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS2, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH3, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS3, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH4, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS4, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM_DCH2 (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH2, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH5, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL, dch_QualityTarget{ bler_QualityValue -20 }, dummy OMIT } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoList_DCH_DSCH (p_DedicatedTransChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { c_DL_AddReconfTransChInfo_DSCH (p_DedicatedTransChTFS), c_DL_AddReconfTransChInfo_3_4k (tsc_DL_DCH5) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfo_3_4k (p_DITrChId:TransportChannelIdentity)
Group	:
ASN1 Type	: DL_AddReconfTransChInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dl_TransportChannelType dch, dl_transportChannelIdentity p_DITrChId, tfs_SignallingMode explicit_config : dedicatedTransChTFS : c_DCH_148_TFS_UE_DL , dch_QualityTarget{ bler_QualityValue -20 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfo_DSCH (p_DedicatedTransChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dl_TransportChannelType dsch, dl_transportChannelIdentity tsc_DSCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedicatedTransChTFS , dch_QualityTarget OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_AM_0To3
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To3_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_AM_0To5
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To5_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_AM_0To9
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To9_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM1_AM1_0To7
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To7_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To119
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To23
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_1_11_12_13_23_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To239
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To239_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To359
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To359_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To541
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl_0_To541_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommTrChInfo_TM3_AM1_0To599
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl_0_To599_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonInformationRB_SetUp_DTX_fixed (p_Sf: SF512_AndPilot)
Group	:
ASN1 Type	: DL_CommonInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_DPCH_InfoCommon{ cfnHandling maintain :NULL, modeSpecificInfo fdd:{ dl_DPCH_PowerControlInfo { modeSpecificInfo fdd:{ dpc_Mode singleTPC } }, powerOffsetPilot_pdpdch tsc_DPCH_PowerOffsetPILOT, dl_rate_matching_restriction OMIT, spreadingFactorAndPilot p_Sf, positionFixedOrFlexible fixed, tfci_Existence TRUE } }, modeSpecificInfo fdd:{ defaultDPCH_OffsetValue OMIT, dpch_CompressedModeInfo OMIT , tx_DiversityMode noDiversity, ssdt_Information OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM0To5
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To5_Rx } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM1_TM1_0_49
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To49_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM1_TM1_0_59
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To59_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_11
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To11_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_13
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To13_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_15
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To15_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_17
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To17_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_21
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To21_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_3
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To3_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_37
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To37_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_143
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To143_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_167
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To167_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_215
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To215_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_263
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To263_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_455
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To455_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_47
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To47_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM4_0_119
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To119_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM4_0_143
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To143_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM4_0_191
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To191_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM4_0_47
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To47_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM_0_11
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To11_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM_0_15
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To15_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM_0_3
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To3_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM_0_9
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To9_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_DPCH_128K (p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dl_CommonInformation p_DL_CommonInformation, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), dl_ChannelisationCodeList { {secondaryScramblingCode OMIT, sf_AndCodeNumber tsc_Sfc16 } }, tpc_CombinationIndex 0 }, powerOffsetOfTFCl_PO1 tsc_DPCH_PowerOffsetTFCl, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower 0, dl_TxPowerMax 15, dl_TxPowerMin -35 }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_InformationPerRL_DSCH (p_pdsch_codmapping : PDSCH_CodeMapping ; p_ScrambCode: PrimaryScramblingCode; p_Sf: SF512_AndCodeNumber; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_InformationPerRL_List
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ modeSpecificInfo fdd: { primaryCPICH_Info { primaryScramblingCode p_ScrambCode }, pdsch_SHO_DCH_Info OMIT, pdsch_CodeMapping p_pdsch_codmapping }, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 secondaryCPICH_Info OMIT, dl_ChannelisationCodeList {{ secondaryScramblingCode p_SecondaryScramblingCode , sf_AndCodeNumber p_Sf, scramblingCodeChange OMIT }}, tpc_CombinationIndex tsc_TPC_CombinationIndex, ssdt_CellIdentity OMIT, closedLoopTimingAdjMode OMIT }, sccpch_InfoforFACH OMIT }} </pre>	
Detailed Comments	: c_pdsch_CodeMapping

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_PDSCH_Information (p_pdsch_CodeMapping : PDSCH_CodeMapping)
Group	:
ASN1 Type	: DL_PDSCH_Information
Derivation Path	:
Encoding Variation	:
Comments	: Physical Downlink Shared Channel Information.
Constraint Value	
<pre> { pdsch_CodeMapping p_pdsch_CodeMapping } </pre>	
Detailed Comments	: c_pdsch_CodeMapping

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_354_TFS_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti10 : {{ tb_Size 354, numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_354_TFS_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti10 : {{ rlc_Size bitMode : sizeType3 : {part1 6, part2 2 } , numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_354_TFS_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti20 : {{tb_Size 354, numberOfTbSizeList {zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12, small : 16, large : 20, large : 24 }, logicalChannelList configured : NULL}}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_354_TFS_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti20 : {{ rlc_Size bitMode : sizeType3 : {part1 6, part2 2 } , numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12, small : 16, large : 20, large : 24}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_674_TFS_10_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti10 : {{ tb_Size 674 , numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12, small: 16, large: 20, large: 24, large: 28, large: 32}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_674_TFS_10_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tti tti10 : {{ rlc_Size bitMode : sizeType3 : {part1 26, part2 2 } , numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12, small: 16, large: 20, large: 24, large: 28, large: 32}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_674_TFS_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tti tti20 : {{tb_Size 674, numberOfTbSizeList {zero : NULL, one : NULL, small : 2, small : 4, small : 8, small : 12, small: 16, large: 20, large: 24, large: 28, large: 32, large: 36, large: 40, large: 44, large: 48, large: 52, large: 56, large: 60, large: 64 }, logicalChannelList configured : NULL}}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_674_TFS_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tti tti20 : {{ rlc_Size bitMode : sizeType3 : {part1 26, part2 2 } , numberOfTbSizeList { zero : NULL, one : NULL, small : 2,small : 4, small: 8, small: 12, small: 16, large: 20, large: 24, large: 28, large: 32}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DSCH_TFS_Info(p_TransportChInfo:CommonOrDedicatedTFS;p_TFCS:TFCS)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_DSCH1, dl_TransportChannelType dsch, transportChannelInfo p_TransportChInfo } }, dlTFCS p_TFCS --- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_BCCH_CCCH_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti tti10 : { { tb_Size 168, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TransportBlock = 0, Size = 168. coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 220

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_CTCH_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti tti10 :{ { tb_Size 168, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TansportBlock = 0, Size = 168. coding = convolutional; rate = 1/3; CRCsize = 16; RateMatching = 220

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS_UE_2ndSCCPCH
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType1: 15}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TransportBlock = 0, Size = 168. rlc_Size = TB_Size (FACH) coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 220

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS_UE_3rdSCCPCH
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType1: 15}, numberOfTbSizeList { zero : NULL, one : NULL,small:2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TransportBlock = 0, Size = 168. rlc_Size = TB_Size (FACH) coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 220	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListAM_DCH_2_No_Pdcp (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_sdu4, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }}, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListAM_DCH_4_No_Pdcp (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_sdu4, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH4, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH4, logicalChannelIdentity OMIT }} }}, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListAM_DCH_4_No_PDCP_twrw512 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcP_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_twrw512, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH4, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH4, logicalChannelIdentity OMIT }} }}, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListDCH_PS_64k_No_Pdcp (p_Reestimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RLC_Info: RLC_Info) Group : ASN1 Type : RAB_InformationSetupList Derivation Path : Encoding Variation : Comments : –	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_Reestimer }, rb_InformationSetupList {{ rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : p_RLC_Info, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel : { ul_TransportChannelType dch : tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch : tsc_DL_DCH1, logicalChannelIdentity OMIT }} }}, }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListDCH_PS_DSCH_64k (p_Reestimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RLC_Info: RLC_Info) Group : ASN1 Type : RAB_InformationSetupList Derivation Path : Encoding Variation : Comments : –	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_Reestimer }, rb_InformationSetupList {{ rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : p_RLC_Info, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel : { ul_TransportChannelType dch : tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dsch : tsc_DSCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments : In RB mapping Information the RB Multiplexing Option chosen 1 RB MuxOption. Generally for PS RAB we use the 2 RB MuxOption. 1 is for DCH and the other is for RACH/FACH. In case of DSCH, it can not be present with the FACH in Down Link. So the RB MuxOption chosen is 1 only.	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM1 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)	
Group :	
ASN1 Type : RAB_InformationSetupList	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT }, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM1_DCH2_Seg_False (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)	
Group :	
ASN1 Type : RAB_InformationSetupList	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT }, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM1_Seg_False (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)	
Group :	
ASN1 Type : RAB_InformationSetupList	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT , rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListTM2_Seg_False(p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id, p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} }, { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id2, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListTM2_1 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	: for 14.2.7a bulid from c_RAB_InfoListTM3 but excluding RB12 info
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH1 }} }} }, { rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity tsc_DL_DTCH2 }} }} } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListTM3 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB12, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH3, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> dl_TransportChannelType dch: tsc_DL_DCH3, logicalChannelIdentity OMIT }} }} } } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_Tx_Info (p_TestData : BITSTRING;p_NoOfRBs : INTEGER ;p_RB_Tx_Info1,p_RB_Tx_Info2,p_RB_Tx_Info3,p_RB_Tx_Info4 : RbTxInfo)	
Group :	
ASN1 Type : RabTxInfo	
Derivation Path :	
Encoding Variation :	
Comments : Provide test data, number of RBs, and RB Tx info of each RB (RB id, SDU size and number of SDUs) to be transmitted in consecutive TTIs	
Constraint Value	
<pre> { testData p_TestData, rbTxInfoList { p_RB_Tx_Info1,p_RB_Tx_Info2,p_RB_Tx_Info3,p_RB_Tx_Info4} } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RB_Tx_Info (p_RB_Id , p_SduSize , p_NomOfSdus : INTEGER)	
Group :	
ASN1 Type : RbTxInfo	
Derivation Path :	
Encoding Variation :	
Comments : Info on RB id and the actual DL test data size (SDU_Size * number of SDUs). The actual test data is extracted from the first (SDU_Size * number of SDUs) bits in the raw testData buffer. SS shall transmit the actual test data in every TTI.The value nomOfSdu = T / TTI , whereby T=1200 is the duration of the data transmitting in the RAB test, taking into account the test tolerance (+50%) of the UE loop back delay (<800 ms).	
Constraint Value	
<pre> { rB_Identity p_RB_Id , sduSize p_SduSize , nomOfSdu p_NomOfSdus -- 0 is set for no data on this RB } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoAM_Def_sdu4
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_AM_RLC_Mode: c_UL_AM_RLC_sdu4 , dl_RLC_Mode dl_AM_RLC_Mode : cb_DL_AM_RLC }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoAM_Def_twrw512
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_AM_RLC_Mode: c_UL_AM_RLC_tc_14_2_43_1_tw512 , dl_RLC_Mode dl_AM_RLC_Mode : cd_DL_AM_RLC_rw512 }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SB1_Def_witoutSIB6
Group	:
ASN1 Type	: SysInfoTypeSB1
Derivation Path	:
Encoding Variation	:
Comments	: Default setting of SB1. scheduling is defined in 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre> { sib_ReferenceList { { sib_Type sysInfoType7 : NULL, scheduling {scheduling { sib_Pos rep16 : 2 }} }, { sib_Type sysInfoType11 : 1, scheduling { scheduling { segCount 3, sib_Pos rep64 : 29, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType12 : 1, scheduling { scheduling { segCount 3, sib_Pos rep64 : 13, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType18 : 1, scheduling { scheduling { sib_Pos rep64 : 18 }} } }, nonCriticalExtensions OMIT } </pre>	
Detailed Comments	<p>: Assum no segmentation for SIB7.</p> <p>Contains scheduling informations for SIB7, SIB11, SIB12, SIB18 only, the scheduling information for other SIBs may be added later.</p> <p>The value 1 of valueTags is a place holder. actual values of them will be non-zero and assigned dynamically in various SendSystemInformation test Steps.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_5_6_7_11_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_5_6_7_11_Tx(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 22, powerOffsetInformation OMIT }, { ctfc6 29, powerOffsetInformation OMIT }, { ctfc6 30, powerOffsetInformation OMIT }, { ctfc6 31, powerOffsetInformation OMIT }, { ctfc6 38, powerOffsetInformation OMIT }, { ctfc6 45, powerOffsetInformation OMIT }, { ctfc6 52, powerOffsetInformation OMIT }, { ctfc6 59, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 29, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 30, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 31, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 38, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 45, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 52, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 59, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 8, powerOffsetInformation OMIT }, { ctfc8 15, powerOffsetInformation OMIT }, { ctfc8 22, powerOffsetInformation OMIT }, { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 68, powerOffsetInformation OMIT }, { ctfc8 75, powerOffsetInformation OMIT }, { ctfc8 82, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 68, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 75, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_2_4_1_3_5_6_8_10_7_9_11_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 10, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT }, { ctfc4 11, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_2_4_1_3_5_6_8_10_7_9_11_Tx (p_PowerOffsetInformation: PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCL_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 11, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To11_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre>normalTFCL_Signalling: complete: { ctfcSize ctfc6Bit: { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT } }}</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre>normalTFCL_Signalling: complete: { ctfcSize ctfc6Bit: { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation p_PowerOffsetInformation } }}</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To13_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc4Bit : { { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT }, { ctfc4 10, powerOffsetInformation OMIT }, { ctfc4 11, powerOffsetInformation OMIT }, { ctfc4 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc4 13, powerOffsetInformation OMIT } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To13_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc4Bit : { { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc4 13, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To143_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc8 48 , powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT } , { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT }, { ctfc8 72, powerOffsetInformation OMIT }, { ctfc8 73, powerOffsetInformation OMIT }, { ctfc8 83, powerOffsetInformation OMIT }, { ctfc8 84, powerOffsetInformation OMIT }, { ctfc8 85, powerOffsetInformation OMIT }, { ctfc8 95, powerOffsetInformation OMIT }, { ctfc8 96, powerOffsetInformation OMIT }, { ctfc8 97, powerOffsetInformation OMIT }, { ctfc8 107, powerOffsetInformation OMIT },</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>{ ctfc8 108, powerOffsetInformation OMIT }, { ctfc8 109, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT }, { ctfc8 120, powerOffsetInformation OMIT }, { ctfc8 121, powerOffsetInformation OMIT }, { ctfc8 131, powerOffsetInformation OMIT }, { ctfc8 132, powerOffsetInformation OMIT }, { ctfc8 133, powerOffsetInformation OMIT }, { ctfc8 143, powerOffsetInformation OMIT } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To143_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc8 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 107, powerOffsetInformation c_PowerOffsetInfoComputed }, },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc8 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 143, powerOffsetInformation p_PowerOffsetInformation } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To15_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc4Bit : { { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT }, { ctfc4 10, powerOffsetInformation OMIT }, { ctfc4 11, powerOffsetInformation OMIT }, { ctfc4 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc4 13, powerOffsetInformation OMIT }, { ctfc4 14, powerOffsetInformation OMIT }, { ctfc4 15, powerOffsetInformation OMIT } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To15_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc4Bit : { { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>}, { ctfc4 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 15, powerOffsetInformation p_PowerOffsetInformation } } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To167_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc8 48 , powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT } , { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT }, { ctfc8 72, powerOffsetInformation OMIT }, { ctfc8 73, powerOffsetInformation OMIT }, { ctfc8 83, powerOffsetInformation OMIT }, { ctfc8 84, powerOffsetInformation OMIT }, { ctfc8 85, powerOffsetInformation OMIT }, { ctfc8 95, powerOffsetInformation OMIT }, { ctfc8 96, powerOffsetInformation OMIT }, { ctfc8 97, powerOffsetInformation OMIT }, { ctfc8 107, powerOffsetInformation OMIT },</pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc8 108, powerOffsetInformation OMIT }, { ctfc8 109, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT }, { ctfc8 120, powerOffsetInformation OMIT }, { ctfc8 121, powerOffsetInformation OMIT }, { ctfc8 131, powerOffsetInformation OMIT }, { ctfc8 132, powerOffsetInformation OMIT }, { ctfc8 133, powerOffsetInformation OMIT }, { ctfc8 143, powerOffsetInformation OMIT }, { ctfc8 144, powerOffsetInformation OMIT }, { ctfc8 145, powerOffsetInformation OMIT }, { ctfc8 155, powerOffsetInformation OMIT }, { ctfc8 156, powerOffsetInformation OMIT }, { ctfc8 157, powerOffsetInformation OMIT }, { ctfc8 167, powerOffsetInformation OMIT } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To167_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc8 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 107, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc8 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 143, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 144, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 145, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 155, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 156, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 157, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 167, powerOffsetInformation p_PowerOffsetInformation } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To17_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, }, } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc6 12, powerOffsetInformation OMIT }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To17_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To191_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc8 48 , powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT }, { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT }, { ctfc8 72, powerOffsetInformation OMIT }, { ctfc8 73, powerOffsetInformation OMIT }, { ctfc8 83, powerOffsetInformation OMIT }, { ctfc8 84, powerOffsetInformation OMIT }, { ctfc8 85, powerOffsetInformation OMIT }, { ctfc8 95, powerOffsetInformation OMIT }, { ctfc8 96, powerOffsetInformation OMIT }, { ctfc8 97, powerOffsetInformation OMIT }, { ctfc8 107, powerOffsetInformation OMIT },</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc8 108, powerOffsetInformation OMIT }, { ctfc8 109, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT }, { ctfc8 120, powerOffsetInformation OMIT }, { ctfc8 121, powerOffsetInformation OMIT }, { ctfc8 131, powerOffsetInformation OMIT }, { ctfc8 132, powerOffsetInformation OMIT }, { ctfc8 133, powerOffsetInformation OMIT }, { ctfc8 143, powerOffsetInformation OMIT }, { ctfc8 144, powerOffsetInformation OMIT }, { ctfc8 145, powerOffsetInformation OMIT }, { ctfc8 155, powerOffsetInformation OMIT }, { ctfc8 156, powerOffsetInformation OMIT }, { ctfc8 157, powerOffsetInformation OMIT }, { ctfc8 167, powerOffsetInformation OMIT }, { ctfc8 168,</pre>

Continued on next page

Continued from previous page

ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>powerOffsetInformation OMIT }, { ctfc8 169, powerOffsetInformation OMIT }, { ctfc8 179, powerOffsetInformation OMIT }, { ctfc8 180, powerOffsetInformation OMIT }, { ctfc8 181, powerOffsetInformation OMIT }, { ctfc8 191, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To191_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc8 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 107, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc8 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 143, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 144, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 145, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 155, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 156, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 157, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 167, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 168,</pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 169, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 191, powerOffsetInformation p_PowerOffsetInformation } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To215_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
	<pre> }, { ctfc8 48 , powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT } , { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT }, { ctfc8 72, powerOffsetInformation OMIT }, { ctfc8 73, powerOffsetInformation OMIT }, { ctfc8 83, powerOffsetInformation OMIT }, { ctfc8 84, powerOffsetInformation OMIT }, { ctfc8 85, powerOffsetInformation OMIT }, { ctfc8 95, powerOffsetInformation OMIT }, { ctfc8 96, powerOffsetInformation OMIT }, { ctfc8 97, powerOffsetInformation OMIT }, { ctfc8 107, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>{ ctfc8 108, powerOffsetInformation OMIT }, { ctfc8 109, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT }, { ctfc8 120, powerOffsetInformation OMIT }, { ctfc8 121, powerOffsetInformation OMIT }, { ctfc8 131, powerOffsetInformation OMIT }, { ctfc8 132, powerOffsetInformation OMIT }, { ctfc8 133, powerOffsetInformation OMIT }, { ctfc8 143, powerOffsetInformation OMIT }, { ctfc8 144, powerOffsetInformation OMIT }, { ctfc8 145, powerOffsetInformation OMIT }, { ctfc8 155, powerOffsetInformation OMIT }, { ctfc8 156, powerOffsetInformation OMIT }, { ctfc8 157, powerOffsetInformation OMIT }, { ctfc8 167, powerOffsetInformation OMIT }, { ctfc8 168,</pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>powerOffsetInformation OMIT }, { ctfc8 169, powerOffsetInformation OMIT }, { ctfc8 179, powerOffsetInformation OMIT }, { ctfc8 180, powerOffsetInformation OMIT }, { ctfc8 181, powerOffsetInformation OMIT }, { ctfc8 191, powerOffsetInformation OMIT }, { ctfc8 192, powerOffsetInformation OMIT }, { ctfc8 193, powerOffsetInformation OMIT }, { ctfc8 203, powerOffsetInformation OMIT }, { ctfc8 204, powerOffsetInformation OMIT }, { ctfc8 205, powerOffsetInformation OMIT }, { ctfc8 215, powerOffsetInformation OMIT } } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To215_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc8 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 107, powerOffsetInformation c_PowerOffsetInfoComputed }, },</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc8 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 143, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 144, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 145, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 155, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 156, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 157, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 167, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 168,</pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 169, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 191, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 192, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 193, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 203, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 204, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 205, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 215, powerOffsetInformation p_PowerOffsetInformation } } } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To21_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT }, { ctfc6 20, powerOffsetInformation OMIT }, { ctfc6 21, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To21_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 20, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 21, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To239_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCL_Signalling: complete: { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 8, powerOffsetInformation OMIT }, { ctfc8 15, powerOffsetInformation OMIT }, { ctfc8 22, powerOffsetInformation OMIT }, { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 68, powerOffsetInformation OMIT }, { ctfc8 75, powerOffsetInformation OMIT }, { ctfc8 82, powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT }, }, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc8 120, powerOffsetInformation OMIT }, { ctfc8 121, powerOffsetInformation OMIT }, { ctfc8 128, powerOffsetInformation OMIT }, { ctfc8 135, powerOffsetInformation OMIT }, { ctfc8 142, powerOffsetInformation OMIT }, { ctfc8 179, powerOffsetInformation OMIT }, { ctfc8 180, powerOffsetInformation OMIT }, { ctfc8 181, powerOffsetInformation OMIT }, { ctfc8 188, powerOffsetInformation OMIT }, { ctfc8 195, powerOffsetInformation OMIT }, { ctfc8 202, powerOffsetInformation OMIT }, { ctfc8 239, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To239_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCL_Signalling: complete: { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 68, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 75, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 128, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 135, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 142, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 188, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 195, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 202, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 239, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To263_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc12Bit : { { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 11, powerOffsetInformation OMIT }, { ctfc12 12, powerOffsetInformation OMIT }, { ctfc12 13, powerOffsetInformation OMIT }, { ctfc12 23, powerOffsetInformation OMIT }, { ctfc12 24, powerOffsetInformation OMIT }, { ctfc12 25, powerOffsetInformation OMIT }, { ctfc12 35, powerOffsetInformation OMIT }, { ctfc12 36, powerOffsetInformation OMIT }, { ctfc12 37, powerOffsetInformation OMIT }, { ctfc12 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc12 48 , powerOffsetInformation OMIT }, { ctfc12 49, powerOffsetInformation OMIT } , { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 71, powerOffsetInformation OMIT }, { ctfc12 72, powerOffsetInformation OMIT }, { ctfc12 73, powerOffsetInformation OMIT }, { ctfc12 83, powerOffsetInformation OMIT }, { ctfc12 84, powerOffsetInformation OMIT }, { ctfc12 85, powerOffsetInformation OMIT }, { ctfc12 95, powerOffsetInformation OMIT }, { ctfc12 96, powerOffsetInformation OMIT }, { ctfc12 97, powerOffsetInformation OMIT }, { ctfc12 107, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 108, powerOffsetInformation OMIT }, { ctfc12 109, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, { ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 131, powerOffsetInformation OMIT }, { ctfc12 132, powerOffsetInformation OMIT }, { ctfc12 133, powerOffsetInformation OMIT }, { ctfc12 143, powerOffsetInformation OMIT }, { ctfc12 144, powerOffsetInformation OMIT }, { ctfc12 145, powerOffsetInformation OMIT }, { ctfc12 155, powerOffsetInformation OMIT }, { ctfc12 156, powerOffsetInformation OMIT }, { ctfc12 157, powerOffsetInformation OMIT }, { ctfc12 167, powerOffsetInformation OMIT }, { ctfc12 168,</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation OMIT }, { ctfc12 169, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 180, powerOffsetInformation OMIT }, { ctfc12 181, powerOffsetInformation OMIT }, { ctfc12 191, powerOffsetInformation OMIT }, { ctfc12 192, powerOffsetInformation OMIT }, { ctfc12 193, powerOffsetInformation OMIT }, { ctfc12 203, powerOffsetInformation OMIT }, { ctfc12 204, powerOffsetInformation OMIT }, { ctfc12 205, powerOffsetInformation OMIT }, { ctfc12 215, powerOffsetInformation OMIT }, { ctfc12 216, powerOffsetInformation OMIT }, { ctfc12 217, powerOffsetInformation OMIT }, { ctfc12 227, powerOffsetInformation OMIT }, { ctfc12 228, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>{ ctfc12 229, powerOffsetInformation OMIT }, { ctfc12 239, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 251, powerOffsetInformation OMIT }, { ctfc12 252, powerOffsetInformation OMIT }, { ctfc12 253, powerOffsetInformation OMIT }, { ctfc12 263, powerOffsetInformation OMIT } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To263_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc12Bit : { { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc12 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 107, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 143, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 144, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 145, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 155, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 156, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 157, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 167, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 168, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 169, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 191, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 192, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 193, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 203, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 204, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 205, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 215, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 216, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 217, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 227, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 228, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 229, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 239, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 251, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 252, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 253, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 263, powerOffsetInformation p_PowerOffsetInformation } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To359_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 8, powerOffsetInformation OMIT }, { ctfc12 15, powerOffsetInformation OMIT }, { ctfc12 22, powerOffsetInformation OMIT }, { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 75, powerOffsetInformation OMIT }, { ctfc12 82, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, { ctfc12 120, powerOffsetInformation OMIT }, }, } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 135, powerOffsetInformation OMIT }, { ctfc12 142, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 180, powerOffsetInformation OMIT }, { ctfc12 181, powerOffsetInformation OMIT }, { ctfc12 188, powerOffsetInformation OMIT }, { ctfc12 195, powerOffsetInformation OMIT }, { ctfc12 202, powerOffsetInformation OMIT }, { ctfc12 239, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 255, powerOffsetInformation OMIT }, { ctfc12 262, powerOffsetInformation OMIT }</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { ctfc12 315, powerOffsetInformation OMIT }, { ctfc12 322, powerOffsetInformation OMIT }, { ctfc12 359, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To359_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 68, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 75, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, }, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 128, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 135, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 142, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 188, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 195, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 202, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 239, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 248, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 255, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 262, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 308, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 315, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 322, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 359, powerOffsetInformation p_PowerOffsetInformation } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To37_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT }, { ctfc6 20, powerOffsetInformation OMIT }, }, { ctfc6 21, powerOffsetInformation OMIT }, { ctfc6 22, powerOffsetInformation OMIT }, { ctfc6 23, powerOffsetInformation OMIT }, { ctfc6 24, powerOffsetInformation OMIT }, { ctfc6 25, powerOffsetInformation OMIT }, { ctfc6 26, powerOffsetInformation OMIT }, { ctfc6 27, powerOffsetInformation OMIT }, },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc6 28, powerOffsetInformation OMIT }, { ctfc6 29, powerOffsetInformation OMIT }, { ctfc6 30, powerOffsetInformation OMIT }, { ctfc6 31, powerOffsetInformation OMIT }, { ctfc6 32, powerOffsetInformation OMIT }, { ctfc6 33, powerOffsetInformation OMIT }, { ctfc6 34, powerOffsetInformation OMIT }, { ctfc6 35, powerOffsetInformation OMIT }, { ctfc6 36, powerOffsetInformation OMIT }, { ctfc6 37, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To37_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 20, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 21, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 26, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 27, powerOffsetInformation c_PowerOffsetInfoComputed }, },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc6 28, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 29, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 30, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 31, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 32, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 33, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 34, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 37, powerOffsetInformation p_PowerOffsetInformation } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To3_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To455_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc12Bit : { { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 11, powerOffsetInformation OMIT }, { ctfc12 12, powerOffsetInformation OMIT }, { ctfc12 13, powerOffsetInformation OMIT }, { ctfc12 23, powerOffsetInformation OMIT }, { ctfc12 24, powerOffsetInformation OMIT }, { ctfc12 25, powerOffsetInformation OMIT }, { ctfc12 35, powerOffsetInformation OMIT }, { ctfc12 36, powerOffsetInformation OMIT }, { ctfc12 37, powerOffsetInformation OMIT }, { ctfc12 47 , powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> }, { ctfc12 48 , powerOffsetInformation OMIT }, { ctfc12 49, powerOffsetInformation OMIT } , { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 71, powerOffsetInformation OMIT }, { ctfc12 72, powerOffsetInformation OMIT }, { ctfc12 73, powerOffsetInformation OMIT }, { ctfc12 83, powerOffsetInformation OMIT }, { ctfc12 84, powerOffsetInformation OMIT }, { ctfc12 85, powerOffsetInformation OMIT }, { ctfc12 95, powerOffsetInformation OMIT }, { ctfc12 96, powerOffsetInformation OMIT }, { ctfc12 97, powerOffsetInformation OMIT }, { ctfc12 107, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc12 108, powerOffsetInformation OMIT }, { ctfc12 109, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, { ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 131, powerOffsetInformation OMIT }, { ctfc12 132, powerOffsetInformation OMIT }, { ctfc12 133, powerOffsetInformation OMIT }, { ctfc12 143, powerOffsetInformation OMIT }, { ctfc12 144, powerOffsetInformation OMIT }, { ctfc12 145, powerOffsetInformation OMIT }, { ctfc12 155, powerOffsetInformation OMIT }, { ctfc12 156, powerOffsetInformation OMIT }, { ctfc12 157, powerOffsetInformation OMIT }, { ctfc12 167, powerOffsetInformation OMIT }, { ctfc12 168,</pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation OMIT }, { ctfc12 169, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 180, powerOffsetInformation OMIT }, { ctfc12 181, powerOffsetInformation OMIT }, { ctfc12 191, powerOffsetInformation OMIT }, { ctfc12 192, powerOffsetInformation OMIT }, { ctfc12 193, powerOffsetInformation OMIT }, { ctfc12 203, powerOffsetInformation OMIT }, { ctfc12 204, powerOffsetInformation OMIT }, { ctfc12 205, powerOffsetInformation OMIT }, { ctfc12 215, powerOffsetInformation OMIT }, { ctfc12 216, powerOffsetInformation OMIT }, { ctfc12 217, powerOffsetInformation OMIT }, { ctfc12 227, powerOffsetInformation OMIT }, { ctfc12 228, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 229, powerOffsetInformation OMIT }, { ctfc12 239, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 251, powerOffsetInformation OMIT }, { ctfc12 252, powerOffsetInformation OMIT }, { ctfc12 253, powerOffsetInformation OMIT }, { ctfc12 263, powerOffsetInformation OMIT }, { ctfc12 264, powerOffsetInformation OMIT }, { ctfc12 265, powerOffsetInformation OMIT }, { ctfc12 275, powerOffsetInformation OMIT }, { ctfc12 276, powerOffsetInformation OMIT }, { ctfc12 277, powerOffsetInformation OMIT }, { ctfc12 287, powerOffsetInformation OMIT }, { ctfc12 288, powerOffsetInformation OMIT }, { ctfc12 289,</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation OMIT }, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { ctfc12 311, powerOffsetInformation OMIT }, { ctfc12 312, powerOffsetInformation OMIT }, { ctfc12 313, powerOffsetInformation OMIT }, { ctfc12 323, powerOffsetInformation OMIT }, { ctfc12 324, powerOffsetInformation OMIT }, { ctfc12 325, powerOffsetInformation OMIT }, { ctfc12 335, powerOffsetInformation OMIT }, { ctfc12 336, powerOffsetInformation OMIT }, { ctfc12 337, powerOffsetInformation OMIT }, { ctfc12 347, powerOffsetInformation OMIT }, { ctfc12 348, powerOffsetInformation OMIT }, { ctfc12 349, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc12 359, powerOffsetInformation OMIT }, { ctfc12 360, powerOffsetInformation OMIT }, { ctfc12 361, powerOffsetInformation OMIT }, { ctfc12 371, powerOffsetInformation OMIT }, { ctfc12 372, powerOffsetInformation OMIT }, { ctfc12 373, powerOffsetInformation OMIT }, { ctfc12 383, powerOffsetInformation OMIT }, { ctfc12 384, powerOffsetInformation OMIT }, { ctfc12 385, powerOffsetInformation OMIT }, { ctfc12 395, powerOffsetInformation OMIT }, { ctfc12 396, powerOffsetInformation OMIT }, { ctfc12 397, powerOffsetInformation OMIT }, { ctfc12 407, powerOffsetInformation OMIT }, { ctfc12 408, powerOffsetInformation OMIT }, { ctfc12 409, powerOffsetInformation OMIT }, { ctfc12 419,</pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>powerOffsetInformation OMIT }, { ctfc12 420, powerOffsetInformation OMIT }, { ctfc12 421, powerOffsetInformation OMIT }, { ctfc12 431, powerOffsetInformation OMIT }, { ctfc12 432, powerOffsetInformation OMIT }, { ctfc12 433, powerOffsetInformation OMIT }, { ctfc12 443, powerOffsetInformation OMIT }, { ctfc12 444, powerOffsetInformation OMIT }, { ctfc12 445, powerOffsetInformation OMIT }, { ctfc12 455, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To455_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc12Bit : { { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 47 , powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc12 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 107, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 131, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 132, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 133, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 143, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 144, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 145, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 155, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 156, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 157, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 167, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 168, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 169, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 191, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 192, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 193, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 203, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 204, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 205, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 215, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 216, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 217, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 227, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 228, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 229, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 239, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 251, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 252, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 253, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 263, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 264, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 265, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 275, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 276, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 277, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 287, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 288, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 289, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 311, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 312, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 313, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 323, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 324, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 325, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 335, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 336, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 337, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 347, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 348, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 349, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 359, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 360, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 361, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 371, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 372, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 373, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 383, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 384, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 385, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 395, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 396, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 397, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 407, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 408, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 409, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 419,</pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 420, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 421, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 431, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 432, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 433, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 443, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 444, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 445, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 455, powerOffsetInformation p_PowerOffsetInformation } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To47_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 23, powerOffsetInformation OMIT }, { ctfc6 24, powerOffsetInformation OMIT }, { ctfc6 25, powerOffsetInformation OMIT }, { ctfc6 35, powerOffsetInformation OMIT }, { ctfc6 36, powerOffsetInformation OMIT }, { ctfc6 37, powerOffsetInformation OMIT }, { ctfc6 47, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 47, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To49_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT }, { ctfc6 20, powerOffsetInformation OMIT }, { ctfc6 21, powerOffsetInformation OMIT }, { ctfc6 22, powerOffsetInformation OMIT }, { ctfc6 23, powerOffsetInformation OMIT }, { ctfc6 24, powerOffsetInformation OMIT }, { ctfc6 25, powerOffsetInformation OMIT }, { ctfc6 26, powerOffsetInformation OMIT }, { ctfc6 27, powerOffsetInformation OMIT }, {</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc6 28, powerOffsetInformation OMIT }, { ctfc6 29, powerOffsetInformation OMIT }, { ctfc6 30, powerOffsetInformation OMIT }, { ctfc6 31, powerOffsetInformation OMIT }, { ctfc6 32, powerOffsetInformation OMIT }, { ctfc6 33 , powerOffsetInformation OMIT }, { ctfc6 34, powerOffsetInformation OMIT }, { ctfc6 35, powerOffsetInformation OMIT }, { ctfc6 36, powerOffsetInformation OMIT }, { ctfc6 37, powerOffsetInformation OMIT }, { ctfc6 38, powerOffsetInformation OMIT }, { ctfc6 39, powerOffsetInformation OMIT }, { ctfc6 40, powerOffsetInformation OMIT }, { ctfc6 41, powerOffsetInformation OMIT }, { ctfc6 42, powerOffsetInformation OMIT }, { ctfc6 43, powerOffsetInformation OMIT }</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 44, powerOffsetInformation OMIT }, { ctfc6 45, powerOffsetInformation OMIT }, { ctfc6 46, powerOffsetInformation OMIT }, { ctfc6 47 , powerOffsetInformation OMIT }, { ctfc6 48 , powerOffsetInformation OMIT }, { ctfc6 49, powerOffsetInformation OMIT } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To49_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 20, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 21, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 26, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 27, powerOffsetInformation c_PowerOffsetInfoComputed }, {</pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> ctfc6 28, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 29, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 30, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 31, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 32, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 33, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 34, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 38, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 39, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 40, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 41, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 42, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 43, powerOffsetInformation c_PowerOffsetInfoComputed </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 44, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 45, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 46, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 47 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 49, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To541_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 22, powerOffsetInformation OMIT }, { ctfc12 82, powerOffsetInformation OMIT }, { ctfc12 15, powerOffsetInformation OMIT }, { ctfc12 8, powerOffsetInformation OMIT }, { </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { ctfc12 360, powerOffsetInformation OMIT }, { ctfc12 420, powerOffsetInformation OMIT }, { ctfc12 540, powerOffsetInformation OMIT }, { ctfc12 359, powerOffsetInformation OMIT }, { ctfc12 419, powerOffsetInformation OMIT }, { ctfc12 479, powerOffsetInformation OMIT }, { ctfc12 599, powerOffsetInformation OMIT }, { ctfc12 322, powerOffsetInformation OMIT }, { ctfc12 382, powerOffsetInformation OMIT }, {</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 315, powerOffsetInformation OMIT }, { ctfc12 308, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { ctfc12 361, powerOffsetInformation OMIT }, { ctfc12 421, powerOffsetInformation OMIT }, { ctfc12 541, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To541_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 142, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 262, powerOffsetInformation c_PowerOffsetInfoComputed }, }, } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 75, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 135, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 255, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 68, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 128, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 248, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 360, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 420, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 540, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 359, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 419, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 479, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 599, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 322, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 382, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 442, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 562, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 315, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 375, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 435, powerOffsetInformation c_PowerOffsetInfoComputed }, {</pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> ctfc12 555, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 308, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 368, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 428, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 548, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 361, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 421, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 541, powerOffsetInformation p_PowerOffsetInformation } } </pre>
Detailed Comments :

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 128, powerOffsetInformation OMIT }, { ctfc12 135, powerOffsetInformation OMIT }, { ctfc12 142, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 248, powerOffsetInformation OMIT }, { ctfc12 255, powerOffsetInformation OMIT }, { ctfc12 262, powerOffsetInformation OMIT }, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 308, powerOffsetInformation OMIT }, { ctfc12 315, powerOffsetInformation OMIT }, { ctfc12 322, powerOffsetInformation OMIT }, { ctfc12 359, powerOffsetInformation OMIT }, { ctfc12 360, powerOffsetInformation OMIT }, { ctfc12 361, powerOffsetInformation OMIT }, { ctfc12 368, powerOffsetInformation OMIT }, { ctfc12 375, powerOffsetInformation OMIT }, { ctfc12 382, powerOffsetInformation OMIT }, { ctfc12 419, powerOffsetInformation OMIT }, { ctfc12 420, powerOffsetInformation OMIT }, { ctfc12 421, powerOffsetInformation OMIT }, { ctfc12 428, powerOffsetInformation OMIT }, { ctfc12 435, powerOffsetInformation OMIT }, },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 442, powerOffsetInformation OMIT }, { ctfc12 479, powerOffsetInformation OMIT }, { ctfc12 540, powerOffsetInformation OMIT }, { ctfc12 541, powerOffsetInformation OMIT }, { ctfc12 548, powerOffsetInformation OMIT }, { ctfc12 555, powerOffsetInformation OMIT }, { ctfc12 562, powerOffsetInformation OMIT }, { ctfc12 599, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration
<pre> -- ASN.1 Type Constraint Declaration -- -- This block contains the ASN.1 type constraint declarations for the -- various data types used in the system. -- -- The declarations are organized into sections, each corresponding to a -- specific data type or set of related types. -- -- Section 1: Basic Types -- -- Section 2: Complex Types -- -- Section 3: Enumerations -- -- Section 4: Sets and Sequences -- -- Section 5: Other Constraints -- -- The declarations are as follows: -- -- Section 1: Basic Types -- -- Section 2: Complex Types -- -- Section 3: Enumerations -- -- Section 4: Sets and Sequences -- -- Section 5: Other Constraints -- </pre>

Constraint Name	: c_TFCS_Cmpl0_To599_Tx(p_PowerOffsetInformation : PowerOffsetInformation)
------------------------	--

Group	:
-------	---

ASN1 Type	: TFCS
-----------	--------

Derivation Path :

Encoding Variation :

Comments	: TFCS information with power offset information – for transmitter
-----------------	--

Constraint Value	
------------------	--

```
normalTFCl_Signalling: complete: {
ctfcSize ctfc12Bit:{
{
ctfc12 0,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 1,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 8,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 15,
powerOffsetInformation c_PowerOffsetInfoComputed
},

{
ctfc12 22,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 59,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 60,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 61,
powerOffsetInformation c_PowerOffsetInfoComputed
},
},
{
ctfc12 68,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 75,
powerOffsetInformation c_PowerOffsetInfoComputed
},
},
{
ctfc12 82,
powerOffsetInformation c_PowerOffsetInfoComputed
},
{
ctfc12 119,
powerOffsetInformation c_PowerOffsetInfoComputed
},
},
}
```

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 128, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 135, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 142, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 180, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 181, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 188, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 195, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 202, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 239, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, {</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 248, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 255, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 262, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 308, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 315, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 322, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 359, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 360, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 361, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 368, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 375, powerOffsetInformation c_PowerOffsetInfoComputed },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 382, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 419, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 420, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 421, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 428, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 435, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 442, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 479, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 480, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 481, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 488, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 495, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 502, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 539, powerOffsetInformation c_PowerOffsetInfoComputed }, },</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 540, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 541, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 548, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 555, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 562, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 599, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To59_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT }, { ctfc6 20, powerOffsetInformation OMIT }, { ctfc6 21, powerOffsetInformation OMIT }, { ctfc6 22, powerOffsetInformation OMIT }, { ctfc6 23, powerOffsetInformation OMIT }, { ctfc6 24, powerOffsetInformation OMIT }, { ctfc6 25, powerOffsetInformation OMIT }, { ctfc6 26, powerOffsetInformation OMIT }, { ctfc6 27, powerOffsetInformation OMIT }, { </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> ctfc6 28, powerOffsetInformation OMIT }, { ctfc6 29, powerOffsetInformation OMIT }, { ctfc6 30, powerOffsetInformation OMIT }, { ctfc6 31, powerOffsetInformation OMIT }, { ctfc6 32, powerOffsetInformation OMIT }, { ctfc6 33 , powerOffsetInformation OMIT }, { ctfc6 34, powerOffsetInformation OMIT }, { ctfc6 35, powerOffsetInformation OMIT }, { ctfc6 36, powerOffsetInformation OMIT }, { ctfc6 37, powerOffsetInformation OMIT }, { ctfc6 38, powerOffsetInformation OMIT }, { ctfc6 39, powerOffsetInformation OMIT }, { ctfc6 40, powerOffsetInformation OMIT }, { ctfc6 41, powerOffsetInformation OMIT }, { ctfc6 42, powerOffsetInformation OMIT }, { ctfc6 43, powerOffsetInformation OMIT </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc6 44, powerOffsetInformation OMIT }, { ctfc6 45, powerOffsetInformation OMIT }, { ctfc6 46, powerOffsetInformation OMIT }, { ctfc6 47 , powerOffsetInformation OMIT }, { ctfc6 48 , powerOffsetInformation OMIT }, { ctfc6 49, powerOffsetInformation OMIT } , { ctfc6 50, powerOffsetInformation OMIT }, { ctfc6 51, powerOffsetInformation OMIT }, { ctfc6 52, powerOffsetInformation OMIT }, { ctfc6 53, powerOffsetInformation OMIT }, { ctfc6 54, powerOffsetInformation OMIT }, { ctfc6 55, powerOffsetInformation OMIT }, { ctfc6 56, powerOffsetInformation OMIT }, { ctfc6 57 , powerOffsetInformation OMIT }, { ctfc6 58 , powerOffsetInformation OMIT }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc6 59, powerOffsetInformation OMIT } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To59_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 20, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 21, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 26, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 27, powerOffsetInformation c_PowerOffsetInfoComputed }, {</pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc6 28, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 29, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 30, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 31, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 32, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 33, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 34, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 38, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 39, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 40, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 41, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 42, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 43, powerOffsetInformation c_PowerOffsetInfoComputed</pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { ctfc6 44, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 45, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 46, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 47 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 48 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 49, powerOffsetInformation c_PowerOffsetInfoComputed } , { ctfc6 50, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 51, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 52, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 53, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 54, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 55, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 56, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 57 , powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 58 , powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc6 59, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To71_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47, powerOffsetInformation OMIT }, }, }, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc8 48, powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT }, { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To71_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47, powerOffsetInformation c_PowerOffsetInfoComputed }, }, } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> { ctfc8 48, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_TFCS_Cmpl0_To7_Rx Group : ASN1 Type : TFCS Derivation Path : Encoding Variation : Comments : TFCS information with power offset information – for transmitter	
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc4Bit : { { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_CmplFACH_BCCH_CCCH_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 2, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl_0_To541_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 22, powerOffsetInformation OMIT }, { ctfc12 82, powerOffsetInformation OMIT }, { ctfc12 142, powerOffsetInformation OMIT }, { ctfc12 262, powerOffsetInformation OMIT }, }, } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 15, powerOffsetInformation OMIT }, { ctfc12 75, powerOffsetInformation OMIT }, { ctfc12 135, powerOffsetInformation OMIT }, { ctfc12 255, powerOffsetInformation OMIT }, { ctfc12 8, powerOffsetInformation OMIT }, { ctfc12 68, powerOffsetInformation OMIT }, { ctfc12 128, powerOffsetInformation OMIT }, { ctfc12 248, powerOffsetInformation OMIT }, { ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> ctfc12 360, powerOffsetInformation OMIT }, { ctfc12 420, powerOffsetInformation OMIT }, { ctfc12 540, powerOffsetInformation OMIT }, { ctfc12 359, powerOffsetInformation OMIT }, { ctfc12 419, powerOffsetInformation OMIT }, { ctfc12 479, powerOffsetInformation OMIT }, { ctfc12 599, powerOffsetInformation OMIT }, { ctfc12 322, powerOffsetInformation OMIT }, { ctfc12 382, powerOffsetInformation OMIT }, { ctfc12 442, powerOffsetInformation OMIT }, { ctfc12 562, powerOffsetInformation OMIT }, { ctfc12 315, powerOffsetInformation OMIT }, { ctfc12 375, powerOffsetInformation OMIT }, { ctfc12 435, powerOffsetInformation OMIT }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 555, powerOffsetInformation OMIT }, { ctfc12 308, powerOffsetInformation OMIT }, { ctfc12 368, powerOffsetInformation OMIT }, { ctfc12 428, powerOffsetInformation OMIT }, { ctfc12 548, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { ctfc12 361, powerOffsetInformation OMIT }, { ctfc12 421, powerOffsetInformation OMIT }, { ctfc12 541, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl_0_To541_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 360, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 420, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 540, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 359, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 419, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 479, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 599, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 322, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 382, powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>ctfc12 315, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 308, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 361, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 421, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 541, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl_0_To599_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation OMIT }, { ctfc12 1, powerOffsetInformation OMIT }, { ctfc12 8, powerOffsetInformation OMIT }, { ctfc12 15, powerOffsetInformation OMIT }, { ctfc12 22, powerOffsetInformation OMIT }, { ctfc12 59, powerOffsetInformation OMIT }, { ctfc12 60, powerOffsetInformation OMIT }, { ctfc12 61, powerOffsetInformation OMIT }, { ctfc12 68, powerOffsetInformation OMIT }, { ctfc12 75, powerOffsetInformation OMIT }, { ctfc12 82, powerOffsetInformation OMIT }, { ctfc12 119, powerOffsetInformation OMIT }, }, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>{ ctfc12 120, powerOffsetInformation OMIT }, { ctfc12 121, powerOffsetInformation OMIT }, { ctfc12 128, powerOffsetInformation OMIT }, { ctfc12 135, powerOffsetInformation OMIT }, { ctfc12 142, powerOffsetInformation OMIT }, { ctfc12 179, powerOffsetInformation OMIT }, { ctfc12 180, powerOffsetInformation OMIT }, { ctfc12 181, powerOffsetInformation OMIT }, { ctfc12 188, powerOffsetInformation OMIT }, { ctfc12 195, powerOffsetInformation OMIT }, { ctfc12 202, powerOffsetInformation OMIT }, { ctfc12 239, powerOffsetInformation OMIT }, { ctfc12 240, powerOffsetInformation OMIT }, { ctfc12 241, powerOffsetInformation OMIT }, {</pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> ctfc12 248, powerOffsetInformation OMIT }, { ctfc12 255, powerOffsetInformation OMIT }, { ctfc12 262, powerOffsetInformation OMIT }, { ctfc12 299, powerOffsetInformation OMIT }, { ctfc12 300, powerOffsetInformation OMIT }, { ctfc12 301, powerOffsetInformation OMIT }, { ctfc12 308, powerOffsetInformation OMIT }, { ctfc12 315, powerOffsetInformation OMIT }, { ctfc12 322, powerOffsetInformation OMIT }, { ctfc12 359, powerOffsetInformation OMIT }, { ctfc12 360, powerOffsetInformation OMIT }, { ctfc12 361, powerOffsetInformation OMIT }, { ctfc12 368, powerOffsetInformation OMIT }, { ctfc12 375, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> { ctfc12 382, powerOffsetInformation OMIT }, { ctfc12 419, powerOffsetInformation OMIT }, { ctfc12 420, powerOffsetInformation OMIT }, { ctfc12 421, powerOffsetInformation OMIT }, { ctfc12 428, powerOffsetInformation OMIT }, { ctfc12 435, powerOffsetInformation OMIT }, { ctfc12 442, powerOffsetInformation OMIT }, { ctfc12 479, powerOffsetInformation OMIT }, { ctfc12 480, powerOffsetInformation OMIT }, { ctfc12 481, powerOffsetInformation OMIT }, { ctfc12 488, powerOffsetInformation OMIT }, { ctfc12 495, powerOffsetInformation OMIT }, { ctfc12 502, powerOffsetInformation OMIT }, { ctfc12 539, powerOffsetInformation OMIT }, </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>{ ctfc12 540, powerOffsetInformation OMIT }, { ctfc12 541, powerOffsetInformation OMIT }, { ctfc12 548, powerOffsetInformation OMIT }, { ctfc12 555, powerOffsetInformation OMIT }, { ctfc12 562, powerOffsetInformation OMIT }, { ctfc12 599, powerOffsetInformation OMIT } } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl_0_To599_Tx (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for Receiver
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc12Bit:{ { ctfc12 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 22, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 68, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 75, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 82, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 119, powerOffsetInformation c_PowerOffsetInfoComputed }, }, } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> { ctfc12 120, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 121, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 128, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 135, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 142, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 179, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 240, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 241, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 248, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 255, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 262, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 299, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 300, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 301, powerOffsetInformation c_PowerOffsetInfoComputed }, { </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> ctfc12 308, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 315, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 322, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 359, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 360, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 361, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 368, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 375, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 382, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 419, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 420, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 421, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 428, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 435, powerOffsetInformation c_PowerOffsetInfoComputed }, </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ ctfc12 442, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 479, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 540, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 541, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 548, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 555, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 562, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc12 599, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_ComplFACH_BMC_NoPCH_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed}, { ctfc2 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 2, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_10_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation OMIT }, { ctfc2 1, powerOffsetInformation OMIT } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc4 10, powerOffsetInformation OMIT } } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_10_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 10, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_18_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType logicalSplit, tfci_Field2_Length 6, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation OMIT }, { ctfc2 1, powerOffsetInformation OMIT } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_18_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType logicalSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation p_PowerOffsetInformation } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_5_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation OMIT }, { ctfc2 1, powerOffsetInformation OMIT } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_5_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation p_PowerOffsetInformation } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_8_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation OMIT }, { ctfc2 1, powerOffsetInformation OMIT } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT } } } } </pre>	

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ASN.1 Type Constraint Declaration
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_DCH_0_To_1_DSCH_0_To_8_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> splitTFCl_Signalling : { splitType hardSplit, tfci_Field2_Length 5, tfci_Field1_Information complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } }, tfci_Field2_Information explicit_config : complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation p_PowerOffsetInformation } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>} } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 1 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_10_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1 , 10 , 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_12_13
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 , 12,13}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_15_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 15, 16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_18_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,1,18,19}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_27_28
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 27, 28 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_10_12
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,2, 10,12 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_12_13_15_16_27_28
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 12, 13, 15, 16, 27, 28 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_12_14_15_17_27_29
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 12, 14, 15, 17, 27, 29 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_12_15_27
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 12, 15, 27 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_15_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 15, 16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_15_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 15, 17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_15_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 15, 18 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_15_16_18_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 15, 16, 18, 19 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_10_12_16_18_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 10, 12, 16, 18, 22 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_11_12_17_18_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 11, 12, 17, 18, 23 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_13
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 13 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_14
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 14 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_15
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 15 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_12_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 12, 18 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 7 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_7_12_13_18_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 7, 12, 13, 18, 19 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_8
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 8 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_8_9_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 8, 9, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_8_12_14_18_20
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 8, 12, 14, 18, 20 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 3, 4, 5, 6, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_5_6_9_12_15_18_21
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 5, 6, 9, 12, 15, 18, 21 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_4_6_7_9_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 4, 6, 7, 9, 10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5_15_17_18_20
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5, 15, 17, 18, 20 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_5_6_8_9_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 5, 6, 8, 9, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_15_21
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 6, 15, 21 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 6, 7 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_7_15_16_21_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0, 1, 2, 3, 6, 7, 15, 16, 21, 22}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_8
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 6, 8 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_8_15_17_21_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 6, 8, 15, 17, 21, 23}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_6_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 6, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_9_10_15_16_24_25
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 9, 10, 15, 16, 24, 25}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_9_11_15_17_24_26
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 9, 11, 15, 17, 24, 26 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_3_9_15_24
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 3, 9, 15, 24 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_4_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 4, 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_4_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 4, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,2 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_5_10_12
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 5, 10,12 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_5_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 5, 7 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_5_7_10_12_15_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 2, 5, 7, 10, 12, 15, 17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_6_8
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 2, 6, 8 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_8_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,2, 8, 10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_2_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 2, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 3 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_10_13
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 3, 10, 13 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_4
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 3, 4 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_4_6_7_9_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1,3,4,6,7,9,10}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,3 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_5_10_13
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 3, 5, 10,13 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_5_8
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 3, 5, 8 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_5_8_10_13_15_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 3, 5, 8, 10, 13, 15, 18 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 3, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_6_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 3, 6, 7 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_6_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 3, 6, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_8_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 ,1, 3, 8, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 3, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_10_14
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 ,1, 4 , 10, 14}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 4, 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_5_10_14
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 4, 5, 10, 14 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_5_8_13
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,4,5, 8, 13 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_5_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1, 4 , 5 , 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_5_9_10_14_15_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 4, 5, 9, 10, 14, 15, 19 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 4, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_6_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 4, 6, 10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_6_8_14
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,4,6, 8, 14 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_7_8_15
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,4,7, 8, 15 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_4_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 4, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_10_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 5, 10,11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_10_15
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1, 5, 10, 15 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1 , 5 , 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_6_10_11_15_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0, 1, 5, 6, 10, 11, 15, 16}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_6_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 1, 5, 6, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,5, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_5_6_10_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 ,1,5,6, 10 ,16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_6_10_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 ,1,6, 10 ,16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_6_7
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1, 6, 7 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_6_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,6, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_7_10_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,7, 10 ,17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_7_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1,7, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_8_10_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,8, 10 ,18 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_8_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 8, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 1 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 1, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_9_10_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 1,9, 10 ,19 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_12_14
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,2,12,14 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_15_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 2, 15,17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_18_20
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,2,18,20}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_27_29
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2, 27, 29 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 2, 3 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 2, 3, 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_4_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 2, 4, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 ,2 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 2 , 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_6_8
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 2, 6, 8 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_10_15_25
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3,10, 15, 25 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_10_27_37
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1, 3, 10, 27, 37 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_11_15_26
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 ,3, 11, 15, 26 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_11_27_38
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 ,2,3,11, 27, 38 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_12_15
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3,12,15 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_12_15_27
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3,12,15,27}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_12_18_30
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,12, 18, 30}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_12_27_39
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3, 12, 27, 39 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_13_15_28
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3,13, 15, 28 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_13_27_40
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1,3, 13, 27, 40 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_14_15_29
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3, 14, 15, 29 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_14_27_41
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,2,3, 14, 27, 41 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_15_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3, 15,18 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_15_18_33
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,15, 18, 33 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_15_27_42
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3, 15, 27, 42 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_16_27_43
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1,3, 16, 27, 43 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_17_27_44
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,17, 27, 44 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_18_21
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,3,18,21}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_18_27_45
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,18, 27, 45 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_19_27_46
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1, 3,19, 27, 46 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_20_27_47
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,2,3, 20, 27, 47 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_21_27_48
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3, 21, 27, 48 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_22_27_49
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1,3,22, 27, 49 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_23_27_50
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,23, 27, 50 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_24_27_51
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,24, 27, 51 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_25_27_52
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1,3, 25, 27, 52 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_26_27_53
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,2,3, 26, 27, 53 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_27_30
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3, 27, 30 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_4_15_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3, 4, 15, 19 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_4_27_31
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,1,3, 4, 27, 31 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_4_6_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3, 4 , 6 , 10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 3 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_5_15_20
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3,5 , 15, 20 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_5_27_32
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 2,3,5, 27, 32 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_5_6_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,5, 6, 11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 3, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_6_15_21
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 ,3, 6, 15, 21}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_6_18_24
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,3,6,18,24}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_6_27_33
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3, 6, 27, 33 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_6_9
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 3, 6, 9 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_7_15_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3, 7, 15, 22 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_1_3_7_27_34
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 1,3,7, 27, 34 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_8_15_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 3,8,15,23 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_2_3_8_27_35
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,2,3, 8, 27, 35 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_9_15_24
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0, 3,9, 15, 24 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_9_18_27
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,3,9,18,27}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_3_9_27_36
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,3, 9, 27, 36 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_4_12_16
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,4,12,16 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_4_5
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 4 , 5 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_4_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0 , 4 , 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_4_6_10
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 4, 6,10 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_4_8_12
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 2 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 4, 8, 12 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_5_10_15
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech + Interactive or background combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 5,10, 15 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_5_12_17
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,5,12,17 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_5_6
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
allowedTFC_List: { 0, 5, 6 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_5_6_11
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0 , 5, 6,11 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_10_12_22
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,6,10,12,22}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_11_12_23
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 3 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,6, 11, 12, 23 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_12_18
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: { 0,6,12,18}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_7_12_19
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,6,7,12,19}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_8_12_20
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,6,8,12,20}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Allowed_0_6_9_12_21
Group	:
ASN1 Type	: TFC_Subset
Derivation Path	:
Encoding Variation	:
Comments	: For speech combination with 4 RBs + DCCH
Constraint Value	
allowedTFC_List: {0,6,9,12,21}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_336_8_148(p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_336_8_TFS}, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL}}, dlTFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_3_0To19_Order1 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: Here the list of CTFCs is "0,2,4,6,8,1,3,5,7,9,10,12,14,16,18,11,13,15,17 and 19" unlike c_TrCHInfoDL_3_0To19 where the order is "0, 1, 2, 3,4,5,6,7,8,9,10,11,12,13,14,15,16,17, 18 and 19"
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To19_Tx_Order1 (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_3_0To19 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0 To 19
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To19_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_3_0To7 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7
Constraint Value	
<pre> { dlconnectedTrCHList {{ trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To119_14_2_38e (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11, 12, 13, 23
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To23 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11, 12, 13, 23
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To239 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 8, 15, 22, 59, 60, 61, 68, 75, 82, 119, 120, 121, 128, 135, 142, 179, 180, 181, 188, 195, 202, 239
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To239_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To359 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13,23,24,25,35,36,37,47
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To359_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To47 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13,23,24,25,35,36,37,47
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To541 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list :
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To541_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To599 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list :
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To599_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To71 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23, 24, 25, 35, 36, 37, 47,48,49,59,60,61,71
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To71_Tx(p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_2_0To11 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5, 6, 7, 8, 9,10,11
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To11_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_3_0To11 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 2, 4, 1, 3, 5, 6, 8, 10, 7, 9, 11
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_2_4_1_3_5_6_8_10_7_9_11_Rx } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_3_0To7 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To7_Rx } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To23 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To239 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To239_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To359 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23, 24, 25, 35, 36, 37, 47.
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To359_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To47 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23, 24, 25, 35, 36, 37, 47.
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 },{ trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To47_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To541 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list :
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To541_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To599 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list :
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To599_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To71 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23, 24, 25, 35, 36, 37, 47,48,49,59,60,61,71
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To71_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To11 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11
Constraint Value	
<pre>{ dlconnectedTrCHList {{ trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To11_tc55 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11
Constraint Value	
<pre>{ dlconnectedTrCHList {{ trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To13 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11,12,13
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To13_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To15 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11,12,13,14,15
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To15_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To17 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11,12,13,14,15, 16,17
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To17_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To21 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11,12,13,14,15, 16,17,18, 19,20, 21
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To21_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To3 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To3_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To5 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To5_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To9_tc54 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_3_0To15 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To15_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_3_0To49 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS; p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To49_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_3_0To59 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50,51,52,53,54,55,56,57,58,59
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To59_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To143 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119., 120,121,131,132,133,143
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To143_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To167 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ; p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119, 120, 121, 131,132, 133, 143, 144, 145, 155, 156, 157, 167
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To167_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To215 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119,1 20,121,131,132,133,143,144,145,155,156,157,167,168,169,179,180,181,191,192,193,203,204, 205,215.
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To215_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To263 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119,1 20,121,131,132,133,143,144,145,155,156,157,167,168,169,179,180,181,191,192,193,203,204, 205,215,216,217,227,228,229,239,240,241,251,252,253,263.
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To263_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To455 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119,1 20,121,131,132,133,143,144,145,155,156,157,167,168,169,179,180,181,191,192,193,203,204, 205,215,216,217,227,228,229,239,240,241,251,252,253,263,264,265,275,276,277,287,288,289 ,299,300,301,311,312,313,323,324,325,335,336,337,347,348,349,359,360,361,371,372,373,38 3,384,385,395,396,397,407,408,409,419,420,421,431,432,433,443,444,445,455
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To455_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To11 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5, 6, 7, 8, 9,10,11
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To11_Rx }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To17 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To17_Rx }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To3_tc54 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To3_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To5 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4,5
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To5_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_3_0To7(p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To7_Rx } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_122_AMR_1
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_81_TFS_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_12_2k_AMR(p_TFCS:TFCS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_81_TFS_6_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_103_TFS_5 }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL } }, dlTFCS p_TFCS } </pre>	
Detailed Comments	: c_TFCS_Cmpl0_To239_Tx (c_PowerOffsetInfoBelow64k)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_2_0To37 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9,10,11,12,13,14,15, 16,17,18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 35, 36, 37
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To37_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_3_4 (p_dITFCS : TFCS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL }, dITFCS p_dITFCS } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_3_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2 :CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dITFCS c_TFCS_Cmpl0_1_5_6_7_11_Tx (c_PowerOffsetInfoBelow64k) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_576_148_1
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_576_TFS}, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL}}, dlTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoBelow64k) --- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To119_14_2_38e (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0,1,8,15,22,59,60,61,68,75,82,119
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDSCH (p_transportChannellInfo : CommonOrDedicatedTFS; p_dITFCS : TFCS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DSCH1, transportChannellInfo p_transportChannellInfo }}, dITFCS p_dITFCS } }</pre>	
Detailed Comments	: c_DSCH_354_TTI_10_TFS_256k c_TFCS_DSCH_DCH_1

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoFACH_BCCH_CCCH_DCCH_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS) Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_FACH3, transportChannellInfo c_FACH_TFS }, { trchid tsc_FACH4, transportChannellInfo c_FACH_TFS_PS }}, dITFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) -- sent to SS } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoFACH_BCCH_CCCH_DCCH_SCCPCH2_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS) Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_FACH1, transportChannelInfo c_FACH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplIFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) -- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoFACH_BCCH_CCCH_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS) Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_FACH1, transportChannelInfo c_FACH_BCCH_CCCH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplIFACH_BCCH_CCCH_Tx (c_PowerOffsetInfoBelow64k) -- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoFACH_BCCH_CCCH_SCCPCH3_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS) Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_FACH3, transportChannelInfo c_FACH_BCCH_CCCH_TFS }, { trchid tsc_FACH4, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplIFACH_BCCH_CCCH_Tx (c_PowerOffsetInfoBelow64k) --- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_10_2k_AMR_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_65_TFS_6 }, { trchid tsc_UL_DCH2, transportChannelInfo c_DCH_99_TFS_5 }, { trchid tsc_UL_DCH3, transportChannelInfo c_DCH_40_TFS }, { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx --- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_12_2k_AMR(p_TFCS:TFCS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_81_TFS_6 }, { trchid tsc_UL_DCH2, transportChannelInfo c_DCH_103_TFS_5 }, { trchid tsc_UL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS p_TFCS -- sent to SS } </pre>	
Detailed Comments	: c_TFCS_Cmpl0_To239_Rx -- sent to SS

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_3_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_5_6_7_11_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_7_4k_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_61_TFS_6 }, { trchid tsc_UL_DCH2, transportChannelInfo c_DCH_87_TFS_5 }, { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Rx -- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfo_FACH_BMC
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_FACH1, transportChannelInfo c_FACH_CTCH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_CTCH_TFS } }, dlTFCS c_TFCS_CmplFACH_BMC_NoPCH_Tx (c_PowerOffsetInfoBelow64k) -- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_2_PS_CS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB10 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_3_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } }}, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB22 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_AM1_TM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } }, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_TM2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }}, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB11 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} }}</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_TM3_AM1_Multiplex
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }}, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB11 } }}, {trchid tsc_DL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH3, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB12 } }}, {trchid tsc_DL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH5, logicalChannelType dTCH, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB22 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_TM4
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }}, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB11 } }}, {trchid tsc_DL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH3, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB12 } }}, {trchid tsc_DL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB13 } }}, {trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDSCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DSCH1, trCH_LogCHMappingList {{ logicalChannel_Mapping dl_LogicalChannelMapping: { macHeaderManipulation normalMacHeader, dl_TransportChannelType dsch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB20 }} } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_CCCH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map CCCH, BCCH(for BCCH_FACH) to FACH1, and DTCH to FACH2. Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_CCCH_PS_2a
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map CCCH, BCCH(for BCCH_FACH) to FACH1, and DTCH to FACH2. Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_CCCH_SCCPCH3_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map CCCH, BCCH(for BCCH_FACH) to FACH1, and DTCH to FACH2. Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_CCCH_SCCPCH3_PS_2a
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map CCCH, BCCH(for BCCH_FACH) to FACH1, and DTCH to FACH2. Used for the configuration cell_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH3, and DTCH to FACH4. Used for the configuration cell_FACH_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH4, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } } }, { trchid tsc_FACH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH7, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH_RAB }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH6, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB29 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS_2a
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH3, and DTCH to FACH4. Used for the configuration cell_FACH_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH4, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB24 } } }, { trchid tsc_FACH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH7, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH_RAB }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH6, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 } } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, rB_Identity tsc_RB29 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_DCCH_CCCH_SCCPCH2_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH3, and DTCH to FACH4. Used for the configuration cell_FACH_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH7, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH_RAB }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH6, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB29 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB29 } } } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_BCCH_DCCH_CCCH_SCCPCH2_PS_2a
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH3, and DTCH to FACH4. Used for the configuration cell_FACH_3_SCCPCH_4_FACH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH7, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH_RAB }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH6, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB29 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }, { trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB24 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_PS_2a
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH2, and DTCH to FACH1. Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB24 } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB0 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingRACH_2_DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_RACH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_CCCH5, logicalChannelType cCCH }, rB_Identity tsc_RB0 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DTCH1, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>logicalChannelType dTCH }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DTCH4, logicalChannelType dTCH }, rB_Identity tsc_RB24 } } }, dlconnectedTrCHList OMIT }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_2_PS_CS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>} } }, dlconnectedTrCHList OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_3_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB20 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2, logicalChannelType dTCH }, rB_Identity tsc_RB22 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2 , logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3 , logicalChannelType dCCH }, rB_Identity tsc_RB3 } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_AM1_TM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB20 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_TM2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, }, } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } }}</pre>	
}}	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_TM3_AM1_Multiplex
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH3 , logicalChannelType dTCH }, rB_Identity tsc_RB12 } } }, { trchid tsc_UL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH4 , </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> logicalChannelType dTCH }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH5 , logicalChannelType dTCH }, rB_Identity tsc_RB22 } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_TM4
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH3 , logicalChannelType dTCH }, rB_Identity tsc_RB12 } } }, { trchid tsc_UL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH4 , logicalChannelType dTCH }, </pre>	

Continued on next page

ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB13 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMapping_FACH_BMC
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only. map CTCH to FACH2 and CCCH, BCCH (for BCCH_FACH) to FACH1
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_CTCH, logicalChannelType cTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB30 } } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AM_RLC_sdu4
Group	:
ASN1 Type	: UL_AM_RLC_Mode
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ transmissionRLC_Discard noDiscard : dat15, transmissionWindowSize tw128, timerRST tr500, max_RST rst4, pollingInfo { timerPollProhibit tpp200, timerPoll tp200, poll_PDU OMIT, poll_SDU sdu4, lastTransmissionPDU_Poll TRUE, lastRetransmissionPDU_Poll TRUE, pollWindow pw99, timerPollPeriodic OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AM_RLC_tc_14_2_43_1_tw512
Group	:
ASN1 Type	: UL_AM_RLC_Mode
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ transmissionRLC_Discard noDiscard : dat15, transmissionWindowSize tw512, timerRST tr500, max_RST rst4, pollingInfo { timerPollProhibit tpp200, timerPoll tp200, poll_PDU OMIT, poll_SDU sdu4, lastTransmissionPDU_Poll TRUE, lastRetransmissionPDU_Poll TRUE, pollWindow pw99, timerPollPeriodic OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListAM2 (p_TFS1, p_TFS2 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS : p_TFS2 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : c_DCH_148_TFS_UE_UL } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListAM_DCH1 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }} </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM4 (p_TFS1, p_TFS2, p_TFS3, p_TFS4 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS : p_TFS2 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH3, transportFormatSet dedicatedTransChTFS : p_TFS3 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH4, transportFormatSet dedicatedTransChTFS : p_TFS4 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : c_DCH_148_TFS_UE_UL }} </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_DCH2 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : c_DCH_148_TFS_UE_UL } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoAM0To3(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd :{ ul_TFCS c_TFCS_Cmpl0_To3_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM0To11(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM0To9(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM2_0To7(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM4_0To119 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM4_0To47(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_0_To_3(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To3_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_12_2k_AMR
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (c_PowerOffsetInfoBelow64k) } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_7_4k_AMR
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Tx (c_PowerOffsetInfoBelow64k) } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTmSpeech(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_5_6_7_11_Tx(p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM0To11(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM0To17(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To17_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM0To5(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To5_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM0To7 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM1_TM1_0To19(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To19_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM0To3(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To3_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM1_AM1_0To11 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_2_4_1_3_5_6_8_10_7_9_11_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM1_AM1_0To7 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To119_14_2_38e(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To23(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To239(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To239_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To359(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To359_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To47 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To541(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl_0_To541_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To599(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl_0_To599_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To71(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To71_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM4_0To239(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To239_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM4_0To47(p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration

Constraint Name	: c_pdsch_CodeMapping_0To10 (p_primaryScramblingCode : PrimaryScramblingCode)
Group	:
ASN1 Type	: PDSCH_CodeMapping
Derivation Path	:
Encoding Variation	:
Comments	:

Constraint Value	
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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration
<pre> -- ASN.1 Type Constraint Declaration -- -- This block contains the ASN.1 type constraint declarations for the -- various data types used in the system. -- -- The declarations are organized into sections, each corresponding to a -- specific data type or set of related types. -- -- The first section defines the basic types, including integers, -- strings, and booleans. -- -- The second section defines the more complex types, including -- sets, sequences, and unions. -- -- The third section defines the types used for the system's -- configuration and state. -- -- The fourth section defines the types used for the system's -- data and results. -- -- The fifth section defines the types used for the system's -- control and management. -- -- The sixth section defines the types used for the system's -- logging and debugging. -- -- The seventh section defines the types used for the system's -- security and authentication. -- -- The eighth section defines the types used for the system's -- network and communication. -- -- The ninth section defines the types used for the system's -- hardware and software. -- -- The tenth section defines the types used for the system's -- miscellaneous and other. -- -- The final section defines the types used for the system's -- overall and general. -- -- The declarations are organized into sections, each corresponding to a -- specific data type or set of related types. -- -- The first section defines the basic types, including integers, -- strings, and booleans. -- -- The second section defines the more complex types, including -- sets, sequences, and unions. -- -- The third section defines the types used for the system's -- configuration and state. -- -- The fourth section defines the types used for the system's -- data and results. -- -- The fifth section defines the types used for the system's -- control and management. -- -- The sixth section defines the types used for the system's -- logging and debugging. -- -- The seventh section defines the types used for the system's -- security and authentication. -- -- The eighth section defines the types used for the system's -- network and communication. -- -- The ninth section defines the types used for the system's -- hardware and software. -- -- The tenth section defines the types used for the system's -- miscellaneous and other. -- -- The final section defines the types used for the system's -- overall and general. -- </pre>

Constraint Name	: c_pdsch_CodeMapping_0To18 (p_primaryScramblingCode : PrimaryScramblingCode)
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Group	:
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ASN1 Type : PDSCH_CodeMapping

Derivation Path :

Encoding Variation :

Comments	:	
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Constraint Value	
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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp4, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) } } } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_pdsch_CodeMapping_0To5 (p_primaryScramblingCode : PrimaryScramblingCode)
Group	:
ASN1 Type	: PDSCH_CodeMapping
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_ScramblingCode p_primaryScramblingCode, signallingMethod explicit_config : { { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_pdsch_CodeMapping_0To8 (p_primaryScramblingCode : PrimaryScramblingCode)
Group	:
ASN1 Type	: PDSCH_CodeMapping
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_ScramblingCode p_primaryScramblingCode, -- tsc_DSCH_2nd_ScrCode signallingMethod explicit_config : { { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) }, { spreadingFactor sfp8, codeNumber 0, -- (0..Spreading factor-1) multiCodeInfo 1 -- (1..16) } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DL_DPCH_256K_PS (p_DL_CommonInformation : DL_CommonInformation; p_sf : SF512_AndCodeNumber)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { {secondaryScramblingCode 1, -- p_SecondaryScramblingCode , sf_AndCodeNumber p_sf } }, tpc_CombinationIndex 0 }, }, powerOffsetOfTFCL_PO1 tsc_DPCH_PowerOffsetTFCL, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower 5, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM1_No_Pdcp_RlcSizeList1 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_PS, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }}, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM1_No_Pdcp_sdu4 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def_sdu4, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }}, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM2_No_Pdcp (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH1 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2} }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} } } }} }, { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id2, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB22, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH2 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH2 }} } } }} } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM2_No_Pdcp_2a (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_UL_DTCH1 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_DL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2} }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} } } }} }, { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id2, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB24, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity tsc_UL_DTCH4, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity tsc_DL_DTCH4 }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH4, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH4 }} } } }} } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM2_PS_No_Pdcp (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2} }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} } } }} }, { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id2, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB22, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo { { --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel: { --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH2, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH2 }} } } }} } } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListTM4 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id, p_RAB_Id2: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB12, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH3, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 6 }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH3, logicalChannelIdentity OMIT }} }} }, { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id2, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB13, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def_Seg_False, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH4, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH4, logicalChannelIdentity OMIT }} }} }} } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB5_Def_2SCCPCH (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType5
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 5
Constraint Value	
<pre> { sib6indicator FALSE, pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList {{ prach_RACH_Info { modeSpecificInfo fdd : { availableSignatures tsc_PRACH1_Signatures, availableSF tsc_PRACH1_SF, preambleScramblingCodeWordNumber tsc_PRACH1_ScrC, puncturingLimit pl1, availableSubChannelNumbers '111111111111'B } }, transportChannelIdentity tsc_RACH1, rach_TransportFormatSet commonTransChTFS : c_RACH_TFS_UE, rach_TFCS normalTFCl_Signalling : complete : { ctfcSize ctfc2Bit : {{ ctfc2 0, powerOffsetInformation { gainFactorInformation computedGainFactors : 0, powerOffsetPp_m 0 } }, { ctfc2 1, powerOffsetInformation { gainFactorInformation signalledGainFactors : { modeSpecificInfo fdd : { gainFactorBetaC 11 }, gainFactorBetaD 15, referenceTFC_ID 0 }, powerOffsetPp_m 0 } } }, prach_Partitioning fdd : {{ accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }}, persistenceScalingFactorList { psf0_9, psf0_9, psf0_9, psf0_9, psf0_9, psf0_9 }, ac_To_ASC_MappingTable { 6, 5, 4, 3, 2, 1, 0 }, modeSpecificInfo fdd : { primaryCPICH_TX_Power 31, constantValue -10, prach_PowerOffset { powerRampStep 3, -- db preambleRetransMax 4 }, rach_TransmissionParameters { mmax 2, nb01Min 3, nb01Max 10 }, aich_Info { channelisationCode256 tsc_AICH1_ChC, sttd_Indicator FALSE, aich_TransmissionTiming e0 } } }}, sCCPCH_SystemInformationList { { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber sf128:4, --from 34.108 pilotSymbolExistence FALSE, tfci_Existence FALSE, positionFixedOrFlexible fixed, timingOffset 30 } }, tfcs normalTFCI_Signalling : complete: {ctfcSize ctfc2Bit : { {ctfc2 0 }, {ctfc2 1 } }}, fach_PCH_InformationList { { </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> transportFormatSet commonTransChTFS : c_PCH_TFS_UE, transportChannelIdentity tsc_PCH1, -- PCH ctch_Indicator FALSE } }, pich_Info fdd :{ channelisationCode256 tsc_PICH1_ChC, pi_CountPerFrame e18, sttd_Indicator FALSE } }, { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber sf64:1,--34.108 pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 0 } }, tfcs normalTFCL_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 }, {ctfc4 3 }, {ctfc4 4 } }}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS : c_FACH_TFS_UE, transportChannelIdentity tsc_FACH1, -- FACH ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_PS_UE, transportChannelIdentity tsc_FACH2, -- FACH ctch_Indicator FALSE } } }}, cbs_DRX_Level1Information OMIT -- nonCriticalExtensions OMIT } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB5_Def_3SCCPCH (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType5
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 5
Constraint Value	
<pre> { sib6indicator FALSE, pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList {{ prach_RACH_Info { modeSpecificInfo fdd : { availableSignatures tsc_PRACH1_Signatures, availableSF tsc_PRACH1_SF, preambleScramblingCodeWordNumber tsc_PRACH1_ScrC, puncturingLimit pl1, availableSubChannelNumbers '111111111111'B } }, transportChannelIdentity tsc_RACH1, rach_TransportFormatSet commonTransChTFS : c_RACH_TFS_UE, rach_TFCS normalTFCl_Signalling : complete : { ctfcSize ctfc2Bit : {{ ctfc2 0, powerOffsetInformation { gainFactorInformation computedGainFactors : 0, powerOffsetPp_m 0 } }, { ctfc2 1, powerOffsetInformation { gainFactorInformation signalledGainFactors : { modeSpecificInfo fdd : { gainFactorBetaC 11 }, gainFactorBetaD 15, referenceTFC_ID 0 }, powerOffsetPp_m 0 } } }, prach_Partitioning fdd : {{ accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }}, persistenceScalingFactorList { psf0_9, psf0_9, psf0_9, psf0_9, psf0_9, psf0_9 }, ac_To_ASC_MappingTable { 6, 5, 4, 3, 2, 1, 0 }, modeSpecificInfo fdd : { primaryCPICH_TX_Power 31, constantValue -10, prach_PowerOffset { powerRampStep 3, -- db preambleRetransMax 4 }, rach_TransmissionParameters { mmax 2, nb01Min 3, nb01Max 10 }, aich_Info { channelisationCode256 tsc_AICH1_ChC, sttd_Indicator FALSE, aich_TransmissionTiming e0 } } }}, sCCPCH_SystemInformationList { { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber tsc_S_CCPCH1_DL_ChC, --from 34.108 pilotSymbolExistence FALSE, tfci_Existence FALSE, positionFixedOrFlexible fixed, timingOffset 30 } }, tfcs normalTFCI_Signalling : complete: {ctfcSize ctfc2Bit : { {ctfc2 0 }, {ctfc2 1 } }}, fach_PCH_InformationList { { </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> transportFormatSet commonTransChTFS : c_PCH_TFS_UE, transportChannelIdentity tsc_PCH1, -- PCH ctch_Indicator FALSE } }, pich_Info fdd :{ channelisationCode256 tsc_PICH1_ChC, pi_CountPerFrame e18, sttd_Indicator FALSE } }, { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber tsc_S_CCPCH2_DL_ChC, pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 0 } }, tfcs normalTFCL_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 }, {ctfc4 3 }, {ctfc4 4 } }}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS : c_FACH_TFS_UE_3rdSCCPCH, transportChannelIdentity tsc_FACH1, -- FACH ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_PS_UE, transportChannelIdentity tsc_FACH2, -- FACH ctch_Indicator FALSE } } }, { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber tsc_S_CCPCH3_ChC, pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 90 } }, tfcs normalTFCL_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 }, {ctfc4 3 }, {ctfc4 4 } }}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS : c_FACH_TFS_UE_3rdSCCPCH, transportChannelIdentity tsc_FACH3, -- FACH ctch_Indicator FALSE }, </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ transportFormatSet commonTransChTFS : c_FACH_TFS_PS_UE, transportChannelIdentity tsc_FACH4, -- FACH ctch_Indicator FALSE } }}, cbs_DRX_Level1Information OMIT -- nonCriticalExtensions OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB5_Def_3SCCPCH_1 (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType5
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 5
Constraint Value	
<pre> { sib6indicator TRUE, pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList {{ prach_RACH_Info { modeSpecificInfo fdd : { availableSignatures tsc_PRACH1_Signatures, availableSF tsc_PRACH1_SF, preambleScramblingCodeWordNumber tsc_PRACH1_ScrC, puncturingLimit pl1, availableSubChannelNumbers '111111111111'B } }, transportChannelIdentity tsc_RACH1, rach_TransportFormatSet commonTransChTFS : c_RACH_TFS_UE, rach_TFCS normalTFCl_Signalling : complete : { ctfcSize ctfc2Bit : {{ ctfc2 0, powerOffsetInformation { gainFactorInformation computedGainFactors : 0, powerOffsetPp_m 0 } }, { ctfc2 1, powerOffsetInformation { gainFactorInformation signalledGainFactors : { modeSpecificInfo fdd : { gainFactorBetaC 11 }, gainFactorBetaD 15, referenceTFC_ID 0 }, powerOffsetPp_m 0 } } }, prach_Partitioning fdd : {{ accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }}, persistenceScalingFactorList { psf0_9, psf0_9, psf0_9, psf0_9, psf0_9, psf0_9 }, ac_To_ASC_MappingTable { 6, 5, 4, 3, 2, 1, 0 }, modeSpecificInfo fdd : { primaryCPICH_TX_Power 31, constantValue -10, prach_PowerOffset { powerRampStep 3, -- db preambleRetransMax 4 }, rach_TransmissionParameters { mmax 2, nb01Min 3, nb01Max 10 }, aich_Info { channelisationCode256 tsc_AICH1_ChC, sttd_Indicator FALSE, aich_TransmissionTiming e0 } } }}, sCCPCH_SystemInformationList { { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber sf128:4, --from 34.108 pilotSymbolExistence FALSE, tfci_Existence FALSE, positionFixedOrFlexible fixed, timingOffset 30 } }, tfcs normalTFCI_Signalling : complete: {ctfcSize ctfc2Bit : { {ctfc2 0 }, {ctfc2 1 } }}, fach_PCH_InformationList { { </pre>

Continued on next page

ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> transportFormatSet commonTransChTFS : c_PCH_TFS_UE, transportChannelIdentity tsc_PCH1, -- PCH ctch_Indicator FALSE } }, pich_Info fdd : { channelisationCode256 tsc_PICH1_ChC, pi_CountPerFrame e18, sttd_Indicator FALSE } }, { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber sf128:5, pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 0 } }, tfcs normalTFCL_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 } }}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS : c_FACH_TFS_UE_2ndSCCPCH, transportChannelIdentity tsc_FACH1, -- FACH ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_UE_2ndSCCPCH, transportChannelIdentity tsc_FACH2, -- FACH ctch_Indicator TRUE } } }}, cbs_DRX_Level1Information { ctch_AllocationPeriod 2, cbs_FrameOffset 0 } } -- nonCriticalExtensions OMIT } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB6_Def_3SCCPCH (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType6
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 6, used in connected mode.
Constraint Value	
<pre> { pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList OMIT, sCCPCH_SystemInformationList { { secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber tsc_S_CCPCH1_ChC, pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 90 } }, tfcs normalTFCl_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 }, {ctfc4 3 }, {ctfc4 4 } }}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS :c_FACH_TFS_UE, transportChannelIdentity tsc_FACH3, -- FACH ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_PS_UE, transportChannelIdentity tsc_FACH4, -- FACH ctch_Indicator FALSE } } } }, cbs_DRX_Level1Information OMIT -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments	: Similar to cb_SIB6_Def, except "AC-to-ASC mapping table" not present

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoAM_Def_PS
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_AM_RLC_Mode: cb_UL_AM_RLC_rst4_tp200 , dl_RLC_Mode dl_AM_RLC_Mode : cb_DL_AM_RLC }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_UL_AM_RLC_rst4_tp200
Group	:
ASN1 Type	: UL_AM_RLC_Mode
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ transmissionRLC_Discard noDiscard : dat15, transmissionWindowSize tw128, timerRST tr500, max_RST rst4, pollingInfo { timerPollProhibit tpp200, timerPoll tp200, poll_PDU OMIT, poll_SDU sdu1, lastTransmissionPDU_Poll TRUE, lastRetransmissionPDU_Poll TRUE, pollWindow pw99, timerPollPeriodic OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_256k_384k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_AMR.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_AMR by changing dl_TxPower to 5 instead of tsc_DL_TxPower_DPCH. For a data rate of 256KBPS or 384KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList BY {{ secondaryScramblingCode OMIT, sf_AndCodeNumber p_sf }}, REPLACE dl_TxPower BY 5	
Detailed Comments	: { dl_CommonInformation p_DL_CommonInformation , dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 dl_ChannelisationCodeList { { secondaryScramblingCode 1, sf_AndCodeNumber p_sf } }, tpc_CombinationIndex 0 }, powerOffsetOfTFPI_PO1 0, powerOffsetOfTPC_PO2 0, powerOffsetOfPILOT_PO3 0, dl_TxPower 5, dl_TxPowerMax 15, dl_TxPowerMin -35 }

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_144k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_AMR.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_AMR by changing dl_TxPower to 1 instead of tsc_DL_TxPower_DPCH. For a data rate of 144KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList BY {{ secondaryScramblingCode OMIT, sf_AndCodeNumber p_sf }}, REPLACE dl_TxPower BY 1	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_128k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_AMR.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_AMR by changing dl_TxPower to 0 instead of tsc_DL_TxPower_DPCH. For a data rate of 128KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList BY {{ secondaryScramblingCode OMIT, sf_AndCodeNumber p_sf }}, REPLACE dl_TxPower BY 0	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_64k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_AMR.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_AMR by changing dl_TxPower to -2 instead of tsc_DL_TxPower_DPCH. For a data rate of 64KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_DPCH_InfoPerRL.fdd.dl_ChannelisationCodeList BY {{ secondaryScramblingCode OMIT, sf_AndCodeNumber p_sf }}, REPLACE dl_TxPower BY -2	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_Info_256k_384k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation ;p_secondaryScramblingCode: SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_Info.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_Info by changing dl_TxPower to -2 instead of tsc_DL_TxPower_DPCH. For a data rate of 64KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_TxPower BY 5	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_Info_128k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation ;p_secondaryScramblingCode: SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_Info.
Encoding Variation	:
Comments	: constaraint derived from cb_DL_DPCH_Info by changing dl_TxPower to -2 instead of tsc_DL_TxPower_DPCH. For a data rate of 64KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_TxPower BY 0	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_DPCH_Info_64k (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation ;p_secondaryScramblingCode: SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	: cb_DL_DPCH_Info.
Encoding Variation	:
Comments	: constarint derived from cb_DL_DPCH_Info by changing dl_TxPower to -2 instead of tsc_DL_TxPower_DPCH. For a data rate of 64KBPS the DL DPCH and CPICH should be of same power level
Constraint Value	
REPLACE dl_TxPower BY -2	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_17_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti80 : {{ rlc_Size bitMode : sizeType2 : {part1 2, part2 4}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 170, crc_Size crc16 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.1: TTI = 80 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_UE_DL
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti40 :{{ rlc_Size bitMode : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 192, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.2: TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size = TB_Size -4 =144 bits.(DCH, w/ MUX) coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_3_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in PS 32k. TS34.108 cl. 6.10.2.4.1.23
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2 ; coding = turbo; CRCsize = 16; RateMatching = 155	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_3_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in PS 32k. TS34.108 cl. 6.10.2.4.1.23
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2 ; coding = turbo; CRCsize = 16; RateMatching = 155

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_23_DL_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.8kbps This constraint refers to table 6.10.2.4.1.23.2.1.1 of 34.108 V3.4
Constraint Value	
<pre> { tti tti40 :{ {tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL}, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; Two transport formats: TransportBlocks = 0, TB size = 336 bits; TransportBlock = 1, TB Size =336 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_24_UL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.64kbps This corresponds to 6.10.2.4.1.24.1.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL, small : 2,small : 3,small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_25_DL_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.64kbps This corresponds to 6.10.2.4.1.25.2.1.1 of 34.108
Constraint Value	
<pre> { tti tti20 :{{ tb_Size 336, numberOfTbSizeList { zero : NULL,one : NULL,small : 2,small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_25_DL_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /DL:.64kbps This corresponds to 6.10.2.4.1.25.2.1.1 of 34.108
Constraint Value	
<pre>{ tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL,one : NULL,small : 2,small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; five transport formats: TransportBlocks = 0, TB size = 336 bits; TansportBlock = 1, Size =336 bits; TransportBlocks = 2, TB size = 336 bits., TransportBlocks = 3, TB size = 336 bits,TransportBlocks = 4, TB size = 336 bits coding = turbo; CRCsize = 16; RateMatching = parameter	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_340_TFS_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.8 kbps This corresponds to 6.10.2.4.1.56.1.1.1 uplink of 34.108
Constraint Value	
<pre>{ tti tti40 :{ {tb_Size 340, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 155, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; two transport formats: TransportBlocks = 0, TB size = 340 bits; TansportBlock = 1, Size =340 bits; coding = turbo; CRCsize = 16; RateMatching = parameter(135–175)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_40_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 40, numberOfTbSizeList { zero : NULL , one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 40 bits; TansportBlock = 1, Size = 40. coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_40_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 40, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 40 bits; TansportBlock = 1, Size = 40. coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_42_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.“: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 42. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_42_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 42, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.“: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 42 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 42. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_49_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 49, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; three transport formats: TransportBlocks =1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 49 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_49_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 49, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 49, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 49 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 49 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_53_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 53, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.11: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 53 bits; TansportBlock = 1, Size = 53. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_54_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 54, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.10: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 54 bits; TansportBlock = 1, Size = 54. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_55_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 55. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_55_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 55, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 55 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 55. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_2
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/UL:14.4 DL:14.4kbps
Constraint Value	
<pre> { tti tti40 :{ { tb_Size 576, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 165, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = 165

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_2_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Streaming/Unknown/UL:14.4 DL:14.4kbps
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 9, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 165, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = 165

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_3 (p_RM : INTEGER)
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Conversational/Unknown/UL:28.8 DL:28.8kbps and Streaming/Unknown/UL:28.8 DL:28.8kbps
Constraint Value	
<pre> { tti40 : { { tb_Size 576, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute p_RM, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_3_UE (p_RM : INTEGER)
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Conversational/Unknown/UL:28.8 DL:28.8kbps and Streaming/Unknown/UL:28.8 DL:28.8kbps
Constraint Value	
<pre> { tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 9, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute p_RM, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = parameter

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_58_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 58 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_58_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 58, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 58 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 58 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 61 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_61_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 61, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 61 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 61 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_63_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 63, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.9: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 63 bits; TansportBlock = 1, Size = 63. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_20_1
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 640, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 187, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.14: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 1, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 187	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_20_1_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 11, part2 2}, numberOfTbSizeList { zero : NULL , one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 187, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.14: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 1, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 187	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_40_2_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 11, part2 2}, numberOfTbSizeList { zero : NULL, small : 2 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 187, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.14: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 172	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_40_4_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 11, part2 2}, numberOfTbSizeList { zero : NULL, small : 4 }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType turbo : NULL , rateMatchingAttribute 172, crc_Size crc16 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.13: TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits. coding = turbo; CRCsize = 16; RateMatching = 172

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 65, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { tb_Size 65, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_65_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 65, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 65 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 65. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_75_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 0, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 75, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.6: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 7 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_75_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 75, numberOfTbSizeList { zero : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }, { rlc_Size bitMode : sizeType1 : 75, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 subclause 6.10.2.4.1.6: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 75 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 7 coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_76_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 76, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.8: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 76 bits; TansportBlock = 1, Size = 76 coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_84_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 84, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.6: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 75 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 7 coding = convolutional; coding rate = 1/3; CRCsize = NA; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_87_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 87, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.7: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 87 bits; TansportBlock = 1, Size = 87. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_99_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ {tb_Size 99, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 99 bits; TransportBlocks = 1, TB size = 99 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_99_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 99, numberOfTbSizeList { zero : NULL , one : NULL }, logicalChannelList configured : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subclause 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 99 bits; TransportBlocks = 1, TB size = 99 bits; coding = convolutional; coding rate = 1/3; CRCsize = 0; RateMatching = 190	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM_1
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { c_DL_AddReconfTransChInfo(tsc_DL_DCH1, tsc_UL_DCH1), c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM_2
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_DL_AddReconfTransChInfo(tsc_DL_DCH1, tsc_UL_DCH1), c_DL_AddReconfTransChInfo(tsc_DL_DCH2, tsc_UL_DCH2), c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_TM3_AM1_0_119
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To119_Rx } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_MIB_MulRatOrBMC (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: MasterInformationBlock
Derivation Path	:
Encoding Variation	:
Comments	: Default setting of MIB. 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre> { mib_ValueTag 1, plmn_Type gsm_MAP : { plmn_Identity { mcc o_HexToDigitsMCC (p_CellInfo.mcc), mnc o_HexToDigitsMNC (p_CellInfo.mnc) } }, sibSb_ReferenceList { { sibSb_Type sysInfoTypeSB1 : 1, scheduling {scheduling { segCount 2, sib_Pos rep16 : 1 }} }, { sibSb_Type sysInfoType1 : 1, scheduling {scheduling { sib_Pos rep128 : 5 }} }, { sibSb_Type sysInfoType2 : 1, scheduling {scheduling { sib_Pos rep128 : 7 }} }, { sibSb_Type sysInfoType3 : 1, scheduling {scheduling { sib_Pos rep64 : 13 }} }, { sibSb_Type sysInfoType4 : 1, scheduling {scheduling { sib_Pos rep64 : 15 }} }, { sibSb_Type sysInfoType5 : 1, scheduling {scheduling { segCount 4, sib_Pos rep128 : 19, sib_PosOffsetInfo {so4, so2, so2} }} } } } </pre>	
Detailed Comments	<p>: Assum no segmentation for SIB1, SIB2, SIB3, SIB4.</p> <p>Contains scheduling informations for SIB1, SIB2, SIB3, SIB4, SIB5 and SB1 only, the scheduling information for other SIBs are in SysInfoTypeSB1.</p> <p>The value 1 of valueTags is a place holder. actual values of them will be non–zero and assigned dynamically in various SendSystemInformation test Steps.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoListTM_1 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM_2 (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)	
Group :	
ASN1 Type : RAB_InformationSetupList	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoTM_Def_Seg_False
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_TM_RLC_Mode:{ transmissionRLC_Discard timerBasedNoExplicit : dt100, segmentationIndication FALSE }, dl_RLC_Mode dl_TM_RLC_Mode:{ segmentationIndication FALSE } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SB1_BMC_2PRACH_2SCCPCH
Group	:
ASN1 Type	: SysInfoTypeSB1
Derivation Path	:
Encoding Variation	:
Comments	: Specific setting of SB1, where two PRACH or two SCCPCH are required and SIB5 may be segmented upto 8 segments. Scheduling defined in 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre> { sib_ReferenceList {{ sib_Type sysInfoType7 : NULL, scheduling {scheduling { sib_Pos rep32 : 11 }} }}, { sib_Type sysInfoType11 : 1, scheduling { scheduling { segCount 3, sib_Pos rep128 : 29, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType12 : 1, scheduling { scheduling { segCount 3, sib_Pos rep128 : 61, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType18 : 1, scheduling { scheduling { segCount 1, sib_Pos rep128 : 6 }} } }, nonCriticalExtensions OMIT } </pre>	
Detailed Comments	<p>: Assum no segmentation for SIB7.</p> <p>Contains scheduling informations for SIB7, SIB11, SIB12, SIB18 only, the scheduling information for other SIBs may be added later.</p> <p>The value 1 of valueTags is a place holder. actual values of them will be non-zero and assigned dynamically in various SendSystemInformation test Steps.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SB1_Schedul2
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SB1 of two segment
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep16 :1, sib_PosOffsetInfo {so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul1_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of one segment, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { sib_Pos rep128 :29 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul2_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of two segments, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep128 :29, sib_PosOffsetInfo {so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul3_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of three segments, 3GPP TS 34.123-3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep128 :29, sib_PosOffsetInfo {so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul1_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of one segment, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { sib_Pos rep128 :61 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul2_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of two segments, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep128 :61, sib_PosOffsetInfo {so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul3_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of three segments, 3GPP TS 34.123–3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep128 :61, sib_PosOffsetInfo {so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul1_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of one segment, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { sib_Pos rep128 :19 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul2_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of two segments, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep128 :19, sib_PosOffsetInfo {so4} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul3_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of three segments, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul4_MulRatOrBMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of four segments, 3GPP TS 34.123–3 clause 8.4.4.1 and 8.4.4.2
Constraint Value	
<pre>{ scheduling { segCount 4, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul5_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of five segments, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 5, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2, so2, so24} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul6_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of six segments, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 6, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2, so2, so24, so4} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul7_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of seven segments, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 7, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2, so2, so24, so4, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul8_BMC
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of eight segments, 3GPP TS 34.123-3 clause 8.4.4.1
Constraint Value	
<pre>{ scheduling { segCount 8, sib_Pos rep128 :19, sib_PosOffsetInfo {so4, so2, so2, so24, so4, so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To119_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc8Bit:{ { ctfc8 0, powerOffsetInformation OMIT }, { ctfc8 1, powerOffsetInformation OMIT }, { ctfc8 11, powerOffsetInformation OMIT }, { ctfc8 12, powerOffsetInformation OMIT }, { ctfc8 13, powerOffsetInformation OMIT }, { ctfc8 23, powerOffsetInformation OMIT }, { ctfc8 24, powerOffsetInformation OMIT }, { ctfc8 25, powerOffsetInformation OMIT }, { ctfc8 35, powerOffsetInformation OMIT }, { ctfc8 36, powerOffsetInformation OMIT }, { ctfc8 37, powerOffsetInformation OMIT }, { ctfc8 47, powerOffsetInformation OMIT }, { ctfc8 48, powerOffsetInformation OMIT }, { ctfc8 49, powerOffsetInformation OMIT }, { ctfc8 59, powerOffsetInformation OMIT }, { ctfc8 60, powerOffsetInformation OMIT }, { ctfc8 61, powerOffsetInformation OMIT }, { ctfc8 71, powerOffsetInformation OMIT }, { ctfc8 72, powerOffsetInformation OMIT }, { ctfc8 73, powerOffsetInformation OMIT }, { ctfc8 83, powerOffsetInformation OMIT }, { ctfc8 84, powerOffsetInformation OMIT }, { ctfc8 85, powerOffsetInformation OMIT }, { ctfc8 95, powerOffsetInformation OMIT }, { ctfc8 96, powerOffsetInformation OMIT }, { ctfc8 97, powerOffsetInformation OMIT }, { ctfc8 107 , powerOffsetInformation OMIT }, { ctfc8 108, powerOffsetInformation OMIT }, { ctfc8 109 , powerOffsetInformation OMIT }, { ctfc8 119, powerOffsetInformation OMIT } }} </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
normalTFCL_Signalling : complete : { ctfcSize ctfc8Bit : { { ctfc8 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 23, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 24, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 25, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 35, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 36, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 37, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 47, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 48, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 49, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 59, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 60, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 61, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 71, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 72, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 73, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 83, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 84, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 85, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 95, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 96, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 97, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 107, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 108, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 109, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc8 119, powerOffsetInformation p_PowerOffsetInformation } }}	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To19_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 2, powerOffsetInformation OMIT }, { ctfc6 3, powerOffsetInformation OMIT }, { ctfc6 4, powerOffsetInformation OMIT }, { ctfc6 5, powerOffsetInformation OMIT }, { ctfc6 6, powerOffsetInformation OMIT }, { ctfc6 7, powerOffsetInformation OMIT }, { ctfc6 8, powerOffsetInformation OMIT }, { ctfc6 9, powerOffsetInformation OMIT }, { ctfc6 10, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>}, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 14, powerOffsetInformation OMIT }, { ctfc6 15, powerOffsetInformation OMIT }, { ctfc6 16, powerOffsetInformation OMIT }, { ctfc6 17, powerOffsetInformation OMIT }, { ctfc6 18, powerOffsetInformation OMIT }, { ctfc6 19, powerOffsetInformation OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To19_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit : { { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 9, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 10, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> }, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 14, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 15, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 16, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 17, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 18, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 19, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_TFCS_Cmpl0_To3_Rx Group : ASN1 Type : TFCS Derivation Path : Encoding Variation : Comments : TFCS information with power offset information – for transmitter	
Constraint Value	
<pre> normalTFCL_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To5_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To5_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_CmplFACH_NoPCH_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_CmplPCH_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Restriction_DL(p_Restrictallowed_TFCI:TFC_Subset)
Group	:
ASN1 Type	: TFC_Restriction
Derivation Path	:
Encoding Variation	:
Comments	: This type is used to specify the allowed TFCs within the current TFCS. A TFC restriction is applicable until a subsequent TFC restriction is applied. TFC restrictions are not cumulative, so each TFC restriction completely replaces the previous TFC restriction. The downlink restriction can be used to ensure that the SS uses a specific TFC for transmission of data, by only allowing the 'No data' TFC, and the 'desired' TFC. It may also be necessary to include one or more 'signalling only' TFCs to allow signalling to occur. The uplink restriction can be used to verify that the UE has used a specific TFC. Any data received by the SS using a forbidden TFCI shall be discarded.
Constraint Value	
<pre>{ ulTFCI_Restriction OMIT, dlTFCI_Restriction p_Restrictallowed_TFCI }</pre>	
Detailed Comments	: SS requirements for downlink. 1. The SS MAC layer shall not use a restrictednon-allowed TFC for DL. 2. The SS MAC layer shall not use a TFC that requires the SS RLC layer to provide padding PDUs (Ref 3G TS 25.322)3. In the case that there is data pending on one or more RLC entities, but not enough to use one of the allowed TFCs:a. The SS MAC layer shall use the 'No data' TFC until there is enough data in the RLC to use another allowed TFC.b. The SS RLC layer shall buffer the data until there is enough data in the RLC entities for the MAC layer to use an allowed TFC other than the 'No data' TFC for transmission of the data.NB: The TTCN author is responsible for ensuring:1. The SDU discard function is not configured for TM and UM entities in the UE, and is configured to no_discard for AM entities in the UE. 2. That RLC SDUs that are expected to be sent in the same TTI (due to a TFC restriction) are sent as quickly as possible to minimise the number of 'no data' TFCs used by the MAC layer, and the amount of buffering that must be performed by the RLC layer. SS requirements for uplink: The SS shall discard all data received using a restricted non-allowed TFC.

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFC_Restriction_UL(p_TFC:TFC_Subset)
Group	:
ASN1 Type	: TFC_Restriction
Derivation Path	:
Encoding Variation	:
Comments	: This type is used to specify the allowed TFCs within the current TFCS. A TFC restriction is applicable until a subsequent TFC restriction is applied. TFC restrictions are not cumulative, so each TFC restriction completely replaces the previous TFC restriction. The downlink restriction can be used to ensure that the SS uses a specific TFC for transmission of data, by only allowing the 'No data' TFC, and the 'desired' TFC. It may also be necessary to include one or more 'signalling only' TFCs to allow signalling to occur. The uplink restriction can be used to verify that the UE has used a specific TFC. Any data received by the SS using a forbidden TFCI shall be discarded.
Constraint Value	
<pre>{ ulTFCI_Restriction p_TFC, dlTFCI_Restriction OMIT }</pre>	
Detailed Comments	: SS requirements for downlink. 1. The SS MAC layer shall not use a restricted non-allowed TFC for DL. 2. The SS MAC layer shall not use a TFC that requires the SS RLC layer to provide padding PDUs (Ref 3G TS 25.322) 3. In the case that there is data pending on one or more RLC entities, but not enough to use one of the allowed TFCs: a. The SS MAC layer shall use the 'No data' TFC until there is enough data in the RLC to use another allowed TFC. b. The SS RLC layer shall buffer the data until there is enough data in the RLC entities for the MAC layer to use an allowed TFC other than the 'No data' TFC for transmission of the data. NB: The TTCN author is responsible for ensuring: 1. The SDU discard function is not configured for TM and UM entities in the UE, and is configured to no_discard for AM entities in the UE. 2. That RLC SDUs that are expected to be sent in the same TTI (due to a TFC restriction) are sent as quickly as possible to minimise the number of 'no data' TFCs used by the MAC layer, and the amount of buffering that must be performed by the RLC layer. SS requirements for uplink: The SS shall discard all data received using a restricted non-allowed TFC.

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoDL_5_0To119 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119.
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoPCH
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only On second secondary CCPCH Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH.
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_PCH1, transportChannelInfo c_PCH_TFS }}, dlTFCS c_TFCS_CmplPCH_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_2_0_To5 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4,5
Constraint Value	
<pre>{ ulconnectedTrCHList {{ trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }}, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To5_Rx }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfoUL_5_0To119 (p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 11,12,13, 23, 24, 25, 35, 36, 37, 47,48,49,59,60,61,71, 72, 73, 83, 84, 85, 95, 96, 97, 107, 108, 109, 119
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To119_Rx }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_3_0To19 (p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To19_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_5_0To119 (p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71,72,73,83,84,85,95,96,97,107,108,109,119.
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To3 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3
Constraint Value	
<pre> { ulconnectedTrCHList {{ trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To3_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_3_0To19(p_DchTFS5 , p_DchTFS1,p_DchTFS2 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To19_Rx } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_102_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList {{ trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL }, { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_65_TFS }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_99_TFS }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_40_TFS } }, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_2_0_To3 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3
Constraint Value	
<pre> { dlconnectedTrCHList {{ trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }}, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (c_PowerOffsetInfoHigher64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_2_0_To5 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }}, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To5_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoFACH_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS) Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_FACH1, transportChannelInfo c_FACH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplIFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) -- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_102_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ulconnectedTrCHList {{ trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL }, { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_65_TFS }, { trchid tsc_UL_DCH2, transportChannelInfo c_DCH_99_TFS }, { trchid tsc_UL_DCH3, transportChannelInfo c_DCH_40_TFS } }, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx -- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_2_0_To3 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To3_Rx } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_3_Speech
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB10 } } }, { trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB11 } } }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_TM1_AM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB20 } }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }} } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_TM3_AM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }}, {trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB11 } }}, {trchid tsc_DL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH3, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB12 } }}, {trchid tsc_DL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH4, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB20 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>} } , dlconnectedTrCHList OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_3_Speech
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2, logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre>logicalChannelIdentity tsc_UL_DCCH3 , logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } }, dlconnectedTrCHList OMIT }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_TM1_AM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB20 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
	<pre>logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } }</pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_TM3_AM1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2 , logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH3 , logicalChannelType dTCH }, rB_Identity tsc_RB12 } } }, { trchid tsc_UL_DCH4, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH4 , </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> logicalChannelType dTCH }, rB_Identity tsc_RB20 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListAM_DCH2 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS : p_TFS1 }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListAM_DCH4 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH4, transportFormatSet dedicatedTransChTFS : p_TFS1 }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_1 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : c_DCH_148_TFS_UE_UL } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_2 (p_TFS1, p_TFS2, p_TFS3 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS :p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS :p_TFS2 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : p_TFS3 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_3 (p_TFS1 , p_TFS2, p_TFS3, p_TFS4 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS : p_TFS2 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH3, transportFormatSet dedicatedTransChTFS : p_TFS3 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : p_TFS4 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_0_To3
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS normalTFCL_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoBelow64k } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_0_To5
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoBelow64k } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_TM3_AM1_0To119 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_DPCH (p_PuncturingLimit : PuncturingLimit ; p_SpreadingFactor : SpreadingFactor)
Group	:
ASN1 Type	: UL_DPCH_Info
Derivation Path	:
Encoding Variation	:
Comments	: for AMR
Constraint Value	
<pre>{ modeSpecificInfo fdd : { scramblingCodeType longSC, scramblingCode 0, numberOfDPDCH 1, spreadingFactor p_SpreadingFactor, tfci_Existence TRUE, puncturingLimit p_PuncturingLimit } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : cb_DL_DPCH_AMR (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation) Group : ASN1 Type : DL_DPCHInfo Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation , dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 dl_ChannelisationCodeList { { secondaryScramblingCode 1, sf_AndCodeNumber p_sf } }, tpc_CombinationIndex 0 }, powerOffsetOfTFPI_PO1 0, powerOffsetOfTPC_PO2 0, powerOffsetOfPILOT_PO3 0, dl_TxPower tsc_DL_TxPower_DPCH, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_RAB_InfoListAM1_No_Pdcp (p_ReEstTimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetupList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoAM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }, {{ --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach: NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2 } }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach:NULL, logicalChannelIdentity tsc_DL_DTCH1 }} }} }} } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfo_AM_0_9
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd : { dl_Parameters dl_DCH_TFCS : c_TFCS_Cmpl0_To9_Rx } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_40_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kPS_RAB_SRB
Constraint Value	
<pre>{ tti tti40 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments	: TTI = 40 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,3 and 4 ; coding = turbo; CRCsize = 16; RateMatching = 150

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_2_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1 }, logicalChannelType dTCH }, rB_Identity tsc_RB20 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2 , logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3 , logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_40_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kPS_RAB_SRB
Constraint Value	
<pre> { tti tti40 :{{ tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 40 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,3 and 4 ; coding = turbo; CRCsize = 16; RateMatching = 150	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DL_DPCH_Info (p_sf : SF512_AndCodeNumber; p_DL_CommonInformation : DL_CommonInformation ;p_secondaryScramblingCode: SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation , dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { { secondaryScramblingCode p_secondaryScramblingCode, sf_AndCodeNumber p_sf } }, tpc_CombinationIndex 0 }, powerOffsetOfTFCL_PO1 tsc_DPCH_PowerOffsetTFCL, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower tsc_DL_TxPower_DPCH, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfo_AM0To9 (p_PowerOffsetInformation:PowerOffsetInformation)
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_103_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel
Constraint Value	
<pre>{ tti tti20 :{ { tb_Size 103, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } }</pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 103 bits; TansportBlock = 1, Size = 103. coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_103_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#2 on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1 : 103, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 190, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 103 bits; TansportBlock = 1, Size = 103. rlc_Size = TB_Size (DCH, w/o MUX) coding = convolutional; coding rate = 1/3; CRCsize = N/A; RateMatching = 190

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DCH_336_148_DL_Info (p_ActTime : ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_336_TFS}, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL } }, dlTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoHigher64k) -- sent to SS } </pre>	
Detailed Comments	<p>: For DCH1 (34.108 cl. 6.10.2.4.26):</p> <p>TTI = 20 ms;</p> <p>5 transport formats: TransportBlocks = 0,1,2,3 and 4; all TB sizes = 336 bits;</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 130</p> <p>For DCH5 (34.108 cl. 6.10.2.4.26):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DCH_336_148_UL_Info (p_ActTime : ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_336_TFS}, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx, -- sent to SS dlconnectedTrCHList OMIT, dlTFCS OMIT } </pre>	
Detailed Comments	<p>: For DCH1 (34.108 cl. 6.10.2.4.1.26.1.1.1):</p> <p>TTI = 20 ms;</p> <p>5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,3 and 4 ;</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 130</p> <p>For DCH5 (34.108 cl. 6.10.2.4.1.26.1.1.2):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kPS_RAB_SRB
Constraint Value	
<pre>{ tti tti20 :{{ tb_Size 336, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,3 and 4 ; coding = turbo; CRCsize = 16; RateMatching = 150	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_340_TFS_20_TC
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in Interactive or Background /UL:.64kbps This corresponds to 6.10.2.4.1.57.1.1.1 uplink of 34.108
Constraint Value	
<pre>{ tti tti20 :{ {tb_Size 340, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } }</pre>	
Detailed Comments : TTI = 40 ms; three transport formats: TransportBlocks = 0, TB size = 340 bits; TansportBlock = 1, Size =340 bits; TransportBlocks = 2 Size =340 bits; TansportBlock = 3, Size =340 bits; TransportBlocks = 4 Size =340 bits;. coding = turbo; CRCsize = 16; RateMatching = parameter(130–170)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_340_TFS_20_TC_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 40 ms; 3 transport formats: TB size always=340 bits; TransportBlocks = 0, 1, 2, 3, 4 ; coding = turbo; CRCsize = 16; RateMatching = 150

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_148_UL_Info(p_ActTime: ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_576_TFS}, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx , -- sent to SS dlconnectedTrCHList OMIT, dlTFCS OMIT } </pre>	
Detailed Comments	<p>: For DCH1 (34.108 cl. 6.10.2.4.17):</p> <p>TTI = 40 ms;</p> <p>five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits.</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 125</p> <p>For DCH2 (34.108 cl. 6.10.2.4.17):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155..</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_57_6kCS_RAB_SRB
Constraint Value	
<pre> { tti tti40 :{{tb_Size 576, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments	: 3GPP TS 34.108 clause 6.10.2.4.17 TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits. coding = turbo; CRCsize = 16; RateMatching = 125

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_576_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_57_6kCS_RAB_SRB for message sent to UE
Constraint Value	
<pre> { tti tti40 : { { rlc_Size octetModeType1 : sizeType2 : {part1 9, part2 2}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 145, crc_Size crc16 } } </pre>	
Detailed Comments : 3GPP TS 34.108 clause 6.10.2.4.17 TTI = 40 ms; five transport formats: TransportBlocks = 0, TB size = 576 bits; TansportBlock = 1, Size = 576 bits; TransportBlocks = 2, TB size = 576 bits; TansportBlock = 3, Size = 576 bits; TransportBlocks = 4, TB size = 576 bits. rlc_Size = TB_Size (DCH, w/o MUX) coding = turbo; CRCsize = 16; RateMatching = 125	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_60_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel
Constraint Value	
<pre> { tti tti20 : { { tb_Size 60, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments : TS 34.108 subcl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 60 bits; TansportBlock = 1, Size = 60. coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_60_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#3 on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1: 60, numberOfTbSizeList { zero : NULL, one : NULL }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 235, crc_Size crc0 } } </pre>	
Detailed Comments	: TS 34.108 subcl. 6.10.2.4.1.4: TTI = 20 ms; two transport formats: TransportBlocks = 0, TB size = 60 bits; TansportBlock = 1, Size = 60. rlc_Size = TB_Size (DCH, w/o MUX) coding = convolutional; coding rate = 1/2; CRCsize = N/A; RateMatching = 235

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_148_DL_Info(p_ActTime: ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_640_TFS}, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL} }, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (c_PowerOffsetInfoHigher64k) --- sent to SS } </pre>	
Detailed Comments	<p>: For DCH1 (34.108 cl. 6.10.2.4.13):</p> <p>TTI = 20 ms;</p> <p>two transport formats: TransportBlocks = 0, TB size = 640 bits; TransportBlocks = 2, TB size = 640 bits.</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 150</p> <p>For DCH5 (34.108 cl. 6.10.2.4.13):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_148_UL_Info(p_ActTime: ActivationTime)
Group	:
ASN1 Type	: CphyTrchConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { activationTime activationCFN : p_ActTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_640_TFS}, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL} }, ulTFCS c_TFCS_Cmpl0_1_2_3_Rx , -- sent to SS dlconnectedTrCHList OMIT, dlTFCS OMIT } </pre>	
Detailed Comments	<p>: For DCH1 (34.108 cl. 6.10.2.4.13):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 0, TB size = 640 bits; TansportBlock = 2, Size = 640 bits.</p> <p>coding = turbo;</p> <p>CRCsize = 16;</p> <p>RateMatching = 150</p> <p>For DCH5 (34.108 cl. 6.10.2.4.13):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kCS_RAB_SRB
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 640, numberOfTbSizeList { zero : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 172, crc_Size crc16 } } </pre>	
Detailed Comments : For DCH1 (34.108 cl. 6.10.2.4.13): TTI = 20 ms; two transport formats: TransportBlocks = 2, TB size = 640 bits; TansportBlock = 0, Size = 640. coding = turbo; CRCsize = 16; RateMatching = 172	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_640_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kCS_RAB_SRB for message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 11, part2 2}, numberOfTbSizeList { zero : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 172, crc_Size crc16 } } </pre>	
Detailed Comments : For DCH1 (34.108 cl. 6.10.2.4.13): TTI = 20 ms; two transport formats: TransportBlocks = 2, TB size = 640 bits; TansportBlock = 0, Size = 640. rlc_Size = TB_Size (DCH, w/o MUX) coding = turbo; CRCsize = 16; RateMatching = 172	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 81, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { tb_Size 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { tb_Size 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_DL_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1: 0, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 1, TB size = 0 bits; TransportBlocks = 1, TB size = 39 bits; TransportBlock = 1, Size = 81. coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_81_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RAB subflow#1 on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size bitMode : sizeType1: 81, numberOfTbSizeList { zero : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1 : 39, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL }, { rlc_Size bitMode : sizeType1: 81, numberOfTbSizeList { one : NULL }, logicalChannelList allSizes : NULL } }, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 200, crc_Size crc12 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.4: TTI = 20 ms; three transport formats: TransportBlocks = 0, TB size = 81 bits; TransportBlocks = 1, TB size = 39 bits; TansportBlock = 1, Size = 81. rlc_Size = TB_Size (DCH, w/o MUX) coding = convolutional; coding rate = 1/3; CRCsize = 12; RateMatching = 200

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListAM_3_4k (p_DedTranChTFS:DedicatedTransChTFS)
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { dl_TransportChannelType dch, dl_transportChannelIdentity tsc_DL_DCH1, tfs_SignallingMode explicit_config : dedicatedTransChTFS : p_DedTranChTFS, dch_QualityTarget { bler_QualityValue -20 }, dummy OMIT }, c_DL_AddReconfTransChInfo (tsc_DL_DCH5, tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListTM_64k
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_DL_AddReconfTransChInfo(tsc_DL_DCH1, tsc_UL_DCH1), c_DL_AddReconfTransChInfo(tsc_DL_DCH5, tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonInformationRB_SetUpSpeech (p_Sf: SF512_AndPilot)
Group	:
ASN1 Type	: DL_CommonInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_DPCH_InfoCommon{ cfnHandling maintain :NULL, modeSpecificInfo fdd:{ dl_DPCH_PowerControlInfo { modeSpecificInfo fdd:{ dpc_Mode singleTPC } }, powerOffsetPilot_pdpdch tsc_DPCH_PowerOffsetPILOT, dl_rate_matching_restriction OMIT, spreadingFactorAndPilot p_Sf, positionFixedOrFlexible fixed, tfci_Existence FALSE } }, modeSpecificInfo fdd:{ defaultDPCH_OffsetValue OMIT, dpch_CompressedModelInfo OMIT , tx_DiversityMode noDiversity, ssdt_Information OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PagingType2 (p_RRC_Ti: RRC_TransactionIdentifier; p_Domain : CN_DomainIdentity; p_PagingCause : PagingCause)
Group	:
ASN1 Type	: PagingType2
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { rrc_TransactionIdentifier p_RRC_Ti, pagingCause p_PagingCause, cn_DomainIdentity p_Domain, pagingRecordTypeID tmsi_GSM_MAP_P_TMSI , laterNonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM_57_6k (p_Reestimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RLC_Info: RLC_Info) Group : ASN1 Type : RAB_InformationSetupList Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_Reestimer }, rb_InformationSetupList {{ rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : p_RLC_Info, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListTM_64k (p_Reestimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RLC_Info: RLC_Info) Group : ASN1 Type : RAB_InformationSetupList Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> { { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_Reestimer }, rb_InformationSetupList {{ rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : p_RLC_Info, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 7 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }} } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RAB_InfoSetupTM_12_2k (p_ReEstTimer : Re_EstablishmentTimer ; p_RAB_Id : BITSTRING)
Group	:
ASN1 Type	: RAB_InformationSetup
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { rab_Info { rab_Identity gsm_MAP_RAB_Identity: p_RAB_Id, cn_DomainIdentity cs_domain, re_EstablishmentTimer p_ReEstTimer }, rb_InformationSetupList {{ --RB_InformationSetupList rb_Identity tsc_RB10, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ --RB_MappingOption ul_LogicalChannelMappings oneLogicalChannel : { ul_TransportChannelType dch : tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch : tsc_DL_DCH1, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB11, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH2, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH2, logicalChannelIdentity OMIT }} }} }, { rb_Identity tsc_RB12, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : c_RLC_InfoTM_Def, rb_MappingInfo {{ ul_LogicalChannelMappings oneLogicalChannel:{ ul_TransportChannelType dch: tsc_UL_DCH3, logicalChannelIdentity OMIT, rlc_SizeList configured :NULL, mac_LogicalChannelPriority 6 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch: tsc_DL_DCH3, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>logicalChannelIdentity OMIT }} }} }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_ReEstTimerT314
Group	:
ASN1 Type	: Re_EstablishmentTimer
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
useT314	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoTM_Def
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_TM_RLC_Mode:{ transmissionRLC_Discard OMIT, segmentationIndication FALSE }, dl_RLC_Mode dl_TM_RLC_Mode:{ segmentationIndication FALSE } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_11_12_13_23_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation OMIT }, { ctfc6 1, powerOffsetInformation OMIT }, { ctfc6 11, powerOffsetInformation OMIT }, { ctfc6 12, powerOffsetInformation OMIT }, { ctfc6 13, powerOffsetInformation OMIT }, { ctfc6 23, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_11_12_13_23_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling : complete : { ctfcSize ctfc6Bit:{ { ctfc6 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 11, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 12, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 13, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc6 23, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To9_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_DL_2_0To9 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation :PowerOffsetInformation)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5,6 ,7, 8, 9
Constraint Value	
<pre>{ dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrCHInfo_UL_2_0To9 (p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: With CTFC list : 0, 1, 2, 3, 4, 5, 6, 7, 8, 9
Constraint Value	
<pre>{ ulconnectedTrCHList {{ trchid tsc_UL_DCH1, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To9_Rx }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_122_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_81_TFS_DL }, { trchid tsc_DL_DCH2, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_DL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL } }, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrChInfoDL_336_148
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_336_TFS}, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL}}, dlTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoHigher64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_640_148
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH1, transportChannelInfo c_DCH_640_TFS}, { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TFS_DL}}, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (c_PowerOffsetInfoHigher64k) -- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_122_AMR
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_81_TFS }, { trchid tsc_UL_DCH2, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_UL_DCH3, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx -- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrChInfoUL_336_148
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannellInfo c_DCH_336_TFS }, { trchid tsc_UL_DCH5, transportChannellInfo c_DCH_148_TFS_UL }}, ulTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx --- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_576_148
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannellInfo c_DCH_576_TFS }, { trchid tsc_UL_DCH5, transportChannellInfo c_DCH_148_TFS_UL }}, ulTFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx --- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_640_148
Group	:
ASN1 Type	: TrChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ulconnectedTrCHList { { trchid tsc_UL_DCH1, transportChannelInfo c_DCH_640_TFS }, { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TFS_UL }}, ulTFCS c_TFCS_Cmpl0_1_2_3_Rx -- sent to SS }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_2_Multiplex_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB22 } } }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_2_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { {trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } }}, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured: NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> rB_Identity tsc_RB4 } }}} </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_4DCCH_1DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 7 }, rB_Identity tsc_RB10 } }, }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, }, }, } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrLogMappingDL_4DCCH_1DTCH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } }, }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, }, }, } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_4DCCH_3DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB10 } } }, { trchid tsc_DL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH2, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB11 } } }, { trchid tsc_DL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DTCH3, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB12 } } }, { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingFACH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH2, and DTCH to FACH1. Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingPCH2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only. map PCCH to PCH Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH.
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType pch, logicalChannelIdentity tsc_PCCH1, logicalChannelType pCCH , rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_PCCH2 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_2_Multiplex_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB20 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2, logicalChannelType dTCH }, rB_Identity tsc_RB22 } } }}, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2 , logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3 , logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader , ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>logicalChannelType dCCH }, rB_Identity tsc_RB4 } }} }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_4DCCH_1DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB10 } }, }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>} } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrLogMappingUL_4DCCH_1DTCH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB20 } }, }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>} } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_4DCCH_3DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH1, logicalChannelType dTCH }, rB_Identity tsc_RB10 } } }, { trchid tsc_UL_DCH2, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH2, logicalChannelType dTCH }, rB_Identity tsc_RB11 } } }, { trchid tsc_UL_DCH3, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DTCH3, logicalChannelType dTCH }, rB_Identity tsc_RB12 } } }, { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 } } } } } </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } }, dlconnectedTrCHList OMIT } </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListAM1 (p_TFS1 : DedicatedTransChTFS)
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
	Constraint Value
	<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS : p_TFS1 }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS : c_DCH_148_TFS_UE_UL }} </pre>
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_12_2k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS: c_DCH_81_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH2, transportFormatSet dedicatedTransChTFS: c_DCH_103_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH3, transportFormatSet dedicatedTransChTFS: c_DCH_60_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TFS_UE_UL } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_57_6k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS: c_DCH_576_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TFS_UE_UL } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListTM_64k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS: c_DCH_640_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TFS_UE_UL } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_12_2k
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_57_6k
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoBelow64k) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoTM_64k
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_2_3_Tx (c_PowerOffsetInfoHigher64k) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DL_DPCH_122_AMR (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { { secondaryScramblingCode p_SecondaryScramblingCode, sf_AndCodeNumber tsc_DL_DPCH1_ChC_Speech } }, tpc_CombinationIndex 0 }, powerOffsetOfTFCl_PO1 tsc_DPCH_PowerOffsetTFCl, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower tsc_DL_TxPower_DPCH, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_AICH_Info
Group	:
ASN1 Type	: AICH_Info
Derivation Path	:
Encoding Variation	:
Comments	: no transmission diversity, AICH timing = e0
Constraint Value	
<pre> { channelisationCode256 tsc_AICH1_ChC, sttd_Indicator FALSE, aich_TransmissionTiming e0 } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_DL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti40 :{{ tb_Size 148, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 170, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.2: TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_UE_UL
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti tti40 :{{ rlc_Size bitMode : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 170, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.2: TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size = TB_Size -4 =144 bits.(DCH, w/ MUX) coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TFS_UL
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti40 :{{ tb_Size 148, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 170, crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.2: TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 170	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TTI_10_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel
Constraint Value	
<pre> { tti tti10 :{{ tb_Size 148, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 192, -- dummy value, value is not needed for 13.6 kbps crc_Size crc16 } } </pre>	
Detailed Comments : TS 34.108 cl. 6.10.2.4.1.3: TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size =148 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 192 (dummy value, value is not needed for 13.6 kbps)	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_148_TTI_10_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for signalling bearer on dedicated channel used in message sent to UE
Constraint Value	
<pre> { tti10 :{{ rlc_Size bitMode : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional :third, rateMatchingAttribute 192, -- dummy value, value is not needed for 13.6 kbps crc_Size crc16 } } </pre>	
Detailed Comments	: TS 34.108 cl. 6.10.2.4.1.3: TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU size = TB_Size -4 =144 bits.(DCH, w/ MUX) coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 192

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DCH_336_TFS_UE
Group	:
ASN1 Type	: DedicatedTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for transport channel used in CreateCell_DCH_64kPS_RAB_SRB for message sent to UE
Constraint Value	
<pre> { tti tti20 :{ { rlc_Size octetModeType1 : sizeType2 : {part1 2, part2 OMIT}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2, small : 3, small : 4 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo: NULL, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: 3GPP TS 34.108 clause 6.10.2.4.1.24 TTI = 20 ms; 5 transport formats: TB size always=336 bits; TransportBlocks = 0,1,2,3 and 4 ; rlsc_Size = TB_Size (DCH, w/o MUX) coding = turbo; CRCsize = 16; RateMatching = 150

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfo (<p>p_DITrChId:TransportChannelIdentity;</p> <p>p_UITrChId:TransportChannelIdentity</p>)
Group	:
ASN1 Type	: DL_AddReconfTransChInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_TransportChannelType dch, dl_transportChannelIdentity p_DITrChId, tfs_SignallingMode sameAsULTrCH : { ul_TransportChannelType dch, ul_TransportChannelIdentity p_UITrChId }, dch_QualityTarget{ bler_QualityValue -20 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListDCCH_SRB
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_DL_AddReconfTransChInfo(tsc_DL_DCH5,tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_AddReconfTransChInfoListDCH_PS_64k
Group	:
ASN1 Type	: DL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_DL_AddReconfTransChInfo (tsc_DL_DCH1 , tsc_UL_DCH1), c_DL_AddReconfTransChInfo (tsc_DL_DCH5, tsc_UL_DCH5) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonInformationDCH_DPCH_Offset (p_Sf: SF512_AndPilot)
Group	:
ASN1 Type	: DL_CommonInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_DPCH_InfoCommon{ cfnHandling initialise : { cfnTargetsfnframeoffset OMIT }, modeSpecificInfo fdd:{ dl_DPCH_PowerControlInfo { modeSpecificInfo fdd:{ dpc_Mode singleTPC } }, powerOffsetPilot_pdpdch tsc_DPCH_PowerOffsetPILOT, dl_rate_matching_restriction OMIT, spreadingFactorAndPilot p_Sf, positionFixedOrFlexible flexible, tfci_Existence TRUE } }, modeSpecificInfo fdd:{ defaultDPCH_OffsetValue tsc_DefaultDPCH_OffsetValue, dpch_CompressedModelInfo OMIT, tx_DiversityMode noDiversity, ssdt_Information OMIT } } </pre>	
Detailed Comments	: NOTE : not aligned with 34.108. Reason:The value 'inactive' in 34.018 is not valid with asn.1 definition v360

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonInformationRB_SetUp (p_Sf: SF512_AndPilot)
Group	:
ASN1 Type	: DL_CommonInformation
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_DPCH_InfoCommon{ cfnHandling maintain :NULL, modeSpecificInfo fdd:{ dl_DPCH_PowerControlInfo { modeSpecificInfo fdd:{ dpc_Mode singleTPC } }, powerOffsetPilot_pdpdch tsc_DPCH_PowerOffsetPILOT, dl_rate_matching_restriction OMIT, spreadingFactorAndPilot p_Sf, positionFixedOrFlexible flexible, tfci_Existence TRUE } }, modeSpecificInfo fdd:{ defaultDPCH_OffsetValue OMIT, dpch_CompressedModelInfo OMIT , tx_DiversityMode noDiversity, ssdt_Information OMIT } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfoDCH(p_Tfcs:TFCS)
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { sccpch_TFCS OMIT, modeSpecificInfo fdd: { dl_Parameters dl_DCH_TFCS : p_Tfcs } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_CommonTransChInfoSameAsUL
Group	:
ASN1 Type	: DL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ sccpch_TFCS OMIT, modeSpecificInfo fdd:{ dl_Parameters sameAsUL: NULL } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_InfoPerRL_DPCH_Offset (p_ScrmbCode : PrimaryScramblingCode ; p_SecScrmbCode : SecondaryScramblingCode ; p_Sf : SF512_AndCodeNumber)
Group	:
ASN1 Type	: DL_InformationPerRL_List
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{{ modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_ScrmbCode } , pdsch_SHO_DCH_Info OMIT, pdsch_CodeMapping OMIT }, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 secondaryCPICH_Info OMIT, dl_ChannelisationCodeList {{ secondaryScramblingCode p_SecScrmbCode, sf_AndCodeNumber p_Sf, scramblingCodeChange noCodeChange }}, tpc_CombinationIndex tsc_TPC_CombinationIndex, ssdt_CellIdentity OMIT, closedLoopTimingAdjMode OMIT }, sccpch_InfoforFACH OMIT }}</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_DL_InformationPerRL (p_ScrmbCode: PrimaryScramblingCode; p_Sf: SF512_AndCodeNumber; p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_InformationPerRL_List
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ modeSpecificInfo fdd: { primaryCPICH_Info { primaryScramblingCode p_ScrmbCode }, pdsch_SHO_DCH_Info OMIT, pdsch_CodeMapping OMIT }, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 secondaryCPICH_Info OMIT, dl_ChannelisationCodeList {{ secondaryScramblingCode p_SecondaryScramblingCode , sf_AndCodeNumber p_Sf, scramblingCodeChange OMIT }}, tpc_CombinationIndex tsc_TPC_CombinationIndex, ssdt_CellIdentity OMIT, closedLoopTimingAdjMode OMIT }, sccpch_InfoforFACH OMIT }} </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti tti10 :{ { tb_Size 168, numberOfTbSizeList { zero : NULL, one : NULL, small : 2 }, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TransportBlock = 0, Size = 168. coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 220	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS_PS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH (PS) used for SS configuration
Constraint Value	
<pre> { tti tti10 :{ { tb_Size 360, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 360 bits; TransportBlock = 0, Size = 360. coding = turbo; CRCsize = 16; RateMatching = 130	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS_PS_UE
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH (PS)
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType2: 3}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType turbo : NULL, rateMatchingAttribute 130, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 360 bits; TansportBlock = 0, Size = 360. rlc_Size = TB_Size (FACH) coding = turbo; CRCsize = 16; RateMatching = 130

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FACH_TFS_UE
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for FACH
Constraint Value	
<pre> { tti10 : { { rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType1: 15}, numberOfTbSizeList { zero : NULL, one : NULL, small : 2}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 220, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; three transport formats: TransportBlocks = 2, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits; TransportBlock = 0, Size = 168. rlc_Size = TB_Size (FACH) coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 220	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_FreqInfo (p_uarfcnUL , p_uarfcnDL : UARFCN)
Group	:
ASN1 Type	: FrequencyInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { modeSpecificInfo fdd: { uarfcn_UL p_uarfcnUL , uarfcn_DL p_uarfcnDL } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_MAC_PagingCfg(p_E18: B18; p_dRX : INTEGER)
Group	:
ASN1 Type	: CmacPagingConfigReq
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ pl_BitMapInfo e18: p_E18, dRX_CycleLength p_dRX, iMSI o_ConvertIMSI(px_IMSI_Def), t_pich_T_sccpch FALSE }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_MIB_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: MasterInformationBlock
Derivation Path	:
Encoding Variation	:
Comments	: Default setting of MIB. 3GPP TS 34.123–3 clause 8.4.3
Constraint Value	
<pre> { mib_ValueTag 1, plmn_Type gsm_MAP : { plmn_Identity { mcc o_HexToDigitsMCC (p_CellInfo.mcc), mnc o_HexToDigitsMNC (p_CellInfo.mnc) } }, sibSb_ReferenceList { { sibSb_Type sysInfoTypeSB1 : 1, scheduling {scheduling { sib_Pos rep16 : 1 }} }, { sibSb_Type sysInfoType1 : 1, scheduling {scheduling { sib_Pos rep64 : 11 }} }, { sibSb_Type sysInfoType2 : 1, scheduling {scheduling { sib_Pos rep64 : 11 }} }, { sibSb_Type sysInfoType3 : 1, scheduling {scheduling { sib_Pos rep64 : 10 }} }, { sibSb_Type sysInfoType4 : 1, scheduling {scheduling { sib_Pos rep64 : 26 }} }, { sibSb_Type sysInfoType5 : 1, scheduling {scheduling { segCount 4, sib_Pos rep64 : 19, sib_PosOffsetInfo {so4, so2, so2} }} } } } </pre>	
Detailed Comments	<p>: Assum no segmentation for SIB1, SIB2, SIB3, SIB4.</p> <p>Contains scheduling informations for SIB1, SIB2, SIB3, SIB4, SIB5 and SB1 only, the scheduling information for other SIBs are in SysInfoTypeSB1.</p> <p>The value 1 of valueTags is a place holder. actual values of them will be non–zero and assigned dynamically in various SendSystemInformation test Steps.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PCH_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for PCH
Constraint Value	
<pre> { tti tti10 :{ { tb_Size 240, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 230, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 240 bits; TansportBlock = 0, Size = 240. coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 230	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PCH_TFS_UE
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for PCH
Constraint Value	
<pre> { tti tti10 :{ { rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType1: 24}, numberOfTbSizeList { zero : NULL, one : NULL}, logicalChannelList allSizes : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 230, crc_Size crc16 } } </pre>	
Detailed Comments : TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 240 bits; TansportBlock = 0, Size = 240. rlc_Size = TB_Size (PCH) coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 230	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PagingType1_P_TMSI (<p>p_PagCause : PagingCause;</p> <p>p_P_Tmsi : P_TMSI_GSM_MAP;</p> <p>p_Domain : CN_DomainIdentity)</p>
Group	:
ASN1 Type	: PagingType1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ pagingRecordList { cn_Identity:{ pagingCause p_PagCause, cn_DomainIdentity p_Domain, cn_pagedUE_Identity p_TMSI_GSM_MAP : p_P_Tmsi } }, bcch_ModificationInfo OMIT, laterNonCriticalExtensions OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PagingType1_TMSI (p_PagCause: PagingCause; p_Tmsi: TMSI_GSM_MAP; <p>p_Domain : CN_DomainIdentity)</p>
Group	:
ASN1 Type	: PagingType1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ pagingRecordList { cn_Identity:{ pagingCause p_PagCause, cn_DomainIdentity p_Domain, cn_pagedUE_Identity tmsi_GSM_MAP: p_Tmsi } }, bcch_ModificationInfo OMIT, laterNonCriticalExtensions OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_PichInfo
Group	:
ASN1 Type	: PICH_Info
Derivation Path	:
Encoding Variation	:
Comments	: value for pi_CountPerFrame FFS
Constraint Value	
<pre>fdd :{ channelisationCode256 tsc_PICH1_ChC, pi_CountPerFrame e18, sttd_Indicator FALSE }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : c_RAB_InfoListFACH_PS (p_Reestimer: Re_EstablishmentTimer; p_RAB_Id: BITSTRING; p_RLC_Info: RLC_Info) Group : ASN1 Type : RAB_InformationSetupList Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> { { rab_Info { -- RAB_Info rab_Identity gsm_MAP_RAB_Identity : p_RAB_Id, cn_DomainIdentity ps_domain, re_EstablishmentTimer p_Reestimer }, rb_InformationSetupList { --RB_InformationSetupList rb_Identity tsc_RB20, pdcp_Info OMIT, rlc_InfoChoice rlc_Info : p_RLC_Info, rb_MappingInfo { { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType dch : tsc_UL_DCH1, logicalChannelIdentity OMIT, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType dch : tsc_DL_DCH1, logicalChannelIdentity OMIT }} }, { --RB_MappingInfo ul_LogicalChannelMappings oneLogicalChannel:{ --UL_LogicalChannelMapping, ul_TransportChannelType rach : NULL, logicalChannelIdentity tsc_UL_DTCH1, rlc_SizeList explicitList : { { rlc_SizeIndex 2} }, mac_LogicalChannelPriority 8 }, dl_LogicalChannelMappingList {{ dl_TransportChannelType fach : NULL, logicalChannelIdentity tsc_DL_DTCH1 }} } } } } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RACH_TFS
Group	:
ASN1 Type	: CommonOrDedicatedTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RACH
Constraint Value	
<pre> { tti tti20 :{{ tb_Size 168, numberOfTbSizeList { one : NULL}, logicalChannelList configured : NULL }, { tb_Size 360, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; two transport formats: TransportBlocks = 1, TB size = 168 bits and TransportBlocks = 1, TB size = 360 bits; coding = convolutional; rate = 1/2; CRCsize = 16

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RACH_TFS_UE
Group	:
ASN1 Type	: CommonTransChTFS
Derivation Path	:
Encoding Variation	:
Comments	: transport format set for RACH used in message sent to UE
Constraint Value	
<pre> { tti tti20 :{{ rlc_Size fdd : {octetModeRLC_SizeInfoType2 sizeType1: 15}, numberOfTbSizeList { one : NULL}, logicalChannelList configured : NULL }, { rlc_Size fdd : { octetModeRLC_SizeInfoType2 sizeType2 : 3}, numberOfTbSizeList { one : NULL }, logicalChannelList configured : NULL }}, semistaticTF_Information { channelCodingType convolutional : half, rateMatchingAttribute 150, crc_Size crc16 } } </pre>	
Detailed Comments	: TTI = 20 ms; two transport formats: TransportBlocks = 1, TB size = 168 bits and TransportBlocks = 1, TB size = 360 bits; rlc_Size = TB_Size (RACH) coding = convolutional; rate = 1/2; CRCsize = 16; RateMatching = 1

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RB2_IntegrityProtActivationInfoList(p_RRCSN : RRC_MessageSequenceNumber)
Group	:
ASN1 Type	: IntegrityProtActivationInfoList
Derivation Path	:
Encoding Variation	:
Comments	: To Start Integrity on RB2 in UL
Constraint Value	
<pre> {{ rb_Identity tsc_RB2, rrc_MessageSequenceNumber p_RRCSN }} </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RB_ActTimeInfo(p_RB_Id: INTEGER; p_N: RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ rb_Identity p_RB_Id, rlc_SequenceNumber p_N }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RLC_InfoAM_Def
Group	:
ASN1 Type	: RLC_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ ul_RLC_Mode ul_AM_RLC_Mode: cb_UL_AM_RLC , dl_RLC_Mode dl_AM_RLC_Mode : cb_DL_AM_RLC }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_RestRB_IntegrityProtActivationInfoList(p_SN0, p_SN1, p_SN3, p_SN4: RRC_MessageSequenceNumber)
Group	:
ASN1 Type	: IntegrityProtActivationInfoList
Derivation Path	:
Encoding Variation	:
Comments	: To Start Integrity on RB2 in UL
Constraint Value	
<pre> {{ rb_Identity tsc_RB0, rrc_MessageSequenceNumber p_SN0 }, { rb_Identity tsc_RB1, rrc_MessageSequenceNumber p_SN1 }, { rb_Identity tsc_RB3, rrc_MessageSequenceNumber p_SN3 }, { rb_Identity tsc_RB4, rrc_MessageSequenceNumber p_SN4 }} </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SB1_Def
Group	:
ASN1 Type	: SysInfoTypeSB1
Derivation Path	:
Encoding Variation	:
Comments	: Default setting of SB1. scheduling is defined in 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre> { sib_ReferenceList { { sib_Type sysInfoType6 : 1, scheduling {scheduling { segCount 4, sib_Pos rep64 : 3, sib_PosOffsetInfo {so4, so2, so2} }} }, { sib_Type sysInfoType7 : NULL, scheduling {scheduling { sib_Pos rep16 : 2 }} }, { sib_Type sysInfoType11 : 1, scheduling { scheduling { segCount 3, sib_Pos rep64 : 29, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType12 : 1, scheduling { scheduling { segCount 3, sib_Pos rep64 : 13, sib_PosOffsetInfo {so2, so2} }} }, { sib_Type sysInfoType18 : 1, scheduling { scheduling { sib_Pos rep64 : 18 }} } }, nonCriticalExtensions OMIT } </pre>	
Detailed Comments	<p>: Assum no segmentation for SIB7.</p> <p>Contains scheduling informations for SIB6, SIB7, SIB11, SIB12, SIB18 only, the scheduling information for other SIBs may be added later.</p> <p>The value 1 of valueTags is a place holder. actual values of them will be non-zero and assigned dynamically in various SendSystemInformation test Steps.</p>

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SB1_Schedul1
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SB1 of one segment
Constraint Value	
<pre>{ scheduling { sib_Pos rep16 :1 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul1
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of one segment, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { sib_Pos rep64 :29 } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul2
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of two segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep64 :29, sib_PosOffsetInfo {so2} } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB11_Schedul3
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB11 of three segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep64 :29, sib_PosOffsetInfo {so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul1
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of one segment, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { sib_Pos rep64 :13 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul2
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of two segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep64 : 13, sib_PosOffsetInfo {so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB12_Schedul3
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB12 of three segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep64 :13, sib_PosOffsetInfo {so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB18_2PLMN (p_InterCellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType18
Derivation Path	:
Encoding Variation	:
Comments	: The Cell Info will be of the cell of other PLMN
Constraint Value	
<pre>{ idleModePLMNIdentities { plmnsOfIntraFreqCellsList OMIT, plmnsOfInterFreqCellsList {{ plmn_Identity { mcc o_HexToDigitsMCC (p_InterCellInfo.mcc), mnc o_HexToDigitsMNC (p_InterCellInfo.mnc) } }}, plmnsOfInterRATCellsList OMIT }, connectedModePLMNIdentities OMIT, nonCriticalExtensions OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB18_3PLMN (p_Inter1CellInfo, p_Inter2CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType18
Derivation Path	:
Encoding Variation	:
Comments	: The Cell Info will be of the one cell each of other PLMN's
Constraint Value	
<pre> { idleModePLMNIdentities { plmnsOfIntraFreqCellsList OMIT, plmnsOfInterFreqCellsList {{ plmn_Identity { mcc o_HexToDigitsMCC (p_Inter1CellInfo.mcc), mnc o_HexToDigitsMNC (p_Inter1CellInfo.mnc) } },{ plmn_Identity { mcc o_HexToDigitsMCC (p_Inter1CellInfo.mcc), mnc o_HexToDigitsMNC (p_Inter1CellInfo.mnc) } },{ plmn_Identity { mcc o_HexToDigitsMCC (p_Inter1CellInfo.mcc), mnc o_HexToDigitsMNC (p_Inter1CellInfo.mnc) } },{ plmn_Identity { mcc o_HexToDigitsMCC (p_Inter2CellInfo.mcc), mnc o_HexToDigitsMNC (p_Inter2CellInfo.mnc) } } }}, plmnsOfInterRATCellsList OMIT }, connectedModePLMNIdentities OMIT, nonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB18_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType18
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ idleModePLMNIdentities { plmnsOfIntraFreqCellsList OMIT, plmnsOfInterFreqCellsList OMIT, plmnsOfInterRATCellsList OMIT }, connectedModePLMNIdentities OMIT, nonCriticalExtensions OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB2_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType2
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 2
Constraint Value	
<pre>{ ura_IdentityList { p_CellInfo.ura_Identity }, nonCriticalExtensions OMIT }</pre>	
Detailed Comments	: for cell 1 and cell 2.

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul1
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of one segment, 3GPP TS 34.123–3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { sib_Pos rep64 :19 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul2
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of two segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep64 :19, sib_PosOffsetInfo {so4} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul3
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of three segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep64 :19, sib_PosOffsetInfo {so4, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB5_Schedul4
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB5 of four segments, 3GPP TS 34.123-3 clause 8.4.3
Constraint Value	
<pre>{ scheduling { segCount 4, sib_Pos rep64 :19, sib_PosOffsetInfo {so4, so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB6_Schedul1
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB6 of one segment
Constraint Value	
<pre>{ scheduling { sib_Pos rep64 : 3 } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB6_Schedul2
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB6 of two segments
Constraint Value	
<pre>{ scheduling { segCount 2, sib_Pos rep64 :3, sib_PosOffsetInfo {so4} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB6_Schedul3
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB6 of three segments
Constraint Value	
<pre>{ scheduling { segCount 3, sib_Pos rep64 :3, sib_PosOffsetInfo {so4, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB6_Schedul4
Group	:
ASN1 Type	: SchedulingInformation
Derivation Path	:
Encoding Variation	:
Comments	: SIB6 of four segments
Constraint Value	
<pre>{ scheduling { segCount 4, sib_Pos rep64 :3, sib_PosOffsetInfo {so4, so2, so2} } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SIB7_Def
Group	:
ASN1 Type	: SysInfoType7
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 7
Constraint Value	
<pre>{ modeSpecificInfo fdd : { ul_Interference -100 }, prach_Information_SIB5_List { 2 }, prach_Information_SIB6_List { 2 }, nonCriticalExtensions OMIT }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : c_SRB_InfoSetupAM_DCH (p_UL_IgchId : LogicalChannelIdentity; p_MAC_Igch_prtDCH , p_MAC_Igch_prtRACH : MAC_LogicalChannelPriority ; p_DL_IgchId : LogicalChannelIdentity) Group : ASN1 Type : SRB_InformationSetup Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> { rb_Identity OMIT, rlc_InfoChoice rlc_Info : { ul_RLC_Mode ul_AM_RLC_Mode : cd_UL_AM_RLC_SRB, dl_RLC_Mode dl_AM_RLC_Mode : cd_DL_AM_RLC_SRB }, rb_MappingInfo { { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH5, logicalChannelIdentity p_UL_IgchId, rlc_SizeList configured :NULL, mac_LogicalChannelPriority p_MAC_Igch_prtDCH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType dch: tsc_DL_DCH5, logicalChannelIdentity p_DL_IgchId }} }, { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType rach:NULL, logicalChannelIdentity p_UL_IgchId, rlc_SizeList explicitList : { { rlc_SizeIndex 1 } }, mac_LogicalChannelPriority p_MAC_Igch_prtRACH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType fach: NULL, logicalChannelIdentity p_DL_IgchId }} } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SRB_InfoSetupAM_FACH (p_RB_Id: RB_Identity; p_UL_IgchId: LogicalChannelIdentity; p_MAC_Igch_prtDCH , p_MAC_Igch_prt: MAC_LogicalChannelPriority; p_DL_IgchId: LogicalChannelIdentity)
Group	:
ASN1 Type	: SRB_InformationSetup
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { rb_Identity p_RB_Id, rlc_InfoChoice rlc_Info : { ul_RLC_Mode ul_AM_RLC_Mode: cd_UL_AM_RLC_SRB , dl_RLC_Mode dl_AM_RLC_Mode: cd_DL_AM_RLC_SRB }, rb_MappingInfo { { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType rach: NULL, logicalChannelIdentity p_UL_IgchId, rlc_SizeList explicitList : { { rlc_SizeIndex 1} }, mac_LogicalChannelPriority p_MAC_Igch_prt }, dl_LogicalChannelMappingList{{ dl_TransportChannelType fach: NULL, logicalChannelIdentity p_DL_IgchId }} }, { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH5, logicalChannelIdentity p_UL_IgchId, rlc_SizeList configured :NULL, mac_LogicalChannelPriority p_MAC_Igch_prtDCH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType dch: tsc_DL_DCH5, logicalChannelIdentity p_DL_IgchId }} } } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SRB_InfoSetupUM_DCH (p_UL_IgchId : LogicalChannelIdentity ; p_MAC_Igch_prtDCH, p_MAC_Igch_prtRACH : MAC_LogicalChannelPriority ; p_DL_IgchId : LogicalChannelIdentity)
Group	:
ASN1 Type	: SRB_InformationSetup
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { rb_Identity OMIT, rlc_InfoChoice rlc_Info : { ul_RLC_Mode ul_UM_RLC_Mode: { transmissionRLC_Discard OMIT }, dl_RLC_Mode dl_UM_RLC_Mode: NULL }, rb_MappingInfo { { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH5, logicalChannelIdentity p_UL_IgchId, rlc_SizeList configured :NULL, mac_LogicalChannelPriority p_MAC_Igch_prtDCH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType dch: tsc_DL_DCH5, logicalChannelIdentity p_DL_IgchId }} }, { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType rach:NULL, logicalChannelIdentity p_UL_IgchId, rlc_SizeList explicitList : { { rlc_SizeIndex 1 } }, mac_LogicalChannelPriority p_MAC_Igch_prtRACH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType fach: NULL, logicalChannelIdentity p_DL_IgchId }} } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_SRB_InfoSetupUM_FACH (p_RB_Id: RB_Identity; p_UL_IgchId: LogicalChannelIdentity; p_MAC_Igch_prtDCH , p_MAC_Igch_prt: MAC_LogicalChannelPriority; p_DL_IgchId: LogicalChannelIdentity)
Group	:
ASN1 Type	: SRB_InformationSetup
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { rb_Identity p_RB_Id, rlc_InfoChoice rlc_Info : { ul_RLC_Mode ul_UM_RLC_Mode: { transmissionRLC_Discard timerBasedNoExplicit : dt50 }, dl_RLC_Mode dl_UM_RLC_Mode: NULL }, rb_MappingInfo { { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType rach:NULL, logicalChannelIdentity p_UL_IgchId, rlc_SizeList explicitList : { { rlc_SizeIndex 1 } }, mac_LogicalChannelPriority p_MAC_Igch_prt }, dl_LogicalChannelMappingList{{ dl_TransportChannelType fach: NULL, logicalChannelIdentity p_DL_IgchId }} }, { ul_LogicalChannelMappings oneLogicalChannel: { ul_TransportChannelType dch: tsc_UL_DCH5, logicalChannelIdentity p_UL_IgchId, rlc_SizeList configured :NULL, mac_LogicalChannelPriority p_MAC_Igch_prtDCH }, dl_LogicalChannelMappingList{{ dl_TransportChannelType dch: tsc_DL_DCH5, logicalChannelIdentity p_DL_IgchId }} } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_StartListCS
Group	:
ASN1 Type	: STARTList
Derivation Path	:
Encoding Variation	:
Comments	: Start list with CS_Domain only
Constraint Value	
<pre>{ { cn_DomainIdentity cs_domain, start_Value '00000000000000000000'B } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0 (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation p_PowerOffsetInformation } } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT }, { ctfc4 4, powerOffsetInformation OMIT }, { ctfc4 5, powerOffsetInformation OMIT }, { ctfc4 6, powerOffsetInformation OMIT }, { ctfc4 7, powerOffsetInformation OMIT }, { ctfc4 8, powerOffsetInformation OMIT }, { ctfc4 9, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> normalTFCL_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 7, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 9, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_2_3_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation OMIT }, { ctfc4 1, powerOffsetInformation OMIT }, { ctfc4 2, powerOffsetInformation OMIT }, { ctfc4 3, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_2_3_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_Rx
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information without power offset information – for receiver
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation OMIT }, { ctfc2 1, powerOffsetInformation OMIT } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_Cmpl0_1_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc2Bit:{ { ctfc2 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc2 1, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TFCS_CmplFACH_Tx (p_PowerOffsetInformation : PowerOffsetInformation)
Group	:
ASN1 Type	: TFCS
Derivation Path	:
Encoding Variation	:
Comments	: TFCS information with power offset information – for transmitter
Constraint Value	
<pre> normalTFCl_Signalling: complete: { ctfcSize ctfc4Bit:{ { ctfc4 0, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 1, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 2, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 3, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 4, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 5, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 6, powerOffsetInformation c_PowerOffsetInfoComputed }, { ctfc4 8, powerOffsetInformation p_PowerOffsetInformation } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChConfigTypeDCH_NoSHO
Group	:
ASN1 Type	: TrChConfigType
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
dch : normal	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoBCH1
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_BCH1, transportChannelInfo { tti tti20 : {{tb_Size 246, numberOfTbSizeList {one : NULL}, logicalChannelList configured : NULL}}, semistaticTF_Information { channelCodingType convolutional :half, rateMatchingAttribute 1, crc_Size crc16 } } } }, dlTFCS c_TFCS_Cmpl0 (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoDL_13_6_StandAlone
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_DL_DCH5, transportChannelInfo c_DCH_148_TTI_10_TFS } }, dlTFCS c_TFCS_Cmpl0_1_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoPCH_FACH
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_PCH1, transportChannelInfo c_PCH_TFS }, { trchid tsc_FACH1, transportChannelInfo c_FACH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplFACH_Tx (c_PowerOffsetInfoBelow64k) --- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoPCH_FACH_PS
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS)
Constraint Value	
<pre> { dlconnectedTrCHList { { trchid tsc_PCH1, transportChannelInfo c_PCH_TFS }, { trchid tsc_FACH1, transportChannelInfo c_FACH_TFS }, { trchid tsc_FACH2, transportChannelInfo c_FACH_TFS_PS } }, dlTFCS c_TFCS_CmplFACH_Tx (c_PowerOffsetInfoBelow64k) --- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrChInfoUL_13_6_StandAlone
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH5, transportChannelInfo c_DCH_148_TTI_10_TFS }}, ulTFCS c_TFCS_Cmpl0_1_Rx --- sent to SS } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingBCH1
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_BCH1, trCH_LogCHMappingList {{ logicalChannel_Mapping dl_LogicalChannelMapping: { macHeaderManipulation normalMacHeader, dl_TransportChannelType bch, logicalChannelIdentity tsc_BCCH1, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_BCCH }} } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingDL_4DCCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_DL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType dch, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingPCH_FACH_CellDCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only. map PCCH to PCH and CCCH and BCCH(for BCCH_FACH) To be used for the Cell DCH configuration
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType pch, logicalChannelIdentity tsc_PCCH1, logicalChannelType pCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_PCCH } } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 } } } } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingPCH_FACH_PS
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	: For FDD mode only (PS). map PCCH to PCH and DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH2, and DTCH to FACH1
Constraint Value	
<pre> { ulconnectedTrCHList OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType pch, logicalChannelIdentity tsc_PCCH1, logicalChannelType pCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB_PCCH } } }, { trchid tsc_FACH1, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_BCCH6, logicalChannelType bCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 6 }, rB_Identity tsc_RB_BCCH_FACH }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_CCCH5, logicalChannelType cCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, rB_Identity tsc_RB0 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH1, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 1 }, </pre>	

Continued on next page

ASN.1 Type Constraint Declaration
Constraint Value
<pre> rB_Identity tsc_RB1 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH2, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 2 }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH3, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 3 }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DCCH4, logicalChannelType dCCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 4 }, rB_Identity tsc_RB4 } },{ trchid tsc_FACH2, trCH_LogCHMappingList { { logicalChannel_Mapping dl_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, dl_TransportChannelType fach, logicalChannelIdentity tsc_DL_DTCH1, logicalChannelType dTCH, rlc_SizeList configured : NULL, mac_LogicalChannelPriority 8 }, rB_Identity tsc_RB20 } } } } } } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingRACH_DTCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_RACH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_CCCH5, logicalChannelType cCCH }, rB_Identity tsc_RB0 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_DTCH1, </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
<pre>logicalChannelType dTCH }, rB_Identity tsc_RB20 } } } }, dlconnectedTrCHList OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_TrLogMappingUL_4DCCH
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_UL_DCH5, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH1, logicalChannelType dCCH }, rB_Identity tsc_RB1 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH2, logicalChannelType dCCH }, rB_Identity tsc_RB2 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH3, logicalChannelType dCCH }, rB_Identity tsc_RB3 }, { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType dch, logicalChannelIdentity tsc_UL_DCCH4, logicalChannelType dCCH }, rB_Identity tsc_RB4 } } } }, dlconnectedTrCHList OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UE_IdDefIMSI
Group	:
ASN1 Type	: InitialUE_Identity
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
imsi : o_ConvertIMSI(px_IMSI_Def)	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UE_Info (p_U_RNTI : U_RNTI ; p_CRNTI : BITSTRING)
Group	:
ASN1 Type	: UE_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
{ u_RNTI p_U_RNTI, c_RNTI p_CRNTI }	
Detailed Comments	: p_SRNCId: BITSTRING (SIZE (12)) p_SRNTI: BITSTRING (SIZE (20)) p_CRNTI: BITSTRING (SIZE (16))

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListDCCH_13_6k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TTI_10_TFS_UE }	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListDCCH_3_4k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TFS_UE_UL }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_AddReconfTransChInfoListDCH_PS_64k
Group	:
ASN1 Type	: UL_AddReconfTransChInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> {{ ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH1, transportFormatSet dedicatedTransChTFS: c_DCH_336_TFS_UE }, { ul_TransportChannelType dch, transportChannelIdentity tsc_UL_DCH5, transportFormatSet dedicatedTransChTFS: c_DCH_148_TFS_UE_UL }}</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoDCCH_13_6k
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_Tx (c_PowerOffsetInfoBelow64k) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_CommTrChInfoDCH_PS_64k
Group	:
ASN1 Type	: UL_CommonTransChInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ tfc_Subset OMIT, prach_TFCS OMIT, modeSpecificInfo fdd:{ ul_TFCS c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Tx (c_PowerOffsetInfoHigher64k) } }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_UL_DPCH_13_6_StandAlone (p_UL_ScramblingCode : UL_ScramblingCode)
Group	:
ASN1 Type	: UL_DPCH_Info
Derivation Path	:
Encoding Variation	:
Comments	: this DPCH is the same as stand-alone UL:13.6 DL:13.6 kbps SRBs for DCCH. Used for RLC AM and UM mode testing.
Constraint Value	
<pre> { ul_DPCH_PowerControlInfo fdd:{ dpcch_PowerOffset tsc_DPCCH_PowerOffset, pc_Preamble 1, sRB_delay 7, powerControlAlgorithm algorithm1: tsc_TpcStepSize }, modeSpecificInfo fdd :{ scramblingCodeType longSC , scramblingCode p_UL_ScramblingCode, numberOfDPDCH OMIT, spreadingFactor tsc_UL_DPDCH_SF_SRB, tfci_Existence TRUE, numberOfFBI_Bits OMIT, puncturingLimit pl1 } } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_U_RNTI
Group	:
ASN1 Type	: U_RNTI
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { srnc_Identity '000000000001'B, s_RNTI '00000000000000000001'B } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: c_U_RNTI_Def
Group	:
ASN1 Type	: U_RNTI
Derivation Path	:
Encoding Variation	:
Comments	: Default U_RNTI identity
Constraint Value	
<pre>{ srnc_Identity px_SRNC_Id, s_RNTI px_SRNTI }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DL_AM_RLC
Group	:
ASN1 Type	: DL_AM_RLC_Mode
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ inSequenceDelivery TRUE, receivingWindowSize rw128, dl_RLC_StatusInfo { timerStatusProhibit tsp200, --timerEPC OMIT missingPDU_Indicator TRUE, timerStatusPeriodic OMIT } }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : cb_DL_DPCH_64K_CS (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)	
Group :	
ASN1 Type : DL_DPCHInfo	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { {secondaryScramblingCode p_SecondaryScramblingCode , sf_AndCodeNumber tsc_DL_DPCH1_ChC_64k_CS } }, tpc_CombinationIndex 0 }, powerOffsetOfTFCl_PO1 tsc_DPCH_PowerOffsetTFCl, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower tsc_DL_TxPower_DPCH, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name : cb_DL_DPCH_64K_PS (p_DL_CommonInformation : DL_CommonInformation; p_SecondaryScramblingCode : SecondaryScramblingCode)	
Group :	
ASN1 Type : DL_DPCHInfo	
Derivation Path :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { dl_CommonInformation p_DL_CommonInformation, dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { {secondaryScramblingCode p_SecondaryScramblingCode , sf_AndCodeNumber tsc_DL_DPCH1_ChC_64k_PS } }, tpc_CombinationIndex 0 }, }, powerOffsetOfTFCl_PO1 tsc_DPCH_PowerOffsetTFCl, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower tsc_DL_TxPower_DPCH_64k, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_DL_DPCH_SRB_StandAloneDPCH_Offset (p_SecondaryScramblingCode : SecondaryScramblingCode)
Group	:
ASN1 Type	: DL_DPCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { dl_CommonInformation cd_DL_CommonInformationDCH_DPCH_Offset (tsc_DL_DPCH1_SFP_SRB), dl_DPCH_InfoPerRL fdd : { pCPICH_UsageForChannelEst mayBeUsed, dpch_FrameOffset (((tsc_DefaultDPCH_OffsetValue*512) MOD 38400) / 256), -- DPCH-FrameOffset = DefaultDPCH-OffsetValueFDD MOD 38400 -- Actual value DPCH-FrameOffset = IE value * 256 -- Actual value DefaultDPCH-OffsetValueFDD = IE value * 512 , dl_ChannelisationCodeList { { secondaryScramblingCode p_SecondaryScramblingCode , sf_AndCodeNumber tsc_DL_DPCH1_ChC_SRB } }, tpc_CombinationIndex 0 }, powerOffsetOfTFCL_PO1 tsc_DPCH_PowerOffsetTFCL, powerOffsetOfTPC_PO2 tsc_DPCH_PowerOffsetTPC, powerOffsetOfPILOT_PO3 tsc_DPCH_PowerOffsetPILOT, dl_TxPower tsc_DL_TxPower_DPCH, dl_TxPowerMax 15, dl_TxPowerMin -35 } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB11_Def (p_ActiveCellInfo, p_IntraCellInfo2, p_IntraCellInfo3, p_IntraCellInfo4, p_IntraCellInfo5, p_InterCellInfo6, p_InterCellInfo7, p_InterCellInfo8 : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType11
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 11. To be used by cell A,B,C,G and H: – 5 intra cells frequency of the same frequency – 3 inter cell frequency of the same frequency.
Constraint Value	
<pre> { sib12indicator TRUE, measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo { intraFreqMeasurementID OMIT, -- default value intraFreqCellInfoSI_List { removedIntraFreqCellList OMIT, -- removedIntraFreqCellList in SIB11 is not used and ignored by the UE newIntraFreqCellList { intraFreqCellID p_ActiveCellInfo.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_ActiveCellInfo.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT } }, }, { intraFreqCellID p_IntraCellInfo2.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo2.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { intraFreqCellID p_IntraCellInfo3.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo3.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { intraFreqCellID p_IntraCellInfo4.cellId, cellInfo { cellIndividualOffset OMIT, -- default value } } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo4.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { intraFreqCellID p_IntraCellInfo5.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo5.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }, intraFreqMeasQuantity { filterCoefficient OMIT, -- default value modeSpecificInfo fdd : { intraFreqMeasQuantity_FDD cpich_RSCP } }, reportingInfoForCellDCH { intraFreqReportingQuantity { activeSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } }, monitoredSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator TRUE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } }, measurementReportingMode { measurementReportTransferMode acknowledgedModeRLC, periodicalOrEventTrigger eventTrigger }, reportCriteria intraFreqReportingCriteria : { eventCriteriaList {{ event e1a : { triggeringCondition monitoredSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 w 1, reportDeactivationThreshold t2, reportingAmount ra4, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1b : { triggeringCondition activeSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 forbiddenAffectCellList OMIT, w 1}, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1c : { replacementActivationThreshold t3, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 } } } }, interFreqMeasurementSysInfo { interFreqCellInfoSI_List { removedInterFreqCellList OMIT, -- removedInterFreqCellList in SIB11 is not used and ignored by the UE newInterFreqCellList { { interFreqCellID p_InterCellInfo6.cellId, frequencyInfo p_InterCellInfo6.frequencyInfo, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo6.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo7.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo7.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }, </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre>{ interFreqCellID p_InterCellInfo8.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo8.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }}} -- nonCriticalExtensions OMIT }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB11_Freq2 (p_ActiveCellInfo, p_IntraCellInfo2, p_IntraCellInfo3, p_InterCellInfo4, p_InterCellInfo5, p_InterCellInfo6, p_InterCellInfo7, p_InterCellInfo8 : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType11
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 11. To be used by cell D,E,F: – 3 intra cells frequency of the same frequency – 5 inter cell frequency of the same frequency.
Constraint Value	
<pre> { sib12indicator TRUE, measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo { intraFreqMeasurementID OMIT, -- default value intraFreqCellInfoSI_List { removedIntraFreqCellList OMIT, -- removeNoIntraFreqCells in SIB11 is not used and ignored by the UE newIntraFreqCellList {{ intraFreqCellID p_ActiveCellInfo.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_ActiveCellInfo.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT } }, }, { intraFreqCellID p_IntraCellInfo2.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo2.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { intraFreqCellID p_IntraCellInfo3.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo3.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }, intraFreqMeasQuantity { filterCoefficient OMIT, -- default value modeSpecificInfo fdd : { </pre>	

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> intraFreqMeasQuantity_FDD cpich_RSCP } }, reportingInfoForCellDCH { intraFreqReportingQuantity { activeSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } }, monitoredSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator TRUE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } }, measurementReportingMode { measurementReportTransferMode acknowledgedModeRLC, periodicalOrEventTrigger eventTrigger }, reportCriteria intraFreqReportingCriteria : { eventCriteriaList {{ event e1a : { triggeringCondition monitoredSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 w 1, reportDeactivationThreshold t2, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1b : { triggeringCondition activeSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 forbiddenAffectCellList OMIT, w 1}, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1c : { replacementActivationThreshold t3, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 } } } </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> }} } }, interFreqMeasurementSysInfo { interFreqCellInfoSI_List { removedInterFreqCellList OMIT, newInterFreqCellList { { interFreqCellID p_InterCellInfo4.cellId, frequencyInfo p_InterCellInfo4.frequencyInfo, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo4.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo5.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo5.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo6.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo6.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo7.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo7.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo8.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo8.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } } }}} --nonCriticalExtensions OMIT } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB11_Freq3_PLMN1Or2 (p_ActiveCellInfo, p_IntraCellInfo2, p_IntraCellInfo3, p_InterCellInfo4, p_InterCellInfo5, p_InterCellInfo6, p_InterCellInfo7, p_InterCellInfo8 : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType11
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 11. To be used by cell D,E,F: – 3 intra cells frequency of the same frequency – 5 inter cell frequency of the same frequency.
Constraint Value	
<pre> { sib12indicator TRUE, measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo { intraFreqMeasurementID OMIT, -- default value intraFreqCellInfoSI_List { removedIntraFreqCellList OMIT, -- removeNoIntraFreqCells in SIB11 is not used and ignored by the UE newIntraFreqCellList {{ intraFreqCellID p_ActiveCellInfo.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_ActiveCellInfo.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT } }, }, { intraFreqCellID p_IntraCellInfo2.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo2.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { intraFreqCellID p_IntraCellInfo3.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo3.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }, intraFreqMeasQuantity { filterCoefficient OMIT, -- default value </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> modeSpecificInfo fdd : { intraFreqMeasQuantity_FDD cpich_RSCP } }, reportingInfoForCellDCH { intraFreqReportingQuantity { activeSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } }, monitoredSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator TRUE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } }, measurementReportingMode { measurementReportTransferMode acknowledgedModeRLC, periodicalOrEventTrigger eventTrigger }, reportCriteria intraFreqReportingCriteria : { eventCriteriaList {{ event e1a : { triggeringCondition monitoredSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 w 1, reportDeactivationThreshold t2, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1b : { triggeringCondition activeSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 forbiddenAffectCellList OMIT, w 1}, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1c : { replacementActivationThreshold t3, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 } } } } } } </pre>

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ASN.1 Type Constraint Declaration	
	Constraint Value
	<pre> } }} } }, interFreqMeasurementSysInfo { interFreqCellInfoSI_List { removedInterFreqCellList OMIT, newInterFreqCellList { { interFreqCellID p_InterCellInfo4.cellId, frequencyInfo p_InterCellInfo4.frequencyInfo, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo4.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo5.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo5.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo6.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo6.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo7.cellId, frequencyInfo p_InterCellInfo7.frequencyInfo, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo7.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE } } } } } } </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo8.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo8.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }}} -- nonCriticalExtensions OMIT } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB11_Freq3_PLMN3 (p_ActiveCellInfo, p_IntraCellInfo2, p_InterCellInfo3, p_InterCellInfo4, p_InterCellInfo5, p_InterCellInfo6, p_InterCellInfo7, p_InterCellInfo8 : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType11
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 11. To be used by cell D,E,F: – 3 intra cells frequency of the same frequency – 5 inter cell frequency of the same frequency.
Constraint Value	
<pre> { sib12indicator TRUE, measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo { intraFreqMeasurementID OMIT, -- default value intraFreqCellInfoSI_List { removedIntraFreqCellList OMIT, -- removeNoIntraFreqCells in SIB11 is not used and ignored by the UE newIntraFreqCellList { intraFreqCellID p_ActiveCellInfo.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_ActiveCellInfo.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT } }, }, { intraFreqCellID p_IntraCellInfo2.cellId, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_IntraCellInfo2.priScrmCode }, readSFN_Indicator TRUE, tx_DiversityIndicator FALSE }, }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }, intraFreqMeasQuantity { filterCoefficient OMIT, -- default value modeSpecificInfo fdd : { intraFreqMeasQuantity_FDD cpich_RSCP } }, reportingInfoForCellIDCH { intraFreqReportingQuantity { activeSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_NO_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } } } } } </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> }, monitoredSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator TRUE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } }, measurementReportingMode { measurementReportTransferMode acknowledgedModeRLC, periodicalOrEventTrigger eventTrigger }, reportCriteria intraFreqReportingCriteria : { eventCriteriaList {{ event e1a : { triggeringCondition monitoredSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 w 1, reportDeactivationThreshold t2, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1b : { triggeringCondition activeSetCellsOnly, reportingRange 10, -- Actual value ReportingRange = IE value * 0.5 forbiddenAffectCellList OMIT, w 1}, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 }, { event e1c : { replacementActivationThreshold t3, reportingAmount ra4, reportingInterval ri4 }, hysteresis 0, timeToTrigger ttt640, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e3 } }} } }, interFreqMeasurementSysInfo { interFreqCellInfoSI_List { removedInterFreqCellList OMIT, newInterFreqCellList { { interFreqCellID p_InterCellInfo3.cellId, frequencyInfo p_InterCellInfo3.frequencyInfo, cellInfo { </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo3.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo4.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo4.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo5.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo5.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo6.cellId, frequencyInfo p_InterCellInfo6.frequencyInfo, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo6.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo7.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo7.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE } } } </pre>

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } }, { interFreqCellID p_InterCellInfo8.cellId, frequencyInfo OMIT, cellInfo { cellIndividualOffset OMIT, -- default value referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_InterCellInfo8.priScrmCode }, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE }, cellSelectionReselectionInfo OMIT -- value same as the serving cell } } }}} -- nonCriticalExtensions OMIT } </pre>
Detailed Comments :

ASN.1 Type Constraint Declaration
Constraint Name : cb_SIB12_Def Group : ASN1 Type : SysInfoType12 Derivation Path : Encoding Variation : Comments : Default system information block type 12, used in connected mode. To be used by cell A,B,C,G and H: – 5 intra cells frequency of the same frequency – 3 inter cell frequency of the same frequency.
Constraint Value
<pre> { measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo OMIT, interFreqMeasurementSysInfo OMIT }, interRATMeasurementSysInfo OMIT } } } -- nonCriticalExtensions OMIT } </pre>
Detailed Comments : Similar to c_SIB11_def except that "detectedSetReportingQuantities" is not present and "timeToTrigger" = 0

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB12_Freq2
Group	:
ASN1 Type	: SysInfoType12
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 12, used in connected mode. To be used by cell D,E,F: – 3 intra cells frequency of the same frequency – 5 inter cell frequency of the same frequency.
Constraint Value	
<pre> { measurementControlSysInfo { use_of_HCS hcs_not_used : { cellSelectQualityMeasure cpich_RSCP : { intraFreqMeasurementSysInfo OMIT, interFreqMeasurementSysInfo OMIT }, interRATMeasurementSysInfo OMIT } } --nonCriticalExtensions OMIT } </pre>	
Detailed Comments	: Similar to c_SIB11_def except that "detectedSetReportingQuantities" is not present and "timeToTrigger" = 0

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB1_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { cn_CommonGSM_MAP_NAS_SysInfo p_CellInfo.lac, cn_DomainSysInfoList {{cn_DomainIdentity ps_domain, cn_Type gsm_MAP: o_OctetstringConcat (p_CellInfo.rac, p_CellInfo.nmo), cn_DRX_CycleLengthCoeff p_CellInfo.dRX_CycleLength.cN_PS_DRX_CycleLength }, {cn_DomainIdentity cs_domain, cn_Type gsm_MAP: o_OctetstringConcat (p_CellInfo.t3212, o_IntToOct (p_CellInfo.attFlag,1)), cn_DRX_CycleLengthCoeff p_CellInfo.dRX_CycleLength.cN_CS_DRX_CycleLength } }, ue_ConnTimersAndConstants { t_301 OMIT, n_301 OMIT, t_302 OMIT, n_302 OMIT, t_304 OMIT, n_304 OMIT, t_305 OMIT, t_307 OMIT, t_308 OMIT, t_309 OMIT, t_310 OMIT, n_310 OMIT, t_311 OMIT, t_312 OMIT, n_312 OMIT, t_313 OMIT, n_313 OMIT, t_314 OMIT, t_315 OMIT, n_315 OMIT, t_316 OMIT, t_317 OMIT }, ue_IdleTimersAndConstants { t_300 ms4000, n_300 tsc_N300, t_312 10, n_312 s1 }, v3a0NonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB3_DefUTRAN (p_CellInfoCfg : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType3
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 3 for UTRAN only
Constraint Value	
<pre> { sib4indicator TRUE, cellIdentity INT_TO_BIT (p_CellInfoCfg.cellId , 28) , cellSelectReselectInfo { mappingInfo OMIT, cellSelectQualityMeasure cpich_RSCP: NULL, modeSpecificInfo fdd : { s_Intrasearch 8, -- IE value * 2 s_Intersearch 8, -- IE value * 2 s_SearchHCS OMIT, rat_List OMIT, q_QualMin -24, q_RxlevMin -40 -- (IE value * 2) + 1 }, q_Hyst_I_S 1, -- (IE value*2) t_Reselection_S 0, hcs_ServingCellInformation OMIT, maxAllowedUL_TX_Power 21 }, cellAccessRestriction { cellBarred notBarred : NULL, cellReservedForOperatorUse notReserved, cellReservationExtension notReserved, accessClassBarredList { notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred } } -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB3_DefUTRAN_GERAN (p_CellInfoCfg : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType3
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 3 for UTRAN/GERAN
Constraint Value	
<pre> { sib4indicator TRUE, cellIdentity INT_TO_BIT (p_CellInfoCfg.cellId ,28) , cellSelectReselectInfo { mappingInfo OMIT, cellSelectQualityMeasure cpich_RSCP : NULL, modeSpecificInfo fdd : { s_Intrasearch 8, -- IE value * 2 s_Intersearch 8, -- IE value * 2 s_SearchHCS OMIT, rat_List {{ rat_Identifier gsm, s_SearchRAT -16, s_HCS_RAT OMIT, s_Limit_SearchRAT 0 }}, q_QualMin -24, q_RxlevMin -40 -- (IE value * 2) + 1 }, q_Hyst_I_S 1, -- (IE value*2) t_Reselection_S 0, hcs_ServingCellInformation OMIT, maxAllowedUL_TX_Power 21 }, cellAccessRestriction { cellBarred notBarred : NULL, cellReservedForOperatorUse notReserved, cellReservationExtension notReserved, accessClassBarredList { notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred, notBarred } } -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB4_DefUTRAN (p_CellInfoCfg : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType4
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 4 for UTRAN only, used in connected mode.
Constraint Value	
<pre> { cellIdentity INT_TO_BIT (p_CellInfoCfg.cellId ,28) , cellSelectReselectInfo { mappingInfo OMIT, cellSelectQualityMeasure cpich_RSCP : NULL, modeSpecificInfo fdd : { s_Intrasearch 8, -- IE value * 2 s_Intersearch 8, -- IE value * 2 s_SearchHCS OMIT, rat_List OMIT, q_QualMin -24, q_RxlevMin -40 -- (IE value * 2) + 1 }, q_Hyst_I_S 1, -- (IE value*2) t_Reselection_S 0, hcs_ServingCellInformation OMIT, maxAllowedUL_TX_Power 21 }, cellAccessRestriction { cellBarred notBarred : NULL, cellReservedForOperatorUse notReserved, cellReservationExtension notReserved, accessClassBarredList OMIT } -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB4_DefUTRAN_GERAN (p_CellInfoCfg : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType4
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 4 for UTRAN/GERAN, used in connected mode.
Constraint Value	
<pre> { cellIdentity INT_TO_BIT (p_CellInfoCfg.cellId ,28) , cellSelectReselectInfo { mappingInfo OMIT, cellSelectQualityMeasure cpich_RSCP : NULL, modeSpecificInfo fdd : { s_Intrasearch 8, -- IE value * 2 s_Intersearch 8, -- IE value * 2 s_SearchHCS OMIT, rat_List {{ rat_Identifier gsm, s_SearchRAT -16, s_HCS_RAT OMIT, s_Limit_SearchRAT 0 }}, q_QualMin -24, q_RxlevMin -40 -- (IE value * 2) + 1 }, q_Hyst_I_S 1, -- (IE value*2) t_Reselection_S 0, hcs_ServingCellInformation OMIT, maxAllowedUL_TX_Power 21 }, cellAccessRestriction { cellBarred notBarred : NULL, cellReservedForOperatorUse notReserved, cellReservationExtension notReserved, accessClassBarredList OMIT } -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB5_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType5
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 5
Constraint Value	
<pre> { sib6indicator TRUE, pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList {{ prach_RACH_Info { modeSpecificInfo fdd : { availableSignatures tsc_PRACH1_Signatures, availableSF tsc_PRACH1_SF, preambleScramblingCodeWordNumber tsc_PRACH1_ScrC, puncturingLimit pl1, availableSubChannelNumbers '111111111111'B } }, transportChannelIdentity tsc_RACH1, rach_TransportFormatSet commonTransChTFS : c_RACH_TFS_UE, rach_TFCS normalTFCl_Signalling : complete : { ctfcSize ctfc2Bit : {{ ctfc2 0, powerOffsetInformation { gainFactorInformation computedGainFactors : 0, powerOffsetPp_m 0 } }, { ctfc2 1, powerOffsetInformation { gainFactorInformation signalledGainFactors : { modeSpecificInfo fdd : { gainFactorBetaC 11 }, gainFactorBetaD 15, referenceTFC_ID 0 }, powerOffsetPp_m 0 } } }, prach_Partitioning fdd : {{ accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, </pre>	

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ASN.1 Type Constraint Declaration
Constraint Value
<pre> assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }, { accessServiceClass_FDD OMIT }, { accessServiceClass_FDD { availableSignatureStartIndex 0, availableSignatureEndIndex 7, assignedSubChannelNumber '1111'B } }}, persistenceScalingFactorList { psf0_9, psf0_9, psf0_9, psf0_9, psf0_9, psf0_9 }, ac_To_ASC_MappingTable { 6, 5, 4, 3, 2, 1, 0 }, modeSpecificInfo fdd : { primaryCPICH_TX_Power 31, constantValue -10, prach_PowerOffset { powerRampStep 3, -- db preambleRetransMax 4 }, rach_TransmissionParameters { mmax 2, nb01Min 3, nb01Max 10 }, aich_Info { channelisationCode256 tsc_AICH1_ChC, sttd_Indicator FALSE, aich_TransmissionTiming e0 } } }}, sCCPCH_SystemInformationList {{ secondaryCCPCH_Info { modeSpecificInfo fdd : { dummy1 mayBeUsed, -- mandatory ie secondaryScramblingCode OMIT, sttd_Indicator FALSE, sf_AndCodeNumber tsc_S_CCPCH1_ChC, pilotSymbolExistence FALSE, tfci_Existence TRUE, positionFixedOrFlexible flexible, timingOffset 0 } }, tfcs normalTFCl_Signalling : complete: {ctfcSize ctfc4Bit : { {ctfc4 0 }, {ctfc4 1 }, {ctfc4 2 }, {ctfc4 3 }, {ctfc4 4 }, {ctfc4 5 }, {ctfc4 6 }, {ctfc4 8}}}, fach_PCH_InformationList { { transportFormatSet commonTransChTFS : c_PCH_TFS_UE, transportChannelIdentity tsc_PCH1, -- PCH </pre>

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ASN.1 Type Constraint Declaration	
Constraint Value	
<pre> ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_UE, transportChannelIdentity tsc_FACH1, -- FACH ctch_Indicator FALSE }, { transportFormatSet commonTransChTFS : c_FACH_TFS_PS_UE, transportChannelIdentity tsc_FACH2, -- FACH ctch_Indicator FALSE } }, pich_Info fdd :{ channelisationCode256 tsc_PICH1_ChC, pi_CountPerFrame e18, sttd_Indicator FALSE } }}, cbs_DRX_Level1Information OMIT -- nonCriticalExtensions OMIT } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_SIB6_Def (p_CellInfo : CellInfoCfg)
Group	:
ASN1 Type	: SysInfoType6
Derivation Path	:
Encoding Variation	:
Comments	: Default system information block type 6, used in connected mode.
Constraint Value	
<pre> { pich_PowerOffset p_CellInfo.powerPICH, modeSpecificInfo fdd : { aich_PowerOffset p_CellInfo.powerAICH }, primaryCCPCH_Info OMIT, prach_SystemInformationList OMIT, sCCPCH_SystemInformationList OMIT, cbs_DRX_Level1Information OMIT } </pre>	
Detailed Comments : Similar to cb_SIB5_Def, except "AC-to-ASC mapping table" not present	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrChInfoRACH1
Group	:
ASN1 Type	: TrCHInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_RACH1, transportChannelInfo c_RACH_TFS } }, ulTFCS c_TFCS_Cmpl0_1_Rx, -- sent to SS dlconnectedTrCHList OMIT, dlTFCS OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_TrLogMappingRACH2
Group	:
ASN1 Type	: TrCH_LogCHMappingList1
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ulconnectedTrCHList { { trchid tsc_RACH1, trCH_LogCHMappingList { { logicalChannel_Mapping ul_LogicalChannelMapping : { macHeaderManipulation normalMacHeader, ul_TransportChannelType rach, logicalChannelIdentity tsc_UL_CCCH5, logicalChannelType cCCH }, rB_Identity tsc_RB0 } } } }, dlconnectedTrCHList OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_UL_AM_RLC
Group	:
ASN1 Type	: UL_AM_RLC_Mode
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { transmissionRLC_Discard noDiscard : dat15, transmissionWindowSize tw128, timerRST tr500, max_RST rst4, pollingInfo { timerPollProhibit tpp200, timerPoll tp200, poll_PDU OMIT, poll_SDU sdu1, lastTransmissionPDU_Poll TRUE, lastRetransmissionPDU_Poll TRUE, pollWindow pw99, timerPollPeriodic OMIT } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cb_UL_DPCH_Info (p_SprdFct: SpreadingFactor; p_PuncLimit: PuncturingLimit; p_UL_ScramblingCode : UL_ScramblingCode)
Group	:
ASN1 Type	: UL_DPCH_Info
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { ul_DPCH_PowerControlInfo fdd:{ dpcch_PowerOffset tsc_DPCCH_PowerOffset, pc_Preamble 1, sRB_delay 7, powerControlAlgorithm algorithm1: tsc_TpcStepSize }, modeSpecificInfo fdd :{ scramblingCodeType longSC , scramblingCode p_UL_ScramblingCode , numberOfDPDCH OMIT, spreadingFactor p_SprdFct, tfci_Existence TRUE, numberOfFBI_Bits OMIT, puncturingLimit p_PuncLimit } } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_AM_RLC_SRB
Group	:
ASN1 Type	: DL_AM_RLC_Mode
Derivation Path	: cb_DL_AM_RLC.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE receivingWindowSize BY rw32	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_DL_CommonInformationDCH_DPCH_Offset (p_Sf: SF512_AndPilot)
Group	:
ASN1 Type	: DL_CommonInformation
Derivation Path	: c_DL_CommonInformationDCH_DPCH_Offset.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE dl_DPCH_InfoCommon.modeSpecificInfo.fdd.positionFixedOrFlexible BY fixed, REPLACE dl_DPCH_InfoCommon.modeSpecificInfo.fdd.tpci_Existence BY FALSE	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cd_UL_AM_RLC_SRB
Group	:
ASN1 Type	: UL_AM_RLC_Mode
Derivation Path	: cb_UL_AM_RLC.
Encoding Variation	:
Comments	:
Constraint Value	
REPLACE transmissionWindowSize BY tw32, REPLACE max_RST BY rst1	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_CipheringModeCmdOn (p_CipheringAlgorithm : CipheringAlgorithm)
Group	:
ASN1 Type	: CipheringModeCommand
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
startRestart : p_CipheringAlgorithm	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_IntegrityCheckInfo0
Group	:
ASN1 Type	: IntegrityCheckInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
{ messageAuthenticationCode tsc_MessAuthCode, rrc_MessageSequenceNumber tsc_MSN }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_IntegrityProtectModify_P(p1,p2,p3,p4,p5 : RRC_MessageSequenceNumber)
Group	:
ASN1 Type	: IntegrityProtectionModelInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
{ integrityProtectionModeCommand modify : { dl_IntegrityProtActivationInfo { rrc_MessageSequenceNumberList {p1,p2,p3,p4,p5 } -- fixed reasonable value } }, integrityProtectionAlgorithm uia1 }	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_IntegrityProtectStart (p_Integrityprotnumber: BITSTRING)
Group	:
ASN1 Type	: IntegrityProtectionModelInfo
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityProtectionModeCommand startIntegrityProtection :{ integrityProtInitNumber p_Integrityprotnumber }, integrityProtectionAlgorithm uia1 }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_Null_CipheringModeCommand
Group	:
ASN1 Type	: CipheringModeCommand
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
dummy : NULL	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoList20 (p_RLC_SN20 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo(tsc_RB20, p_RLC_SN20) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoList20_21(p_RLC_SN20, p_RLC_SN21 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo(tsc_RB20, p_RLC_SN20), c_RB_ActTimeInfo(tsc_RB21, p_RLC_SN21) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoList20_22(p_RLC_SN20, p_RLC_SN22 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo(tsc_RB20, p_RLC_SN20), c_RB_ActTimeInfo(tsc_RB22, p_RLC_SN22) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoList20_24(p_RLC_SN20, p_RLC_SN24 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo(tsc_RB20, p_RLC_SN20), c_RB_ActTimeInfo(tsc_RB24, p_RLC_SN24) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoList21(p_RLC_SN21 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo(tsc_RB21, p_RLC_SN21) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoListSRBs (p_RLC_SN1, p_RLC_SN2, p_RLC_SN3, p_RLC_SN4 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo (tsc_RB1, p_RLC_SN1) , c_RB_ActTimeInfo (tsc_RB2, p_RLC_SN2) , c_RB_ActTimeInfo (tsc_RB3, p_RLC_SN3) , c_RB_ActTimeInfo (tsc_RB4, p_RLC_SN4) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoListSRBs_20 (p_RLC_SN1, p_RLC_SN2, p_RLC_SN3, p_RLC_SN4, p_RLC_SN20 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo (tsc_RB1, p_RLC_SN1) , c_RB_ActTimeInfo (tsc_RB2, p_RLC_SN2) , c_RB_ActTimeInfo (tsc_RB3, p_RLC_SN3) , c_RB_ActTimeInfo (tsc_RB4, p_RLC_SN4) , c_RB_ActTimeInfo (tsc_RB20, p_RLC_SN20) }</pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoListSRBs_20_21 (p_RLC_SN1, p_RLC_SN2, p_RLC_SN3, p_RLC_SN4, p_RLC_SN20, p_RLC_SN21 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo (tsc_RB1, p_RLC_SN1) , c_RB_ActTimeInfo (tsc_RB2, p_RLC_SN2) , c_RB_ActTimeInfo (tsc_RB3, p_RLC_SN3) , c_RB_ActTimeInfo (tsc_RB4, p_RLC_SN4) , c_RB_ActTimeInfo (tsc_RB20, p_RLC_SN20) , c_RB_ActTimeInfo (tsc_RB21, p_RLC_SN21) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoListSRBs_20_22 (p_RLC_SN1, p_RLC_SN2, p_RLC_SN3, p_RLC_SN4, p_RLC_SN20, p_RLC_SN22 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ c_RB_ActTimeInfo (tsc_RB1, p_RLC_SN1) , c_RB_ActTimeInfo (tsc_RB2, p_RLC_SN2) , c_RB_ActTimeInfo (tsc_RB3, p_RLC_SN3) , c_RB_ActTimeInfo (tsc_RB4, p_RLC_SN4) , c_RB_ActTimeInfo (tsc_RB20, p_RLC_SN20) , c_RB_ActTimeInfo (tsc_RB22, p_RLC_SN22) }</pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RB_ActTimeInfoListSRBs_21 (p_RLC_SN1, p_RLC_SN2, p_RLC_SN3, p_RLC_SN4, p_RLC_SN21 : RLC_SequenceNumber)
Group	:
ASN1 Type	: RB_ActivationTimeInfoList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { c_RB_ActTimeInfo (tsc_RB1, p_RLC_SN1) , c_RB_ActTimeInfo (tsc_RB2, p_RLC_SN2) , c_RB_ActTimeInfo (tsc_RB3, p_RLC_SN3) , c_RB_ActTimeInfo (tsc_RB4, p_RLC_SN4) , c_RB_ActTimeInfo (tsc_RB21, p_RLC_SN21) } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name : cs_RRC_SecModeCmdCiphInt (<p> p_RRC_Ti : RRC_TransactionIdentifier; p_CipheringModeCommand : CipheringModeCommand; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList ; p_ActTimeDPCH : INTEGER; p_cn_domain : CN_DomainIdentity; p_integrityMode : IntegrityProtectionModelInfo; p_SecurityCapability:BITSTRING; p_SystemSpecCap: InterRAT_UE_SecurityCapList) </p> Group : ASN1 Type : SecurityModeCommand Derivation Path : Encoding Variation : Comments :	
Constraint Value	
<pre> r3 : { securityModeCommand_r3 { rrc_TransactionIdentifier p_RRC_Ti, securityCapability { cipheringAlgorithmCap p_SecurityCapability, integrityProtectionAlgorithmCap tsc_IntegrProtAlgCap }, cipheringModelInfo { cipheringModeCommand p_CipheringModeCommand, activationTimeForDPCH p_ActTimeDPCH, rb_DL_CiphActivationTimeInfo p_RB_ActivationTimeInfoList }, integrityProtectionModelInfo p_integrityMode, cn_DomainIdentity p_cn_domain, ue_SystemSpecificSecurityCap p_SystemSpecCap }, laterNonCriticalExtensions OMIT } </pre>	
Detailed Comments :	

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_RRC_SecModeCmdInt (p_RRC_Ti: RRC_TransactionIdentifier; p_cn_domain: CN_DomainIdentity; p_integrityMode : IntegrityProtectionModelInfo; p_SecurityCapability:BITSTRING; p_SystemSpecCap: InterRAT_UE_SecurityCapList)
Group	:
ASN1 Type	: SecurityModeCommand
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> r3 : { securityModeCommand_r3 { rrc_TransactionIdentifier p_RRC_Ti, securityCapability { cipheringAlgorithmCap p_SecurityCapability, integrityProtectionAlgorithmCap tsc_IntegrProtAlgCap }, cipheringModelInfo OMIT, integrityProtectionModelInfo p_integrityMode, cn_DomainIdentity p_cn_domain, ue_SystemSpecificSecurityCap p_SystemSpecCap }, laterNonCriticalExtensions OMIT } </pre>	
Detailed Comments	:

ASN.1 Type Constraint Declaration	
Constraint Name	: cs_UE_SysSpecCap(p_SysSpecCap : BITSTRING)
Group	:
ASN1 Type	: InterRAT_UE_SecurityCapList
Derivation Path	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre> { gsm : { gsmSecurityCapability p_SysSpecCap }} </pre>	
Detailed Comments	:

ASP Constraint Declaration		
Constraint Name : car_RLC_DataInd(p_CellId: INTEGER; p_RB_Identity : SS_RB_Identity; p_PDU: PDU)		
Group :		
ASP Type : RLC_TR_TestDataInd		
Derivation Path :		
Comments : This constraint is used to receive a data PDU for RB test cases		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	RB identity (RB3 or RB4)
rB_Id	p_RB_Identity	
data	p_PDU	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : cas_RLC_DataReq(p_CellId: INTEGER; p_RB_Identity : SS_RB_Identity; p_PDU: PDU)		
Group :		
ASP Type : RLC_TR_TestDataReq		
Derivation Path :		
Comments : This constraint is used to send a data PDU fro RB test cases.		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	RB identity (RB3 or RB4)
rB_Id	p_RB_Identity	
data	p_PDU	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_AT_CmdCnf		
Group :		
ASP Type : AT_CmdCnf		
Derivation Path :		
Comments : The ASP is used get a positive result only for a requested command to the UT (UT →LT).		
Parameter Name	Parameter Value	Comments
result	TRUE IF_PRESENT	Defined for future development
resultString	tsc_AT_ResultOK IF_PRESENT	
sMS_BlockMode	–	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_AT_CmdReq (p_Cmd : IA5String)		
Group :		
ASP Type : AT_CmdReq		
Derivation Path :		
Comments : The ASP is used to request a command to the UT (LT →UT).		
Parameter Name	Parameter Value	Comments
cmd	p_Cmd	Defined for future development
sMS_BlockMode	–	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_DataReq (p_CellId : INTEGER; p_Rb : SS_RB_Identity ; p_Pdu : PDU)		
Group :		
ASP Type : RRC_DataReq		
Derivation Path :		
Comments : The ASP is used to request the transmission of the NAS PDU message using acknowledged operation (NAS → RRC).		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	GERAN only GERAN only
rB_Id	p_Rb	
ch	–	
sapId	–	
cN_Domain	tsc_SS_CS_Domain	
msg	p_Pdu	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_MMI_CmdCnf		
Group :		
ASP Type : MMI_CmdCnf		
Derivation Path :		
Comments : The ASP is used get the result of a requested MMI command to the UT (UT →LT).		
Parameter Name	Parameter Value	Comments
result	TRUE	
resultString	*	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_MMI_CmdReq (p_Cmd : IA5String)		
Group :		
ASP Type : MMI_CmdReq		
Derivation Path :		
Comments : The ASP is used to request a MMI command to the UT (LT →UT).		
Parameter Name	Parameter Value	Comments
cmd	p_Cmd	command line
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : ca_PS_DataReq (p_CellId : INTEGER; p_Rb : SS_RB_Identity ; p_Pdu : PDU)		
Group :		
ASP Type : RRC_DataReq		
Derivation Path :		
Comments :		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	GERAN only
rB_Id	p_Rb	
ch	–	
sapId	–	
cN_Domain	tsc_SS_PS_Domain	
msg	p_Pdu	GERAN only
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_InitDirectTransfer (p_CellId: INTEGER; p_Rb : SS_RB_Identity; p_Pdu : PDU)		
Group :		
ASP Type : RRC_DataInd		
Derivation Path :		
Comments : The ASP is used to indicate the receipt of the NAS PDU message using acknowledged operation (NAS ← RRC).		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	GERAN only GERAN only
rB_Id	p_Rb	
ch	–	
sapId	–	
cN_Domain	tsc_SS_CS_Domain	
start	?	
msg	p_Pdu	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_PS_InitDirectTransfer (p_CellId: INTEGER; p_Rb :SS_RB_Identity; p_Pdu : PDU)		
Group :		
ASP Type : RRC_DataInd		
Derivation Path :		
Comments : The ASP is used to indicate the receipt of the NAS PDU message using acknowledged operation (NAS <- RRC).		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	GERAN only GERAN only
rB_Id	p_Rb	
ch	–	
sapId	–	
cN_Domain	tsc_SS_PS_Domain	
start	?	
msg	p_Pdu	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_PS_UplinkDirectTransfer (p_CellId: INTEGER; p_Rb :SS_RB_Identity; p_Pdu : PDU)		
Group :		
ASP Type : RRC_DataInd		
Derivation Path :		
Comments : The ASP is used to indicate the receipt of the NAS PDU message using acknowledged operation (NAS <- RRC).		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	GERAN only GERAN only
rB_Id	p_Rb	
ch	–	
sapId	–	
cN_Domain	tsc_SS_PS_Domain	
start	–	
msg	p_Pdu	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_RLC_AM_DataInd(p_CellId: INTEGER; p_SS_RB_Identity: SS_RB_Identity; p_PDU: PDU)		
Group :		
ASP Type : RLC_AM_TestDataInd		
Derivation Path :		
Comments : This constraint is used to receive a data PDU for RB test cases		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	
rB_Id	p_SS_RB_Identity	
data	p_PDU	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_StatusInd (p_RB_Id: SS_RB_Identity) Group : ASP Type : RLC_TR_TestDataInd Derivation Path : Comments : This constraint is used to receive a STATUS PDU with the given super fields, and using the given RB Id. Any padding octets present are ignored. Parameters: p_RB_Id: The identifier for the RB to be used for reception of data. This is expected to be one of the following values, depending on the RLC configuration being tested. tsc_RB_AM_7_RLC, tsc_RB_UM_7_RLC, tsc_RB_AM_15_RLC, tsc_RB_UM_15_RLC p_SuperFields: The super fields expected to be included in the STATUS PDU.		
Parameter Name	Parameter Value	Comments
cellId	tsc_CellDedicated	
rB_Id	p_RB_Id	
data	cr_StatusAny	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : car_UplinkDirectTransfer (p_CellId: INTEGER; p_Rb : SS_RB_Identity; p_Pdu : PDU) Group : ASP Type : RRC_DataInd Derivation Path : Comments : The ASP is used to indicate the receipt of the NAS PDU message using acknowledged operation (NAS <- RRC).		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	
rB_Id	p_Rb	
ch	–	GERAN only
sapId	–	GERAN only
cN_Domain	tsc_SS_CS_Domain	
start	–	
msg	p_Pdu	
Detailed Comments :		

ASP Constraint Declaration		
Constraint Name : cas_RLC_AM_DataReq(p_CellId: INTEGER; p_SS_RB_Identity: SS_RB_Identity; p_PDU: PDU) Group : ASP Type : RLC_AM_TestDataReq Derivation Path : Comments : This constraint is used to send a data PDU fro RB test cases.		
Parameter Name	Parameter Value	Comments
cellId	p_CellId	
rB_Id	p_SS_RB_Identity	
data	p_PDU	
Detailed Comments :		

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_4_DCH_0_To11_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 , p_DchTFS2,p_DchTFS3 : CommonOrDedicatedTFS; p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_5_6_7_11_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_0_To11_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 , p_DchTFS2,p_DchTFS3 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_5_6_7_11_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_AM_Info_RAB_twrw512(p_CellId: INTEGER; p_RB_Id: INTEGER;p_TimerPollProhbt :TimerPollProhibit; p_Timer_poll: TimerPoll; p_PollSDU: Poll_SDU; p_PollWindw: PollWindow; p_LogChMapping : RB_LogCH_Mapping; p_PayLoad : INTEGER)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_AM_RLC_Mode :cd_DL_AM_RLC_rw512, sS_dl_RLC_Mode { dl_PayloadSize p_PayLoad, dl_RLCModelInfo ul_AM_RLC_Mode : c_UL_AM_RLC_tc_14_2_43_1_tw512 } }, rB_LogCH_Mapping p_LogChMapping } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_sCCPCH_InfoPCH_RAB_StandAloneCodNum4 (p_CellId: INTEGER; p_PhyChId: INTEGER; p_SndScramCode : INTEGER; p_SlotFormat: SCCPCHSlotFormat; p_TxPower : DL_TxPower)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: For FDD mode only,
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo secondaryCCPCHInfo : { scramblingCode p_SndScramCode, dl_ChannelizationCode sf128:4, sCCPCHSlotFormat p_SlotFormat, timingOffset 30, positionFixedOrFlexible fixed, sttd_Indicator FALSE, dl_TxPower p_TxPower, powerOffsetOfTFCI_PO1 tsc_sCCPCH_PowerOffsetTFCI, powerOffsetOfPILOT_PO3 tsc_sCCPCH_PowerOffsetPILOT } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_7_4k_AMR_DL_Info_DiffRM_DCH5 (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_61_TFS_6_DL }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo c_DCH_87_TFS_5 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_10_2k_AMR_DL_Info_DiffRM_DCH5 (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_65_TFS_6_DL }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo c_DCH_99_TFS_5 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo c_DCH_40_TFS }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL_RM192 } }, dlTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To19_Order1_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_SS_ActivationTime : SS_ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To19_Tx_Order1 (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To19_Order1_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2 : CommonOrDedicatedTFS ; p_SS_ActivationTime : SS_ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 , 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To19_Rx_Order1 } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_CRLC_SendContinuousData_REQ (p_cellId :INTEGER ; p_RabTxInfo : RabTxInfo)
Group	:
ASP Type	: CRLC_SendContinuousData_REQ
Derivation Path	:
Comments	: To request sending data in every TTI on each RB identified.After the CMAC_Restriction_REQ, the TFC under test will be the one corresponding to the maximum CTFC value in the Restricted list, so that SS can select the number of Transport blocks and the size of Transport blocks on individual Transport channels derived from this CTFC.SS shall take care about all kind of discard info in all RLC modes and the final goal is that the DL TFCs under test shall be selected in downlink for sending data on the request RBs in each TTI.
Constraint Value	
<pre> { cellId p_cellId , rabTxInfo p_RabTxInfo } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_SendContinuousData_CNF_Success (p_cellId : INTEGER)
Group	:
ASP Type	: CRLC_SendContinuousData_CNF
Derivation Path	:
Comments	: Confirm sending data in every TTI on each requested RB
Constraint Value	
<pre>{ cellId p_cellId , result success }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_TM_Info_Segmented(p_CellId: INTEGER; p_RB_Id: INTEGER; p_PayloadSize: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: For use in RAB test cases for Non Segmented RLC TM configuration where local SS is configured for segmentated operation,
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_TM_RLC_Mode :{ segmentationIndication TRUE }, sS_dl_RLC_Mode { dl_PayloadSize p_PayloadSize, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication TRUE } } }, rB_LogCH_Mapping p_LogChMapping }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_UM_DL_InfoCTCH_RAB(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: downlink RAB (UM+CTCH+FACH)
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize 160, dl_RLCModeInfo ul_UM_RLC_Mode :{ transmissionRLC_Discard timerBasedNoExplicit : dt100 } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments	: di_PayloadSize = TB_Size -8 = 160

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_FACH_Info_BMC_ActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : For FDD mode only	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch : NULL , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, dl_TransportChannelType fach, transportChannelInfo c_FACH_BCCH_CCCH_TFS }, { trchid tsc_FACH2, dl_TransportChannelType fach, transportChannelInfo c_FACH_CTCH_TFS } }, dlTFCS c_TFCS_ComplFACH_BMC_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For PCH transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 240 bits; and TransportBlocks = 1, TB size = 240 bits; coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210 For FACH transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_sCCPCH_InfoPCH_RAB_StandAlone (p_CellId: INTEGER; p_PhyChId: INTEGER; p_SndScramCode : INTEGER; p_SlotFormat: SCCPCHSlotFormat; p_TxPower : DL_TxPower)	
Group :	
ASP Type :	CPHY_RL_Setup_REQ
Derivation Path :	
Comments :	For FDD mode only,
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo secondaryCCPCHInfo : { scramblingCode p_SndScramCode, dl_ChannelizationCode tsc_S_CCPCH1_DL_ChC, sCCPCHSlotFormat p_SlotFormat, timingOffset 30, positionFixedOrFlexible fixed, sttd_Indicator FALSE, dl_TxPower p_TxPower, powerOffsetOfTFCI_PO1 tsc_sCCPCH_PowerOffsetTFCI, powerOffsetOfPILOT_PO3 tsc_sCCPCH_PowerOffsetPILOT } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_2_FACH_BCCH_DCCH_CCCH_DTCH_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : Used for the configuration cell_FACH_3_SCCPCH_4_FACH For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch : NULL , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH3, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH4, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_2_FACH_BCCH_CCCH_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : Used for the configuration cell_3_SCCPCH_4_FACH For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch : NULL , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH2, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To11_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To11_DL_Info_tc55 (p_CellId : INTEGER; p_PhyChId : INTEGER; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType dch : normal , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To11_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To11_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 11.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd , trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To11_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To13_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To13_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To15_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To15_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To17_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To17_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To17_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 17
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To17_Tx (OMIT) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To21_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To21_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To37_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To37_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To9_DL_Info_tc54 (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To3_UL_Info_tc54 (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 3.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_2_3_Rx --- sent to SS } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To_3_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To49_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To49_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To59_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To59_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To7_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To7_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To7_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 , 1,2, 3, 4, 5, 6, 7
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To7_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To11_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 , p_DchTFS2 : CommonOrDedicatedTFS; p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_5_6_7_11_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To11_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 , p_DchTFS2 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_1_5_6_7_11_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_0_To23_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER ; p_Type : TrChConfigType; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_81_TFS_DL }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo c_DCH_60_TFS },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL_RM192 }}, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments	: For DCH1 transport channel the following parameters are fixed by core spec.(34.123 cl. 7.2): TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 155

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_0_To23_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_65_TFS }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo c_DCH_99_TFS }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo c_DCH_40_TFS },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL }}, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx } } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_102_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType;p_TFS1,p_TFS2,p_TFS3,p_TFS5:CommonOrDedicatedTFS;p_TFCS:TFCS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_TFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_TFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_TFS3 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_TFS5 } }, dlTFCS p_TFCS } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To143_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ; p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type :	CPHY_TrCH_Config_REQ
Derivation Path :	
Comments :	For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To143_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To167_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 :CommonOrDedicatedTFS;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To167_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To191_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type :	CPHY_TrCH_Config_REQ
Derivation Path :	
Comments :	For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To191_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To215_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To215_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To263_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in acknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To263_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To455_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To455_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To47_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }, }, dlTFCS c_TFCS_Cmpl0_To47_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To47_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)	
Group :	
ASP Type :	CPHY_TrCH_Config_REQ
Derivation Path :	
Comments :	For FDD mode only, used in acknowledged mode RLC testing. With ctfc 0,1,11,12,13,23,24,25,35,36,37,47.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To47_Rx } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_5_DCH_0_To71_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS; p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 , 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71 .
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To71_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To71_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing. With ctfc from 0 , 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71 .	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To71_Tx (OMIT) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_102_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType;p_TFS1,p_TFS2,p_TFS3,p_TFS5:CommonOrDedicatedTFS;p_TFCS:TFCS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_TFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_TFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_TFS3 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_TFS5 } }, ulTFCS p_TFCS } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4_DCH_10_2k_AMR_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_65_TFS_6 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo c_DCH_99_TFS_5 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo c_DCH_40_TFS }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_7_4k_AMR_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_61_TFS_6 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo c_DCH_87_TFS_5 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_8_15_22_29_30_31_38_45_52_59_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To3_DL_Info_1 (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To239_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in acknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To239_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_5_DCH_0_To239_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To239_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To359_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing. With ctfc 0,1,11,12,13,23,24,25,35,36,37,47.	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To359_Rx } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To359_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in acknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To359_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To541_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To541_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To541_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing. With ctfc 0,1,11,12,13,23,24,25,35,36,37,47.	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To541_Rx } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_5_DCH_0_To599_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing. With ctfc
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To599_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To599_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in acknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To599_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To23_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in acknowledged mode RLC testing	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_5_DCH_0_To23_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing. With ctfc
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To119_DL_Info_14_2_38e (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1,p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)	
Group :	
ASP Type :	CPHY_TrCH_Config_REQ
Derivation Path :	
Comments :	For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_5_DCH_0_To119_UL_Info_14_2_38e (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2,p_DchTFS3,p_DchTFS4 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing. With ctfc
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To11_UL_Info_1 (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 , p_DchTFS2 : CommonOrDedicatedTFS ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_2_4_1_3_5_6_8_10_7_9_11_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_2_FACH_BCCH_CCCH_SCCPCH3_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : Used for the configuration cell_3_SCCPCH_4_FACH For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch : NULL , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH3, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH4, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_2_FACH_BCCH_DCCH_CCCH_DTCH_SCCPCH2_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : Used for the configuration cell_FACH_3_SCCPCH_4_FACH For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch : NULL , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH2, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To3_DL_Info_56tc (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_PowerOffsetInformation:PowerOffsetInformation ; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To15_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList {{ trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }}, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To15_Tx (p_PowerOffsetInformation) } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_pDSCH_Info (p_CellId: INTEGER; p_ScrmbCode : PrimaryScramblingCode; p_pdsch_CodeMapping : PDSCH_CodeMapping; p_dl_TxPower : DL_TxPower)	
Group :	
ASP Type :	CPHY_RL_Setup_REQ
Derivation Path :	
Comments :	For FDD mode only
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: tsc_DL_PDSCH1, ratType fdd, setupMessage { physicalChannelInfo pDSCHInfo : { fdd_tdd fdd : { pdsch_CodeMapping p_pdsch_CodeMapping }, dl_TxPower p_dl_TxPower } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DCH_DSCH_148_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_TFCS:TFCS)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only. The configuration is defined in TS 34.123-1 cl. 6.10.2.4.1.3
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activateNow : NULL, dlconnectedTrCHList {{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL }}, dlTFCS p_TFCS } } </pre>	
Detailed Comments	<p>: For DCH1 transport channel the following parameters are fixed by core spec.</p> <p>TTI =40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>rlc_Size = RLC PDU SIZE = 144 bits.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 192</p> <p>c_TFCS_DCH_0_To_1_DSCH_0_To_5_Tx (c_PowerOffsetInfoHigher64k)</p>

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To9_UL_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 9.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime activateNow : NULL, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To9_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_Bind_TestData_TTI_CNF_Success (p_cellId : INTEGER)
Group	:
ASP Type	: CRLC_Bind_TestData_TTI_CNF
Derivation Path	:
Comments	: To confirm the request of binding subsequent data sending RLC_TR_TestDataReq on the different DL RBs in the same TTI.
Constraint Value	
<pre> { cellId p_cellId , result success } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_CRLC_Bind_TestData_TTI_REQ (p_cellId , p_NoOfRBs : INTEGER)
Group	:
ASP Type	: CRLC_Bind_TestData_TTI_REQ
Derivation Path	:
Comments	: To request binding subsequent data sending RLC_TR_TestDataReq on the different DL RBs in the same TTI. On the request, the transmission of the test data is temporarily suppressed on those radio bearers which follow subsequently this CRLC_Bind_TestData_TTI_REQ and have 'numOfDiffRb' different RB IDs. Having received the number 'numOfDiffRb' of RLC_TR_TestDataReq, the SS RLC sends the test data on those RBs in the same TTI according to the allowed DL TFCS.
Constraint Value	
<pre>{ cellId p_cellId , numOfDiffRb p_NoOfRBs -- Number of different RB IDs }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_SendContinuousData_CNF_Failure (p_cellId : INTEGER)
Group	:
ASP Type	: CRLC_SendContinuousData_CNF
Derivation Path	:
Comments	: Confirm sending data in every TTI on each requested RB
Constraint Value	
<pre>{ cellId p_cellId , result failure }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_Bind_TestData_TTI_CNF_Failure (p_cellId : INTEGER)
Group	:
ASP Type	: CRLC_Bind_TestData_TTI_CNF
Derivation Path	:
Comments	: To confirm the request of binding subsequent data sending RLC_TR_TestDataReq on the different DL RBs in the same TTI.
Constraint Value	
<pre>{ cellId p_cellId , result failure }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To3_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_ActTime: ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activationCFN : p_ActTime, dlconnectedTrCHList {{ trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }}, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_1_2_3_Tx (c_PowerOffsetInfoHigher64k) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To3_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_ActTime: ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 3.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activationCFN : p_ActTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_1_2_3_Rx -- sent to SS } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To5_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_ActTime: ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activationCFN : p_ActTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To5_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To5_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ; p_ActTime: ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing. With ctfc from 0 to 5.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activationCFN : p_ActTime, ulconnectedTrCHList {{ trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }}, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To5_Rx -- sent to SS } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_2_FACH_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch: NULL, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_FACH1, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH2, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_NoPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To19_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2: CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_SS_ActivationTime : SS_ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To19_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_3_DCH_0_To19_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2 : CommonOrDedicatedTFS ; p_SS_ActivationTime : SS_ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing. With ctfc from 0 , 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, ulTFCS c_TFCS_Cmpl0_To19_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To119_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER;p_Type : TrChConfigType; p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_SS_ActivationTime : SS_ActivationTime)	
Group :	
ASP Type :	CPHY_TrCH_Config_REQ
Derivation Path :	
Comments :	For FDD mode only, Used for PS + Speech config
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_DL_DCH4, dl_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 }}, dlTFCS c_TFCS_Cmpl0_To119_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_5_DCH_0_To119_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1, p_DchTFS2, p_DchTFS3, p_DchTFS4 : CommonOrDedicatedTFS; p_SS_ActivationTime : SS_ActivationTime)	
Group :	
ASP Type : CPHY_TrCH_Config_REQ	
Derivation Path :	
Comments : For FDD mode only, used in aknowledged mode RLC testing. With ctfc from 0 , 1,11,12,13,23,24,25,35,36,37,47,48,49,59,60,61,71, 72, 73, 83, 84, 85, 95, 96, 97, 107, 108, 109, 119.	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type , configMessage { activationTime p_SS_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 }, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo p_DchTFS2 }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo p_DchTFS3 }, { trchid tsc_UL_DCH4, ul_TransportChannelType dch, transportChannelInfo p_DchTFS4 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To119_Rx } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_RestrictCnf(p_CellId: INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CMAC_Restriction_CNF
Derivation Path	:
Comments	: For MAC emulator to report that a previous attempt of restricting TFCs have been successful.
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_RestrictReq (p_CellId: INTEGER; p_PhyChId: INTEGER;p_TFC_Restriction :
	TFC_Restriction)
Group	:
ASP Type	: CMAC_Restriction_REQ
Derivation Path	:
Comments	: To request to configure MAC entity. The field restrictAllowedTFCs is provided to allow the UL and/or DL SS TFCS to be restricted for a specific transport channel. This information only needs to be sent to the MAC layer, since it is the MAC layer's responsibility to determine the set of valid TFCs each TTI.
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId, ratType fdd, restrictAllowedTFCs p_TFC_Restriction }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_PCH_Info2 (p_CellId : INTEGER; p_PhyChId : INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only. One second S-CCPCH Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch: NULL, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, dl_TransportChannelType pch, transportChannelInfo c_PCH_TFS } }, dlTFCS c_TFCS_ComplPCH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments	: For PCH transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 240 bits; and TransportBlocks = 1, TB size = 240 bits; coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_sCCPCH_InfoPCH_StandAlone (p_CellId: INTEGER; p_PhyChId: INTEGER; p_SndScramCode : INTEGER; p_SlotFormat: SCCPCHSlotFormat; p_TxPower : DL_TxPower)	
Group :	
ASP Type :	CPHY_RL_Setup_REQ
Derivation Path :	
Comments :	For FDD mode only,
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo secondaryCCPCHInfo : { scramblingCode p_SndScramCode, dl_ChannelizationCode sf128 : 4, sCCPCHSlotFormat p_SlotFormat, timingOffset 30, positionFixedOrFlexible fixed, sttd_Indicator FALSE, dl_TxPower p_TxPower, powerOffsetOfTFCI_PO1 tsc_sCCPCH_PowerOffsetTFCI, powerOffsetOfPILOT_PO3 tsc_sCCPCH_PowerOffsetPILOT } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_MeasurementReport(p_CellId: INTEGER; p_RB_Id: SS_RB_Identity; p_PDU: UL_DCCH_Message)	
Group :	
ASP Type :	RLC_AM_DATA_IND
Derivation Path :	
Comments :	to recieve a Measurement Report PDU in AM mode
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, integrityResult ?, aM_message uL_DCCH_Message : p_PDU } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : cas_MeasurementControl (p_CellId: INTEGER; p_RB_Id: SS_RB_Identity; p_PDU: DL_DCCH_Message) Group : ASP Type : RLC_AM_DATA_REQ Derivation Path : Comments : to send a Measurement Control PDU in AM mode	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, confirmationRequest noConfirmationRequest: NULL, aM_message dL_DCCH_Message : p_PDU }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : cas_TranportFormatCombCtrlAM (p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: DL_DCCH_Message) Group : ASP Type : RLC_AM_DATA_REQ Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, confirmationRequest noConfirmationRequest: NULL, aM_message dL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To9_UL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing. With ctfc from 0 to 9.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type, configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList { { trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, ulTFCS c_TFCS_Cmpl0_To9_Rx } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_2_DCH_0_To9_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_DchTFS5 , p_DchTFS1 : CommonOrDedicatedTFS ;p_PowerOffsetInformation:PowerOffsetInformation; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type, configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList { { trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo p_DchTFS1 },{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo p_DchTFS5 } }, dlTFCS c_TFCS_Cmpl0_To9_Tx (p_PowerOffsetInformation) } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4DCH_DL_Info (p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType;p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in aknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type, configMessage { activationTime activationCFN : p_ActivationTime, dlconnectedTrCHList {{ trchid tsc_DL_DCH1, dl_TransportChannelType dch, transportChannelInfo c_DCH_81_TFS_DL }, { trchid tsc_DL_DCH2, dl_TransportChannelType dch, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_DL_DCH3, dl_TransportChannelType dch, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_DL } }, dlTFCS c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k) } </pre>	
Detailed Comments	: For DCH1 transport channel the following parameters are fixed by core spec.(34.123 cl. 7.2): TTI = 40 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 155

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_4DCH_UL_Info(p_CellId : INTEGER; p_PhyChId : INTEGER; p_Type : TrChConfigType; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only, used in acknowledged mode RLC testing
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type, configMessage { activationTime activationCFN : p_ActivationTime, ulconnectedTrCHList {{ trchid tsc_UL_DCH1, ul_TransportChannelType dch, transportChannelInfo c_DCH_81_TFS }}, { trchid tsc_UL_DCH2, ul_TransportChannelType dch, transportChannelInfo c_DCH_103_TFS }, { trchid tsc_UL_DCH3, ul_TransportChannelType dch, transportChannelInfo c_DCH_60_TFS }, { trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TFS_UL } }, ulTFCS c_TFCS_Cmpl0_1_11_12_13_23_Rx } </pre>	
Detailed Comments	<p>: For DCH1 transport channel the following parameters are fixed by core spec.(34.123 cl. 7.2):</p> <p>TTI = 40 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 155</p>

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DL_DPCH_ModifyInfoActNow (p_CellId: INTEGER; p_PhyChId: INTEGER; p_DL_DPCHInfo: DL_DPCHInfo)
Group	:
ASP Type	: CPHY_RL_Modify_REQ
Derivation Path	:
Comments	: To modify down link physical channel DPCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, modifyMessage { activationTime activateNow : NULL, physicalChannelInfo dPCHInfo : { dl_DPCHInfo p_DL_DPCHInfo } } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_UL_DPCH_ModifyInfoActNow (p_CellId: INTEGER; p_PhyChId: INTEGER; p_UL_DPCHInfo: UL_DPCH_Info)
Group	:
ASP Type	: CPHY_RL_Modify_REQ
Derivation Path	:
Comments	: To setup uplink physical channel DPDCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, modifyMessage { activationTime activateNow : NULL, physicalChannelInfo dPCHInfo : { ul_DPCHInfo p_UL_DPCHInfo } } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : cas_PagingType2(p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: DL_DCCH_Message)	
Group :	
ASP Type :	RLC_AM_DATA_REQ
Derivation Path :	
Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, confirmationRequest noConfirmationRequest : NULL, aM_message dL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_AichInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_AICH_Info: AICH_Info; p_TxPower: AICH_PowerOffset)	
Group :	
ASP Type :	CPHY_RL_Setup_REQ
Derivation Path :	
Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo aICHInfo : { aichinfo p_AICH_Info, dl_TxPower p_TxPower } } }</pre>	
Detailed Comments : The following are fixed in this constraint (34.108): transmission diversity is off, spreading factor is set to 256, AICH timing scheme is 0	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_BCH_InfoActNow (p_CellId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : For FDD mode only	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: tsc_P_CCPCH, ratType fdd, trchConfigType nonDch: NULL, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList {{ trchid tsc_BCH1, dl_TransportChannelType bch, transportChannelInfo { tti tti20 : {{tb_Size 246, numberOfTbSizeList {one : NULL}, logicalChannelList configured : NULL}}, semistaticTF_Information { channelCodingType convolutional :half, rateMatchingAttribute 1, crc_Size crc16 } } }}, dlTFCS c_TFCS_Cmpl0 (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For BCH transport channel the following parameters are fixed by core spec.(25.212 and 25.302): TTI = 20 ms; TransportBlocks = 1; transport block size = 246 bits; coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 1 (this parameter is not relevant to BCH, value can be any number between 1 to hiRM)	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_CellCfgCnf(p_CellId : INTEGER) Group : ASP Type : CPHY_Cell_Config_CNF Derivation Path : Comments :	
Constraint Value	
<pre> { cellId p_CellId } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CellCfgReq(p_CellId : INTEGER; p_Tcell: INTEGER; p_FreqInfo : FrequencyInfo; p_PriScmCode : INTEGER; p_DL_TxAttenLevel: INTEGER; p_SfnOffset : INTEGER; p_totalCellPower : CellTxPowerLevel)
Group	:
ASP Type	: CPHY_Cell_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, tcell p_Tcell, sfnOffset p_SfnOffset, frequencyInfo p_FreqInfo, primaryScramblingCode_SS p_PriScmCode, cellTxPowerLevel p_totalCellPower, dLTxAttenuationLevel p_DL_TxAttenLevel }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMACE_CfgCnf (p_CellId: INTEGER; p_PhyChId : INTEGER)
Group	:
ASP Type	: CMACE_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_CfgInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_UEInfo: UE_Info; p_TrCHInfo: TrCHInfo; p_TrCH_LogCHMapping: TrCH_LogCHMappingList1)
Group	:
ASP Type	: CMAC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, configMessage setup: { activationTime activateNow : NULL, uE_Info p_UEInfo, trCHInfo p_TrCHInfo, trCH_LogCHMapping p_TrCH_LogCHMapping } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_CipherActCnf(p_CellId: INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CMAC_Ciphering_Activate_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_DL_CipherActReq (p_CellId: INTEGER; p_PhyChId: INTEGER; p_CipherMode: CipheringModeCommand; p_ActTimeDPCH: INTEGER;p_IncrDcr : Increment_Mode)
Group	:
ASP Type	: CMAC_Ciphering_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, cn_DomainIdentity cs_domain, --the domain is hard coded as no TM RAB in PS domain cipheringModelInfo { cipheringModeCommand p_CipherMode, activationTimeForDPCH p_ActTimeDPCH, rb_DL_CiphActivationTimeInfo OMIT }, incHFN p_IncrDcr } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_PagingCfgCnf(p_CellId: INTEGER; p_PhysicalChannelIdentity: PhysicalChannelIdentity)
Group	:
ASP Type	: CMAC_PAGING_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhysicalChannelIdentity } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_CMAC_PagingCfgReq(p_CellId: INTEGER; p_PhysicalChannelIdentity: PhysicalChannelIdentity; p_RatType: RatType; p_Pdu: CmacPagingConfigReq) Group : ASP Type : CMAC_PAGING_Config_REQ Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhysicalChannelIdentity, ratType p_RatType, configMessage p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_CMAC_ReconfigInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_UEInfo: UE_Info; p_TrCHInfo: TrCHInfo; p_TrCH_LogCHMapping: TrCH_LogCHMappingList1; p_ActivationTime : ActivationTime) Group : ASP Type : CMAC_Config_REQ Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, configMessage reconfigure: { activationTime activationCFN : p_ActivationTime, uE_Info p_UEInfo, trCHInfo p_TrCHInfo, trCH_LogCHMapping p_TrCH_LogCHMapping } }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_ReconfigInfoActNow (p_CellId: INTEGER; p_PhyChId: INTEGER; p_UEInfo: UE_Info; p_TrCHInfo: TrCHInfo; p_TrCH_LogCHMapping: TrCH_LogCHMappingList1)
Group	:
ASP Type	: CMAC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, configMessage reconfigure: { activationTime activateNow : NULL, uE_Info p_UEInfo, trCHInfo p_TrCHInfo, trCH_LogCHMapping p_TrCH_LogCHMapping } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMAC_SecurityModeCfgCnf (p_CellId: INTEGER)
Group	:
ASP Type	: CMAC_SecurityMode_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMACE_SecurityModeCfgReq (p_CellId: INTEGER; p_Domain : CN_DomainIdentity; p_Hfn: HyperFrameNumber; p_KC: KeyCiphering; p_IK: IntegrityKey; p_GSM_ck : GSM_CipheringKey)
Group	:
ASP Type	: CMACE_SecurityMode_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, macCipheringInfo { cn_DomainIdentity p_Domain , startValue p_Hfn, cipheringKey p_KC, integrityKey p_IK, gsmCipheringKey p_GSM_ck } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CMACE_UL_CipherActReq (p_CellId: INTEGER; p_PhyChId: INTEGER; p_CipherMode: CipheringModeCommand; p_ActTimeDPCH: INTEGER; p_IncrDcr : Increment_Mode)
Group	:
ASP Type	: CMACE_Ciphering_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, cn_DomainIdentity cs_domain, -- domain hard coded as no TM RAB in PS Domain cipheringModelInfo { cipheringModeCommand p_CipherMode, activationTimeForDPCH p_ActTimeDPCH, rb_DL_CiphActivationTimeInfo OMIT }, incHFN p_IncrDcr } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CPHY_Cell_Release_CNF(p_CellId: INTEGER)
Group	:
ASP Type	: CPHY_Cell_Release_CNF
Derivation Path	:
Comments	: 1. This Primitive with "Soft_Reset" flag ON gives a common known starting point/state of SS for a test case. The SS performs the following whenever it receives this primitive with "Soft_Reset" flag ON:Releases all configured Channels and cells (if any) irrespective of Cell ID list IE. 2. Releases the associated Memory Buffers (if any). 3. Cancels all active timers (if any) With "Soft_Reset" flag OFF: 1. Releases cells listed in IE Cell_ID_List and associated configured Channels (if any) 2. Releases the Memory Buffers(if any) associated with Cells listed in IE Cell_ID_List 3. Cancels all active timers (if any) associated with Cells listed in IE Cell_ID_List.
Constraint Value	
{ soft_Reset FALSE, cell_ID_List { p_CellId } }	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CPHY_Cell_Release_REQ(p_CellId: INTEGER)
Group	:
ASP Type	: CPHY_Cell_Release_REQ
Derivation Path	:
Comments	: 1. This Primitive with "Soft_Reset" flag ON gives a common known starting point/state of SS for a test case. The SS performs the following whenever it receives this primitive with "Soft_Reset" flag ON:Releases all configured Channels and cells (if any) irrespective of Cell ID list IE. 2. Releases the associated Memory Buffers (if any). 3. Cancels all active timers (if any) With "Soft_Reset" flag OFF: 1. Releases cells listed in IE Cell_ID_List and associated configured Channels (if any) 2. Releases the Memory Buffers(if any) associated with Cells listed in IE Cell_ID_List 3. Cancels all active timers (if any) associated with Cells listed in IE Cell_ID_List.
Constraint Value	
{ soft_Reset FALSE, cell_ID_List { p_CellId } }	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_CfgCnf(p_CellId: INTEGER; p_RB_Id: INTEGER)
Group	:
ASP Type	: CRLC_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_CipherActCnf(p_CellId: INTEGER)
Group	:
ASP Type	: CRLC_Ciphering_Activate_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_DL_CipherActReq(p_CellId: INTEGER; p_CN_Domain : CN_DomainIdentity; p_RB_Id : INTEGER; p_CipherMode: CipheringModeCommand; p_N: RLC_SequenceNumber ;p_IncMode : RLC_IncMode)
Group	:
ASP Type	: CRLC_Ciphering_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, ratType fdd, cn_DomainIdentity p_CN_Domain, ciphActivationInfo cipheringModelInfo : { cipheringModeCommand p_CipherMode, activationTimeForDPCH OMIT, rb_DL_CiphActivationTimeInfo { c_RB_ActTimeInfo(p_RB_Id, p_N) } }, incHFN p_IncMode } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_DL_IntegrityActivateReq(p_CellId: INTEGER; p_Domain : CN_DomainIdentity;p_IntegrityProtectionModelInfo : IntegrityProtectionModelInfo)
Group	:
ASP Type	: CRLC_Integrity_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, cn_DomainIdentity p_Domain, integrityActivationInfo integrityProtectionModelInfo : p_IntegrityProtectionModelInfo } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_IntegrityActivateCnf(p_CellId: INTEGER)
Group	:
ASP Type	: CRLC_Integrity_Activate_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_RB_RelReq (p_CellId: INTEGER; p_RB_Id: INTEGER)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage release : NULL }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_SecurityModeCfgCnf(p_CellId: INTEGER)
Group	:
ASP Type	: CRLC_SecurityMode_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_SecurityModeCfgReq(p_CellId: INTEGER; p_Domain : CN_DomainIdentity; p_Hfn: HyperFrameNumber; p_KC: KeyCiphering; p_Ik: IntegrityKey; p_GSM_ck: GSM_CipheringKey)
Group	:
ASP Type	: CRLC_SecurityMode_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, rlcSecurityInfo { cn_DomainIdentity p_Domain , startValue p_Hfn, cipheringKey p_KC, integrityKey p_Ik, gsmCipheringKey p_GSM_ck } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_SetRRC_MSN_CNF (p_CellID :INTEGER ; p_RBID : INTEGER)
Group	:
ASP Type	: CRLC_SetRRC_MessageSN_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellID, routingInfo rB_Identity : p_RBID } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_UL_CipherActReq (p_CellId: INTEGER; p_CN_Domain : CN_DomainIdentity; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList;p_IncMode : RLC_IncMode)
Group	:
ASP Type	: CRLC_Ciphering_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, ratType fdd, cn_DomainIdentity p_CN_Domain, ciphActivationInfo rb_UL_CipheringActivationTimeInfo : p_RB_ActivationTimeInfoList, incHFN p_IncMode }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_CRLC_UL_IntegrityActivateReq (p_CellId: INTEGER ; p_Domain : CN_DomainIdentity; p_IntegrityProtActivationInfo : IntegrityProtActivationInfoList)
Group	:
ASP Type	: CRLC_Integrity_Activate_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, cn_DomainIdentity p_Domain , integrityActivationInfo ul_IntegProtActivationInfo : p_IntegrityProtActivationInfo }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DCH_148_TTI_10_DL_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only. The configuration is defined in TS 34.123-1 cl. 6.10.2.4.1.3
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activateNow : NULL, dlconnectedTrCHList {{ trchid tsc_DL_DCH5, dl_TransportChannelType dch, transportChannelInfo c_DCH_148_TTI_10_TFS }}, dlTFCS c_TFCS_Cmpl0_1_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments	<p>: For DCH1 transport channel the following parameters are fixed by core spec.</p> <p>TTI = 10 ms;</p> <p>two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148.</p> <p>rlc_Size = RLC PDU SIZE = 144 bits.</p> <p>coding = convolutional;</p> <p>coding rate = 1/3;</p> <p>CRCsize = 16;</p> <p>RateMatching = 192</p>

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DCH_148_TTI_10_UL_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only. The configuration is defined in TS 34.123-1 cl. 6.10.2.4.1.3
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType c_TrChConfigTypeDCH_NoSHO, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList {{ trchid tsc_UL_DCH5, ul_TransportChannelType dch, transportChannelInfo c_DCH_148_TTI_10_TFS }}, ulTFCS c_TFCS_Cmpl0_1_Rx } } </pre>	
Detailed Comments	: For DCH1 transport channel the following parameters are fixed by core spec.: TTI = 10 ms; two transport formats: TransportBlocks = 1, TB size = 148 bits; TansportBlock = 0, Size = 148. rlc_Size = RLC PDU Size = 144 bits. coding = convolutional; coding rate = 1/3; CRCsize = 16; RateMatching = 192

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DL_CRLC_SetRRC_MSN_REQ (p_CellID :INTEGER ; p_RBID : INTEGER; p_DL_MSN : RRC_SequenceNumber)
Group	:
ASP Type	: CRLC_SetRRC_MessageSN_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellID, routingInfo rB_Identity : p_RBID, count_I_LSB_UL OMIT, count_I_LSB_DL p_DL_MSN } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DL_DPCH_Info(p_CellId: INTEGER; p_PhyChId: INTEGER; p_DL_DPCHInfo: DL_DPCHInfo)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: To setup down link physical channel DPCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo dPCHInfo : { dl_DPCHInfo p_DL_DPCHInfo } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_DL_DPCH_ModifyInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_DL_DPCHInfo: DL_DPCHInfo; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_RL_Modify_REQ
Derivation Path	:
Comments	: To modify down link physical channel DPCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, modifyMessage { activationTime activationCFN : p_ActivationTime, physicalChannelInfo dPCHInfo : { dl_DPCHInfo p_DL_DPCHInfo } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_OutOfSyncInd (p_PhysicalChannelIdentity : PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_Out_of_Sync_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId ?, routingInfo physicalChannelIdentity: p_PhysicalChannelIdentity }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_pCCPCH_Info(p_CellId: INTEGER; p_TxPower : DL_TxPower)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: For FDD mode only
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: tsc_P_CCPCH, ratType fdd, setupMessage { physicalChannelInfo primaryCCPCHInfo : { sttd_Indicator FALSE, dl_TxPower p_TxPower } } }</pre>	
Detailed Comments : PhysycalChannelIdentity for p-CCPCH is fixed as tsc_pCCPCH;	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_PCH_2_FACH_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER) Group : ASP Type : CPHY_TrCH_Config_REQ Derivation Path : Comments : For FDD mode only (PS)	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch: NULL, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList OMIT, ulTFCS OMIT, dlconnectedTrCHList { { trchid tsc_PCH1, dl_TransportChannelType pch, transportChannelInfo c_PCH_TFS}, { trchid tsc_FACH1, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS}, { trchid tsc_FACH2, dl_TransportChannelType fach, transportChannelInfo c_FACH_TFS_PS} }, dlTFCS c_TFCS_CmplFACH_Tx (c_PowerOffsetInfoBelow64k) } } </pre>	
Detailed Comments : For PCH transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 240 bits; and TransportBlocks = 1, TB size = 240 bits; coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210 For FACH1 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3.2): TTI = 10 ms; two transport formats: TransportBlocks = 0, TB size = 360 bits; TransportBlocks = 1, TB size = 360 bits; coding = turbo; CRCsize = 16; RateMatching = 110 For FACH2 transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.3): TTI = 10 ms; three transport formats: TransportBlocks = 0, TB size = 168 bits; TransportBlocks = 1, TB size = 168 bits;and TransportBlocks = 2, TB size = 168 bits coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 210	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_pCPICH_Info(p_CellId: INTEGER; p_TxPower: DL_TxPower_PCPICH)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: For FDD mode only
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: tsc_P_CPICH, ratType fdd, setupMessage { physicalChannelInfo primaryCPICHInfo:{ dl_TxPower_PCPICH p_TxPower, txdiversityIndicator FALSE } } }</pre>	
Detailed Comments : PhysycalChannelIdentity for p-CPICH is fixed as tsc_pCPICH;	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_PHY_RelCnf (p_CellId : INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Release_CNF
Derivation Path	:
Comments	: To confirm to release tthe Radio Link
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_PHY_RelReqDCH_NoSHO (p_CellId : INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Release_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, trchConfigType dch : normal }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_PHY_RelReqNonDch (p_CellId : INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Release_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, trchConfigType nonDch: NULL }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_PICH_Info(p_CellId: INTEGER; p_PICH_Info: PICH_Info; p_TxPower : PICH_PowerOffset;p_SCCPCH_Ass: INTEGER)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: For FDD mode only
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : tsc_PICH1, ratType fdd, setupMessage { physicalChannelInfo pICHInfo : { pichinfo p_PICH_Info, dl_TxPower p_TxPower, sccpchId_associated p_SCCPCH_Ass } } }</pre>	
Detailed Comments	: Value of PI per frame is fixed to 18 (34.108) Value of sndScramCode can be 2 (34.108)

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_PRACH_Info(p_CellId: INTEGER; p_PhyChId: INTEGER; p_Signatures: AvailableSignatures; p_PreScramCodeWord: PreambleScramblingCodeWordNumber; p_PuncLimit: PuncturingLimit; p_SF_PRACH: SF_PRACH; p_SubChNum: AvailableSubChannelNumbers) Group : ASP Type : CPHY_RL_Setup_REQ Derivation Path : Comments :	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd , setupMessage { physicalChannelInfo pRACHInfo :{ fdd_tdd fdd : { preambleSignature p_Signatures, spreadingFactorForDataPart p_SF_PRACH, preambleScramblingCode p_PreScramCodeWord, puncturingLimit p_PuncLimit, accessSlot p_SubChNum } } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_pSCH_Info(p_CellId: INTEGER; p_TxPower : DL_TxPower) Group : ASP Type : CPHY_RL_Setup_REQ Derivation Path : Comments : For FDD mode only	
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: tsc_P_SCH, ratType fdd, setupMessage { physicalChannelInfo primarySCHInfo : { tstdIndicator FALSE, dl_TxPower p_TxPower } } } </pre>	
Detailed Comments : PhysycalChannelIdentity for p-SCH is fixed as tsc_pSCH;	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_AM_Info_RAB(p_CellId: INTEGER; p_RB_Id: INTEGER;p_TimerPollProhbt :TimerPollProhibit; p_Timer_poll: TimerPoll; p_PollSDU: Poll_SDU; p_PollWindw: PollWindow; p_LogChMapping : RB_LogCH_Mapping; p_PayLoad : INTEGER)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: Used to setup AM RLC entity
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_AM_RLC_Mode :cb_DL_AM_RLC, sS_dl_RLC_Mode { dl_PayloadSize p_PayLoad, dl_RLCModelInfo ul_AM_RLC_Mode : cb_UL_AM_RLC } }, rB_LogCH_Mapping p_LogChMapping } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_AM_Info_SRB(p_CellId: INTEGER; p_RB_Id: INTEGER;p_TimerPollProhbt :TimerPollProhibit; p_Timer_poll: TimerPoll; p_PollSDU: Poll_SDU; p_PollWindw: PollWindow; p_LogChMapping : RB_LogCH_Mapping; p_PayLoad : INTEGER)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: Used to setup AM RLC entity
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_AM_RLC_Mode :cd_DL_AM_RLC_SRB, sS_dl_RLC_Mode { dl_PayloadSize p_PayLoad, dl_RLCModelInfo ul_AM_RLC_Mode : cd_UL_AM_RLC_SRB } }, rB_LogCH_Mapping p_LogChMapping } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_BCCH_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize 246, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication FALSE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments : dl_PayloadSize = TB_Size	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_PCCH_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize 240, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication FALSE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments : dl_PayloadSize = TB_Size	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_TM_DL_InfoNoSeg(p_CellId: INTEGER; p_RB_Id: INTEGER; p_PayloadSize: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize p_PayloadSize, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication FALSE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_TM_DL_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_PayloadSize: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize p_PayloadSize, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication TRUE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_TM_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_PayloadSize: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_TM_RLC_Mode :{ segmentationIndication FALSE }, sS_dl_RLC_Mode { dl_PayloadSize p_PayloadSize, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication FALSE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_TM_UL_Info(p_CellId: INTEGER; p_RB_Id: INTEGER;p_PayloadSize: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_TM_RLC_Mode :{ segmentationIndication FALSE }, sS_dl_RLC_Mode { dl_PayloadSize p_PayloadSize, dl_RLCModelInfo ul_TM_RLC_Mode :{ segmentationIndication FALSE } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_UM_DL_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_dl_RLC_Mode { dl_PayloadSize 152, dl_RLCModeInfo ul_UM_RLC_Mode : { transmissionRLC_Discard timerBasedNoExplicit : dt100 } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_UM_Info(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: Used to setup UM RLC entity
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage setup : { sS_rlc_Info { sS_ul_RLC_Mode dl_UM_RLC_Mode :NULL, sS_dl_RLC_Mode{ dl_PayloadSize 136, dl_RLCModeInfo ul_UM_RLC_Mode : { transmissionRLC_Discard timerBasedNoExplicit : dt100 } } }, rB_LogCH_Mapping p_LogChMapping } </pre>	
Detailed Comments	: dl_PayloadSize = TB_Size – 12 = 136

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RB_UM_ReconfInfo (p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	:
Comments	: Used to setup UM RLC entity
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity: p_RB_Id, ratType fdd, configMessage reconfigure : { sS_rlc_Info { sS_ul_RLC_Mode dl_UM_RLC_Mode :NULL, sS_dl_RLC_Mode{ dl_PayloadSize 136, dl_RLCModelInfo ul_UM_RLC_Mode : { transmissionRLC_Discard timerBasedNoExplicit : dt100 } } }, rB_LogCH_Mapping p_LogChMapping } } </pre>	
Detailed Comments : dl_PayloadSize = TB_Size – 12 = 136	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RL_ModifyCnf(p_CellId : INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_RL_Modify_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RL_RelCnf (p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_RL_Release_CNF
Derivation Path	:
Comments	: To confirm that a specified physical channel has been released
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyCH }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RL_RelReq(p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_RL_Release_REQ
Derivation Path	:
Comments	: To release the specified physical channel.
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyCH }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_RL_SetupCnf(p_CellId : INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_RL_Setup_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhyChId }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_sCCPCH_Info (p_CellId: INTEGER; p_PhyChId: INTEGER; p_SndScramCode : INTEGER; p_ChannelizationCode: SF256_AndCodeNumber; p_SlotFormat: SCCPCHSlotFormat; p_TxPower : DL_TxPower; p_Timing: INTEGER)	
Group :	
ASP Type :	CPHY_RL_Setup_REQ
Derivation Path :	
Comments :	For FDD mode only,
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo secondaryCCPCHInfo : { scramblingCode p_SndScramCode, dl_ChannelizationCode p_ChannelizationCode, sCCPCHSlotFormat p_SlotFormat, timingOffset p_Timing, positionFixedOrFlexible flexible, sttd_Indicator FALSE, dl_TxPower p_TxPower, powerOffsetOfTFCI_PO1 tsc_sCCPCH_PowerOffsetTFCI, powerOffsetOfPILOT_PO3 tsc_sCCPCH_PowerOffsetPILOT } } } </pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : ca_SchedulLater(p_CellId: INTEGER;p_REP : INTEGER; p_POS : INTEGER; p_Timing : BCCH_ModificationTime)	
Group :	
ASP Type :	CMAC_SYSINFO_Config_REQ
Derivation Path :	
Comments :	scheduling information for system information change at the frame = p_Timing.
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity : tsc_RB_BCCH, ratType fdd, configMessage { sg_REP p_REP, sg_POS p_POS, bcch_ModificationTime p_Timing } } </pre>	
Detailed Comments : ?? for MIB	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_SchedulNow(p_CellId: INTEGER; p_REP : INTEGER; p_POS : INTEGER)
Group	:
ASP Type	: CMAC_SYSINFO_Config_REQ
Derivation Path	:
Comments	: scheduling information for immediately change
Constraint Value	
<pre> { cellId p_CellId, routingInfo rB_Identity : tsc_RB_BCCH, ratType fdd, configMessage { sg_REP p_REP, sg_POS p_POS, bcch_ModificationTime OMIT } } </pre>	
Detailed Comments	: ?? for MIB

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_sSCH_Info(p_CellId: INTEGER; p_TxPower : DL_TxPower)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: For FDD mode only
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: tsc_S_SCH, ratType fdd, setupMessage { physicalChannelInfo secondarySCHInfo : { tstdIndicator FALSE, dl_TxPower p_TxPower } } } </pre>	
Detailed Comments	: PhysycalChannelIdentity for s–SCH is fixed as tsc_sSCH;

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_SyncInd (p_PhysicalChannelIdentity : PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_Sync_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId ?, routingInfo physicalChannelIdentity: p_PhysicalChannelIdentity }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_SysInfoCfgCnf(p_CellId: INTEGER; p_RB_Identity: SS_RB_Identity)
Group	:
ASP Type	: CMAC_SYSINFO_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB_Identity }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_TR_DataReq(p_CellId : INTEGER; p_RB : SS_RB_Identity ; p_Message : BCCH_BCH_Message)
Group	:
ASP Type	: RLC_TR_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB , tM_message bCCH_BCH_Message : p_Message }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_TrChCfgCnf(p_CellId: INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Config_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_TrChCfgInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_Type : TrChConfigType;p_TrChConfig: CphyTrchConfigReq)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType p_Type, configMessage p_TrChConfig }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_UL_DPCH_Info(p_CellId: INTEGER; p_PhyChId: INTEGER; p_UL_DPCHInfo: UL_DPCH_Info)
Group	:
ASP Type	: CPHY_RL_Setup_REQ
Derivation Path	:
Comments	: To setup uplink physical channel DPCH.
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, setupMessage { physicalChannelInfo dPCHInfo : { ul_DPCHInfo p_UL_DPCHInfo } } }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: ca_UL_DPCH_ModifyInfo(p_CellId: INTEGER; p_PhyChId: INTEGER; p_UL_DPCHInfo: UL_DPCH_Info; p_ActivationTime : ActivationTime)
Group	:
ASP Type	: CPHY_RL_Modify_REQ
Derivation Path	:
Comments	: To setup uplink physical channel DPDCH.
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, modifyMessage { activationTime activationCFN : p_ActivationTime, physicalChannelInfo dPCHInfo : { ul_DPCHInfo p_UL_DPCHInfo } } } </pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cab_RACH_InfoActNow (p_CellId : INTEGER; p_PhyChId : INTEGER)
Group	:
ASP Type	: CPHY_TrCH_Config_REQ
Derivation Path	:
Comments	: For FDD mode only
Constraint Value	
<pre> { cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, trchConfigType nonDch: NULL, configMessage { activationTime activateNow : NULL, ulconnectedTrCHList {{ trchid tsc_RACH1, ul_TransportChannelType rach, transportChannelInfo c_RACH_TFS }}, ulTFCS c_TFCS_Cmpl0_1_Rx, dlconnectedTrCHList OMIT, dlTFCS OMIT } } </pre>	
Detailed Comments	: For RACH transport channel the following parameters are fixed by core spec.(34.108 cl. 6.10.2.4.4): TTI = 20 ms; two transport format: TransportBlocks = 1, TB size = 168 bits and TransportBlocks = 1, TB size = 360 bits; coding = convolutional; coding rate = 1/2; CRCsize = 16; RateMatching = 1

ASN.1 ASP Constraint Declaration	
Constraint Name	: cad_RB_UM_ReconfInfo(p_CellId: INTEGER; p_RB_Id: INTEGER; p_LogChMapping : RB_LogCH_Mapping)
Group	:
ASP Type	: CRLC_Config_REQ
Derivation Path	: ca_RB_UM_ReconfInfo.
Comments	: Used to setup UM RLC entity for 120 RLC size
Constraint Value	
REPLACE configMessage.reconfigure.sS_rlc_Info.sS_dl_RLC_Mode.dl_PayloadSize BY 120	
Detailed Comments	: dl_PayloadSize = TB_Size – 12 = 136 – 16 (U–RNTI –C–RNTI = 32–16 =16) ==>120

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_AM_DataCnf(p_CellId: INTEGER; p_RB : INTEGER)
Group	:
ASP Type	: RLC_AM_DATA_CNF
Derivation Path	:
Comments	:
Constraint Value	
{ cellId p_CellId, routingInfo rB_Identity : p_RB , mui ? }	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_AM_DataMuiCnf(p_CellId: INTEGER; p_RB : INTEGER; p_Mui : Mui)
Group	:
ASP Type	: RLC_AM_DATA_CNF
Derivation Path	:
Comments	:
Constraint Value	
{ cellId p_CellId, routingInfo rB_Identity : p_RB , mui p_Mui }	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_IntegrityFail
Group	:
ASP Type	: CRLC_Integrity_Failure_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId ?, routingInfo ?, failureCause codeNotMatched }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_CRLC_RRC_MessageSN_CNF(p_CellId, p_RBID : INTEGER)
Group	:
ASP Type	: CRLC_RRC_MessageSN_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RBID, count_I_MSB_UL ?, count_I_LSB_UL ?, count_I_MSB_DL ?, count_I_LSB_DL ? }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_GetFrameNum (p_CellId: INTEGER; p_PhysicalChannelIdentity: PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_Frame_Number_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity : p_PhysicalChannelIdentity, frameNumber ? }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_GetRLC_SeqNum(p_CellId: INTEGER; p_RB_Id: INTEGER)
Group	:
ASP Type	: CRLC_SequenceNumber_CNF
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, count_C_MSB_UL ?, count_C_LSB_UL ?, count_C_MSB_DL ?, count_C_LSB_DL ? }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_IniCnf
Group	:
ASP Type	: CPHY_Ini_CNF
Derivation Path	:
Comments	: Confirm the test initialisation
Constraint Value	
<pre>{ confirmation NULL }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_MeasRepAM (p_CellId : INTEGER; p_RB : INTEGER; p_Pdu: UL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB , integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_MeasRepUM(p_CellId : INTEGER; p_RB : INTEGER ; p_Pdu: UL_DCCH_Message) Group : ASP Type : RLC_UM_DATA_IND Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB , integrityResult ?, uM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_RB_SetUpCmpl(p_CellId : INTEGER; p_RB :INTEGER ; p_Pdu: UL_DCCH_Message) Group : ASP Type : RLC_AM_DATA_IND Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB , integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_ResumeRB(p_CellId: INTEGER; p_RB_Id: INTEGER) Group : ASP Type : CRLC_Resume_CNF Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_RRC_ConnRelCmpl(p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: UL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_RRC_ConnRelCmplUM(p_CellId: INTEGER; p_RB_Id: INTEGER ; p_Pdu:UL_DCCH_Message)
Group	:
ASP Type	: RLC_UM_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, integrityResult ?, uM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_RRC_ConnReq(p_CellId: INTEGER; p_RB_Id: SS_RB_Identity; p_Pdu: UL_CCCH_Message)
Group	:
ASP Type	: RLC_TR_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, tM_message uL_CCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_RRC_ConnSetupCmpl(p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: UL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: car_RRC_SecModeCmpl(p_CellId: INTEGER; p_RB : INTEGER ; p_Pdu: UL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_IND
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB , integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name : car_RRC_SecModeFail(p_CellId: INTEGER; p_RB : INTEGER; p_Pdu: UL_DCCH_Message)	
Group :	
ASP Type : RLC_AM_DATA_IND	
Derivation Path :	
Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB , integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_RRC_Status(p_CellId: INTEGER; p_RBId :INTEGER; p_Pdu: UL_DCCH_Message)	
Group :	
ASP Type : RLC_AM_DATA_IND	
Derivation Path :	
Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RBId, integrityResult ?, aM_message uL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : car_SuspendRB (p_CellId: INTEGER; p_RB_Id: INTEGER)	
Group :	
ASP Type : CRLC_Suspend_CNF	
Derivation Path :	
Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, vt ? }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_CRLC_RRC_MessageSN_REQ(p_CellId, p_RBID : INTEGER)
Group	:
ASP Type	: CRLC_RRC_MessageSN_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RBID }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_GetFrameNum (p_CellId: INTEGER; p_PhysicalChannelIdentity: PhysicalChannelIdentity)
Group	:
ASP Type	: CPHY_Frame_Number_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhysicalChannelIdentity }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_GetRLC_SeqNum(p_CellId: INTEGER; p_RB_Id: INTEGER)
Group	:
ASP Type	: CRLC_SequenceNumber_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_InitReqDef
Group	:
ASP Type	: CPHY_Ini_REQ
Derivation Path	:
Comments	: Request to initialise the test
Constraint Value	
defaultRadioEnvironment	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_InitReqNonDef
Group	:
ASP Type	: CPHY_Ini_REQ
Derivation Path	:
Comments	: Request to initialise the test
Constraint Value	
nonDefaultMultiCell	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_MAC_Rel (p_CellId: INTEGER; p_PhyChId: INTEGER)
Group	:
ASP Type	: CMAC_Config_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo physicalChannelIdentity: p_PhyChId, ratType fdd, configMessage release : NULL }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : cas_PagingType1(p_CellId: INTEGER; p_RB_Id: SS_RB_Identity ; p_Pdu: PCCH_Message) Group : ASP Type : RLC_TR_DATA_REQ Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, tM_message pCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name : cas_RB_SetUpAM(p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: DL_DCCH_Message) Group : ASP Type : RLC_AM_DATA_REQ Derivation Path : Comments :	
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity : p_RB_Id, confirmationRequest noConfirmationRequest : NULL, aM_message dL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments :	

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RB_SetUpAM_WithCnf(p_CellId: INTEGER; p_RB_Id: INTEGER; p_Mui : Mui; p_Pdu: DL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, confirmationRequest confirmationRequested: p_Mui, aM_message dL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_ResumeRB(p_CellId: INTEGER; p_RB_Id: INTEGER)
Group	:
ASP Type	: CRLC_Resume_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RLC_SuspendRB(p_CellId: INTEGER; p_RB_Id: INTEGER; p_RLC_SeqNum: RLC_SequenceNumber)
Group	:
ASP Type	: CRLC_Suspend_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, n p_RLC_SeqNum }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RRC_ConnRelCCCH(p_CellId: INTEGER; p_RB_Id: INTEGER ; p_Pdu: DL_CCCH_Message)
Group	:
ASP Type	: RLC_UM_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, uM_message dL_CCCH_Message : p_Pdu, specialLI TRUE }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RRC_ConnRelDCCH(p_CellId: INTEGER; p_RB_Id: INTEGER ; p_Pdu : DL_DCCH_Message)
Group	:
ASP Type	: RLC_UM_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, uM_message dL_DCCH_Message : p_Pdu, specialLI FALSE }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RRC_ConnSetup(p_CellId: INTEGER; p_RB_Id: INTEGER ; p_Pdu: DL_CCCH_Message)
Group	:
ASP Type	: RLC_UM_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, uM_message dL_CCCH_Message : p_Pdu, specialLI TRUE }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_RRC_SecModeCmd(p_CellId: INTEGER; p_RB_Id: INTEGER ; p_Pdu: DL_DCCH_Message)
Group	:
ASP Type	: RLC_AM_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, confirmationRequest noConfirmationRequest : NULL, aM_message dL_DCCH_Message : p_Pdu }</pre>	
Detailed Comments	:

ASN.1 ASP Constraint Declaration	
Constraint Name	: cas_SysInfoChangeInd (p_CellId: INTEGER; p_RB_Id: INTEGER; p_Pdu: BCCH_FACH_Message)
Group	:
ASP Type	: RLC_TR_DATA_REQ
Derivation Path	:
Comments	:
Constraint Value	
<pre>{ cellId p_CellId, routingInfo rB_Identity: p_RB_Id, tM_message bCCH_FACH_Message : p_Pdu }</pre>	
Detailed Comments	:

PDU Constraint Declaration			
Constraint Name : c_IMSI_DetachInd			
Group :			
PDU Type : IMSIDETACHINDICATION			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		IMSI DETACH INDICATION message carries TMSI information element
mMProtocolDiscriminator	'0101'B		
msgType	'??000001'B		
mSClsmk1	c_MS_Clsmk1_Def		
mobileId	c_MobileIdTMSI_lv		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_DetachReq (p_type : DetachType; p_ptmsi : GMM_MS_IdentityPTMSI; p_signature : PTMSI_Signature_tlv)			
Group :			
PDU Type : DETACHREQUESTMO			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000101'B		
spare4	?		
detachType	p_type		
ptmsi	p_ptmsi		
ptmsiSignature	p_signature		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_ActivateRB_TestMode Group : PDU Type : ACTIVATERBTESTMODE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000100'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_ActivateRB_TestModeCmpl Group : PDU Type : ACTIVATERBTESTMODECOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000101'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_AuthFailAny Group : PDU Type : AUTHENTICATIONFAILURE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'??011100'B		
rejCau	?		
authFailParam	c_AuthFailParamAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_AuthReq(p_KeySeq: KeySeq; p RAND: MM_RAND; p_AUTN: AUTN)			
Group :			
PDU Type : AUTHENTICATIONREQUEST			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'00010010'B		
spare4	'0000'B		
ciphKeySeqNum	c_CiphKeySeqNum(p_KeySeq)		
rAND	p_RAND		
aUTN	p_AUTN		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_AuthRspAnyExt			
Group :			
PDU Type : AUTHENTICATIONRESPONSE			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments : Any Authentication Response containing the Authentication Response Extension IE			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'??010100'B		
authRsp	?		
authRspExt	c_AuthRspExtAny		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_AuthRspAnyNoExt Group : PDU Type : AUTHENTICATIONRESPONSE Derivation Path : Encoding Rule Name : Encoding Variation : Comments : Any Authentication Response NOT containing the Authentication Response Extension IE			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'??010100'B		
authRsp	?		
authRspExt	–		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_CloseUE_TestLoop(p_UE_TestLoopMode: UE_TestLoopMode; p_UE_TestLoopMode1LB_Setup: UE_TestLoopMode1LB_Setup) Group : PDU Type : CLOSEUETESTLOOP Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000000'B		
uE_TestLoopMode	p_UE_TestLoopMode		
uE_TestLoopMode1LB_Setup	p_UE_TestLoopMode1LB_Setup		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_CloseUE_TestLoopCmpl Group : PDU Type : CLOSEUETESTLOOPCOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000001'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_DeactivateRB_TestMode Group : PDU Type : DEACTIVATERBTESTMODE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000110'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_DeactivateRB_TestModeCmpl Group : PDU Type : DEACTIVATERBTESTMODECOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000111'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_LocUpdAcpTMSI (p_MCC: HEXSTRING; p_MNC: HEXSTRING; p_LAC: OCTETSTRING) Group : PDU Type : LOCATIONUPDATINGACCEPT Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'00000010'B		
locAreald	c_LocAreald_v(p_MCC, p_MNC, p_LAC)		
mobileId	c_MobileIdTMSI_Def		
followOnProceed	'10100001'B		
cTSPerm	'10100010'B		
equivalentPLMN	—		
emergNumList	—		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_LocUpdAcpTMSI_E_PLMN (p_MCC: HEXSTRING; p_MNC: HEXSTRING; p_LAC: OCTETSTRING; p_ePLMN : PLMN_List) Group : PDU Type : LOCATIONUPDATINGACCEPT Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'00000010'B		
locAreald	c_LocAreald_v(p_MCC, p_MNC, p_LAC)		
mobileId	c_MobileIdTMSI_Def		
followOnProceed	'10100001'B		
cTSPerm	'10100010'B		
equivalentPLMN	p_ePLMN		
emergNumList	—		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_OpenUE_TestLoop Group : PDU Type : OPENUETESTLOOP Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000010'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_OpenUE_TestLoopCmpl Group : PDU Type : OPENUETESTLOOPCOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
tCProtocolDiscriminator	'1111'B		
msgType	'01000011'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_PagRsp (p_KeySeq: KeySeq; p_MobileId: MS_Identity_Iv) Group : PDU Type : PAGINGRESPONSE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
rRProtocolDiscriminator	'0110'B		
msgType	'??100111'B		
spare4	'0000'B		
ciphKeySeqNum	c_CiphKeySeqNum(p_KeySeq)		
mSClsmk2	c_MS_Clsmk2_Any_Iv		
mobileId	p_MobileId		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_TMSI_ReallocCmpl Group : PDU Type : TMSIREALLOCATIONCOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		Skip Indicator
mMProtocolDiscriminator	'0101'B		MM Protocol Discriminator
msgType	'??011011'B		Message Type
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : c_TrD_Data (p_DataBitstring : BITSTRING) Group : PDU Type : TrD_PDU Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
data	p_DataBitstring		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cb_LocUpdReqAny (p_KeySeq : KeySeq) Group : PDU Type : LOCATIONUPDATINGREQUEST Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
mMProtocolDiscriminator	'0101'B		
msgType	'??001000'B		
ciphKeySeqNum	c_CiphKeySeqNum (p_KeySeq)		
locUpdType	c_LocUpdTypeAny		
locAreald	c_LocArealdAny_v		
mSClsmk1	c_MS_Clsmk1_Any		
mobileId	c_MobileIdAny_lv		
mSClsmk2	c_MS_Clsmk2_Any		
	IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cbr_Deact_PDP_ContextReq_MO(p_SM_Cause: SM_Cause_v) Group : PDU Type : DEACTIVATEPDPCONTEXTREQUEST Derivation Path : Encoding Rule Name : Encoding Variation : Comments : Deactivate PDP Context Request n <=> ue 24.008, 9.5.14			
Field Name	Field Value	Field Encoding	Comments
ti	cr_TI_Any		
sM_ProtocolDiscriminator	tsc_SMPD		
msgType	'01000110'B		
sM_Cause	p_SM_Cause		
tearDwnInd	cr_TearDwnInd_tv		
	IF_PRESENT		
protocolConfOpts	cr_ProtoCfgOptAny		
	IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cbr_RA_UpdReqAny (p_UpdateType : UpdateType_v; p_RAI : RAI_v; p_KeySeq : KeySeq)			
Group :			
PDU Type : ROUTINGAREAUPDATEREQUEST			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00001000'B		
gprsCiphKeySeqNo	c_CiphKeySeqNum(p_KeySeq)		
updateType	p_UpdateType		
oldRAI	p_RAI		
msRadioAccessCap	c_MS_RadioAccessCapAny_lv		
oldPTMSI_Signature	c_PTMSI_SignatureAny IF_PRESENT		
readyTimer	cr_GPRS_TimerAny IF_PRESENT		
drxParameter	cr_DRXparameter_tv_Any IF_PRESENT		
tmsiStatus	c_TMSI_StatusAny IF_PRESENT		
ptmsi	c_MobileIdPTMSI_Any IF_PRESENT		
msnetworkcap	cr_MS_NetworkCap_tlv_Any IF_PRESENT		
pDP_ContextStatus	cr_PDP_ContextStatusAny IF_PRESENT		
pS_LCS_Capability	cr_PS_LCS_CapabilityAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_ActPDP_ContextReqMO_Any			
Group :			
PDU Type : ACTIVATEPDPCONTEXTREQUESTul			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments : Activate PDP Context Request ue -> n 3GPP 24.008, 9.5.1			
Field Name	Field Value	Field Encoding	Comments
ti	cr_TI_Any		This has to be set to Not Assigned by UE in UMTS domain.
sM_ProtocolDiscriminator	tsc_SMPD		
msgType	'01000001'B		
requestedNSAPI	cr_NSAPI_v		
requestedLLC_SAPI	cr_LLC_SAPI_v		
requestedQoS	cr_QualityOfService_lv_Any		The GGSN logical name or the external packet data network logical name
pDP_Address	cr_StaticPDP_AddressAny		
accessPtName	cr_AccessPtNameAny IF_PRESENT		
protocolConfOpts	cr_ProtoCfgOptAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_AttachComplete Group : PDU Type : ATTACHCOMPLETE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000011'B		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_AttachReq (p_AttachType : AttachType; p_MobId : MS_Identity_lv; p_RAI : RAI_v; p_KeySeq : KeySeq) Group : PDU Type : ATTACHREQUEST Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000001'B		
msNetworkCap	cr_MS_NetworkCapAss_lv		
gprsCiphKeySeqNo	c_CiphKeySeqNum(p_KeySeq)		
attachType	p_AttachType		
drxParameter	c_DRX_ParamterAny		
ptmsiORimsi	p_MobId		
oldRAI	p_RAI		
msRadioAccessCap	c_MS_RadioAccessCapAny_lv		
oldPTMSI_Signature	c_PTMSI_SignatureAny IF_PRESENT		
readyTimer	c_GPRS_TimerAny IF_PRESENT		
tmsiStatus	c_TMSI_StatusAny IF_PRESENT		
pS_LCS_Capability	cr_PS_LCS_CapabilityAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_AuthAndCiphFailureAny Group : PDU Type : AUTHENTICATION_AND_CIPHERING_FAILURE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00011100'B		
gmmCause	?		
authFailurePar	c_AuthFailParamGmmAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_AuthAndCiphRsp(p_authRsp : AuthRsp_tv; p_authRspExt :AuthRspExt) Group : PDU Type : AUTHENTICATIONANDCIPHERINGRESPONSE Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00010011'B		
spare4	'0000'B		
acRefNo	?		Should be the one sent in the auth request
authRsp	p_authRsp		Authentication RES
imeisv	–		No IMEISV requested
authRspExt	p_authRspExt		Authentication paramter AUTN, a UMTS challenge is requested
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_DetachRequest_MO Group : PDU Type : DETACHREQUESTMO Derivation Path : Encoding Rule Name : Encoding Variation : Comments : 3GPP 24.008 V3.6.0 clause 9.4.5.2 (Mobile originating detach, GMM message)			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		Skip Indicator
gMMProtocolDiscriminator	tsc_GMM_PD		GMM Protocol Discriminator
msgType	'00000101'B		Message Type
spare4	'0000'B		M
detachType	c_DetachTypeReAttNotRequiredGPRS		M
ptmsi	c_MobileIdPTMSI_Any IF_PRESENT		O
ptmsiSignature	c_PTMSI_Signature_tlv(?) IF_PRESENT		O
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_ServiceRequest (p_sType : ServiceType_v; p_PTMSI : MS_Identity_Iv; p_KeySeq : KeySeq)			
Group :			
PDU Type : SERVICEREQUEST			
Derivation Path :			
Encoding Rule Name :			
Encoding Variation :			
Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00001100'B		
serviceType	p_sType		
ciphKeySeqNo	c_CiphKeySeqNum(p_KeySeq)		
ptmsi	p_PTMSI		
pDP_ContextStatus	cr_PDP_ContextStatusAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_SetupMO_Any Group : PDU Type : SETUPul Derivation Path : Encoding Rule Name : Encoding Variation : Comments : CC SETUP n <- ue			
Field Name	Field Value	Field Encoding	Comments
ti	cr_TI_MO		
cC_ProtocolDiscriminator	'0011'B		
msgType	'??000101'B		
repeatInd	c_RepeatIndAny IF_PRESENT		
bcap1	cr_BcapAnyMO		
bcap2	cr_BcapAnyMO IF_PRESENT		
facility	cr_Facility_Any IF_PRESENT		
cgps	cr_CGPS_Any IF_PRESENT		
cdpn	cr_CDPN_Any		
cdps	cr_CDPS_Any IF_PRESENT		
llcRepeatInd	c_RepeatIndAny IF_PRESENT		
llc1	cr_LLC_Any IF_PRESENT		
llc2	cr_LLC_Any IF_PRESENT		
hlcRepeatInd	c_RepeatIndAny IF_PRESENT		
hlc1	cr_HLC_Any IF_PRESENT		
hlc2	cr_HLC_Any IF_PRESENT		
userUser	cr_UserUserAny IF_PRESENT		
sS_VersionInd	cr_SS_VersionIndAny IF_PRESENT		
cLIR_Suppression	'10100001'B IF_PRESENT		
cLIR_Invocation	'10100010'B IF_PRESENT		
cC_Capabilities	cr_CC_CapabilitiesAny IF_PRESENT		
facilityCCBS_AdvRecall	cr_FacilityAdvRecall IF_PRESENT		
facilityCCBS_RecallAlign	cr_FacilityAdvRecall IF_PRESENT		
streamId	cr_StreamIdAny IF_PRESENT		
supportedCodecs	cr_CodecListAny IF_PRESENT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cr_StatusAny Group : PDU Type : STATUS_PDU Derivation Path : Encoding Rule Name : Encoding Variation : Comments : This constraint is used to receive an AM STATUS PDU containing the given SUFI list. Any padding included is ignored.			
Field Name	Field Value	Field Encoding	Comments
dC_Field	tsc_DC_ControlPDU		
type	tsc_PDU_TypeStatus		
superFieldsTx	OMIT		
superFieldsAndPadRx	?		
paddingTx	OMIT		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cs_AttachAcc (p_attachRes : AttachResult; p_RAI : RAI_v; p_PTMSIsig : PTMSI_Signature; p_PTMSI : GMM_MS_IdentityPTMSI; p_TMSI : GMM_MS_Identity) Group : PDU Type : ATTACHACCEPT Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000010'B		
forceToStandby	c_ForceToStandby('000'B)		Force to standby not indicated
attachResult	p_attachRes		
periodicRAupdateTimer	c_GPRS_Timer_v('111'B, '00000'B)		Timer deactivated
radioPrioTOM8	c_RadioPriority2('000'B)		Lowest
radioPrioSMS	c_RadioPriority('100'B)		Lowest
rai	p_RAI		
ptmsiSignature	p_PTMSIsig		
negReadyTimer	–		
allocatedPTMSI	p_PTMSI		
msIdentity	p_TMSI		Only required in PS/CS combined procedures
gmmCause	–		
t3302Value	–		
cellNotification	–		
equivalentPLMN	–		
ntwFeatureSupport	–		
emergNumList	–		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cs_AttachAccE_PLMN (p_attachRes : AttachResult; p_RAI : RAI_v; p_PTMSIsig : PTMSI_Signature; p_PTMSI : GMM_MS_IdentityPTMSI; p_TMSI : GMM_MS_Identity; p_ePLMN : PLMN_List) Group : PDU Type : ATTACHACCEPT Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gMMProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000010'B		
forceToStandby	c_ForceToStandby('000'B)		Force to standby not indicated
attachResult	p_attachRes		
periodicRAupdateTimer	c_GPRS_Timer_v('111'B, '00000'B)		Timer deactivated
radioPrioTOM8	c_RadioPriority2('000'B)		Lowest
radioPrioSMS	c_RadioPriority('100'B)		Lowest
rai	p_RAI		
ptmsiSignature	p_PTMSIsig		
negReadyTimer	–		
allocatedPTMSI	p_PTMSI		
msIdentity	p_TMSI		Only required in PS/CS combined procedures
gmmCause	–		
t3302Value	–		
cellNotification	–		
equivalentPLMN	p_ePLMN		
ntwFeatureSupport	–		
emergNumList	–		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cs_AttachRej(p_cause : RejCau) Group : PDU Type : ATTACHREJECT Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gmmProtocolDiscriminator	tsc_GMM_PD		
msgType	'00000100'B		
gmmCause	p_cause		
t3302Value	–		
Detailed Comments :			

PDU Constraint Declaration			
Constraint Name : cs_AuthAndCiphReq(p_rand : AuthenticationParamterRAND; p_ckNo : CiphKeySeqNum_tv; p_authn : GMM_AUTN) Group : PDU Type : AUTHENTICATIONANDCIPHERINGREQUEST Derivation Path : Encoding Rule Name : Encoding Variation : Comments :			
Field Name	Field Value	Field Encoding	Comments
skipIndicator	'0000'B		
gmmProtocolDiscriminator	tsc_GMM_PD		
msgType	'00010010'B		
imeisvReq	cb_IMEISV_Request('000'B)		IMEISV not requested
ciphAlgorithm	c_CiphAlgorithm(px_Cipher Alg)		GPRS encryption algorithm GEA/1
acRefNo	c_AC_RefNum3		Use any reference value
forceToStandby	c_ForceToStandby('000'B)		Force to standby not indicated
authRand	p_rand		Authentication paramter RAND
gprsCiphKeySeqNo	p_ckNo		GPRS ciphering key sequence number
aUTN	p_authn		Authentication paramter AUTN, an UMTS challenge is requested
Detailed Comments :			

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_MeasurementControlDefPeriodic (p_IntegrityCheckInfo : IntegrityCheckInfo; p_RRC_TI: RRC_TransactionIdentifier; p_primaryScramblingCode : PrimaryScramblingCode)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Default MEASUREMENT CONTROL message (see 3GPP 34.123–1 Annex A) with Periodic
Constraint Value	
<pre> { integrityCheckInfo p_IntegrityCheckInfo, message measurementControl : r3 : { measurementControl_r3 { rrc_TransactionIdentifier p_RRC_TI, measurementIdentity 1, measurementCommand setup : intraFrequencyMeasurement : { intraFreqCellInfoList { removedIntraFreqCellList OMIT, newIntraFreqCellList { { intraFreqCellID 1, cellInfo { cellIndividualOffset 0, referenceTimeDifferenceToCell OMIT, modeSpecificInfo fdd : { primaryCPICH_Info { primaryScramblingCode p_primaryScramblingCode }, primaryCPICH_TX_Power OMIT, readSFN_Indicator FALSE, tx_DiversityIndicator FALSE } } } }, cellsForIntraFreqMeasList OMIT }, intraFreqMeasQuantity { filterCoefficient OMIT, -- default value modeSpecificInfo fdd : { intraFreqMeasQuantity_FDD cpich_RSCP } }, intraFreqReportingQuantity { activeSetReportingQuantities { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } } }, monitoredSetReportingQuantities } } } } </pre>	

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ASN.1 PDU Constraint Declaration	
	Constraint Value
	<pre> { dummy noReport, cellIdentity_reportingIndicator TRUE, cellSynchronisationInfoReportingIndicator FALSE, modeSpecificInfo fdd : { cpich_Ec_N0_reportingIndicator FALSE, cpich_RSCP_reportingIndicator TRUE, pathloss_reportingIndicator FALSE } }, detectedSetReportingQuantities OMIT }, measurementValidity OMIT, reportCriteria periodicalReportingCriteria : { periodicalReportingCriteria { reportingAmount ra1, reportingInterval ril0_25}, reportingCellStatus withinActiveAndOrMonitoredUsedFreq : e2 } }, measurementReportingMode { measurementReportTransferMode acknowledgedModeRLC, periodicalOrEventTrigger periodical }, additionalMeasurementList OMIT, dpch_CompressedModeStatusInfo OMIT }, v390nonCriticalExtensions OMIT } } </pre>
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_MeasurementReportAny
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
	Constraint Value
	<pre> { integrityCheckInfo *, message measurementReport : { measurementIdentity *, measuredResults *, measuredResultsOnRACH *, additionalMeasuredResults *, eventResults *, v390nonCriticalExtensions * } } </pre>
Detailed Comments :	

ASN.1 PDU Constraint Declaration

Constraint Name : cs_RRC_RB_SetUp_DSCH (

p_IntegrityInfo : IntegrityCheckInfo;

p_RRC_Ti : RRC_TransactionIdentifier;

p_Activetime : ActivationTime;

p_RRCStateInd : RRC_StateIndicator;

p_FreqInfo : FrequencyInfo;

p_Rablist : RAB_InformationSetupList;

p_ULCommTrChInfo : UL_CommonTransChInfo;

p_UIAddReconfTrChInfo : UL_AddReconfTransChInfoList;

p_DCommTrChInfo : DL_CommonTransChInfo;

p_DIAddReconfTrChInfo : DL_AddReconfTransChInfoList;

p_DLInfoPerRI : DL_InformationPerRL_List;

p_DL_CommonInformation : DL_CommonInformation;

p_UL_DPCH_Info : UL_DPCH_Info;

p_RB_InformationAffectedList : RB_InformationAffectedList;

p_PDSCH_Information : DL_PDSCH_Information;

p_DSCH_RNTI : DSCH_RNTI

)

Group :

PDU Type : DL_DCCH_Message

Derivation Path :

Encoding Rule Name :

Encoding Variation :

Comments :

Constraint Value

```
{
  integrityCheckInfo p_IntegrityInfo,
  message radioBearerSetup: r3 : {
    radioBearerSetup_r3 {
      rrc_TransactionIdentifier p_RRC_Ti,
      integrityProtectionModelInfo OMIT,
      cipheringModelInfo OMIT,
      activationTime p_Activetime,
      new_U_RNTI OMIT,
      new_C_RNTI OMIT,
      rrc_StateIndicator p_RRCStateInd,
      utran_DRX_CycleLengthCoeff OMIT,
      cn_InformationInfo OMIT,
      srb_InformationSetupList OMIT,
      rab_InformationSetupList p_Rablist,
      rb_InformationAffectedList p_RB_InformationAffectedList ,
      ul_CommonTransChInfo p_ULCommTrChInfo,
      ul_deletedTransChInfoList OMIT,
      ul_AddReconfTransChInfoList p_UIAddReconfTrChInfo,
      modeSpecificTransChInfo fdd:{
        cpch_SetID OMIT,
        addReconfTransChDRAC_Info OMIT
      },
      dl_CommonTransChInfo p_DCommTrChInfo,
      dl_DeletedTransChInfoList OMIT,
      dl_AddReconfTransChInfoList p_DIAddReconfTrChInfo,
      frequencyInfo p_FreqInfo,
      maxAllowedUL_TX_Power tsc_MaxAllowPwr,
      ul_ChannelRequirement ul_DPCH_Info : p_UL_DPCH_Info,
      modeSpecificPhysChInfo fdd:{
        dl_PDSCH_Information p_PDSCH_Information
      },
      dl_CommonInformation p_DL_CommonInformation,
      dl_InformationPerRL_List p_DLInfoPerRI
    },
    v3a0NonCriticalExtensions {
      radioBearerSetup_v3a0ext { new_DSCH_RNTI p_DSCH_RNTI }
    }
  }
}
```

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ASN.1 PDU Constraint Declaration	
Constraint Value	
{ } }	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_RB_SetUpFACH_PS_2SCCPCH_2a (p_IntegrityInfo : IntegrityCheckInfo; p_RRC_Ti : RRC_TransactionIdentifier; p_RAB_Id : BITSTRING; p_RAB_Id2 : BITSTRING; p_NewC_RNTI : C_RNTI)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.123-1 annex A condition A.6
Constraint Value	
<pre> { integrityCheckInfo p_IntegrityInfo, message radioBearerSetup: r3 : { radioBearerSetup_r3 { rrc_TransactionIdentifier p_RRC_Ti, integrityProtectionModelInfo OMIT, cipheringModelInfo OMIT, activationTime OMIT, new_U_RNTI OMIT, new_C_RNTI p_NewC_RNTI , rrc_StateIndicator cell_FACH, utran_DRX_CycleLengthCoeff OMIT, cn_InformationInfo OMIT, srb_InformationSetupList OMIT, rab_InformationSetupList cb_RAB_InfoListAM2_No_Pdcp_2a (c_ReEstTimerT315, p_RAB_Id, p_RAB_Id2), rb_InformationAffectedList OMIT , ul_CommonTransChInfo c_UL_CommTrChInfoDCH_PS_64k, ul_deletedTransChInfoList OMIT, ul_AddReconfTransChInfoList c_UL_AddReconfTransChInfoListDCH_PS_64k, modeSpecificTransChInfo fdd:{ cpch_SetID OMIT, addReconfTransChDRAC_Info OMIT }, dl_CommonTransChInfo c_DL_CommonTransChInfoDCH (c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx), dl_DeletedTransChInfoList OMIT, dl_AddReconfTransChInfoList c_DL_AddReconfTransChInfoListDCH_PS_64k, frequencyInfo OMIT, maxAllowedUL_TX_Power OMIT, ul_ChannelRequirement OMIT, modeSpecificPhysChInfo fdd:{ dl_PDSCH_Information OMIT }, dl_CommonInformation OMIT , dl_InformationPerRL_List OMIT }, v3a0NonCriticalExtensions { radioBearerSetup_v3a0ext { new_DSCH_RNTI OMIT }, laterNonCriticalExtensions OMIT } } } </pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbs_TransportFormatCombCtrl (p_IntegrityCheckInfo : IntegrityCheckInfo ; p_RRC_Ti : RRC_TransactionIdentifier; p_TFC : TFC_Subset)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Transport Format Combination Control message used to restrict the UL TFCIs
Constraint Value	
<pre>{ integrityCheckInfo p_IntegrityCheckInfo , message transportFormatCombinationControl : { rrc_TransactionIdentifier p_RRC_Ti, modeSpecificInfo fdd : NULL, dpch_TFCS_InUplink p_TFC, tfc_ControlDuration OMIT, laterNonCriticalExtensions OMIT } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_108_PagingType2 (p_IntegrityCheckInfo : IntegrityCheckInfo; p_RRC_Ti: RRC_TransactionIdentifier; p_Domain : CN_DomainIdentity; p_PagingCause : PagingCause)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityCheckInfo p_IntegrityCheckInfo, message pagingType2: c_PagingType2(p_RRC_Ti, p_Domain, p_PagingCause) }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbr_108_RB_SetUpCmpl (p_RRC_Ti : RRC_TransactionIdentifier; p_Count_C_ActivationTime : ActivationTime; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9. p_Count_C_ActivationTime : The presence of this IE depends on the following 2 factors: (a) There exists RB(s) mapped to RLC–TM and (b) UE is transiting to CELL_DCH state after the RB establishment procedure. Else, this IE is absent p_RB_ActivationTimeInfoList : If ciphering is not activated in RADIO BEARER SETUP message, this IE must be absent. Else, SS checks this IE for the presence of activation times of all ciphered uplink RLC–UM and RLC–AM RBs
Constraint Value	
<pre>{ integrityCheckInfo *, message radioBearerSetupComplete : { rrc_TransactionIdentifier p_RRC_Ti, ul_IntegProtActivationInfo *, ul_TimingAdvance *, start_Value ?, count_C_ActivationTime p_Count_C_ActivationTime, rb_UL_CiphActivationTimeInfo p_RB_ActivationTimeInfoList, ul_CounterSynchronisationInfo *, laterNonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbr_108_RRC_ConnRelCmpl (p_RRC_Ti: RRC_TransactionIdentifier)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo *, message rrcConnectionReleaseComplete : { rrc_TransactionIdentifier p_RRC_Ti, errorIndication *, laterNonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbr_108_RRC_ConnReq (p_EstCause: EstablishmentCause)
Group	:
PDU Type	: UL_CCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo OMIT, message rrcConnectionRequest : { initialUE_Identity (imsi : ? , tmsi_and_LAI : ? , p_TMSI_and_RAI : ?, imei : ?), establishmentCause p_EstCause, protocolErrorIndicator noError, measuredResultsOnRACH *, v3d0NonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbr_108_RRC_SecModeCmpl (p_RRC_Ti : RRC_TransactionIdentifier; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo *, message securityModeComplete : { rrc_TransactionIdentifier p_RRC_Ti , ul_IntegProtActivationInfo *, rb_UL_CiphActivationTimeInfo p_RB_ActivationTimeInfoList, laterNonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbs_108_RB_SetUpFACH_PS (p_IntegrityInfo : IntegrityCheckInfo; p_RRC_Ti : RRC_TransactionIdentifier; p_RAB_Id : BITSTRING; p_NewC_RNTI : C_RNTI)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.123-1 annex A condition A.6
Constraint Value	
<pre> { integrityCheckInfo p_IntegrityInfo, message radioBearerSetup: r3 : { radioBearerSetup_r3 { rrc_TransactionIdentifier p_RRC_Ti, integrityProtectionModelInfo OMIT, cipheringModelInfo OMIT, activationTime OMIT, new_U_RNTI OMIT, new_C_RNTI p_NewC_RNTI , rrc_StateIndicator cell_FACH, utran_DRX_CycleLengthCoeff OMIT, cn_InformationInfo OMIT, srb_InformationSetupList OMIT, rab_InformationSetupList c_RAB_InfoListFACH_PS (useT315, p_RAB_Id, c_RLC_InfoAM_Def), rb_InformationAffectedList OMIT , ul_CommonTransChInfo c_UL_CommTrChInfoDCH_PS_64k, ul_deletedTransChInfoList OMIT, ul_AddReconfTransChInfoList c_UL_AddReconfTransChInfoListDCH_PS_64k, modeSpecificTransChInfo fdd:{ cpch_SetID OMIT, addReconfTransChDRAC_Info OMIT }, dl_CommonTransChInfo c_DL_CommonTransChInfoDCH (c_TFCS_Cmpl0_1_2_3_4_5_6_7_8_9_Rx), dl_DeletedTransChInfoList OMIT, dl_AddReconfTransChInfoList c_DL_AddReconfTransChInfoListDCH_PS_64k, frequencyInfo OMIT, maxAllowedUL_TX_Power OMIT, ul_ChannelRequirement OMIT, modeSpecificPhysChInfo fdd:{ dl_PDSCH_Information OMIT }, dl_CommonInformation OMIT , dl_InformationPerRL_List OMIT }, v3a0NonCriticalExtensions { radioBearerSetup_v3a0ext { new_DSCH_RNTI OMIT }, laterNonCriticalExtensions OMIT } } } </pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cbs_108_RRC_ConnSetupDCH (p_InitUEId : InitialUE_Identity; p_RRC_Ti : RRC_TransactionIdentifier; p_PrmbScrmCode : PrimaryScramblingCode; p_U_RNTI_New : U_RNTI; p_UL_ScramblingCode : UL_ScramblingCode)
Group	:
PDU Type	: DL_CCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre> { integrityCheckInfo OMIT, message rrcConnectionSetup: r3 : { rrcConnectionSetup_r3 --RRCConnectionSetup_r3_IEs { initialUE_Identity p_InitUEId, rrc_TransactionIdentifier p_RRC_Ti, activationTime OMIT, new_U_RNTI p_U_RNTI_New , new_c_RNTI OMIT, rrc_StateIndicator cell_DCH , utran_DRX_CycleLengthCoeff 9, capabilityUpdateRequirement { ue_RadioCapabilityFDDUpdateRequirement TRUE, ue_RadioCapabilityTDDUpdateRequirement FALSE, systemSpecificCapUpdateReqList {gsm} }, srb_InformationSetupList { c_SRB_InfoSetupUM_DCH (tsc_UL_DCCH1, tsc_UL_MAC_Prt1, tsc_UL_MAC_Prt1, tsc_DL_DCCH1), c_SRB_InfoSetupAM_DCH (tsc_UL_DCCH2, tsc_UL_MAC_Prt2, tsc_UL_MAC_Prt2, tsc_DL_DCCH2), c_SRB_InfoSetupAM_DCH (tsc_UL_DCCH3, tsc_UL_MAC_Prt3, tsc_UL_MAC_Prt3, tsc_DL_DCCH3), c_SRB_InfoSetupAM_DCH (tsc_UL_DCCH4, tsc_UL_MAC_Prt4, tsc_UL_MAC_Prt4, tsc_DL_DCCH4) }, ul_CommonTransChInfo c_UL_CommTrChInfoDCCH_13_6k, ul_AddReconfTransChInfoList c_UL_AddReconfTransChInfoListDCCH_13_6k, dl_CommonTransChInfo c_DL_CommonTransChInfoSameAsUL, dl_AddReconfTransChInfoList c_DL_AddReconfTransChInfoListDCCH_SRB, frequencyInfo OMIT, maxAllowedUL_TX_Power OMIT, ul_ChannelRequirement ul_DPCH_Info : c_UL_DPCH_13_6_StandAlone (p_UL_ScramblingCode), dl_CommonInformation cd_DL_CommonInformationDCH_DPCH_Offset (tsc_DL_DPCH1_SFP_SRB), dl_InformationPerRL_List c_DL_InfoPerRL_DPCH_Offset (p_PrmbScrmCode, tsc_DL_DPCH1_2ndScrC, tsc_DL_DPCH1_ChC_SRB) }, laterNonCriticalExtensions OMIT } } </pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name : cbs_108_RRC_ConnSetupFACH (<p> p_InitUEId : InitialUE_Identity; p_RRC_Ti : RRC_TransactionIdentifier; p_PrmbScrmCode: PrimaryScramblingCode; p_U_RNTI_New : U_RNTI; p_CRNTI_New : C_RNTI; p_UL_ScramblingCode : UL_ScramblingCode </p>)	
Group :	
PDU Type :	DL_CCCH_Message
Derivation Path :	
Encoding Rule Name :	
Encoding Variation :	
Comments :	Defined in TS 34.123-1 annex A
Constraint Value	
<pre> { integrityCheckInfo OMIT, message rrcConnectionSetup: r3 : { rrcConnectionSetup_r3 --RRCConnectionSetup_r3_IEs { initialUE_Identity p_InitUEId, rrc_TransactionIdentifier p_RRC_Ti , activationTime OMIT, new_U_RNTI p_U_RNTI_New , new_c_RNTI p_CRNTI_New, rrc_StateIndicator cell_FACH , utran_DRX_CycleLengthCoeff 9, capabilityUpdateRequirement { ue_RadioCapabilityFDDUpdateRequirement TRUE, ue_RadioCapabilityTDDUpdateRequirement FALSE, systemSpecificCapUpdateReqList {gsm} }, srb_InformationSetupList { c_SRB_InfoSetupUM_FACH (tsc_RB1, tsc_UL_DCCH1, tsc_UL_MAC_Prt1, tsc_UL_MAC_Prt1, tsc_DL_DCCH1), c_SRB_InfoSetupAM_FACH (tsc_RB2,tsc_UL_DCCH2, tsc_UL_MAC_Prt2, tsc_UL_MAC_Prt2, tsc_DL_DCCH2), c_SRB_InfoSetupAM_FACH (tsc_RB3, tsc_UL_DCCH3, tsc_UL_MAC_Prt3, tsc_UL_MAC_Prt3, tsc_DL_DCCH3), c_SRB_InfoSetupAM_FACH (tsc_RB4, tsc_UL_DCCH4, tsc_UL_MAC_Prt4, tsc_UL_MAC_Prt4, tsc_DL_DCCH4) }, ul_CommonTransChInfo c_UL_CommTrChInfoDCCH_13_6k, ul_AddReconfTransChInfoList c_UL_AddReconfTransChInfoListDCCH_3_4k, dl_CommonTransChInfo c_DL_CommonTransChInfoSameAsUL, dl_AddReconfTransChInfoList c_DL_AddReconfTransChInfoListDCCH_SRB, frequencyInfo OMIT, maxAllowedUL_TX_Power OMIT, ul_ChannelRequirement OMIT, dl_CommonInformation OMIT, dl_InformationPerRL_List OMIT }, laterNonCriticalExtensions OMIT } } </pre>	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_108_RRC_ConnSetupCmpl (p_RRC_Ti : RRC_TransactionIdentifier; p_STARTList : STARTList)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo OMIT, message rrcConnectionSetupComplete : { rrc_TransactionIdentifier p_RRC_Ti, startList p_STARTList , ue_RadioAccessCapability *, ue_RATSpecificCapability *, v370NonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_108_SecModeFail (p_RRC_Ti : RRC_TransactionIdentifier; p_FailureCauseWithProtErr : FailureCauseWithProtErr)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityCheckInfo * , message securityModeFailure : { rrc_TransactionIdentifier p_RRC_Ti, failureCause p_FailureCauseWithProtErr, laterNonCriticalExtensions * } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_RRC_MeasRep
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityCheckInfo *, message measurementReport : ? }</pre>	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_RRC_RB_SetUpCmplNoStartVal (p_RRC_Ti : RRC_TransactionIdentifier; p_Count_C_ActivationTime : ActivationTime; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList)
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: START value is omitted p_Count_C_ActivationTime : The presence of this IE depends on the following 2 factors: (a) There exists RB(s) mapped to RLC-TM and (b) UE is transiting to CELL_DCH state after the RB establishment procedure. Else, this IE is absent p_RB_ActivationTimeInfoList : If ciphering is not activated in RADIO BEARER SETUP message, this IE must be absent. Else, SS checks this IE for the presence of activation times of all ciphered uplink RLC-UM and RLC-AM RBs
Constraint Value	
<pre>{ integrityCheckInfo *, message radioBearerSetupComplete : { rrc_TransactionIdentifier p_RRC_Ti, ul_IntegProtActivationInfo *, ul_TimingAdvance *, start_Value OMIT, count_C_ActivationTime p_Count_C_ActivationTime, rb_UL_CiphActivationTimeInfo p_RB_ActivationTimeInfoList, laterNonCriticalExtensions * } }</pre>	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_RRC_RrcConnReqAny
Group	:
PDU Type	: UL_CCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityCheckInfo OMIT, message rrcConnectionRequest : { initialUE_Identity ?, establishmentCause ?, protocolErrorIndicator noError, measuredResultsOnRACH *, v3d0NonCriticalExtensions * } }</pre>	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cr_RRC_RrcStatus
Group	:
PDU Type	: UL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ integrityCheckInfo *, message rrcStatus : ? }</pre>	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_108_RRC_ConnRelCCCH (p_U_rnti: U_RNTI; p_RRC_Ti: RRC_TransactionIdentifier)
Group	:
PDU Type	: DL_CCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo OMIT, message rrcConnectionRelease : r3 : { rrcConnectionRelease_CCCH_r3 { u_RNTI p_U_rnti, rrcConnectionRelease { rrc_TransactionIdentifier p_RRC_Ti, n_308 OMIT, releaseCause normalEvent } }, laterNonCriticalExtensions OMIT } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_108_RRC_ConnRelDCCH (p_IntegrityCheckInfo : IntegrityCheckInfo; p_RRC_Ti: RRC_TransactionIdentifier; p_N308: INTEGER)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
<pre>{ integrityCheckInfo p_IntegrityCheckInfo, message rrcConnectionRelease : r3 : { rrcConnectionRelease_r3 { rrc_TransactionIdentifier p_RRC_Ti, n_308 p_N308, releaseCause normalEvent, rplmn_information OMIT }, laterNonCriticalExtensions OMIT } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_108_RRC_SecModeCmd (p_IntegrityCheckInfo : IntegrityCheckInfo; p_SecModeCmd: SecurityModeCommand)
Group	:
PDU Type	: DL_DCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: Defined in TS 34.108 clause 9.
Constraint Value	
{ integrityCheckInfo p_IntegrityCheckInfo, message securityModeCommand : p_SecModeCmd }	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_RRC_PagingType1_ModifySI(p_mib_valuetag: MIB_ValueTag)
Group	:
PDU Type	: PCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
{ message pagingType1: { --PagingType1 pagingRecordList OMIT, bcch_ModificationInfo { mib_ValueTag p_mib_valuetag, bcch_ModificationTime OMIT -- changed immediately }, laterNonCriticalExtensions OMIT } }	
Detailed Comments :	

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_RRC_PagingType1_PTMSI (p_PagCause : PagingCause; p_P_tmsi : P_TMSI_GSM_MAP; p_Domain : CN_DomainIdentity)
Group	:
PDU Type	: PCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
{ message pagingType1: c_PagingType1_P_TMSI (p_PagCause, p_P_tmsi, p_Domain) }	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_RRC_PagingType1_TMSI(p_PagCause: PagingCause; p_Tmsi:TMSI_GSM_MAP; p_Domain : CN_DomainIdentity)
Group	:
PDU Type	: PCCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	:
Constraint Value	
{ message pagingType1: c_PagingType1_TMSI(p_PagCause, p_Tmsi, p_Domain) }	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name : cs_RRC_RB_SetUp (p_IntegrityInfo : IntegrityCheckInfo; p_RRC_Ti : RRC_TransactionIdentifier; p_Activetime : ActivationTime; p_RRCStateInd : RRC_StateIndicator; p_FreqInfo : FrequencyInfo; p_Rablist : RAB_InformationSetupList; p_ULCommTrChInfo : UL_CommonTransChInfo; p_UIAddReconfTrChInfo : UL_AddReconfTransChInfoList; p_DICommTrChInfo : DL_CommonTransChInfo; p_DIAddReconfTrChInfo : DL_AddReconfTransChInfoList; p_DLInfoPerRI : DL_InformationPerRL_List; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info; p_RB_InformationAffectedList : RB_InformationAffectedList)	
Group :	
PDU Type :	DL_DCCH_Message
Derivation Path :	
Encoding Rule Name :	
Encoding Variation :	
Comments :	
Constraint Value	
<pre> { integrityCheckInfo p_IntegrityInfo, message radioBearerSetup: r3 : { radioBearerSetup_r3 { rrc_TransactionIdentifier p_RRC_Ti, integrityProtectionModelInfo OMIT, cipheringModelInfo OMIT, activationTime p_Activetime, new_U_RNTI OMIT, new_C_RNTI OMIT, rrc_StateIndicator p_RRCStateInd, utran_DRX_CycleLengthCoeff OMIT, cn_InformationInfo OMIT, srb_InformationSetupList OMIT, rab_InformationSetupList p_Rablist, rb_InformationAffectedList p_RB_InformationAffectedList , ul_CommonTransChInfo p_ULCommTrChInfo, ul_deletedTransChInfoList OMIT, ul_AddReconfTransChInfoList p_UIAddReconfTrChInfo, modeSpecificTransChInfo fdd:{ cpch_SetID OMIT, addReconfTransChDRAC_Info OMIT }, dl_CommonTransChInfo p_DICommTrChInfo, dl_DeletedTransChInfoList OMIT, dl_AddReconfTransChInfoList p_DIAddReconfTrChInfo, frequencyInfo p_FreqInfo, maxAllowedUL_TX_Power tsc_MaxAllowPwr, ul_ChannelRequirement ul_DPCH_Info : p_UL_DPCH_Info, modeSpecificPhysChInfo fdd:{ dl_PDSCH_Information OMIT }, dl_CommonInformation p_DL_CommonInformation, dl_InformationPerRL_List p_DLInfoPerRI }, v3a0NonCriticalExtensions { radioBearerSetup_v3a0ext { new_DSCH_RNTI OMIT }, laterNonCriticalExtensions OMIT } } } </pre>	

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ASN.1 PDU Constraint Declaration
Constraint Value
<pre> }</pre>
Detailed Comments :

ASN.1 PDU Constraint Declaration
Constraint Name : cs_SIB_MsgCmpl(p_SIBType : SIB_Type; p_SIBData: SIB_Data_fixed) Group : PDU Type : BCCH_BCH_Message Derivation Path : Encoding Rule Name : Encoding Variation : Comments : System information message contains complete SIB/SB/MIB. The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value
<pre> { message { sfn_Prime 0, payload completeSIB : { sib_Type p_SIBType, sib_Data_fixed p_SIBData } } } }</pre>
Detailed Comments :

ASN.1 PDU Constraint Declaration
Constraint Name : cs_SIB_MsgCmplList1(p_SIBType : SIB_Type; p_SIBData: SIB_Data_variable) Group : PDU Type : BCCH_BCH_Message Derivation Path : Encoding Rule Name : Encoding Variation : Comments : System information message contains complete SIB/SB/MIB. The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value
<pre> { message { sfn_Prime 0, payload completeSIB_List : {{ sib_Type p_SIBType, sib_Data_variable p_SIBData }} } } }</pre>
Detailed Comments :

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgCmplList2(p_SIBType1 : SIB_Type; p_SIBData1:SIB_Data_variable; p_SIBType2 :SIB_Type; p_SIBData2: SIB_Data_variable)
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: System information message contains two complete SIB's. The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value	
<pre>{ message { sfn_Prime 0, payload completeSIB_List : {{ sib_Type p_SIBType1, sib_Data_variable p_SIBData1 }, { sib_Type p_SIBType2, sib_Data_variable p_SIBData2 }} } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgFirst(p_SIBType : SIB_Type; p_SegCount: INTEGER; p_SIBData: SIB_Data_fixed)
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value	
<pre>{ message { sfn_Prime 0, -- place holder payload firstSegment : { sib_Type p_SIBType, seg_Count p_SegCount, sib_Data_fixed p_SIBData } } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgLast(p_SIBType : SIB_Type; p_SegIndex : INTEGER; p_SIBData: SIB_Data_fixed)
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: The actual value of sfn_Prime shall be filled in by lower layer before sent on air. this message contains last segment which is 222 bits long.
Constraint Value	
<pre>{ message { sfn_Prime 0, payload lastSegment : { sib_Type p_SIBType, segmentIndex p_SegIndex, sib_Data_fixed p_SIBData } } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgLastShort(p_SIBType : SIB_Type; p_SegIndex : INTEGER; p_SIBData: SIB_Data_variable)
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: The actual value of sfn_Prime shall be filled in by lower layer before sent on air. this message contains last segment which is short than 215 bits.
Constraint Value	
<pre>{ message { sfn_Prime 0, payload lastSegmentShort : { sib_Type p_SIBType, segmentIndex p_SegIndex, sib_Data_variable p_SIBData } } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgNoSegment
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: System information message contains no SIB, sent on the frame which there is no any SIB/SB/MIB scheduled on. The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value	
<pre>{ message { sfn_Prime 0, payload noSegment : NULL } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SIB_MsgSubsequent(p_SIBType : SIB_Type; p_SegIndex : INTEGER; p_SIBData: SIB_Data_fixed)
Group	:
PDU Type	: BCCH_BCH_Message
Derivation Path	:
Encoding Rule Name	:
Encoding Variation	:
Comments	: The actual value of sfn_Prime shall be filled in by lower layer before sent on air.
Constraint Value	
<pre>{ message { sfn_Prime 0, payload subsequentSegment : { sib_Type p_SIBType, segmentIndex p_SegIndex, sib_Data_fixed p_SIBData } } }</pre>	
Detailed Comments	:

ASN.1 PDU Constraint Declaration	
Constraint Name	: cs_SysInfoChangeInd(p_mib_valuetag: MIB_ValueTag)
Group	:
PDU Type	: BCCH_FACH_Message
Derivation Path	:
Encoding Rule Name	: PER_Unaligned
Encoding Variation	:
Comments	:
Constraint Value	
<pre>{ message systemInformationChangeIndication : { --System Information Change Indication bcch_ModificationInfo { mib_ValueTag p_mib_valuetag, bcch_ModificationTime OMIT --0 }, laterNonCriticalExtensions OMIT } }</pre>	
Detailed Comments	:

IV

Dynamic Part

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_4					
Group : CombinationOnDPCH/Conversational_Speech/					
Purpose : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.4.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Speech_C110					
Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (speech_12_2k, terminatingConversationalCall, terminatingConversationalCall)			Steps 1–10
4		+ ts_RB_SubTest_RB10 (tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4, c_TFC_Allowed_0_1_3, cb_UE_TestLoopMode1LB_Setup3 (39, tsc_RB10, 103, tsc_RB11, 60, tsc_RB12), 39)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_RB11_RB12 (tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3, cb_UE_TestLoopMode1LB_Setup3 (81, tsc_RB10, 103, tsc_RB11, 60, tsc_RB12), 81, 103, 60)			Subtest 2 Steps 11–17
6		(tcv_TestBody := FALSE)			Steps 20–21
7		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
8		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_4a Group : CombinationOnDPCH/Conversational_Speech/ Purpose : Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.4a. Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			
2		+ ts_InitVariables			Initial Test Case Variables
3		+ ts_RB_InitTest_CS (speech_12_2k_7_95k_5_9k_4_75k, terminatingConversationalCall, terminatingConversationalCall)			Steps 1–10
4		+ ts_RB_SubTest_RB10 (tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_6, cb_UE_TestLoopMode1LB_Setup3 (39, tsc_RB10, 103, tsc_RB11, 60, tsc_RB12), 39)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_RB11(tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4_5_6_8, c_TFC_Allowed_0_2_6, cb_UE_TestLoopMode1LB_Setup3 (42, tsc_RB10, 53, tsc_RB11, 60, tsc_RB12) , 42, 53)			Subtest 2 Steps 11–17
6		+ ts_RB_SubTest_RB10_RB11(tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4_5_6_9, c_TFC_Allowed_0_3_6, cb_UE_TestLoopMode1LB_Setup3 (55, tsc_RB10, 63, tsc_RB11, 60, tsc_RB12) , 55, 63)			Subtest 3 Steps 11–17
7		+ ts_RB_SubTest_RB10_RB11(tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4_5_6_10, c_TFC_Allowed_0_4_6, cb_UE_TestLoopMode1LB_Setup3 (75, tsc_RB10, 84, tsc_RB11, 60, tsc_RB12) , 75, 84)			Subtest 4 Steps 11–17
8		+ ts_RB_SubTest_RB10_RB11_RB12(tsc_RB_DataSpeech_12_2, c_TFC_Allowed_0_1_2_3_4_5_6_11, c_TFC_Allowed_0_5_6, cb_UE_TestLoopMode1LB_Setup3 (81, tsc_RB10, 103, tsc_RB11, 60, tsc_RB12) , 81, 103, 60)			Subtest 5 Steps 11–17
9	TBE	(tcv_TestBody := FALSE)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_5a Group : CombinationOnDPCH/Conversational_Speech/ Purpose : Conversational / speech / UL:(10.2, 6.7, 5.9, 4.75) DL:(10.2, 6.7, 5.9, 4.75) kbps / CS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.5a. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_C57 Description : Conversational / speech / UL:(10.2, 6.7, 5.9, 4.75) DL:(10.2, 6.7, 5.9, 4.75) kbps / CS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables Steps 1–10
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (speech_10_2k_6_7k_5_9k_4_75k, terminatingConversationalCall, terminatingConversationalCall)			
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_6, cb_UE_TestLoopMode1LB_Setup3 (39, tsc_RB10, 99, tsc_RB11, 40, tsc_RB12), 39)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_8, c_TFC_Allowed_0_2_6, cb_UE_TestLoopMode1LB_Setup3 (42, tsc_RB10, 53, tsc_RB11, 40, tsc_RB12), 42, 53)			Subtest 2 Steps 11–17
6		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_9, c_TFC_Allowed_0_3_6, cb_UE_TestLoopMode1LB_Setup3 (55, tsc_RB10, 63, tsc_RB11, 40, tsc_RB12), 55, 63)			Subtest 3 Steps 11–17
7		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_10, c_TFC_Allowed_0_4_6, cb_UE_TestLoopMode1LB_Setup3 (58, tsc_RB10, 76, tsc_RB11, 40, tsc_RB12), 58, 76)			Subtest 4 Steps 11–17
8		+ ts_RB_SubTest_RB10_RB11_RB12 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_11, c_TFC_Allowed_0_5_6, cb_UE_TestLoopMode1LB_Setup3 (65, tsc_RB10, 99, tsc_RB11, 40, tsc_RB12), 65, 99, 40)			Subtest 5 Steps 11–17
9	TBE	(tcv_TestBody := FALSE)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_7a Group : CombinationOnDPCH/Conversational_Speech/ Purpose : Conversational / speech / UL: (7.4, 6.7, 5.9, 4.75) DL: (7.4, 6.7, 5.9, 4.75) kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.7a. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_C58 Description : Conversational / speech / UL:7.4 DL:7.4 kbps / CS RAB+ UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			
2		+ ts_InitVariables			Initial Test Case Variables
3		+ ts_RB_InitTest_CS (speech_7_4k_6_7k_5_9k_4_75k, terminatingConversationalCall, terminatingConversationalCall)			Steps 1–10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup2 (39, tsc_RB10, 87, tsc_RB11), 39)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_8, c_TFC_Allowed_0_2_6, c_UE_TestLoopMode1_LB_Setup2 (42, tsc_RB10, 53, tsc_RB11), 42, 53)			Subtest 2 Steps 11–17
6		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_9, c_TFC_Allowed_0_3_6, c_UE_TestLoopMode1_LB_Setup2 (55, tsc_RB10, 63, tsc_RB11), 55, 63)			Subtest 3 Steps 11–17
7		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_10, c_TFC_Allowed_0_4_6, c_UE_TestLoopMode1_LB_Setup2 (58, tsc_RB10, 76, tsc_RB11), 58, 76)			Subtests 4 Steps 11–17

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8	TBE	+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4_5_6_11, c_TFC_Allowed_0_5_6, c_UE_TestLoopMode1_LB_Setup2 (61, tsc_RB10, 87, tsc_RB11) , 61, 87)			Subtest 5 Steps 11–17
9		(tcv_TestBody := FALSE)			Steps 20–21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ po_ConnectionAndSS_Rel_14_2_7a (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_9					
Group : CombinationOnDPCH/Conversational_Speech/					
Purpose : Conversational / speech / UL:5.9 DL:5.9 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.9.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Speech_C115					
Description : Conversational / speech / UL:5.9 DL:5.9 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (speech_5_9k, terminatingConversationalCall, terminatingConversationalCall)			Steps 1–10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_4, c_TFC_Allowed_0_1_3, cb_UE_TestLoopMode1LB_Setup3 (39, tsc_RB10, 63, tsc_RB11,60, tsc_RB12), 39)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_RB11 (tsc_RB_TestData_103, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3, cb_UE_TestLoopMode1LB_Setup3 (55, tsc_RB10, 63, tsc_RB11,60, tsc_RB12), 55, 63)			Subtest 2 Steps 11–17
6		(tcv_TestBody := FALSE)			Steps 20–21
7		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
8		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_12					
Group : CombinationOnDPCH/Conversational_unknown/					
Purpose : Conversational / unknown / UL:28.8 DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.12.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Conversational_C118					
Description : Conversational / unknown / UL:28.8 DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (convUnknown_28_8k , terminatingConversationalCall, terminatingConversationalCall)			Steps 1–10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_3_4, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 576)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_TM_CS (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10) , 1152,2)			Subtest 2 Steps 11–17
6		(tcv_TestBody := FALSE)			Steps 20–21
7		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
8		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_13_1					
Group : CombinationOnDPCH/Conversational_unknown/					
Purpose : Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.13 for the 20 ms TTI case.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Conversational_C119					
Description : Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (convUnknown_64k_20, terminatingConversationalCall, terminatingConversationalCall)			
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (640, tsc_RB10), 1280)			Subtest 1 Steps 11-17
5		(tcv_TestBody := FALSE)			
6		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20-21
7		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_13_2					
Group : CombinationOnDPCH/Conversational_unknown/					
Purpose : Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.13 for the 40 ms TTI case.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Conversational_C120					
Description : Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (convUnknown_64k_40, terminatingConversationalCall, terminatingConversationalCall)			Steps 1-10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (640, tsc_RB10), 2560)			Subtest 1 Steps 11-17
5		(tcv_TestBody := FALSE)			Steps 20-21
6		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
7		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_14_1					
Group : CombinationOnDPCH/Conversational_unknown/					
Purpose : Conversational / unknown / UL:32 DL:32 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:32 DL:32 kbps / CS RAB / 20 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.14 for the 20 ms TTI case.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Conversational_C121					
Description : Conversational / unknown / UL:32 DL:32 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:32 DL:32 kbps / CS RAB / 20 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (convUnknown_32k_20, terminatingConversationalCall, terminatingConversationalCall)			Steps 1-10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (640, tsc_RB10), 640)			Subtest 1 Steps 11-17
5		(tcv_TestBody := FALSE)			Steps 20-21
6		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
7		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_14_2					
Group : CombinationOnDPCH/Conversational_unknown/					
Purpose : Conversational / unknown / UL:32 DL:32 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:32 DL:32 kbps / CS RAB / 40 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.14 for the 40 ms TTI case.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Conversational_C122					
Description : Conversational / unknown / UL:32 DL:32 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Conversational / unknown / UL:32 DL:32 kbps / CS RAB / 40 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (convUnknown_32k_40, terminatingConversationalCall, terminatingConversationalCall)			Steps 1-10
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (640, tsc_RB10), 1280)			Subtest 1 Steps 11-17
5		(tcv_TestBody := FALSE)			Steps 20-21
6		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
7		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_15					
Group : CombinationOnDPCH/Streaming_unknown/					
Purpose : Streaming / unknown / UL:14.4/DL:14.4 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.15.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Streaming_C123					
Description : Streaming / unknown / UL:14.4/DL:14.4 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables Steps 1–10
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (streamUnknown_14_4k, terminatingStreamingCall, terminatingStreamingCall)			
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_672, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 576)			Subtest 1 Steps 11–17
5		(tcv_TestBody := FALSE)			
6		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
7		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_16					
Group : CombinationOnDPCH/Streaming_unknown/					
Purpose : Streaming / unknown / UL:28.8/DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.16.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Streaming_C124					
Description : Streaming / unknown / UL:28.8/DL:28.8 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables Steps 1–10
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (streamUnknown_28_8k, terminatingStreamingCall, terminatingStreamingCall)			
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_3_4, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 576)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_TM_CS (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 1152,2)			Subtest 2 Steps 11–17
6		(tcv_TestBody := FALSE)			Steps 20–21
7		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
8		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_17					
Group : CombinationOnDPCH/Streaming_unknown/					
Purpose : Streaming / unknown / UL:57.6/DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.17.					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref : RB_Streaming_C125					
Description : Streaming / unknown / UL:57.6/DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE	START t_Guard			Initial Test Case Variables Steps 1–10
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (streamUnknown_57_6k, terminatingStreamingCall, terminatingStreamingCall)			
4		+ ts_RB_SubTest_RB10 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 576)			Subtest 1 Steps 11–17
5		+ ts_RB_SubTest_RB10_TM_CS (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_2_5, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 1152,2)			Subtest 2 Steps 11–17
6		+ ts_RB_SubTest_RB10_TM_CS (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_3_5, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 1728,3)			Subtest 3 Steps 11–17
7		+ ts_RB_SubTest_RB10_TM_CS (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_4_5, c_UE_TestLoopMode1_LB_Setup (576, tsc_RB10), 2304,4)			Subtest 4 Steps 11–17
8		(tcv_TestBody := FALSE)			Steps 20–21
9		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
10		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_23a1 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.23a Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C397 Description : Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS (interact_8k_CC_40, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			Steps 20–21
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15		It_Background			Steps 1–10
16		[pc_Background]			
17	TBE2	+ ts_RB_InitTest_PS (backgrnd_8k_TC_40, terminatingBackgroundCall, terminatingBackgroundCall)			
18		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
		(tcv_TestBody := FALSE)			
		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
19		+ po_ConnectionAndSS_Rel (tsc_CellA)			
20		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_23a2 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. (TC) Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.23a Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS (interact_8k_TC_40, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			Steps 1–10
15		lt_Background			
16		[pc_Background]			
17	TBE2	+ ts_RB_InitTest_PS (backgrnd_8k_TC_40, terminatingBackgroundCall, terminatingBackgroundCall)			Subtest 1 Steps 11–17
18		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3, c_TFC_Allowed_0_1_2, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
19	TBE2	(tcv_TestBody := FALSE)			Steps 20–21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
19		+ po_ConnectionAndSS_Rel (tsc_CellA)			
20		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_23b Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.23b Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS (interact_16k_TC_40, terminatingInteractiveCall, terminatingInteractiveCall)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_3_4, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
9		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_1_2_3, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 1 Steps 11–17
10		(tcv_TestBody := FALSE)			
11		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
12		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
13		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
14		+ po_ConnectionAndSS_Rel (tsc_CellA)			
15		[TRUE]			Steps 1–10
16		lt_Background			
17		[pc_Background]			
18		+ ts_RB_InitTest_PS (backgrnd_16k_TC_40, terminatingBackgroundCall, terminatingBackgroundCall)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17	TBE2	+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_3_4, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11-17
18		+ ts_RB_SubTest_RB20(tsc_RB_TestData_5376, c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_1_2_3, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 1 Steps 11-17
19		(tcv_TestBody := FALSE)			Steps 20-21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
21		+ po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_23c Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.23c. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C131 Description : Interactive or BackGround / UL:32/DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH (TC,40ms TTI)					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS(interact_32k_TC_40, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
9		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			
10		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (952, tsc_RB20), 952)			
11		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			Subtest 2 Steps 11–17
12		(tcv_TestBody := FALSE)			
13		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
14		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			Steps 20–21

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15	TBE2	+ po_ConnectionAndSS_Rel (tsc_CellA)			Steps 1–10
16		[TRUE]			
17		lt_Background			
18		[pc_Background]			
19		+ ts_RB_InitTest_PS (backgrnd_32k_TC_40, terminatingBackgroundCall, terminatingBackgroundCall)			
20		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
21		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			
22		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (952, tsc_RB20), 952)			Subtest 2 Steps 11–17
23		+ ts_RB_SubTest_RB20(tsc_RB_TestData_1360, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			
24		(tcv_TestBody := FALSE)			Subtest 3 Steps 11–17
25		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
26		+ po_ConnectionAndSS_Rel (tsc_CellA)			
27		[TRUE]			Steps 20–21
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_26 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:64 DL: 64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.26. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C140 Description : Interactive or background / UL:64 DL: 64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ ts_RB_InitTest_PS (interact_64k, terminatingInteractiveCall, terminatingInteractiveCall)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_Interactive_64, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5,c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 2 Steps 11–17
9		+ ts_RB_SubTest_RB20 (tsc_RB_Interactive_64, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 3 Steps 11–17
10		+ ts_RB_SubTest_RB20 (tsc_RB_Interactive_64, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (952, tsc_RB20), 952)			Subtest 4 Steps 11–17
11	TBE1	(tcv_TestBody := FALSE)			Steps 20–21
12		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
13		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
14		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
15		+ po_ConnectionAndSS_Rel (tsc_CellA)			
16		[TRUE]			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17	TBE2	It_Background			
18		[pc_Background]			
19		+ ts_RB_InitTest_PS (backgrnd_64k, terminatingBackgroundCall, terminatingBackgroundCall)			Steps 1–10
20		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
21		+ts_RB_SubTest_RB20 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
22		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (952, tsc_RB20), 952)			Subtest 3 Steps 11–17
23		+ts_RB_SubTest_RB20 (tsc_RB_TestData_1360, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			Subtest 4 Steps 11–17
24		(tcv_TestBody := FALSE)			Steps 20–21
25		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
26		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_27 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.27. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C141 Description : Interactive or background / UL:64 DL: 128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS (interact_64k_128k, terminatingInteractiveCall, terminatingInteractiveCall)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
9		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
10		+ ts_RB_SubTest_RB20_AM (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			
11		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 3 Steps 11–17
12		(tcv_TestBody := FALSE)			
13		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
14		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
15		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
		+ po_ConnectionAndSS_Rel (tsc_CellA)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
16	TBE2	[TRUE]			
17		lt_Background			
18		[pc_Background]			
19		+ ts_RB_InitTest_PS (backgrnd_64k_128k, terminatingBackgroundCall, terminatingBackgroundCall)			Steps 1–10
20		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
21		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
22		+ ts_RB_SubTest_RB20_AM (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			Subtest 3 Steps 11–17
23		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
24		(tcv_TestBody := FALSE)			
25		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
26		+ po_ConnectionAndSS_Rel (tsc_CellA)			Steps 20–21
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_28 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:128 DL: 128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.28 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C142 Description : Interactive or BackGround / UL:128/DL: 128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS (interact_128k, terminatingInteractiveCall, terminatingInteractiveCall)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
9		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
10		+ts_RB_SubTest_RB20(tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			
11		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 3 Steps 11–17
12		(tcv_TestBody := FALSE)			
13		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
14		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15	TBE2	+ po_ConnectionAndSS_Rel (tsc_CellA)			Steps 1–10
16		[TRUE]			
17		lt_Background			
18		[pc_Background]			
19		+ ts_RB_InitTest_PS (backgrnd_128k, terminatingBackgroundCall, terminatingBackgroundCall)			
20		+ ts_RB_SubTest_RB20(tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
21		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			
22		+ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			
23		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			
24		(tcv_TestBody := FALSE)			
25		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
26		+ po_ConnectionAndSS_Rel (tsc_CellA)			Steps 20–21
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_29 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or Background / UL:64/DL: 144kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.29 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C143 Description : Interactive or BackGround / UL:64/DL: 144kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ ts_RB_InitTest_PS(interact_64k_144k, terminatingInteractiveCall, terminatingInteractiveCall)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20(tsc_RB_TestData_3024, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 2 Steps 11–17
9		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 3 Steps 11–17
10		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912,1272)			Subtest 4 Steps 11–17
11		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 5 Steps 11–17
12	TBE1	+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (2872, tsc_RB20), 2872)			
13		(tcv_TestBody := FALSE)			Steps 20–21
		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
14	TBE2	+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
15		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
16		+ po_ConnectionAndSS_Rel (tsc_CellA)			
17		[TRUE]			
18		It_Background			
19		[pc_Background]			
20		+ ts_RB_InitTest_PS (backgrnd_64k_144k, terminatingBackgroundCall, terminatingBackgroundCall)			Steps 1–10
21		+ ts_RB_SubTest_RB20(tsc_RB_TestData_3024, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
22		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
23		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912,1272)			Subtest 3 Steps 11–17
24		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
25		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_3024, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (2872, tsc_RB20), 2872)			Subtest 5 Steps 11–17
26		(tcv_TestBody := FALSE)			Steps 20–21
27		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
28		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_31_1 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.31 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C145 Description : interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive [pc_Interactive]			
6		+ ts_RB_InitTest_PS (interact_64k_256k_10, terminatingInteractiveCall, terminatingInteractiveCall)			Steps 1–10
7		+ ts_RB_SubTest_RB20(tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5 , c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
9		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			Subtest 3 Steps 11–17
10		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5 , c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
11	TBE1	(tcv_TestBody := FALSE)			
12		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
13		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
14	TBE2	+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
15		+ po_ConnectionAndSS_Rel (
16		tsc_CellA)			
17		[TRUE]			
18		It_Background			
19		[pc_Background]			
20		+ ts_RB_InitTest_PS (backgrnd_64k_256k_10, terminatingBackgroundCall, terminatingBackgroundCall)			Steps 1–10
21		+ ts_RB_SubTest_RB20(tsc_RB_TestData_2688, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
22		+ ts_RB_SubTest_RB20 (Subtest 2 Steps 11–17
23		tsc_RB_TestData_2688, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			
24		+ts_RB_SubTest_RB20_AM (Subtest 3 Steps 11–17
25		tsc_RB_TestData_2688, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			
26		+ ts_RB_SubTest_RB20 (Subtest 4 Steps 11–17
27		tsc_RB_TestData_2688, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5 , c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			
28		(tcv_TestBody := FALSE)			Steps 20–21
29		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
30		+ po_ConnectionAndSS_Rel (
31		tsc_CellA)			
32		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_32_1 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.32 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C147 Description : Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive [pc_Interactive]			
6		+ ts_RB_InitTest_PS (interact_64k_384k_10, terminatingInteractiveCall, terminatingInteractiveCall)			Steps 1–10
7		+ ts_RB_SubTest_RB20(tsc_RB_TestData_4032, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
9		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			Subtest 3 Steps 11–17
10		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
11		+ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)			Subtest 5 Steps 11–17
12	TBE1	(tcv_TestBody := FALSE)			

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Test Case Dynamic Behaviour						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
13	TBE2	+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21	
14		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)				
15		+ts_GMM_DetachOnSwitchOff(tsc_CellA)				
16		+ po_ConnectionAndSS_Rel (tsc_CellA)				
17		[TRUE]				
		lt_Background				
18		[pc_Background]				
19		+ ts_RB_InitTest_PS (backgrnd_64k_384k_10, terminatingBackgroundCall, terminatingBackgroundCall)				Steps 1–10
20		+ ts_RB_SubTest_RB20(tsc_RB_TestData_8064, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)				Subtest 1 Steps 11–17
21		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)				Subtest 2 Steps 11–17
22		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)				Subtest 3 Steps 11–17
23		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)				Subtest 4 Steps 11–17
24		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)				Subtest 5 Steps 11–17
25		(tcv_TestBody := FALSE)				
26		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)				Steps 20–21
27		+ po_ConnectionAndSS_Rel (tsc_CellA)				
28		[TRUE]				
Detailed Comments :						

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_32_2 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.32 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C148 Description : Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive [pc_Interactive]			
6		+ ts_RB_InitTest_PS (interact_64k_384k_20, terminatingInteractiveCall, terminatingInteractiveCall)			Steps 1–10
7		+ ts_RB_SubTest_RB20(tsc_RB_TestData_8064, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_9, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_9, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
9		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_9, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			Subtest 3 Steps 11–17
10		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_9, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
11		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_5_9, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)			Subtest 5 Steps 11–17

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12	TBE1	+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_6_9, c_UE_TestLoopMode1_LB_Setup (5112, tsc_RB20), 5112)			Subtest 6 Steps 11–17
13		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_7_9, c_UE_TestLoopMode1_LB_Setup (6392, tsc_RB20), 6392)			Subtest 7 Steps 11–17
14		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_8_9, c_UE_TestLoopMode1_LB_Setup (7672, tsc_RB20), 7672)			Subtest 8 Steps 11–17
15		(tcv_TestBody := FALSE)			Steps 20–21
16		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
17		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
18		+ts_GMM_DetachOnSwitch Off(tsc_CellA)			
19		+ po_ConnectionAndSS_Rel (tsc_CellA)			
20		[TRUE]			
21		It_Background			
22		[pc_Background]			
22		+ ts_RB_InitTest_PS (backgrnd_64k_384k_20, terminatingBackgroundCall, terminatingBackgroundCall)			Steps 1–10
23		+ ts_RB_SubTest_RB20(tsc_RB_TestData_8064, c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_9, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
24		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_9, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
25		+ts_RB_SubTest_RB20_AM (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_9, c_UE_TestLoopMode1_LB_Setup (1912, tsc_RB20), 1912, 1272)			Subtest 3 Steps 11–17

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
26	TBE2	+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_9, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
27		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_5_9, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)			Subtest 5 Steps 11–17
28		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_6_9, c_UE_TestLoopMode1_LB_Setup (5112, tsc_RB20), 5112)			Subtest 6 Steps 11–17
29		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_7_9, c_UE_TestLoopMode1_LB_Setup (6392, tsc_RB20), 6392)			Subtest 7 Steps 11–17
30		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_8_9, c_UE_TestLoopMode1_LB_Setup (7672, tsc_RB20), 7672)			Subtest 8 Steps 11–17
31		(tcv_TestBody := FALSE)			Steps 20–21
32		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
33		+ po_ConnectionAndSS_Rel (tsc_CellA)			
34		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_34_1 Group : CombinationOnDPCH/Interactive_Background/ Purpose : Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.34 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C151 Description : Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive [pc_Interactive]			
6		+ ts_RB_InitTest_PS (interact_384k_10, terminatingInteractiveCall, terminatingInteractiveCall)			Steps 1–10
7		+ ts_RB_SubTest_RB20(tsc_RB_TestData_4032, c_TFC_Allowed_0_1_6_7, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_2_6_8, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)			Subtest 2 Steps 11–17
9		+ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_3_6_9, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)			Subtest 3 Steps 11–17
10		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_4_6_10, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17
11		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_4032, c_TFC_Allowed_0_1_5_6_11, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)			Subtest 5 Steps 11–17
12	TBE1	(tcv_TestBody := FALSE)			

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Test Case Dynamic Behaviour								
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments			
13	TBE2	+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21			
14		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)						
15		+ts_GMM_DetachOnSwitchOff(tsc_CellA)						
16		+ po_ConnectionAndSS_Rel (tsc_CellA)						
17		[TRUE]						
		It_Background			Steps 1–10			
18		[pc_Background]						
19		+ ts_RB_InitTest_PS (backgrnd_384k_10, terminatingBackgroundCall, terminatingBackgroundCall)						
20		+ ts_RB_SubTest_RB20(tsc_RB_TestData_8064, c_TFC_Allowed_0_1_6_7, c_TFC_Allowed_0_1_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Subtest 1 Steps 11–17			
21		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_2_6_8, c_TFC_Allowed_0_1_2_6, c_UE_TestLoopMode1_LB_Setup (632, tsc_RB20), 632)						
22		+ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_3_6_9, c_TFC_Allowed_0_1_3_6, c_UE_TestLoopMode1_LB_Setup (1272, tsc_RB20), 1272)						
23		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_4_6_10, c_TFC_Allowed_0_1_4_6, c_UE_TestLoopMode1_LB_Setup (2552, tsc_RB20), 2552)			Subtest 4 Steps 11–17			
24		+ ts_RB_SubTest_RB20 (tsc_RB_TestData_8064, c_TFC_Allowed_0_1_5_6_11, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup (3832, tsc_RB20), 3832)						
25		(tcv_TestBody := FALSE)						
26		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21			
27		+ po_ConnectionAndSS_Rel (tsc_CellA)						
28		[TRUE]						
Detailed Comments :								

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_38a Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.38a Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive [pc_Interactive]			Steps 1–10
6		+ ts_RB_InitTest_CS_PS (speech_12_2k_Interact_0k_0k_CC_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
7		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_4, c_TFC_Allowed_0_1_3_4, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(tsc_RB_DataSpeech_12_2, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1. Test Data on RB10 and Measurm ent control Report on SRB.
8		+ts_RB_SubTest_RAB_SRB_RB10_RB11_R B12(c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3_5, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(tsc_RB_DataSpeech_12_2, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2. Test Data on RB10, RB11, RB12 and Measurm ent control Report on SRB.
9	TBE1	(tcv_TestBody := FALSE)			Steps 20–21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11	TBE1	+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15		It_Background			
16		[pc_Background]			
16		+ ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_0k_0k_CC_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			Steps 1–10
17		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_4, c_TFC_Allowed_0_1_3_4, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(tsc_RB_DataSpeech_12_2, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1. Test Data on RB10, RB11, RB12 and Measurm ent control Report on SRB.
18		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12(c_TFC_Allowed_0_1_2_3_5, c_TFC_Allowed_0_2_3_5, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(tsc_RB_DataSpeech_12_2, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2. Test Data on RB10 and Measurm ent control Report on SRB.
19		(tcv_TestBody := FALSE)			Steps 20–21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
21		+ po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_38b					
Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/					
Purpose : Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Test to verify establishment and data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.38b					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref :					
Description : Conversational / speech / 12.2 kbps / CS RAB + Interactive or background / UL:8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
		lt_Interactive			
5		[pc_Interactive]			Steps 1–10
6		+ ts_RB_InitTest_CS_PS (speech_12_2k_Interact_8k_8k_TC_40, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
7		+ts_Subtests_1_to_5_tc_14_2_38_b(tsc_RB_TestData_5376)			
8		(tcv_TestBody := FALSE)			
9		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
10		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			Steps 20–21
11		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
12		+ po_ConnectionAndSS_Rel (tsc_CellA)			
13	[TRUE]				
	lt_Background				
14	[pc_Background]	Steps 1–10			
15	+ ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_8k_8k_TC_40, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)				
16	+ts_Subtests_1_to_5_tc_14_2_38_b(tsc_RB_TestData_5376)				
17	(tcv_TestBody := FALSE)				
18	+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)				
19	+ po_ConnectionAndSS_Rel (tsc_CellA)	Steps 20–21			
20	[TRUE]				

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Test Case Dynamic Behaviour
Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_38c Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / UL: 12.2 kbps DL: 12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.38c Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / speech / UL: 12.2 kbps DL: 12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard (500)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1-10
6		[pc_Interactive]			
7		+ ts_RB_InitTest_CS_PS (speech_12_2k_Interact_32k_32k_40, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_TC_14_2_38_c(tsc_RB_TestData_5376)			
9		(tcv_TestBody := FALSE)			Steps 20-21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	lt_Background			Steps 1-10
16		[pc_Background]			
17		+ ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_32k_32k_40, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_Subtests_TC_14_2_38_c(tsc_RB_TestData_5376)			
19		(tcv_TestBody := FALSE)			Steps 20-21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
21		+ po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			

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Test Case Dynamic Behaviour
Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_38e Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / UL : (12.2 7.95 5.9 4.75) kbps DL : (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.38e Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / speech / UL : (12.2 7.95 5.9 4.75) kbps DL : (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ ts_RB_InitTest_CS_PS (speech_12_2k_7_95k_5_9k_4_75k_Interact_0k_0k_CC_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_1_to_5_tc_14_2_38_e (tsc_RB_DataSpeech_12_2)			
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	lt_Background			Steps 1–10
16		[pc_Background]			
17		+ ts_RB_InitTest_CS_PS (speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_0k_0k_CC_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_Subtests_1_to_5_tc_14_2_38_e (tsc_RB_DataSpeech_12_2)			
19		(tcv_TestBody := FALSE)			
		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
		+ po_ConnectionAndSS_Rel (tsc_CellA)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_38f					
Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/					
Purpose : Conversational / speech / UL: (12.2 7.95 5.9 4.75) DL: (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Test to verify establishment and data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.38f					
Configuration :					
Default : RRC_Def1					
Comments :					
Selection Ref :					
Description : Conversational / speech / (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard (500)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
		+ ts_RB_InitTest_CS_PS (speech_12_2k_7_95k_5_9k_4_75k_Interact_8k_8k_40, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
7		+ ts_Subtests_1_14_TC_14_2_38f (tsc_RB_TestData_5376)			
8		(tcv_TestBody := FALSE)			Steps 20–21
9		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
10		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
11		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
12		+ po_ConnectionAndSS_Rel (tsc_CellA)			Steps 1–10
13		[TRUE]			
14	lt_Background				
15	[pc_Background]				
16	TBE1	+ ts_RB_InitTest_CS_PS (speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_8k_8k_40, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			Steps 20–21
17		+ ts_Subtests_1_14_TC_14_2_38f (tsc_RB_TestData_5376)			
18		(tcv_TestBody := FALSE)			
19		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
		+po_ConnectionAndSS_Rel (tsc_CellA)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_40 Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.40 Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_IB_C167 Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard (500)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ ts_RB_InitTest_CS_PS (speech_12_2k_Interact_64k, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_1_14_TC_14_2_40 (tsc_RB_TestData_1360)			
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	lt_Background			Steps 1–10
16		[pc_Background]			
17		+ ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_64k , terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_Subtests_1_14_TC_14_2_40 (tsc_RB_TestData_1360)			
19		(tcv_TestBody := FALSE)			
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
21		+po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			

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Test Case Dynamic Behaviour	
Detailed Comments :	

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_41 Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.41 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_IB_C168 Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard (600)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ ts_RB_InitTest_CS_PS (speech_12_2k_Interact_64k_128k, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_RB_SubTests_TC_14_2_41(tsc_RB_TestData_2688)			
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	lt_Background			Steps 1–10
16		[pc_Background]			
17		+ ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_64k_128k , terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_RB_SubTests_TC_14_2_41(tsc_RB_TestData_2688)			
19		(tcv_TestBody := FALSE)			
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
21		+ po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			

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Test Case Dynamic Behaviour
Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_43_1 Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 10 ms TTI Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.43 for the downlink 10 ms TTI case. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_IB_C171 Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 10 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(500)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ts_RB_InitTest_CS_PS (speech_12_2k_Interact_64k_384k_10, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_1_To_10_TC_14_2_43_1(tsc_RB_TestData_2688)			
9		+ts_Subtests_11_To_17_TC_14_2_43_1(tsc_RB_TestData_8064)			
10		(tcv_TestBody := FALSE)			Steps 20–21
11		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
12		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
13		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
14		+ po_ConnectionAndSS_Rel (tsc_CellA)			
15		[TRUE]			Steps 1–10
16		lt_Background			
17		[pc_Background]			
18		+ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_64k_384k_10, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
19		+ts_Subtests_1_To_10_TC_14_2_43_1(tsc_RB_TestData_2688)			
		+ts_Subtests_11_To_17_TC_14_2_43_1(tsc_RB_TestData_8064)			
		(tcv_TestBody := FALSE)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
21		+ po_ConnectionAndSS_Rel (tsc_CellA)			
22		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_43_2 Group : CombinationOnDPCH/ConvSpeech_InteractBackgrnd/ Purpose : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 20 ms TTI Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.43 for the downlink 20 ms TTI case. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_IB_C172 Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / 20 ms TTI					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(600)			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ts_RB_InitTest_CS_PS (speech_12_2k_Interact_64k_384k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_1_To_10_TC_14_2_43_2(tsc_RB_TestData_2688)			
9		+ts_Subtests_11_To_20_TC_14_2_43_2(tsc_RB_TestData_8064)			Steps 20–21
10		+ts_Subtests_21_To_26_TC_14_2_43_2(tsc_RB_TestData_8064)			
11		(tcv_TestBody := FALSE)			
12		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
13		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			Steps 1–10
14		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
15		+ po_ConnectionAndSS_Rel (tsc_CellA)			
16		[TRUE]			
17		It_Background			Steps 1–10
18		[pc_Background]			
19		+ts_RB_InitTest_CS_PS (speech_12_2k_Backgrnd_64k_384k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
20		+ts_Subtests_1_To_10_TC_14_2_43_2(tsc_RB_TestData_2688)			
21		+ts_Subtests_11_To_20_TC_14_2_43_2(tsc_RB_TestData_8064)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20	TBE1	+ts_Subtests_21_To_26_TC_14_2_43_2(t sc_RB_TestData_8064)			Steps 20–21
21		(tcv_TestBody := FALSE)			
22		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
23		+ po_ConnectionAndSS_Rel (
24		tsc_CellA) [TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_49_1 Group : CombinationOnDPCH/ConvSpeech_ConvUnknown/ Purpose : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI. Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.49 for the 20 ms TTI case. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Speech_Conv_C179 Description : Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables Steps 1-10
2		+ ts_InitVariables			
3		+ ts_RB_InitTest_CS (speech_12_2k_ConvUnknown_64k_20, terminatingConversationalCall , terminatingConversationalCall)			
4		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_6_7, c_TFC_Allowed_0_1_6_7, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,640, tsc_RB13), c_RAB_Tx_Info(tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			sub test 1
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_ RB12(c_TFC_Allowed_0_1_2_3_6_8, c_TFC_Allowed_0_2_6_8, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,640, tsc_RB13), c_RAB_Tx_Info(tsc_RB_TestData_2688, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			sub test 2

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6		+ts_RB_SubTest_RAB_SRB_RB13(c_TFC_Allowed_0_1_2_3_6_9, c_TFC_Allowed_0_3_6_9, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,640, tsc_RB13), c_RAB_Tx_Info(tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB13,1280,60), OMIT, OMIT, OMIT), 20, 1)			sub test 3
7		+ts_RB_SubTest_RAB_SRB_RB10_RB 13(c_TFC_Allowed_0_1_2_3_4_6_7_9_10, c_TFC_Allowed_0_4_6_10, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,640, tsc_RB13), c_RAB_Tx_Info(tsc_RB_TestData_2688 , 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB13,1280,60), OMIT, OMIT), 20, 1)			sub test 4
8		+ts_RB_SubTest_RAB_SRB_RB10_R B11_RB12_RB13(c_TFC_Allowed_0_1_2_3_5_6_8_9_1 1, c_TFC_Allowed_0_5_6_11, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,640, tsc_RB13), c_RAB_Tx_Info(tsc_RB_TestData_26 88, 4, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB13,1280,60)), 20, 1)			sub Test 5
9	TBE1	(tcv_TestBody := FALSE)			Steps 20–21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_51_1 Group : CombinationOnDPCH/ConvUnknown_InteractBackgrnd/ Purpose : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL:64 DL:64 kbps / PS RAB Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.51 for the 20 ms TTI case Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Conv_IB_C183 Description : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL:64 DL:64 kbps / PS RAB					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ts_RB_InitTest_CS_PS (convUnknown_64k_Interact_64k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_TC_14_2_51_1(tsc_RB_TestData_2688)			
9		(tcv_TestBody := FALSE)			Steps 20–21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	It_Background			Steps 1–10
16		[pc_Background]			
17		+ts_RB_InitTest_CS_PS (convUnknown_64k_Backgrnd_64k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_Subtests_TC_14_2_51_1(tsc_RB_TestData_2688)			
19		(tcv_TestBody := FALSE)			Steps 20–21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_51a_1 Group : CombinationOnDPCH/ConvUnknown_InteractBackgrnd/ Purpose : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.51a for the 20 ms TTI case Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ts_RB_InitTest_CS_PS (convUnknown_64k_Interact_8k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_4_6, c_TFC_Allowed_0_2_4_6, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Subtest 1 Steps 11–17
8		+ ts_RB_SubTest_RAB_SRB_RB10 (c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB10,1280,60), OMIT, OMIT, OMIT), 20)			Subtest 2 Steps 11–17

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9	TBE1	+ ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_2_3_4_7, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Subtest 3 Steps 11–17
10		(tcv_TestBody := FALSE)			Steps 20–21
11		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
12		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
13		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
14		+ po_ConnectionAndSS_Rel (tsc_CellA)			
15		[TRUE]			Steps 1–10
		It_Background			
16		[pc_Background]			
17		+ts_RB_InitTest_CS_PS (convUnknown_64k_Backgrnd_8k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_4_6, c_TFC_Allowed_0_2_4_6, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Subtest 1 Steps 11–17
19		+ ts_RB_SubTest_RAB_SRB_RB10 (c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_4_5, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 1, c_RB_Tx_Info(tsc_RB10,1280,60), OMIT, OMIT, OMIT), 20)			Subtest 2 Steps 11–17

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20	TBE1	+ ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_2_3_4_7, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (tsc_RB_TestData_2688, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Subtest 3 Steps 11–17
21		(tcv_TestBody := FALSE)			Steps 20–21
22		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
23		+ po_ConnectionAndSS_Rel (
24		tsc_CellA) [TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_51b_1 Group : CombinationOnDPCH/ConvUnknown_InteractBackgrnd/ Purpose : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 16 DL: 64 kbps / PS RAB Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.51b for the 20 ms TTI case Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI + Interactive or background / UL: 16 DL: 64 kbps / PS RAB					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+It_Interactive			
4		+It_Background			
5		It_Interactive			Steps 1–10
6		[pc_Interactive]			
7		+ts_RB_InitTest_CS_PS (convUnknown_64k_Interact_16k_64k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingInteractiveCall)			
8		+ts_Subtests_1_to_9_tc_14_2_51b_1 (tsc_RB_TestData_2688)			
9		(tcv_TestBody := FALSE)			Steps 20–21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14		[TRUE]			
15	TBE1	It_Background			Steps 1–10
16		[pc_Background]			
17		+ts_RB_InitTest_CS_PS (convUnknown_64k_Backgrnd_16k_64k_20, terminatingConversationalCall, terminatingConversationalCall , terminatingBackgroundCall)			
18		+ts_Subtests_1_to_9_tc_14_2_51b_1 (tsc_RB_TestData_2688)			
19		(tcv_TestBody := FALSE)			Steps 20–21
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			

Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_58 Group : CombinationOnDPCH/InteractBackgrnd_StreamUnknown/ Purpose : To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.58 Streaming / unknown / UL: 16 DL: 64 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Streaming / unknown / UL: 16 DL: 64 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive(tsc_RB_TestData_2688)			
4		+lt_Background(tsc_RB_TestData_2688)			
5		lt_Interactive(p_data_string :BITSTRING)			
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS(streamUnknown_16k_64k_Interact_8k, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ ts_Subtests_1_to_7_tc_14_2_58(p_data_string)			
9		(tcv_TestBody := FALSE)			Steps 20-21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14	TBE1	[TRUE]			Steps 20-21
15		lt_Background(p_data_string :BITSTRING)			
16		[pc_Background]			
17		+ ts_RB_InitTest_PS(streamUnknown_16k_64k_Backgrnd_8k, terminatingBackgroundCall, terminatingBackgroundCall)			
18		+ ts_Subtests_1_to_7_tc_14_2_58(p_data_string)			
19		(tcv_TestBody := FALSE)			
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			

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Test Case Dynamic Behaviour
Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_58a Group : CombinationOnDPCH/InteractBackgrnd_StreamUnknown/ Purpose : To verify radio bearer establishment and correct data transfer for reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.58a Streaming / unknown / UL: 16 DL: 128 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Streaming / unknown / UL: 16 DL: 128 kbps / PS RAB + Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive(tsc_RB_TestData_2688)			
4		+lt_Background(tsc_RB_TestData_2688)			
5		lt_Interactive(p_data_string :BITSTRING)			
6		[pc_Interactive]			
7		+ ts_RB_InitTest_PS(streamUnknown_16k_128k_Interact_8k, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ ts_Subtests_1_to_9_tc_14_2_58a(p_data_string)			
9		(tcv_TestBody := FALSE)			Steps 20-21
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14	TBE1	[TRUE]			Steps 20-21
15		lt_Background(p_data_string :BITSTRING)			
16		[pc_Background]			
17		+ ts_RB_InitTest_PS(streamUnknown_16k_128k_Backgrnd_8k, terminatingBackgroundCall, terminatingBackgroundCall)			
18		+ ts_Subtests_1_to_9_tc_14_2_58a(p_data_string)			
19		(tcv_TestBody := FALSE)			
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			
		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			

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Test Case Dynamic Behaviour
Detailed Comments :

Test Case Dynamic Behaviour					
Test Case Name : tc_14_2_57 Group : CombinationOnDPCH/InteractBackgrnd_InteractBackgrnd/ Purpose : Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.2.4.1.57 Configuration : Default : RRC_Def1 Comments : Selection Ref : Description : Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard			Initial Test Case Variables
2		+ ts_InitVariables			
3		+lt_Interactive			
4		+lt_Background			
5		lt_Interactive			
6		[pc_Interactive]			Steps 1–10
7		+ts_RB_InitTest_PS (interact_64k_TC_20, terminatingInteractiveCall, terminatingInteractiveCall)			
8		+ts_Subtests_1_to_5_tc_14_2_57 (tsc_RB_TestData_2688)			
9		(tcv_TestBody := FALSE)			
10		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
11		+ts_RRC_ConnRel (tsc_CellA , cell_Dch)			
12		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
13		+ po_ConnectionAndSS_Rel (tsc_CellA)			
14	TBE1	[TRUE]			Steps 1–10
15		lt_Background			
16		[pc_Background]			
17		+ts_RB_InitTest_PS (backgrnd_64k_TC_20, terminatingBackgroundCall , terminatingBackgroundCall)			
18		+ts_Subtests_1_to_5_tc_14_2_57 (tsc_RB_TestData_2688)			
19		(tcv_TestBody := FALSE)			
20		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20–21
		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2_1 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when two SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and the second SCCPCH carries the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH. Configuration : Default : RRC_Def1 Comments : Two SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and the second SCCPCH carries the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH Selection Ref : RB_Intract_Backgr_C204 Description : One SCCPCH: Interactive/Background 32 kbps PS RAB + SRBs for CCCH + SRB for DCCH + SRB for BCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain :=ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			Configure SS and Activate the test mode
7		[pc_Interactive]			
8		+ts_RB_InitTest_2SCCPCH(terminatingInteractiveCall,terminatingInteractiveCall)			
9		(tcv_CellInfoA.cRNTI := tsc_New_CRNTI2)			
10		+ts_SendRB_SetUp_FACH_2SCCPCH_32k (tsc_CellA, tsc_RAB_DefPS)			
11		+ ts_SetCellCfg (tsc_CellA, cell_FACH_2SCCPCH_StandAlonePCH_P S)			
12		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
13		(tcv_TestBody := FALSE)			
14		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			Steps 20–21
15		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
16		(tcv_RRC_RelStatus := cell_Fach_Dcch)			
17		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
17		+ po_ConnectionAndSS_Rel (tsc_CellA)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18	TBE1	[TRUE]			Configure SS and Activate the test mode
19		lt_Background			
20		[pc_Background]			
21		+ts_RB_InitTest_2SCCPCH(terminatingBackgroundCall,terminatingBackgroundCall)			
22		(tcv_CellInfoA.cRNTI := tsc_New_CRNTI2)			
23		+ts_SendRB_SetUp_FACH_2SCCPCH_32k (tsc_CellA, tsc_RAB_DefPS)			
24		+ ts_SetCellCfg (tsc_CellA, cell_FACH_2SCCPCH_StandAlonePCH_P S)			
25		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
26		(tcv_TestBody := FALSE)			
27		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
28		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			Steps 20–21
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2_2 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C204 Description : Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain := ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_3SCCPCH (terminatingInteractiveCall, terminatingInteractiveCall)			
9		+ts_SendRB_SetUp_FACH_3SCCPCH_32k(t sc_CellA,tsc_RAB_DefPS,tcv_ActTime)			
10		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Steps 20–21
11		(tcv_TestBody := FALSE)			
12		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
13		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
14		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
15		+ po_ConnectionAndSS_Rel (tsc_CellA)			
16		[TRUE]			
17		lt_Background			
18		[pc_Background]			
19		+ts_RB_InitTest_3SCCPCH(terminatingBackgro undCall, terminatingBackgroundCall)			
20		+ts_SendRB_SetUp_FACH_3SCCPCH_32k(t sc_CellA,tsc_RAB_DefPS,tcv_ActTime)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
19	TBE1	+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Steps 20–21
20		(tcv_TestBody := FALSE)			
21		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
22		+ po_ConnectionAndSS_Rel (tsc_CellA)			
23		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2_3 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C204 Description : Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain := ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_3SCCPCH_CTCH (terminatingInteractiveCall, terminatingInteractiveCall)			
9		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_ 1(tsc_CellA,tsc_RAB_DefPS,tcv_ActTime)			
10		+ ts_SetCellCfg (tsc_CellA, cell_FACH_3_SCCPCH_3_FACH_CTCH)			
11		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Steps 20–21
12		(tcv_TestBody := FALSE)			
13		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
14		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
15		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
16		+ po_ConnectionAndSS_Rel (tsc_CellA)			
17		[TRUE]			
18		lt_Background			
19		[pc_Background]			
20		+ts_RB_InitTest_3SCCPCH_CTCH(terminating BackgroundCall, terminatingBackgroundCall)			
21		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_ 1(tsc_CellA,tsc_RAB_DefPS,tcv_ActTime)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20	TBE1	+ ts_SetCellCfg (tsc_CellA, cell_FACH_3_SCCPCH_3_FACH_CTCH)			Steps 20–21
21		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
22		(tcv_TestBody := FALSE)			
23		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
24		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
25		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
26		+ po_ConnectionAndSS_Rel (tsc_CellA)			
27		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2a_1 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when two SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and the second SCCPCH carries the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH. Configuration : Default : RRC_Def1 Comments : Two SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and the second SCCPCH carries the FACH for two Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH Selection Ref : RB_Intract_Backgr_C64 Description : One SCCPCH: Interactive/Background 32 kbps PS RAB + Interactive/Background 32 kbps PS RAB + SRBs for CCCH + SRB for DCCH + SRB for BCCH					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain :=ps_domain)			
4		+lt_Interactive			
5		+lt_Background			Configure SS and Activate the test mode
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_2aSCCPCH(terminatingInteractiveCall,terminatingInteractiveCall)			
9		+ts_SendRB_SetUp_FACH_2SCCPCH_32k_2a (tsc_CellA, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
10		+ ts_SetCellCfg (tsc_CellA, cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)			
11		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
12		+ ts_RB_SubTest_RB24_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB24), 312)			
13		(tcv_TestBody := FALSE)			Steps 20-21
14		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
15		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15	TBE1	+ts_GMM_DetachOnSwitchOff(tsc_CellA)			Configure SS and Activate the test mode
16		+ po_ConnectionAndSS_Rel (tsc_CellA)			
17		[TRUE]			
18		lt_Background			
19		[pc_Background]			
20		+ts_RB_InitTest_2aSCCPCH(terminatingBackgr oundCall,terminatingBackgroundCall)			
21		+ts_SendRB_SetUp_FACH_2SCCPCH_32k_2a (tsc_CellA, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
22		+ ts_SetCellCfg (tsc_CellA, cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)			
23		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
24		+ ts_RB_SubTest_RB24_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB24), 312)			
25		(tcv_TestBody := FALSE)			
26		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
27		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			Steps 20–21
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2a_2 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for two Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C64 Description : Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain := ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_3SCCPCH_2a(terminatingInter activeCall, terminatingInteractiveCall)			
9		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_2a(tsc_CellA,tsc_RAB_DefPS,tsc_RAB2_DefP S,tcv_ActTime)			
10		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
11		(tcv_TestBody := FALSE)			Steps 20–21
12		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
13		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
14		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
15		+ po_ConnectionAndSS_Rel (tsc_CellA)			
16		[TRUE]			
17		lt_Background [pc_Background]			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18	TBE1	+ts_RB_InitTest_3SCCPCH_2a(terminatingBack groundCall, terminatingBackgroundCall)			Steps 20–21
19		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_ 2a(tsc_CellA,tsc_RAB_DefPS,tsc_RAB2_DefP S,tcv_ActTime)			
20		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
21		+ ts_RB_SubTest_RB24_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB24), 312)			
22		(tcv_TestBody := FALSE)			
23		+ ts_TC_DeactivateRB_TestMode (
24		tsc_CellA)			
25		+ po_ConnectionAndSS_Rel (
		tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_2a_3 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.2 and 6.10.2.4.4.1 for the case when three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for two Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C64 Description : Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for two Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain := ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_3SCCPCH_CTCH_2a(terminatingInteractiveCall, terminatingInteractiveCall)			
9		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_1_2a(tsc_CellA,tsc_RAB_DefPS,tsc_RAB2_DefPS,tcv_ActTime)			
10		+ ts_SetCellCfg (tsc_CellA, cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)			
11		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			Steps 20–21
12		+ ts_RB_SubTest_RB24_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB24), 312)			
13		(tcv_TestBody := FALSE)			
14		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
15		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
16		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
17		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18	TBE1	It_Background			Steps 20–21
19		[pc_Background]			
20		+ts_RB_InitTest_3SCCPCH_CTCH_2a(terminatingBackgroundCall, terminatingBackgroundCall)			
21		+ts_SendRB_SetUp_FACH_3SCCPCH_32k_1_2a(tsc_CellA,tsc_RAB_DefPS,tsc_RAB2_DefPS,tcv_ActTime)			
22		+ ts_SetCellCfg (tsc_CellA, cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)			
23		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
24		+ ts_RB_SubTest_RB24_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_1_3, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB24), 312)			
25		(tcv_TestBody := FALSE)			
26		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
27		+ po_ConnectionAndSS_Rel (tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Case Dynamic Behaviour					
Test Case Name : tc_14_4_3 Group : CombinationsOnSCCPCH/ Purpose : To verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clauses 6.10.2.4.3.3 and 6.10.2.4.4.1 for the case when one SCCPCH is used in this SYSTEM INFORMATION (BCCH) configuration. The SCCPCH carries the PCH, the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH. To be able to test the downlink radio bearer using the UE loopback function, the reference radio bearer configuration according to TS 34.108, clause 6.10.2.4.4.1 (Interactive/Background 32 kbps PS RAB + SRB for CCCH + SRB for DCCH on PRACH) is used in uplink. Configuration : Default : RRC_Def1 Comments : Selection Ref : RB_Intract_Backgr_C205 Description : Three SCCPCHs are used in this SYSTEM INFORMATION configuration. The first SCCPCH carries the PCH and both the second and third SCCPCHs carry the FACH for Interactive/Background 32 kbps PS RAB and the FACH for SRBs on CCCH/ DCCH/ BCCH.					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBE1	START t_Guard(300)			Sets domain for testing
2		+ts_InitVariables			
3		(tcv_CN_Domain := ps_domain)			
4		+lt_Interactive			
5		+lt_Background			
6		lt_Interactive			
7		[pc_Interactive]			
8		+ts_RB_InitTest_1SCCPCH(terminatingInteractiveCall,terminatingInteractiveCall)			
9		(tcv_CellInfoA.cRNTI := tsc_New_CRNTI2)			
10		+ts_SendRB_SetUp_FACH_1SCCPCH_32k (tsc_CellA,tsc_RAB_DefPS,tcv_ActTime)			
11		+ ts_SetCellCfgr (tsc_CellA, cell_Fach_PS)			
12		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_2_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
13		(tcv_TestBody := FALSE)			Steps 20–21
14		+ ts_TC_DeactivateRB_TestMode (tsc_CellA)			
15		+ts_RRC_ConnRel (tsc_CellA , cell_Fach_Dcch)			
16		(tcv_RRC_RelStatus := cell_Fach_Dcch)			
17		+ts_GMM_DetachOnSwitchOff(tsc_CellA)			
18		+ po_ConnectionAndSS_Rel (tsc_CellA)			
19		[TRUE]			
		lt_Background			
		[pc_Background]			

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Test Case Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20	TBE1	+ts_RB_InitTest_1SCCPCH(terminatingBackgro undCall,terminatingBackgroundCall)			Steps 20–21
21		(tcv_CellInfoA.cRNTI := tsc_New_CRNTI2)			
22		+ts_SendRB_SetUp_FACH_1SCCPCH_32k (tsc_CellA,tsc_RAB_DefPS,tcv_ActTime)			
23		+ ts_SetCellCfg (tsc_CellA, cell_FACH_PS)			
24		+ ts_RB_SubTest_RB20_FACH(tsc_RB_TestData_3024, c_TFC_Allowed_0_1, c_TFC_Allowed_0_2_6, c_UE_TestLoopMode1_LB_Setup (312, tsc_RB20), 312)			
25		(tcv_TestBody := FALSE)			
26		+ ts_TC_DeactivateRB_TestMode (
27		tsc_CellA)			
28		+ po_ConnectionAndSS_Rel (
		tsc_CellA)			
		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_3SCCPCH(p_PagingCause :PagingCause ; p_EstablishmentCause :EstablishmentCause) Group : RB_Steps/Initialization/ Objective : To setup the environnement for PS test cases Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS1	+ts_SS_CreateCellFACH (tsc_CellA)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_ServiceRequest(c_ServiceType_v('010'B), c_MobileIdPTMSI_lv(tcv_AssignedPTMSI), ?))		Configur ation has to be changed Fetch record correspo nding to current cell
2		+ ts_SetTmpCellInfo (tsc_CellA)			
3		+ts_SendDefSysInfo_withoutSIB6_3SCCPCH (tsc_CellA)			
4		+ ts_IdleUpdated (tsc_CellA)			
5		+ts_SendModifiedSIB5_withoutSIB6_3SCCPCH(tsc_CellA)			
6		+It_ReConfigureCell			
7		(tcv_TestBody:=TRUE)			
8		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagingCause,tsc_RB_PCCH)			
9		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstablishmentCause)			Steps 2–5 Step 6
10		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			
11		(tcv_CellIndInfo.start_PS := tcv_Start)			
12		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
13		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
14		+ ts_TC_ActivateRB_TestMod e (tsc_CellDedicated)			
		It_ReConfigureCell			Steps 7–8

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		+ts_RRC_Delay(5000)			Give delay for UE to listen to new configuration
16		+It_ReleaseCell			
17		+It_ModifyCell			2.
		It_ReleaseCell			
18		+ ts_CRLC_Rel (tsc_CellA, tsc_RB0)			3.
19		+ ts_CRLC_Rel (tsc_CellA, tsc_RB_BCCH_FACH)			
20		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
21		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			
22		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
23		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
24		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
25		+ ts_CMAC_Rel (tsc_CellA, tsc_PRACH1)			
26		+ ts_CPHY_TrChRelNonDch (tsc_CellA , tsc_PRACH1)			
27		+ ts_SS_StopRL (tsc_CellA , tsc_AICH1)			
28		+ ts_SS_StopRL (tsc_CellA , tsc_PRACH1)			
29		+ ts_CRLC_Rel (tsc_CellA, tsc_RB_PCCH)			
30		+ ts_CMAC_Rel (tsc_CellA, tsc_S_CCPCH1)			
31		+ ts_CPHY_TrChRelNonDch (tsc_CellA , tsc_S_CCPCH1)			
32		+ ts_SS_StopRL (tsc_CellA , tsc_PICH1)			
33		+ ts_SS_StopRL (tsc_CellA , tsc_S_CCPCH1)			
		It_ModifyCell			
34		[pc_CS]			
35		[(BIT_TO_INT(o_OctToBit(px_TMSI_Def))MOD 2) = 0]			
36		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA.uRNTI.s_rnc_Identity,tcv_CellInfoA.uRNTI.s_RNTI,12,20))MOD 2) = 1]			
37		+ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg1 (tsc_CellA)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
38		(tcv_CellCnfg :=1)			
39		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.ssrc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
40		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000001'B)			
41		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg1 (tsc_CellA)			
42		(tcv_CellCnfg :=1)			
43		[(BIT_TO_INT(o_OctToBit(px_TMSI_Def))MOD 2) = 1]			
44		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.ssrc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
45		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg2 (tsc_CellA)			
46		(tcv_CellCnfg :=2)			
47		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.ssrc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
48		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000000'B)			
49		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg2 (tsc_CellA)			
50		(tcv_CellCnfg :=2)			
51		[pc_PS]			
52		[
		(BIT_TO_INT(o_OctToBit(px_PTMSI_Def))MOD 2) = 0]			
53		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.ssrc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
54		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg1 (tsc_CellA)			
55		(tcv_CellCnfg :=1)			
56		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.ssrc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
57		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000001'B)			
58		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg1 (tsc_CellA)			
59		(tcv_CellCnfg :=1)			
60		[
		(BIT_TO_INT(o_OctToBit(px_PTMSI_Def))MOD 2) = 1]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
61		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
62		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg2 (tsc_CellA)			
63		(tcv_CellCnfg :=2)			
64		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
65		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000000'B)			
66		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg2 (tsc_CellA)			
67		(tcv_CellCnfg :=2)			
68		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_PagType1_P_TMSI_Cause (p_CellId: INTEGER; p_P_Tmsi:OCTETSTRING; p_PagCause: PagingCause;p_RB_Id: SS_RB_Identity)					
Group : RB_Steps/Initialization/					
Objective :					
Default : RRC_Def1					
Comments : To send PAGING TYPE 1 with IMSI and with a given paging cause					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RRC_Delay(tsc_WaitBeforePaging)			Give delay before paging type1
2		+ts_CMAC_Pag1_Cfg(p_CellId)			
3		TM ! RLC_TR_DATA_REQ	cas_PagingType1 (p_CellId, p_RB_Id, cs_RRC_PagingType1_PTM SI(p_PagCause, o_ConvertPTMSI(p_P_Tmsi), tcv_CN_Domain))		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_CS_PS (p_RB_Type : RB_Type; p_EstCause_CS : EstablishmentCause; p_PagCause_CS: PagingCause;p_PagCause_PS : PagingCause) Group : RB_Steps/Initialization/ Objective : To setup the environnement for CS test cases Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS	(tcv_CN_Domain := cs_domain)			14.1.2 Step 1
2		+ ts_SS_ResetSecurityKey			
3		+ ts_SS_CreateCellDCH (tsc_CellA)			
4		+ ts_SendDefSysInfo (tsc_CellA)			14.1.2 Step 2
5		+ ts_IdleUpdated (tsc_CellA)			
6		(tcv_TestBody:=TRUE)			
7		+ts_RRC_PagType1_TMSI_PTMSI_Cau (tsc_CellA, px_TMSI_Def, p_PagCause_CS)			14.1.2 Steps 3–5
8		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause_CS)			
9		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			
10		(tcv_CellIndInfo.start_CS := tcv_Start)	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, c_PagRsp (?, c_MobileIdTMSI_lv))		14.1.2 Step 6
11		+ ts_MM_Authentication(tsc_CellA)			
12		+ ts_RRC_Security (tsc_CellA, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM, TRUE, cs_domain)			
13		AM ! RLC_AM_DATA_REQ	cas_PagingType2(tsc_CellDedicated, tsc_RB2, cs_108_PagingType2(tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, ps_domain, p_PagCause_PS))		Steps 6a–6b Steps 6c–6d SECURITY MODE COMMAND SECURITY MODE COMPLETE Send a correct Paging Type2 with a Paging Record Type Identifier set to IMSI

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
14		Dc ? RRC_DataInd (tcv_Start := RRC_DataInd.start)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3,		SERVIC E REQUE ST
15		+ ts_SS_SecurityDownload Start (ps_domain, tcv_Start)	cr_ServiceRequest (c_ServiceType_v(?), *,tcv_PS_KeySeq)		
16		+ ts_RRC_Security (tsc_Cella, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			Steps 6c-6d SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
17		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)			14.1.2 Steps 7-8
18		+ ts_RB_SetUpRAB_CS_PS (tsc_Cella, p_RB_Type)			14.1.2 Steps 9-10
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_CS (p_RB_Type : RB_Type; p_EstCause : EstablishmentCause; p_PagCause: PagingCause)					
Group : RB_Steps/Initialization/					
Objective : To setup the environnement for CS test cases					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS	(tcv_CN_Domain := cs_domain)	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, c_PagRsp (?, c_MobileIdTMSI_Iv))		14.1.1 Step 1
2		+ ts_SS_CreateCellDCH (tsc_CellA)			
3		+ ts_SendDefSysInfo (tsc_CellA)			
4		+ ts_IdleUpdated (tsc_CellA)			14.1.1 Step 2
5		(tcv_TestBody:=TRUE)			
6		+ts_RRC_PagType1_TMSI_PTMSI_Cau (tsc_CellA, px_TMSI_Def, p_PagCause)			
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)			14.1.1 Steps 3–5
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			
9		(tcv_CellIndInfo.start_CS := tcv_Start)			
10		+ ts_MM_Authentication(tsc_CellA)			Steps 6a–6b
11		+ ts_RRC_Security (tsc_CellA, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM, TRUE, cs_domain)			
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)			
13		+ ts_RB_SetUpRAB_CS (tsc_CellA, p_RB_Type)			14.1.1 Steps 9–10
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_PS (p_RB_Type : RB_Type; p_EstCause : EstablishmentCause; p_PagCause: PagingCause)					
Group : RB_Steps/Initialization/					
Objective : To setup the environnement for PS test cases					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS	(tcv_CN_Domain := ps_domain, tcv_CellIndInfo := c_CellIndInfoDef)			14.1.1 Step 1
2		+ ts_SS_CreateCellDCH (tsc_CellA)			
3		+ ts_SendDefSysInfo (tsc_CellA)			
4		+ ts_IdleUpdated (tsc_CellA)			14.1.1 Step 2
5		(tcv_TestBody:=TRUE)			
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagCause,tsc_RB_PCCH)			
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)			14.1.1 Steps 3–5
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			14.1.1 Step 6
9		(tcv_CellIndInfo.start_PS := tcv_Start)			SECURITY MODE COMMAND SECURITY MODE COMPLETE
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
11		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)			14.1.1 Steps 7–8
13		+ ts_RB_SetUpRAB_PS (tsc_CellA, p_RB_Type)			14.1.1 Steps 9–10
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_RB_InitTest_2SCCPCH(p_PagCause: PagingCause; p_EstCause: EstablishmentCause)						
Group : RB_Steps/Initialization/						
Objective : To setup the environnement for PS test cases						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	TBS	+ts_SS_CreateCell2_SCCPCH_StandAlonePCH (tsc_CellA)			Configur ation has to be changed	
2		+ ts_SetTmpCellInfo (tsc_CellA)				Fetch record correspo nding to current cell
3		+ts_SendSysInfo_2SCCPCH(tsc_CellA,cb_SI B5_Def_2SCCPCH(tcv_TmpCellInfo))				
4		+ ts_IdleUpdated (tsc_CellA)				
5		(tcv_TestBody:=TRUE)				
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagCause,tsc_RB_PCCH2)				
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)			Steps 2-5 Step 6	
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)				
9		(tcv_CellIndInfo.start_PS := tcv_Start)				
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)				
11		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)				SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)				
Detailed Comments :						

Test Step Dynamic Behaviour						
Test Step Name : ts_RB_InitTest_2aSCCPCH(p_PagCause: PagingCause; p_EstCause: EstablishmentCause)						
Group : RB_Steps/Initialization/						
Objective : To setup the environnement for PS test cases						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	TBS	+ts_SS_CreateCell2_SCCPCH_StandAlonePCH_2a (tsc_CellA)			Configur ation has to be changed	
2		+ ts_SetTmpCellInfo (tsc_CellA)				Fetch record correspo nding to current cell
3		+ts_SendSysInfo_2SCCPCH(tsc_CellA,cb_SIB5_Def_2SCCPCH(tcv_TmpCellInfo))				
4		+ ts_IdleUpdated (tsc_CellA)				
5		(tcv_TestBody:=TRUE)				
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagCause,tsc_RB_PCCH2)				
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)			Steps 2-5 Step 6	
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)				
9		(tcv_CellIndInfo.start_PS := tcv_Start)				
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)				
11		+ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)				SECU RITY MODE COMMA ND SECU RITY MODE COMPL ETE
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)				
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_3SCCPCH_2a(p_PagingCause :PagingCause ; p_EstablishmentCause :EstablishmentCause) Group : RB_Steps/Initialization/ Objective : To setup the environnement for PS test cases Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS1	+ts_SS_CreateCellFACH (tsc_CellA)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_ServiceRequest(c_ServiceType_v('010'B), c_MobileIdPTMSI_lv(tcv_AssignedPTMSI), ?))		Configur ation has to be changed Fetch record correspo nding to current cell
2		+ ts_SetTmpCellInfo (tsc_CellA)			
3		+ts_SendDefSysInfo_withoutSIB6_3SCCPCH (tsc_CellA)			
4		+ ts_IdleUpdated (tsc_CellA)			
5		+ts_SendModifiedSIB5_withoutSIB6_3SCCPCH(tsc_CellA)			
6		+It_ReConfigureCell			
7		(tcv_TestBody:=TRUE)			
8		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagingCause,tsc_RB_PCCH)			
9		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstablishmentCause)			Steps 2–5 Step 6
10		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			
11		(tcv_CellIndInfo.start_PS := tcv_Start)			
12		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
13		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
14		+ ts_TC_ActivateRB_TestMod e (tsc_CellDedicated) It_ReConfigureCell			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		+ts_RRC_Delay(5000)			Give delay for UE to listen to new configuration
16		+It_ReleaseCell			
17		+It_ModifyCell			
		It_ReleaseCell			
18		+ ts_CRLC_Rel (tsc_CellA, tsc_RB0)			
19		+ ts_CRLC_Rel (tsc_CellA, tsc_RB_BCCH_FACH)			
20		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
21		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			
22		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
23		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
24		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
25		+ ts_CMAC_Rel (tsc_CellA, tsc_PRACH1)			
26		+ ts_CPHY_TrChRelNonDch (tsc_CellA , tsc_PRACH1)			
27		+ ts_SS_StopRL (tsc_CellA , tsc_AICH1)			
28		+ ts_SS_StopRL (tsc_CellA , tsc_PRACH1)			
29		+ ts_CRLC_Rel (tsc_CellA, tsc_RB_PCCH)			
30		+ ts_CMAC_Rel (tsc_CellA, tsc_S_CCPCH1)			
31		+ ts_CPHY_TrChRelNonDch (tsc_CellA , tsc_S_CCPCH1)			
32		+ ts_SS_StopRL (tsc_CellA , tsc_PICH1)			
33		+ ts_SS_StopRL (tsc_CellA , tsc_S_CCPCH1)			
		It_ModifyCell			
34		[pc_CS]			
35		[(BIT_TO_INT(o_OctToBit(px_TMSI_Def))MOD 2) = 0]			
36		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA.uRNTI.s_rnc_Identity,tcv_CellInfoA.uRNTI.s_RNTI,12,20))MOD 2) = 1]			
37		+ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg1_2a (tsc_CellA)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
38		(tcv_CellCnfg :=1)			
39		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
40		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000001'B)			
41		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg1_2a (tsc_CellA)			
42		(tcv_CellCnfg :=1)			
43		[(BIT_TO_INT(o_OctToBit(px_TMSI_Def))MOD 2) = 1]			
44		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
45		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg2_2a (tsc_CellA)			
46		(tcv_CellCnfg :=2)			
47		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
48		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000000'B)			
49		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg2_2a (tsc_CellA)			
50		(tcv_CellCnfg :=2)			
51		[pc_PS]			
52		[
		(BIT_TO_INT(o_OctToBit(px_PTMSI_Def))MOD 2) = 0]			
53		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
54		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg1_2a (tsc_CellA)			
55		(tcv_CellCnfg :=1)			
56		[
		(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
57		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000001'B)			
58		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg1_2a (tsc_CellA)			
59		(tcv_CellCnfg :=1)			
60		[
		(BIT_TO_INT(o_OctToBit(px_PTMSI_Def))MOD 2) = 1]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
61		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 0]			
62		+ts_SS_ModifyCell3_SCCPCH_4_FACH_C nfg2_2a (tsc_CellA)			
63		(tcv_CellCnfg :=2)			
64		[(BIT_TO_INT(o_BitstringConcat(tcv_CellInfoA. uRNTI.srnc_Identity,tcv_CellInfoA.uRNTI.s_RN TI,12,20))MOD 2) = 1]			
65		(tcv_CellInfoA.uRNTI.s_RNTI := '00000000000000000000'B)			
66		+ts_SS_ModifyCell3_SCCPCH_4_FACH_ Cnfg2_2a (tsc_CellA)			
67		(tcv_CellCnfg :=2)			
68		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_InitTest_1SCCPCH(p_PagingCause :PagingCause ; p_EstablishmentCause :EstablishmentCause)					
Group : RB_Steps/Initialization/					
Objective : To setup the environnement for PS test cases					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBS	+ts_SS_CreateCellFACH (tsc_CellA)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_ServiceRequest(c_ServiceType_v('010'B), c_MobileIdPTMSI_lv(tcv_AssignedPTMSI), ?))		Configur ation has to be changed
2		+ ts_SetTmpCellInfo (tsc_CellA)			Fetch record correspo nding to current cell
3		+ts_SendDefSysInfo(tsc_CellA)			Steps 2-5
4		+ ts_IdleUpdated (tsc_CellA)			
5		(tcv_TestBody:=TRUE)			
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagingCause ,tsc_RB_PCCH)			
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstablishmentCause)			
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
9		(tcv_CellIndInfo.start_PS := tcv_Start)			
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
11		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			
12					+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_RB_InitTest_3SCCPCH_CTCH_2a(p_PagCause: PagingCause; p_EstCause : EstablishmentCause)						
Group : RB_Steps/Initialization/						
Objective : To setup the environement for PS test cases						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	TBS	+ts_SS_CreateCell3_SCCPCH_3_FACH_CTCH_2a (tsc_CellA)			Configur ation has to be changed	
2		+ ts_SetTmpCellInfo (tsc_CellA)			Fetch record correspo nding to current cell	
3		+ts_SendSysInfoWithSpecialSIB5_And6(tsc_CellA,cb_SIB5_Def_3SCCPCH_1(tcv_TmpCellInfo),cb_SIB6_Def_3SCCPCH(tcv_TmpCellInfo))				
4		+ ts_IdleUpdated (tsc_CellA)				
5		(tcv_TestBody:=TRUE)				
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagCause,tsc_RB_PCCH)				
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)				Steps 2-5
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)			car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_ServiceRequest(c_ServiceType_v('010'B), c_MobileIdPTMSI_lv(tcv_AssignedPTMSI), ?))	Step 6
9		(tcv_CellIndInfo.start_PS := tcv_Start)				
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)				
11		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)				SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)				Steps 7-8
Detailed Comments :						

Test Step Dynamic Behaviour	
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Test Step Name	: ts_RB_InitTest_3SCCPCH_CTCH (p_PagCause: PagingCause; p_EstCause : EstablishmentCause)
Group	: RB_Steps/Initialization/
Objective	: To setup the environnement for PS test cases
Default	: RRC_Def1
Comments	:
Description	:

Group : RB_Steps/Initialization/

Objective	: To setup the environment for PS test cases
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Default	: RRC Def1
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Comments :	
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Description :	
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Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	TBS	+ts_SS_CreateCell3_SCCPCH_3_FACH_CTCH (tsc_CellA)			Configur ation has to be changed Fetch record correspo nding to current cell	
2		+ ts_SetTmpCellInfo (tsc_CellA)				Steps 2–5 Step 6
3		+ts_SendSysInfoWithSpecialSIB5_And6(tsc_CellA,cb_SIB5_Def_3SCCPCH_1(tcv_TmpCellInfo),cb_SIB6_Def_3SCCPCH(tcv_TmpCellInfo))				
4		+ ts_IdleUpdated (tsc_CellA)				
5		(tcv_TestBody:=TRUE)				
6		+ts_RRC_PagType1_P_TMSI_Cause (tsc_CellA, px_PTMSI_Def, p_PagCause,tsc_RB_PCCH)				
7		+ ts_RRC_ConnEst (tsc_CellA, est_MT, p_EstCause)				
8		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)				
9		(tcv_CellIndInfo.start_PS := tcv_Start)				
10		+ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE	
11		+ ts_RRC_Security (tsc_CellA, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)				
12		+ ts_TC_ActivateRB_TestMode (tsc_CellDedicated)				Steps 7–8

Detailed Comments :

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_DCCH_BCCH_DTCH_Cfg_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH1) for FACH. DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH1_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH1 and 2 to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACH_CfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS_2a)		map PCCH to PCH, and map CCCH, BCCH, DTCH and DCCH's to FACH. C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACH_CfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_CreateCell2_SCCPCH_StandAlonePCH_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To setup a baseline cell which consists of the following: physical channels: p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH1, s-CCPCH2 and PRACH; transport channels: BCH, FACH, PCH, RACH; logical channels: BCCH, CCCH, PCCH, DCCH; and signalling radio bearer RB0(CCCH), RB1(UM DCCH), RB2(AM DCCH), RB3(AM DCCH for NAS high priority), RB4(AM DCCH for NAS low priority) on FACH and RACH, RB-3(TM BCCH FACH) on FACH, RB20(AM DTCH) on FACH and RACH, RB-1 (TM, BCCH) on BCH, RB-19 (TM, PCCH, PCH, s-CCPCH2) Default : InitOtherwiseFail Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) This step shall be used for single cell test cases only. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SS_CellCfg (p_CellId)			
2		+ts_SS_BCH_SCH_CPICH_Cfg(p_CellId)			
3		+ ts_SS_SecondSCCPCH_PCH_PCCH_Cfg (p_CellId)			
4		+ ts_SS_2FACH_CCCH_DCCH_BCCH_DTC H_Cfg_2a (p_CellId)			
5		+ts_SS_RACH_CCCH_DCCH_DTCH_Cf g_2a (p_CellId)			
6		+ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
7		+ts_SS_RB_PCCH2_Cfg(p_CellId)			
8		+ts_SS_RB0_Cfg(p_CellId)			
9		+ts_SS_RB1_ToRB4_Cfg			
10		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			
11		+ts_SS_RB20_AM_24_AM_Cfg			
12		+ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandA lonePCH_2a_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg1_2a (p_CellId:INTEGER) Group : RB_Steps/Initialization/ Objective : To create the cell with 3 SCCPCHs map PCCH to PCH to SCCPCH1 map BCCH and CCCH,to FACH1 and DTCH to FACH2 and inturn map FACH1 and FACH2 to SCCPCH2 map BCCH,CCCH,DCCH,to FACH3 and map DTCH to FACH3 and inturn map FACH3 and FACH4 to SCCPCH3 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SS_FirstSCCPCH_PCH_PCCH_Cfg(p_CellId)			PCH->SCCPCH1
2		+ts_SS_2FACH_CCCH_BCCH_SCCPCH2_Cfg_2a(p_CellId)			(BCCH, CCCH, ->FACH1 , DTCH->FACH2) ---->SCCPCH2
3		+ts_SS_2FACH_CCCH_BCCH_DCCH_DTC H_SCCPCH3_Cfg_2a(p_CellId, tsc_S_CCPCH3_ChC)			(BCCH, CCCH, DCCH -> FACH3 ,DTCH->FACH4) ---->SCCPCH3
4		+ts_SS_RACH_CCCH_DCCH_DTCH_Cfg_2a (p_CellId)			
5		+ts_SS_RB_PCCH_Cfg(p_CellId)			
6		+ts_SS_RB0_Cfg(p_CellId)			
7		+ts_SS_RB29_Cfg(p_CellId)			
8		+ts_SS_RB1_ToRB4_Cfg			
9		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			RB9 is on BCCH-FACH
10		+ts_SS_RB_BCCH_FACH_RAB_Cfg(p_CellId)			RB9 is on BCCH-FACH
11		+ts_SS_RB20_AM_24_AM_Cfg			
12		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg2_2a (p_CellId:INTEGER) Group : RB_Steps/Initialization/ Objective : To create the cell with 3 SCCPCHs map PCCH to PCH to SCCPCH1 map BCCH and CCCH,to FACH1 and DTCH to FACH2 and inturn map FACH1 and FACH2 to SCCPCH2 map BCCH,CCCH,DCCH,to FACH3 and map DTCH to FACH3 and inturn map FACH3 and FACH4 to SCCPCH3 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SS_FirstSCCPCH_PCH_PCCH_Cfg(p_CellId)			PCH->SCCPCH1
2		+ts_SS_2FACH_CCCH_BCCH_SCCPCH3_Cfg_2a(p_CellId)			(BCCH, CCCH, ->FACH1 , DTCH->FACH2) ---->SCCPCH2
3		+ts_SS_2FACH_CCCH_BCCH_DCCH_DTC H_SCCPCH2_Cfg_2a(p_CellId)			(BCCH, CCCH, DCCH ->FACH3 ,DTCH->FACH4) ---->SCCPCH3
4		+ts_SS_RACH_CCCH_DCCH_DTCH_Cfg_2a (p_CellId)			
5		+ts_SS_RB_PCCH_Cfg(p_CellId)			
6		+ts_SS_RB0_Cfg(p_CellId)			
7		+ts_SS_RB29_Cfg(p_CellId)			
8		+ts_SS_RB1_ToRB4_Cfg			
9		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			RB9 is on BCCH-FACH
10		+ts_SS_RB_BCCH_FACH_RAB_Cfg(p_CellId)			RB9 is on BCCH-FACH
11		+ts_SS_RB20_AM_24_AM_Cfg			
12		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_SCCPCH2_Cfg_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH2_DL_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_CCCH_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH1 and 2 to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_PS, c_TrLogMappingFACH_BCCH_CCCH_PS_2a)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DTCH_SCCPCH2_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH2_DL_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_CCCH_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH1 and 2 to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_PS, c_TrLogMappingFACH_BCCH_CCCH_PS)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DTCH_SCCPCH3_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH3, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH3_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), 90)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH3)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_CCCH_SCCPCH3_InfoActNow (p_CellId, tsc_S_CCPCH3)		connect FACH3 and 4 to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH3)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_SCCPCH3_PS, c_TrLogMappingFACH_BCCH_CCCH_SCCPCH3_PS)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH3)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_SCCPCH3_Cfg_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		(tcv_TmpCellInfo.timingsCCPCH1 := 90)			
4		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH3, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH3_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
5		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH3)		
6		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_CCCH_SCCPCH3_InfoActNow (p_CellId, tsc_S_CCPCH3)		connect FACH3 and 4 to s-CCPCH1
7		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH3)		
8		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_SCCPCH3_PS, c_TrLogMappingFACH_BCCH_CCCH_SCCPCH3_PS_2a)		map CCCH, BCCH C-RNTI and U-RNTI are required.
9		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH3)		
10	ERR1	[px_RAT = tdd]		I	
11	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DCCH_DTCH_SCCPCH2_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH3), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH,DCCH1,DCCH2,DCCH3,DCCH4, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH,DCCH1, DCCH2,DCCH3,DCCH4,BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH2_DL_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_DCCH_CCCH_DTCH_SCCPCH2_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH3 and 4to s-CCPCH3
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_DCCH_SCCPCH2_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_SCCPCH2_PS)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DCCH_DTCH_SCCPCH2_Cfg_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH3), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH,DCCH1,DCCH2,DCCH3,DCCH4, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH,DCCH1, DCCH2,DCCH3,DCCH4,BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH2_DL_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_DCCH_CCCH_DTCH_SCCPCH2_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH3 and 4to s-CCPCH3
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_DCCH_SCCPCH2_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_SCCPCH2_PS_2a)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DCCH_DTCH_SCCPCH3_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH3), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH,DCCH1,DCCH2,DCCH3,DCCH4, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH,DCCH1, DCCH2,DCCH3,DCCH4,BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH3, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH3_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), 90)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH3)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_DCCH_CCCH_DTCH_InfoActNow (p_CellId, tsc_S_CCPCH3)		connect FACH3 and 4 to s-CCPCH3
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH3)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCCH_DCCH_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DCCH_DTCH_SCCPCH3_Cfg_2a (p_CellId : INTEGER ; p_SF256_AndCodeNumber : SF256_AndCodeNumber) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH3), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH,DCCH1,DCCH2,DCCH3,DCCH4, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH,DCCH1, DCCH2,DCCH3,DCCH4,BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		(tcv_TmpCellInfo.timingsCCPCH1:= 90)			
4		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH3, tsc_S_CCPCH_2ndScrCode, p_SF256_AndCodeNumber, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
5		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH3)		
6		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_DCCH_CCCH_DTCH_InfoActNow (p_CellId, tsc_S_CCPCH3)		connect FACH3 and 4 to s-CCPCH3
7		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH3)		
8		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_DCCH_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS_2a)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
9		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
10	ERR1	[px_RAT = tdd]		I	
11	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_CTCH_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect FACH to the secondary CCPCH . (34.108 cl. 4.2.1), finally to map CCCH, CTCH and BCCH (for BCCH_FACH) to FACH. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH, CTCH and BCCH (for BCCH_FACH) to FACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, sf128:5,6, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_FACH_Info_BMC_ActNow (p_CellId, tsc_S_CCPCH2)		connect FACH1 and FACH2 to s-CCPCH2
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info(tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfo_FACH_BMC, c_TrLogMapping_FACH_BMC)		map CCCH, BCCH, to FACH1 and CTCH to FACH2
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB30_RAB_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : setup radio bearers : RB30 (downlink only) this bearer is for CTCH–FACH (UM + CTCH + FACH + SCCPCH1). Used for CBS messages Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_UM_DL_InfoCTCH_RAB(p_CellId, tsc_RB30, {dLogicalChannelIdentity tsc_CTCH})		cofigure radio bearers (downlin k): RB30 (UM + CTCH + FACH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB30)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB_BCCH_FACH_RAB_Cfg (p_CellId: INTEGER) Group : RB_Steps/Initialization/ Objective : setup radio bearers : RB9 (downlink only) this bearer is for BCCH–FACH (TM + BCCH + FACH + SCCPCH1). The configuration is adapted from 34.108 cl. 6.10.2.4.3. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_DL_Info(p_CellId, tsc_RB_BCCH_FACH_RAB , 166, {dLogicalChannelIdentity tsc_BCCH7})		cofigure radio bearers (downlin k): RB19 (TM + BCCH + FACH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB_BCCH_FACH_RAB)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB29_Cfg (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : to setup radio bearers : RB29 (the downlink is UM + CCCH + FACH + sCCPCH3 and uplink is TM + CCCH + RACH + PRACH).The configuration is adapted from 34.108 cl. 6.10.2.4.3 and 6.10.2.4.4 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_UM_DL_Info(p_CellId, tsc_RB29, {dLlogicalChannelIdentity tsc_DL_CCCH6})		cofigure radio bearers (downlink): RB29 (UM + CCCH + FACH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB29)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RACH_CCCH_DCCH_DTCH_Cfg_2a (p_CellId : INTEGER) Group : RB_Steps/Initialization/ Objective : To configure AICH and PRACH physical channels and connect RACH onto PRACH, then map five logical channels (CCCH, DCCH1, DCCH2, DCCH3, DCCH4) to RACH Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_AichInfo(p_CellId, tsc_AICH1, c_AICH_Info, (tcv_TmpCellInfo.powerAICH))		AICH
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_AICH1)		
5		CPHY!CPHY_RL_Setup_REQ	ca_PRACH_Info(p_CellId, tsc_PRACH1, tsc_PRACH1_Signatures, tsc_PRACH1_ScrC, tcv_TmpCellInfo.puncLimit, tcv_TmpCellInfo.sf_PRACH , tcv_SubChNum)		PRACH
6		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_PRACH1)		
7		CPHY!CPHY_TrCH_Config_REQ	cab_RACH_InfoActNow (p_CellId, tsc_PRACH1)		connect RACH to PRACH
8		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_PRACH1)		
9		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo(p_CellId , tsc_PRACH1, c_UE_Info(OMIT, tcv_TmpCellInfo.cRNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_2_DTCH)		CCCH, DCCH1, DCCH2, DCCH3, DCCH4 to RACH. Only C-RNTI is used for DCCH/ DTCH on PRACH
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_PRACH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_24_AM_Cfg Group : RB_Steps/Initialization/ Objective : setup radio bearers : RB20 and 24 mapped on AM Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB(tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},320)		cofigure radio bearers :
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated, tsc_RB20)		RB22(A M + DTCH)
3		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB(tsc_CellDedicated, tsc_RB24, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH4, dLogicalChannelIdentity tsc_DL_DTCH4},320)		SS configur ation of the radio bearer informati on :
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated, tsc_RB24)		RB231 (AM + DTCH)
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_4DCH_ModifySpeech (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info; p_DchTFS5 , p_DchTFS1 , p_DchTFS1_DL, p_DchTFS2,p_DCHTFS_0B : CommonOrDedicatedTFS; p_sf : SF512_AndCodeNumber) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1and connect DCH1, DCH2,DCH3 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3), to the DCH1, DCH2 & DCH3 transport channel respectively. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (p_sf, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_0_To11_DL_Inf o(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, p_DchTFS5 , p_DchTFS1_DL , p_DchTFS2,p_DCHTFS_0B , c_PowerOffsetInfoBelow64 k ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_4_Speech (p_DchTFS5 , p_DchTFS1_DL , p_DchTFS2,p_DCHTFS_0B) , c_TrLogMappingDL_4DCC H_3DTCH,p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY!CPHY_TrCH_Config_RE Q	ca_4_DCH_0_To11_UL_Inf o (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, p_DchTFS5 , p_DchTFS1 , p_DchTFS2,p_DCHTFS_0B ,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_C NF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_4_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2,p_DCHTFS_0B) , c_TrLogMappingUL_4DCC H_3DTCH,p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1.Configure DL ,UL Radio Link. 2.Configure DL/UL Transport channels 3.Mapping of Logical to Transport channels					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_PS_Cfg_twrw512 (p_Payloadsize: INTEGER) Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : RB20. default values from 34.108 cl. 6.10.2.4.4 and 6.10.2.4.3.3 Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB_twrw512 (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},p_Payload size)		cofigure radio bearers :
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB20)		RB20 (AM + DTCH)
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_FirstSCCPCH_PCH_PCCH_Cfg_CodNum4 (p_CellId : INTEGER) Group : RB_Steps/RB_Configuration/ Objective : To configure the second secondary CCPCH (tsc_S_CCPCH2), then connect PCH to the secondary CCPCH . (34.108 cl. 4.2.1), finally to map PCCH to PCH. Default : InitOtherwiseFail Comments : this step is same as ts_SS_FirstSCCPCH_PCH_PCCH_Cfg except for Code number 6-->4 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_InfoPCH_RA B_StandAloneCodNum4 (p_CellId, tsc_S_CCPCH1, tsc_S_CCPCH_2ndScrCode, 4, (tcv_TmpCellInfo.powersCCPCH1))		s-CCPCH2
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_PCH_Info2 (p_CellId, tsc_S_CCPCH1)		connect PCH and FACH to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoPCH, c_TrLogMappingPCH_NoFACH)		map PCCH to PCH,
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH1)		
9		CPHY!CPHY_RL_Setup_REQ	ca_PICH_Info(p_CellId, c_PichInfo, (tcv_TmpCellInfo.powerPICH) ,1)		PICH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_PICH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyStreamUnknown28_8_NoSc (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info; p_CommonTFS_CPHY : CommonOrDedicatedTFS </p>)					
Group : RB_Steps/RB_Configuration/					
Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Streaming / unknown 28.8kbs.					
Default : SS_Def					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (<p> p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc64, p_DL_CommonInformation) ,p_ActTime) </p>		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_DL_Info (p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, p_CommonTFS_CPHY ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (<p> tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoDL_2_0_To5 (c_DCH_148_TFS_DL, p_CommonTFS_CPHY), c_TrLogMappingDL_2 ,p_ActTime) </p>		3. C-RNTI and U-RNTI is not required on DPCH
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (<p> p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime) </p>		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, p_CommonTFS_CPHY ,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		3. C-RNTI and U-RNTI is not required on DPCH
13		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_2_0_To5 (c_DCH_148_TFS_UL ,p_CommonTFS_CPHY), c_TrLogMappingUL_2 ,p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
15	ERR1	[px_RAT = tdd]		I	
16	ERR2	[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational / unknown / UL:28.8 DL:28.8 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyStreamUnknown14_4_NoSc (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info; p_CommonTFS_CPHY : CommonOrDedicatedTFS </p>)					
Group : RB_Steps/RB_Configuration/					
Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Streaming / unknown 14.4kbps.					
Default : SS_Def					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (<p> p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation),p_ActTime) </p>		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (<p> p_CellId, tsc_DL_DPCH1) </p>		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_DL_Info (<p> p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, p_CommonTFS_CPHY,p_A ctTime) </p>		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (<p> tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, p_CommonTFS_CPHY), c_TrLogMappingDL_2 ,p_ActTime) </p>		3. C-RNTI and U-RNTI are not required on DPCH
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf (<p> tsc_CellDedicated, tsc_DL_DPCH1) </p>		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (<p> p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me) </p>		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (<p> p_CellId, tsc_UL_DPCH1) </p>		
11		CPHY!CPHY_TrCH_Config_RE Q	ca_2_DCH_0_To3_UL_Info (<p> p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, p_CommonTFS_CPHY ,p_ActTime) </p>		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		3. C-RNTI and U-RNTI are not required on DPCH
13		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_2_0_To3 (c_DCH_148_TFS_UL, p_CommonTFS_CPHY), c_TrLogMappingUL_2 ,p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf (tsc_CellDedicated, tsc_UL_DPCH1)		
15	ERR1	[px_RAT = tdd]		I	
16	ERR2	[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational / unknown / UL:28.8 DL:28.8 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_PS_NoSc (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:32kbps, 40ms TTI TC. Default : RRC_Def1 Comments : This step is same as ts_SS_2DCH_ModifyInteractBackg_32k_PS, except for DL Scrambling code Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_40_TC,c _PowerOffsetInfoBelow64k, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_40_TC,c _PowerOffsetInfoBelow64k), c_TrLogMappingDL_2_PS ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO,c_DCH_148_TFS_UL, c_DCH_336_TFS_40_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_40_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown / UL:32 DL:32kbps/PS 40ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_64k_64k_ChC31 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : This step is same as ts_5DCH_ModifyConvSpeech_InteractBackg_64k_64k except for Chanelisation code which is now 31 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64K_PS_ChC31(p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k, activationCFN : p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH 2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_4DCH_Modify_ChC127 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info)					
Group : RB_Steps/RB_Configuration/					
Objective : to modify physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for AMR speech.					
Default : SS_Def					
Comments : this step is same as ts_SS_4DCH_Modify, except for Channelisation code, which is now 127 in place of 0					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_122_AMR_ChC127 (p_DL_CommonInformation, OMIT), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4DCH_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_122_AMR, c_TrLogMappingDL_4DCH_3DTCH, p_ActTime)		3. C-RNTI and U-RNTI are not needed on DPCH
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf (tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_4DCH_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour						
Nr	Label	Behaviour Description		Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ		ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_122_AMR, c_TrLogMappingUL_4DCC H_3DTCH,p_ActTime)		3. C-RNTI and U-RNTI are not needed on DPCH
14		CMAC ? CMAC_Config_CNF		ca_CMAC_CfgCnf (tsc_CellDedicated, tsc_UL_DPCH1)		
15	ERR1	[px_RAT = tdd]			I	
16	ERR2	[TRUE]			I	
Detailed Comments : 1. configure new DPCH1for transporting conversational/speech/UL:12.2 DL:12.2 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1-4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.						

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg1 (p_CellId:INTEGER) Group : RB_Steps/RB_Configuration/ Objective : To create the cell with 3 SCCPCHs map PCCH to PCH to SCCPCH1 map BCCH and CCCH,to FACH1 and DTCH to FACH2 and inturn map FACH1 and FACH2 to SCCPCH2 map BCCH,CCCH,DCCH,to FACH3 and map DTCH to FACH3 and inturn map FACH3 and FACH4 to SCCPCH3 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SS_FirstSCCPCH_PCH_PCCH_Cfg(p_CellId)			PCH-> SCCPC H1
2		+ts_SS_2FACH_CCCH_BCCH_DTCH_SCCPCH2_Cfg(p_CellId)			(BCCH, CCCH, ->FACH1 , DTCH->FACH2) ---->SCCPCH2
3		+ts_SS_2FACH_CCCH_BCCH_DCCH_DTC H_SCCPCH3_Cfg(p_CellId)			(BCCH, CCCH, DCCH -> FACH3 ,DTCH->FACH4) ----> SCCPC H3
4		+ts_SS_RACH_CCCH_DCCH_DTCH_Cfg (p_CellId)			
5		+ts_SS_RB_PCCH_Cfg(p_CellId)			
6		+ts_SS_RB0_Cfg(p_CellId)			
7		+ts_SS_RB29_Cfg(p_CellId)			
8		+ts_SS_RB1_ToRB4_Cfg			
9		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			RB9 is on BCCH-FACH
10		+ts_SS_RB_BCCH_FACH_RAB_Cfg(p_CellId)			RB9 is on BCCH-FACH
11		+ts_SS_RB20_AM_PS_Cfg (320)			
12		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ModifyCell3_SCCPCH_4_FACH_Cnfg2 (p_CellId:INTEGER) Group : RB_Steps/RB_Configuration/ Objective : To create the cell with 3 SCCPCHs map PCCH to PCH to SCCPCH1 map BCCH and CCCH,to FACH1 and DTCH to FACH2 and inturn map FACH1 and FACH2 to SCCPCH2 map BCCH,CCCH,DCCH,to FACH3 and map DTCH to FACH3 and inturn map FACH3 and FACH4 to SCCPCH3 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SS_FirstSCCPCH_PCH_PCCH_Cfg(p_CellId)			PCH-> SCCPC H1
2		+ts_SS_2FACH_CCCH_BCCH_DTCH_SCCPCH3_Cfg(p_CellId)			(BCCH, CCCH, ->FAC H1 , DTCH->FACH2) ---->S CCPCH 2
3		+ts_SS_2FACH_CCCH_BCCH_DCCH_DTC H_SCCPCH2_Cfg(p_CellId)			(BCCH, CCCH, DCCH -> FACH3 ,DTCH->FACH4) ----> SCCPC H3
4		+ts_SS_RACH_CCCH_DCCH_DTCH_Cfg (p_CellId)			
5		+ts_SS_RB_PCCH_Cfg(p_CellId)			
6		+ts_SS_RB0_Cfg(p_CellId)			
7		+ts_SS_RB29_Cfg(p_CellId)			
8		+ts_SS_RB1_ToRB4_Cfg			
9		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			RB9 is on BCCH-FACH
10		+ts_SS_RB_BCCH_FACH_RAB_Cfg(p_CellId)			RB9 is on BCCH-FACH
11		+ts_SS_RB20_AM_PS_Cfg (320)			
12		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_20_DiffRM_DCH5 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL_RM1 92, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_20_2, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL_RM1 92, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_20_2,c_ PowerOffsetInfoBelow64k), c_TrLogMappingDL_TM4,p_ ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_20_2,p_ ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_20_2), c_TrLogMappingUL_TM4,p_ ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI 2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifySpeech7_4k_AMR_DiffRM_DCH5 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info)					
Group : RB_Steps/RB_Configuration/					
Objective : to reconfigure physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for 10.2 kbps speech.					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_7_4k_AMR_DL_Info_DiffRM_DCH5 (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_7_4k_AMR_DiffRM_DCH5, c_TrLogMappingDL_3_Speech, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_7_4k_AMR_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_7_4k_AMR, c_TrLogMappingUL_3_Spee ch,p_ActTime)		3.
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational/speech/UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_4DCH_ModifySpeech10_2k_AMR_DiffRM_DCH5 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info)					
Group : RB_Steps/RB_Configuration/					
Objective : to reconfigure physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for 10.2 kbps speech.					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_10_2k_AMR_DL_Info_DiffRM_DCH5 (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_10_2k_AMR_AMR_DiffRM_DCH5, c_TrLogMappingDL_4DCC H_3DTCH, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_10_2k_AMR_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_10_2k_AMR _AMR, c_TrLogMappingUL_4DCC H_3DTCH ,p_ActTime)		3.
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational/speech/UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_4DCH_ModifySpeech12_2k_AMR_DiffRM_DCH5 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to reconfigure physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for 10.2 kbps speech. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_102_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_148_TFS_DL_RM1 92, c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx(c_PowerOffsetInfoBelow64k), p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoDL_12_2k_AMR_DiffRM_DCH5(c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (c_PowerOffsetInfoBelow64 k)), c_TrLogMappingDL_4DCC H_3DTCH, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY!CPHY_TrCH_Config_RE Q	ca_4_DCH_102_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_81_TFS_6 , c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_148_TFS_UL, c_TFCS_Cmpl0_1_8_15_22 _59_60_61_68_75_82_119 _Rx,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_C NF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoUL_12_2k_AMR (c_TFCS_Cmpl0_1_8_15_2 2_59_60_61_68_75_82_11 9_Rx), c_TrLogMappingUL_4DCC H_3DTCH ,p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational/speech/UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_BCCH_DCCH_DTCH_SCCPCH3_Cfg2 (p_CellId : INTEGER) Group : RB_Steps/RB_Configuration/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH3), then connect 2 FACH's to the secondary CCPCH . Finally to map CCCH,DCCH1,DCCH2,DCCH3,DCCH4, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. The configuration shall be as per section 6.1.2 of 34.108 Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH2) for FACH. CCCH,DCCH1, DCCH2,DCCH3,DCCH4,BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH3, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH1_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), 90)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH3)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_BCCH_DCCH_CCCH_DTCH_InfoActNow (p_CellId, tsc_S_CCPCH3)		connect FACH3 and 4 to s-CCPCH3
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH3)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_CCH_DCCH_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS)		map CCCH, BCCH, DTCH C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_S_CCPCH3)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_3SCCPCH_CTCH(p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : RB_Steps/RB_Configuration/ Objective : econfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti] CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS)		map CCCH, BCCH, DTCH U-RNTI are required.
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
7		[NOT p_urnti]			
8		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DT CH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
9		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
10		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH3, c_UE_Info (OMIT, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS)		map CCCH, BCCH, DTCH C-RNTI are required.
11		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_64k_20_Order1 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:64 DL: 64 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k, activationCFN : p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19_ Order1 (c_DCH_148_TFS_DL, c_DCH_640_TFS, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS, c_DCH_336_TFS_24_UL_20_TC , activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19_Order1 (c_DCH_148_TFS_UL, c_DCH_640_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM1_A M1,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:64 DL: 64 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyConvUnknown (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info ; p_DCH1_UE : DedicatedTransChTFS; p_DCH1 : CommonOrDedicatedTFS ;p_sf : SF512_AndCodeNumber) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1and connect DCH1 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Conversational / unknown / UL:32 or 64 DL:32 or 64 kbps. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (p_sf, p_DL_CommonInformation ,OMIT),p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, p_DCH1,c_PowerOffsetInfoHigher64k ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, p_DCH1), c_TrLogMappingDL_2,p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, p_DCH1 ,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3(c_DCH_148_TFS_UL, p_DCH1), c_TrLogMappingUL_2,p_ActTime)		3.
14		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational / unknown / UL:28.8 DL:28.8 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyConvUnknown_64k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info ; p_DCH1 : CommonOrDedicatedTFS ;p_sf : SF512_AndCodeNumber)					
Group : RB_Steps/RB_Configuration/					
Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Conversational / unknown / UL:32 or 64 DL:32 or 64 kbps.					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (p_sf, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, p_DCH1, c_PowerOffsetInfoHigher64k ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, p_DCH1), c_TrLogMappingDL_2, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, p_DCH1 ,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3(c_DCH_148_TFS_UL, p_DCH1), c_TrLogMappingUL_2 ,p_ActTime)		3.
14		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational / unknown / UL:28.8 DL:28.8 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyConvUnknown_28_8 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for conversational / unknown / UL:28.8 DL:28.8 kbps. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_DL_Info (p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, c_DCH_576_TFS_3 (180),p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To5 (c_DCH_148_TFS_DL, c_DCH_576_TFS_3 (180)), c_TrLogMappingDL_4DCC H_1DTCH,p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_576_TFS_3 (180) ,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_576_TFS_3 (180)), c_TrLogMappingUL_4DCC H_1DTCH ,p_ActTime) ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		3.
14		CMAC ? CMAC_Config_CNF			
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational / unknown / UL:28.8 DL:28.8 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyInteractBackg_32k_8kPS_10_CC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:8 kbps, 10ms TTI, CC Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_40_CC, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_40_CC), c_TrLogMappingDL_2_PS, p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_10_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_CC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown/UL:32 DL:8kbps/PS 10ms TTI, CC RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyInteractBackg_32k_8kPS_10_TC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:8 kbps, 10ms TTI, TC. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_40_TC), c_TrLogMappingDL_2_PS, p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_10_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown / UL:32 DL:8kbps/PS, 10ms TTI, TC RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyStreamUnknown_128k_0k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:128 DL:0 kbps. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT), p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_576_TFS_2, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_576_TFS_2), c_TrLogMappingDL_2 , p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_320_TFS_21_UL_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_2_0To11 (c_DCH_148_TFS_UL, c_DCH_320_TFS_21_UL_4 0_TC), c_TrLogMappingUL_2 ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting streaming / unknown / UL:128 DL:0 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_2DCH_ModifyStreamUnknown_64k_0k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:64 DL:0 kbps. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc128, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_576_TFS_2, c_PowerOffsetInfoBelow64k, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_576_TFS_2), c_TrLogMappingDL_2 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_320_TFS_19_UL_40_TC ,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_320_TFS_19_UL_4 0_TC), c_TrLogMappingUL_2,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting streaming / unknown / UL:64 DL:0 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1-4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_640_TFS_20_2, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To7 (c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_640_TFS_20_2, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM2 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_640_TFS_20_2 ,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_640_TFS_20_2), c_TrLogMappingUL_TM2 ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / unknown / UL:64 DL:64kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_40(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_640_TFS_40_4, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To7(c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4,c_DCH_640_TFS_40_4, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM2 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_640_TFS_40_4,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_640_TFS_40_4), c_TrLogMappingUL_TM2,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / unknown / UL:64 DL:64kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_128k_128k_20(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_28_UL_2 0_TC		2.
11		CPHY?CPHY_TrCH_Config_CN F	,activationCFN :p_ActTime) ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_TM1_A M1,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifySpeech (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info; p_DchTFS5 , p_DchTFS1 , p_DchTFS1_DL, p_DchTFS2 : CommonOrDedicatedTFS; p_sf : SF512_AndCodeNumber) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1, DCH2, and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), to the DCH1, DCH2, transport channel respectively. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (p_sf, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To11_DL_Info(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_DchTFS5 , p_DchTFS1_DL , p_DchTFS2, c_PowerOffsetInfoBelow64k ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_3_Speech (p_DchTFS5 , p_DchTFS1_DL , p_DchTFS2), c_TrLogMappingDL_3_Speech, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To11_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_DchTFS5 , p_DchTFS1 , p_DchTFS2, p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
13		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_3_Speech (p_DchTFS5 , p_DchTFS1 , p_DchTFS2), c_TrLogMappingUL_3_Speech, p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1.Configure DL ,UL Radio Link. 2.Configure DL/UL Transport channels 3.Mapping of Logical to Transport channels					

Test Step Dynamic Behaviour					
Test Step Name : ts_4DCH_ModifySpeech102 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to reconfigure physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for 10.2 kbps speech. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_102_DL_Info(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_65_TFS_DL, c_DCH_99_TFS, c_DCH_40_TFS, c_DCH_148_TFS_DL, c_TFCS_Cmpl0_1_11_12_13_23_Tx (c_PowerOffsetInfoBelow64k), p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_102_AMR, c_TrLogMappingUL_4DCC H_3DTCH, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_0_To23_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_102_AMR, c_TrLogMappingUL_4DCC H_3DTCH ,p_ActTime)		3.
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational/speech/UL:10.2 DL:10.2 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB10_To_11_TM_CS_Config (p_PayloadSize1,p_PayloadSize2: INTEGER) Group : RB_Steps/RB_Configuration/ Objective : Default : InitOtherwiseFail Comments : setup radio bearers : RB10 and RB11 for radio beaer subflow#1, subflow#2 respectively. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRCL ! CRCL_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB10, p_PayloadSize1, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 for AMR subflow #1
2		CRCL ? CRCL_Config_CNF	ca_CRCL_CfgCnf(tsc_Cell Dedicated , tsc_RB10)		
3		CRCL ! CRCL_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB11, p_PayloadSize2, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2})		cofigure radio bearers : RB11 for AMR subflow #2
4		CRCL ? CRCL_Config_CNF	ca_CRCL_CfgCnf(tsc_Cell Dedicated , tsc_RB11)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB10_To_13_TM_CS_Config (p_PayloadSize1, p_PayloadSize2, p_PayloadSize3, p_PayLoadSize4: INTEGER) Group : RB_Steps/RB_Configuration/ Objective : Default : InitOtherwiseFail Comments : setup radio bearers : RB10,RB11,RB12 and RB13 for radio beaer subflow#1, subflow#2,subflow#3 and subflow#4 respectively. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB10, p_PayloadSize1, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 for AMR subflow #1
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated , tsc_RB10)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB11, p_PayloadSize2, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2})		cofigure radio bearers : RB11 for AMR subflow #2
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated , tsc_RB11)		
5		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB12, p_PayloadSize3, {uLogicalChannelIdentity tsc_UL_DTCH3, dLogicalChannelIdentity tsc_DL_DTCH3})		cofigure radio bearers : RB12 for AMR subflow #3
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated , tsc_RB12)		
7		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB13, p_PayLoadSize4, {uLogicalChannelIdentity tsc_UL_DTCH4 , dLogicalChannelIdentity tsc_DL_DTCH4 })		cofigure radio bearers : RB13 for AMR subflow #4
8		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated , tsc_RB13)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB10_To_RB11_TM_ConfigSpeech (p_PayloadSize1 : INTEGER; p_PayloadSize2 : INTEGER) Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : RB10, RB11 for radio beaer subflow#1, subflow#2 Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB10, p_PayloadSize1, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 for AMR subflow #1
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated , tsc_RB10)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB11, p_PayloadSize2, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2})		cofigure radio bearers : RB11 for AMR subflow #2
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated , tsc_RB11)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB10_To_RB12_TM_ConfigSpeech (p_PayloadSize1 : INTEGER; p_PayloadSize2 : INTEGER; p_PayloadSize3 : INTEGER) Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : RB10, RB11, RB12 for radio beaer subflow#1, subflow#2, subflow#3 Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB10, p_PayloadSize1, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 for AMR subflow #1
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated , tsc_RB10)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB11, p_PayloadSize2, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2})		cofigure radio bearers : RB11 for AMR subflow #2
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated , tsc_RB11)		
5		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB12, p_PayloadSize3, {uLogicalChannelIdentity tsc_UL_DTCH3, dLogicalChannelIdentity tsc_DL_DTCH3})		cofigure radio bearers : RB12 for AMR subflow #3
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated , tsc_RB12)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_4DCH_Modify_1 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to modify physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for AMR speech. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_122_AMR_ChC127 (p_DL_CommonInformation , OMIT) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_0_To23_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO ,p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_122_AMR_1 , c_TrLogMappingDL_4DCH_3DTCH ,p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf (p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_4DCH_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO ,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_122_AMR, c_TrLogMappingUL_4DCC H_3DTCH,p_ActTime) ca_CMAC_CfgCnf (tsc_CellDedicated, tsc_UL_DPCH1)		3.
14		CMAC ? CMAC_Config_CNF			
15	ERR1	[px_RAT = tdd]		I	
16	ERR2	[TRUE]		I	
Detailed Comments : 1. configure new DPCH1for transporting conversational/speech/UL:12.2 DL:12.2 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SetUpRAB_CS (p_CellId: INTEGER; p_RB_Type : RB_Type) Group : RB_Steps/RB_Configuration/ Objective : To setup a RADIO BEARER for the configuration pgiven in p_RB_Type and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_CalculateActTime (p_CellId)			
2		+ lt_SendSetup			
3		lt_SendSetup			
4		[p_RB_Type = speech_12_2k]			
5		+ ts_SendRB_SetUpDCH_Speech_DiffRM_DCH5 (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			tc_14.2.4
6		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
7		[p_RB_Type = speech_10_2k]			
8		+ ts_SendRB_SetUpSpeech_10_2k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
9		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
10		[p_RB_Type = speech_7_95k]			
11		+ ts_SendRB_SetUpSpeech_7_95k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
12		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
13		[p_RB_Type = speech_7_4k]			
14		+ ts_SendRB_SetUpSpeech_7_4k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
15		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
16		[p_RB_Type = speech_6_7k]			
17		+ ts_SendRB_SetUpSpeech_6_7k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
18		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
19		[p_RB_Type = speech_5_9k]			
20		+ ts_SendRB_SetUpSpeech_5_9k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
21		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
22		[p_RB_Type = speech_5_15k]			
23		+ ts_SendRB_SetUpSpeech_5_15k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
24		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
25		[p_RB_Type = speech_4_75k]			
26		+ ts_SendRB_SetUpSpeech_4_75k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
27		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
28		[p_RB_Type = convUnknown_28_8k]			
29		+ ts_SendRB_SetUpConvUnknown_28_8k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			tc 14.2.12
30		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
30		[p_RB_Type = convUnknown_64k_20]			tc_14_2 _13_1
31		+ ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
32		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
33		[p_RB_Type = convUnknown_64k_40]			
34		+ ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			tc_14_2 _14_1
35		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
36		[p_RB_Type = convUnknown_32k_20]			
37		+ ts_SendRB_SetUpConvUnknown_32k_20TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
38		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
39		[p_RB_Type = convUnknown_32k_40]			
40		+ ts_SendRB_SetUpConvUnknown_32k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
41		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
42		[p_RB_Type = streamUnknown_14_4k]			
43		+ ts_RB_SendRB_SetUpStreamUnknown14_4k_ NoSc (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
44		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
45		[p_RB_Type = streamUnknown_28_8k]			
46		+ ts_SendRB_SetUpStreamUnknown28_8k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			tc 14.2.16
47		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
48		[p_RB_Type = streamUnknown_57_6k]			tc 14.2.17
49		+ ts_SendRB_SetUpDCH_57_6k_CS(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
50		+ ts_SetCellCfg (p_CellId, cell_DCH_57_6kCS_RAB_SRB)			
51		[p_RB_Type = streamUnknown_0k_64k]			
52		+ts_SendRB_SetUp_StreamUnknown_0k_64k(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
53		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
54		[p_RB_Type = streamUnknown_64k_0k]			
55		+ts_SendRB_SetUp_StreamUnknown_64k_0k(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
56		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
57		[p_RB_Type = streamUnknown_0k_128k]			
58		+ts_SendRB_SetUp_StreamUnknown_0k_128k(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
59		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
60		[p_RB_Type = streamUnknown_128k_0k]			
61		+ts_SendRB_SetUp_StreamUnknown_128k_0k (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
62		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
63		[p_RB_Type = streamUnknown_0k_384k]			
64		+ts_SendRB_SetUp_StreamUnknown_0k_384k(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
65		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
66		[p_RB_Type = speech_12_2k_ConvUnknown_64k_20]			
67		+ts_SendRB_SetUpSpeech_ConvUnknown_64k _20TTI_DiffRM_DCH5(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			tc_14.2. 49_1
68		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
69		[p_RB_Type = (speech_12_2k_ConvUnknown_64k_40)]			tc_14_2 _49_2
70		+ts_SendRB_SetUpSpeech_ConvUnknown_64k _40TTI(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
71		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
72		[p_RB_Type = speech_12_2k_StreamUnknown_0_384k]			
73		+ts_SendRB_SetUpSpeech_ConvUnknown_0_ 384k_40TTI(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
74		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
75		[p_RB_Type = convUnknown_64k_ConvUnknown_64k_20]			tc_14_2 _50_1
76		+ts_SendRB_SetUpConvUnknown_64k_ConvU nknown_64k_20TTI (p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
77		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
78		[p_RB_Type = convUnknown_64k_ConvUnknown_64k_40]			tc_14_2 _50_2
79		+ts_SendRB_SetUpConvUnknown_64k_ConvU nknown_64k_40TTI(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
80		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			
81		[p_RB_Type = convUnknown_64k_ConvUnknown_64k_40]			
82		+ts_SendRB_SetUpConvUnknown_64k_ConvU nknown_64k_40TTI(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			Repeate d
83		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
84		[p_RB_Type = speech_12_2k_StreamUnknown_57_6k]			
85		+ts_SendRB_SetUpConvSpeech_StreamUnknown_57_6k(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
86		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
87		[p_RB_Type = speech_12_2k_StreamUnknown_0_64k]			
88		+ts_SendRB_SetUpConvSpeech_StreamUnknown_0_64k(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
89		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
90		[p_RB_Type = speech_12_2k_StreamUnknown_0_128k]			
91		+ts_SendRB_SetUpConvSpeech_StreamUnknown_0_128k(p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
92		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
93		[p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k]			14.2.4a
94		+ ts_SendRB_SetUpSpeech_12_2k_AMR_DiffRM_DCH5 (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
95		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
96		[p_RB_Type = speech_10_2k_6_7k_5_9k_4_75k]			14.2.5a
97		+ ts_SendRB_SetUpSpeech_10_2k_AMR_DiffRM_DCH5 (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
98		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
99		[p_RB_Type = speech_7_4k_6_7k_5_9k_4_75k]			14.2.7a
100		+ ts_SendRB_SetUpSpeech_7_4k_AMR_DiffRM_DCH5 (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
101		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
102		[p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_ConvUnknown_64k_20]			14.2.49. a
103		+ts_SendRB_SetUpSpeech_12_2k_AMR_ConvUnknown_64k_20TTI (p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
104		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
105		[p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_ConvUnknown_64k_40]			14.2.49. a.1
106		+ts_SendRB_SetUpSpeech_12_2k_AMR_ConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tsc_RAB2_DefCS, tcv_ActTime)			
107		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS)			
108		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SetUpRAB_CS_PS (p_CellId: INTEGER; p_RB_Type : RB_Type) Group : RB_Steps/RB_Configuration/ Objective : To setup a RADIO BEARER for the configuration pgiven in p_RB_Type and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_CalculateActTime (p_CellId)			
2		+ lt_SendSetup			
3		lt_SendSetup			
4		[(p_RB_Type = speech_12_2k_Interact_64k) OR (p_RB_Type = speech_12_2k_Backgrnd_64k)]			
5		+ts_SendRB_SetUpConvSpeech_12_2k_Interac tBackg_64k(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
7		[(p_RB_Type = convUnknown_64k_Interact_128k_128k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_128k_128k_20)]			
8		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_128k_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
9		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
10		[(p_RB_Type = speech_12_2k_Interact_32k_64k_TC_10) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_64k_TC_10)]			
11		+ts_SendRB_SetUpConvSpeech_InteractBackg _32k_64k_TC_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
12		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
13		[(p_RB_Type = speech_12_2k_Interact_32k_64k_TC_20) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_64k_TC_20)]			
14		+ts_SendRB_SetUpConvSpeech_InteractBackg _32k_64k_TC_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
15		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
16		[(p_RB_Type = speech_12_2k_Interact_32k_64k_CC_10) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_64k_CC_10)]			
17		+ts_SendRB_SetUpConvSpeech_InteractBackg _32k_64k_CC_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
18		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		[(p_RB_Type = speech_12_2k_Interact_32k_64k_CC_20) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_64k_CC_20)]			
19		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
20		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
21		[(p_RB_Type = speech_12_2k_Interact_64k_128k)OR (p_RB_Type = speech_12_2k_Backgrnd_64k_128k)]			tc_14_2_41
22		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_128k(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
23		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
24		[(p_RB_Type = speech_12_2k_Interact_32k_8k_TC_20) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_8k_TC_20)]			
25		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
26		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
27		[(p_RB_Type = speech_12_2k_Interact_32k_8k_TC_10) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_8k_TC_10)]			
28		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
29		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
30		[(p_RB_Type = speech_12_2k_Interact_32k_8k_CC_20) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_8k_CC_20)]			
31		+ts_SendRB_SetUp_ConvSpeech_InteractBackg_32k_8k_CC_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
32		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
33		[(p_RB_Type = speech_12_2k_Interact_32k_8k_CC_10) OR (p_RB_Type = speech_12_2k_Backgrnd_32k_8k_CC_10)]			
34		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_CC_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
35		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
36		[(p_RB_Type = speech_12_2k_Interact_64k_256k_10) OR (p_RB_Type = speech_12_2k_Backgrnd_64k_256k_10)]			
37		+ts_SendRB_SetUpConvSpeech_InteractBackg _64k_256k(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
38		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
39		[(p_RB_Type = speech_12_2k_Interact_64k_256k_20) OR (p_RB_Type = speech_12_2k_Backgrnd_64k_256k_20)]			
40		+ts_SendRB_SetUpConvSpeech_InteractBackg _64k_256k_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
41		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
42		[(p_RB_Type = speech_12_2k_Interact_64k_384k_20) OR (p_RB_Type = speech_12_2k_Backgrnd_64k_384k_20)]			14.2.43. 2
43		+ts_SendRB_SetUpConvSpeech_InteractBackg _64k_384k_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
44		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
45		[(p_RB_Type = speech_12_2k_Interact_128k_2048k_10) OR (p_RB_Type = speech_12_2k_Backgrnd_128k_2048k_10)]			
46		+ts_SendRB_SetUpConvSpeech_InteractBackg _128k_2048k_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
47		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
48		[(p_RB_Type = speech_12_2k_Interact_128k_2048k_20) OR (p_RB_Type = speech_12_2k_Backgrnd_128k_2048k_20)]			
49		+ts_SendRB_SetUpConvSpeech_InteractBackg _128k_2048k_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
50		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
51		[(p_RB_Type = convUnknown_64k_Interact_64k_40) OR (p_RB_Type = convUnknown_64k_Backgrnd_64k_40)]			tc_14_2 _51_2
52		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_64k_40TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
53		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
54		[(p_RB_Type = convUnknown_64k_Interact_64k_128k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_64k_128k_20)]			tc_14_2_52_1
55		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_64k_128k_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
56		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
57		[(p_RB_Type = convUnknown_64k_Interact_64k_128k_40) OR (p_RB_Type = convUnknown_64k_Backgrnd_64k_128k_40)]			tc_14_2_52_1
58		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_64k_128k_40TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
59		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
60		[(p_RB_Type = convUnknown_64k_Interact_128k_128k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_128k_128k_20)]			
61		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_128k_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			tc_14_2_53_1
62		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
63		[(p_RB_Type = convUnknown_64k_Interact_128k_128k_40) OR (p_RB_Type = convUnknown_64k_Backgrnd_128k_128k_40)]			
64		+ts_SendRB_SetUpConvUnknown_64k_Interac tBackg_128k_40TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			tc_14_2_53_2
65		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
66		[(p_RB_Type = interact_64k_128k_StreamingUnknown_0k_64k) OR (p_RB_Type = backgrnd_64k_128k_StreamingUnknown_0k_64k)]			
67		+ts_SendRB_SetUpInteractBackg_64k_128k_St reamUnknown_0k_64k(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
68		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
69		[(p_RB_Type = interact_64k_128k_StreamingUnknown_0k_128k) OR (p_RB_Type = backgrnd_64k_128k_StreamingUnknown_0k_128k)]			
70		+ts_SendRB_SetUpInteractBackg_64k_128k_St reamUnknown_0k_128k(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
71		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
72		[(p_RB_Type = speech_12_2k_Interact_0k_0k_CC_20) OR (p_RB_Type = speech_12_2k_Backgrnd_0k_0k_CC_20)]			14.2.38a
73		+ ts_SendRB_SetUp_ConvSpeech_InteractBackg _0k_TC_20TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
74		[(p_RB_Type = speech_12_2k_Interact_8k_8k_TC_40) OR (p_RB_Type = speech_12_2k_Backgrnd_8k_8k_TC_40)]			14.2.38 b
75		+ ts_SendRB_SetUpConvSpeech_InteractBackg_ 8k_TC_40TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
76		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interact_0k_0 k_CC_20) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_0k _0k_CC_20)]			14.2.38e
77		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_In teractBackg_0k_CC_20TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
78		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interact_8k_8 k_40) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_8k _8k_40)]			14.2.38f
79		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_In teractBackg_8k_TC_40TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
80		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interact_16k_ 16k_40) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_16 k_16k_40)]			14.2.38 g
81		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_In teractBackg_16k_TC_40TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
82		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interact_32k_ 32k_40) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_32 k_32k_40)]			14.2.38 h
83		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_In teractBackg_32k_TC_40TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
84		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interact_64k_ 64k_20) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_64 k_64k_20)]			14.2.38i

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
85		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractionBackg_64k_TC_20TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
86		[(p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Interaction_64k_128k_20) OR (p_RB_Type = speech_12_2k_7_95k_5_9k_4_75k_Backgrnd_64k_128k_20)]			tc_14_2_38j
87		+ ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractionBackg_64k_128k_TC_20TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
88		[(p_RB_Type = speech_12_2k_Interaction_64k_384k_10) OR (p_RB_Type = speech_12_2k_Backgrnd_64k_384k_10)]			14.2.43.1
89		+ ts_SendRB_SetUpConvSpeech_InteractionBackg_64k_384k_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
90		[(p_RB_Type = convUnknown_64k_Interaction_64k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_64k_20)]			tc_14_2_51_1
91		+ts_SendRB_SetUp_ConvUnknown_64k_InteractionBackg_64k_20 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
92		[(p_RB_Type = convUnknown_64k_Interaction_8k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_8k_20)]			14.2.51a.1
93		+ts_SendRB_SetUp_ConvUnknown_64k_InteractionBackg_8k_20 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
94		[(p_RB_Type = convUnknown_64k_Interaction_8k_40) OR (p_RB_Type = convUnknown_64k_Backgrnd_8k_40)]			14.2.51a.2
95		+ts_SendRB_SetUp_ConvUnknown_64k_InteractionBackg_8k_40 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
96		[(p_RB_Type = convUnknown_64k_Interaction_16k_64k_20) OR (p_RB_Type = convUnknown_64k_Backgrnd_16k_64k_20)]			14.2.51b.1
97		+ts_SendRB_SetUp_ConvUnknown_64k_InteractionBackg_16k_64k_20 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
98		[(p_RB_Type = convUnknown_64k_Interaction_16k_64k_40) OR (p_RB_Type = convUnknown_64k_Backgrnd_16k_64k_40)]			tc_14_2_51b_2
99		+ts_SendRB_SetUp_ConvUnknown_64k_InteractionBackg_16k_64k_40 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
100		[(p_RB_Type =speech_12_2k_Interact_32k_32k_40) OR (p_RB_Type =speech_12_2k_Backgrnd_32k_32k_40)]			14.2.38c
101		+ ts_SendRB_SetUpConvSpeech_12_2k_Interact Backg_32k_TC_40TTI (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
102		[(p_RB_Type =speech_12_2k_Interact_64k_64k_TC_20) OR (p_RB_Type =speech_12_2k_Backgrnd_64k_64k_TC_20)]			14.2.38 d
103		+ ts_RB_SendRB_SetUp_ConvSpeech_12_2k_Int eractiveBackground_64k_64k_TC_20 (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
104		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SetUpRAB_PS (p_CellId: INTEGER; p_RB_Type : RB_Type) Group : RB_Steps/RB_Configuration/ Objective : To setup a RADIO BEARER for the configuration pgiven in p_RB_Type and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_CalculateActTime (p_CellId)			
2		+ lt_SendSetup			
3		lt_SendSetup			
4		[(p_RB_Type = interact_64k) OR (p_RB_Type = backgrnd_64k)]			
5		+ ts_SendRB_SetUpDCH_64k_PS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			tc_14_2_26
6		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
7		[(p_RB_Type = interact_64k_128k) OR (p_RB_Type = backgrnd_64k_128k)]			tc_14_2_26
8		+ ts_SendRB_SetUpDCH_64k_128kPS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
9		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
10		[(p_RB_Type = interact_32k_8k_TC_10) OR (p_RB_Type = backgrnd_32k_8k_TC_10)]			
11		+ ts_SendRB_SetUpDCH_32k_8kPS_10_TC(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
12		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
13		[(p_RB_Type = interact_32k_8k_CC_10) OR(p_RB_Type = backgrnd_32k_8k_CC_10)]			
14		+ts_SendRB_SetUpDCH_32k_8kPS_10_CC (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
15		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
16		[(p_RB_Type = interact_32k_8k_TC_20) OR (p_RB_Type = backgrnd_32k_8k_TC_20)]			
17		+ ts_SendRB_SetUpDCH_32k_8kPS_20_TC(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
18		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
19		[(p_RB_Type = interact_32k_8k_CC_20) OR (p_RB_Type = backgrnd_32k_8k_CC_20)]			
20		+ ts_SendRB_SetUpDCH_32k_8kPS_20_CC(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
21		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
22		[(p_RB_Type = interact_64k_8k) OR (p_RB_Type = backgrnd_64k_8k)]			
23		+ ts_SendRB_SetUpDCH_64k_8kPS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
24		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
24		[(p_RB_Type = interact_64k_8k_CC) OR (p_RB_Type = backgrnd_64k_8k_CC)]			
25		+ ts_SendRB_SetUpDCH_64k_8k_CC_PS (
26		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
27		+ ts_SetCellCfg (p_CellId,			
28		cell_DCH_64kPS_RAB_SRB)			
29		[(p_RB_Type = interact_32k_64k_TC_10) OR (p_RB_Type = backgrnd_32k_64k_TC_10)]			
30		+ ts_SendRB_SetUpDCH_32k_64kPS_10_TC(
31		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
32		+ ts_SetCellCfg (p_CellId,			
33		cell_DCH_64kPS_RAB_SRB)			
34		[(p_RB_Type = interact_32k_64k_TC_20) OR (p_RB_Type = backgrnd_32k_64k_TC_20)]			
35		+ ts_SendRB_SetUpDCH_32k_64kPS_20_TC(
36		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
37		+ ts_SetCellCfg (p_CellId,			
38		cell_DCH_64kPS_RAB_SRB)			
39		[(p_RB_Type = interact_32k_64k_CC_10) OR (p_RB_Type = backgrnd_32k_64k_CC_10)]			
40		+ ts_SendRB_SetUpDCH_32k_64kPS_10_CC(
41		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
42		+ ts_SetCellCfg (p_CellId,			
43		cell_DCH_64kPS_RAB_SRB)			
44		[(p_RB_Type = interact_32k_64k_CC_20) OR (p_RB_Type = backgrnd_32k_64k_CC_20)]			
45		+ ts_SendRB_SetUpDCH_32k_64kPS_20_CC(
46		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
47		+ ts_SetCellCfg (p_CellId,			
48		cell_DCH_64kPS_RAB_SRB)			
49		[(p_RB_Type = interact_128k) OR (p_RB_Type = backgrnd_128k)]			
50		+ ts_SendRB_SetUpDCH_128k_PS (p_CellId,			
51		tsc_RAB_DefPS, tcv_ActTime)			
52		+ ts_SetCellCfg (p_CellId,			
53		cell_DCH_64kPS_RAB_SRB)			
54		[(p_RB_Type = interact_64k_144k) OR (p_RB_Type = backgrnd_64k_144k)]			
55		+ ts_SendRB_SetUpDCH_64k_144kPS (
56		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
57		+ ts_SetCellCfg (p_CellId,			
58		cell_DCH_64kPS_RAB_SRB)			
59		[(p_RB_Type = interact_144k) OR (p_RB_Type = backgrnd_144k)]			
60		+ ts_SendRB_SetUpDCH_144k_144kPS (
61		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
62		+ ts_SetCellCfg (p_CellId,			
63		cell_DCH_64kPS_RAB_SRB)			
64		[(p_RB_Type = interact_64k_256k_10) OR (p_RB_Type = backgrnd_64k_256k_10)]			
65		+ ts_SendRB_SetUpDCH_64k_256kPS_10(
66		p_CellId, tsc_RAB_DefPS, tcv_ActTime)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
50		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
51		[(p_RB_Type = interact_64k_256k_20) OR (p_RB_Type = backgrnd_64k_256k_20)]			
52		+ ts_SendRB_SetUpDCH_64k_256kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
53		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
54		[(p_RB_Type = interact_64k_384k_10) OR (p_RB_Type = backgrnd_64k_384k_10)]			14.2.32. 1
55		+ ts_SendRB_SetUpDCH_64k_384kPS_10(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
56		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
57		[(p_RB_Type = interact_64k_384k_20) OR (p_RB_Type = backgrnd_64k_384k_20)]			14.2.32. 2
58		+ ts_SendRB_SetUpDCH_64k_384kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
59		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
60		[(p_RB_Type = interact_64k_768k_10) OR (p_RB_Type = backgrnd_64k_768k_10)]			14.2.63. 1
61		+ ts_SendRB_SetUpDCH_64k_768kPS_10(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
62		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
63		[(p_RB_Type = interact_64k_768k_20) OR (p_RB_Type = backgrnd_64k_768k_20)]			14.2.63. 2
64		+ ts_SendRB_SetUpDCH_64k_768kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
65		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
66		[(p_RB_Type = interact_128k_384k_10) OR (p_RB_Type = backgrnd_128k_384k_10)]			
67		+ ts_SendRB_SetUpDCH_128k_384kPS_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
68		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
69		[(p_RB_Type = interact_128k_384k_20) OR (p_RB_Type = backgrnd_128k_384k_20)]			
70		+ ts_SendRB_SetUpDCH_128k_384kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
71		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
72		[(p_RB_Type = interact_384k_10) OR (p_RB_Type = backgrnd_384k_10)]			
73		+ ts_SendRB_SetUpDCH_384k_384kPS_10(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
74		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
75		[(p_RB_Type = interact_384k_20) OR (p_RB_Type = backgrnd_384k_20)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
76		+ ts_SendRB_SetUpDCH_384k_384kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
77		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
78		[(p_RB_Type = interact_64k_2048k_10) OR (p_RB_Type = backgrnd_64k_2048k_10)]			
79		+ ts_SendRB_SetUpDCH_64k_2048kPS_10(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
80		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
81		[(p_RB_Type = interact_64k_2048k_20) OR (p_RB_Type = backgrnd_64k_2048k_20)]			
82		+ ts_SendRB_SetUpDCH_64k_2048kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
83		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
84		[(p_RB_Type = interact_128k_2048k_10) OR (p_RB_Type = backgrnd_128k_2048k_10)]			
85		+ ts_SendRB_SetUpDCH_128k_2048kPS_10TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
86		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
87		[(p_RB_Type = interact_128k_2048k_20) OR (p_RB_Type = backgrnd_128k_2048k_20)]			
88		+ ts_SendRB_SetUpDCH_128k_2048kPS_20TTI(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
89		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
90		[(p_RB_Type = interact_384k_2048k_10) OR (p_RB_Type = backgrnd_384k_2048k_10)]			
91		+ ts_SendRB_SetUpDCH_384k_2048kPS_10(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
92		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
93		[(p_RB_Type = interact_384k_2048k_20) OR (p_RB_Type = backgrnd_384k_2048k_20)]			
94		+ ts_SendRB_SetUpDCH_384k_2048kPS_20(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
95		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
96		[(p_RB_Type = interact_32k_TC_40) OR (p_RB_Type = backgrnd_32k_TC_40)]			
97		+ ts_SendRB_SetUpDCH_32k_PS1 (p_CellId,tsc_RAB_DefPS , tcv_ActTime)			14.2.23c
98		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			
99		[(p_RB_Type = interact_8k_CC_40) OR (p_RB_Type = backgrnd_8k_CC_40)]			14.2.23a 1
100		+ ts_SendRB_SetUpDCH_8k_PS(p_CellId, tsc_RAB_DefPS, tcv_ActTime, c_DCH_336_TFS_23_UL_40_CC_UE ,c_DCH_336_TFS_23_DL_40_CC)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
101		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			14.2.23a 2
102		[(p_RB_Type = interact_8k_TC_40) OR (p_RB_Type = backgrnd_8k_TC_40)]			
103		+ ts_SendRB_SetUpDCH_8k_PS(p_CellId, tsc_RAB_DefPS, tcv_ActTime, c_DCH_336_TFS_23_UL_40_TC_UE ,c_DCH_336_TFS_23_UL_40_1_TC)			
104		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			14.2.23 b
105		[(p_RB_Type = interact_16k_TC_40) OR (p_RB_Type = backgrnd_16k_TC_40)]			
106		+ ts_SendRB_SetUpDCH_16k_PS(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
107		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			14.2.23 d
108		[(p_RB_Type = interact_32k_TC_20) OR (p_RB_Type = backgrnd_32k_TC_20)]			
109		+ ts_SendRB_SetUpDCH_32kPS_20_TC(p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
110		+ ts_SetCellCfg (p_CellId, cell_DCH_64kPS_RAB_SRB)			14.2.56
111		[(p_RB_Type = interact_8k_Interact_8k_TC_40) OR (p_RB_Type = backgrnd_8k_Backgrnd_8k_TC_40)]			
112		+ts_RB_SendRB_SetUp_DCH_8k_PS_40_TC (p_CellId, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
113		+ ts_SetCellCfg (p_CellId, cell_DCH_2AM_PS)			14.2.57
114		[(p_RB_Type = interact_64k_TC_20) OR (p_RB_Type = backgrnd_64k_TC_20)]			
115		+ ts_RB_SendRB_SetUp_DCH_64k_PS (p_CellId, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
116		+ ts_SetCellCfg (p_CellId, cell_DCH_2AM_PS)			14.2.58
117		[(p_RB_Type = streamUnknown_16k_64k_Interact_8k) OR (p_RB_Type = streamUnknown_16k_64k_Backgrnd_8k)]			
118		+ ts_RB_SendRB_SetUp_DCH_8k_8k_PS(p_CellId, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
119		+ ts_SetCellCfg (p_CellId, cell_DCH_2_PS_Call)			
120		[(p_RB_Type = interact_64k_384k_DSCH_10) OR (p_RB_Type = backgrnd_64k_384k_DSCH_10)]			
121		+ ts_RRC_SendRB_SetUpDCH_DSCH_64k_384 k_10TTI_PS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
122		+ ts_SetCellCfg (p_CellId, cell_DCH_DSCH_PS)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
123		[(p_RB_Type = interact_64k_384k_DSCH_20) OR (p_RB_Type = backgrnd_64k_384k_DSCH_20)]			
124		+ ts_RRC_SendRB_SetUpDCH_DSCH_64k_384 k_20TTI_PS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
125		+ ts_SetCellCfg (p_CellId, cell_DCH_DSCH_PS)			
126		[(p_RB_Type = interact_64k_2048k_DSCH_10) OR (p_RB_Type = backgrnd_64k_2048k_DSCH_10)]			
127		+ ts_RRC_SendRB_SetUpDCH_DSCH_64k_204 8k_10TTI_PS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
128		+ ts_SetCellCfg (p_CellId, cell_DCH_DSCH_PS)			
129		[(p_RB_Type = interact_64k_2048k_DSCH_20) OR (p_RB_Type = backgrnd_64k_2048k_DSCH_20)]			
130		+ ts_RRC_SendRB_SetUpDCH_DSCH_64k_204 8k_20TTI_PS (p_CellId, tsc_RAB_DefPS, tcv_ActTime)			
131		+ ts_SetCellCfg (p_CellId, cell_DCH_DSCH_PS)			
132		[(p_RB_Type = streamUnknown_16k_128k_Interact_8k) OR (p_RB_Type = streamUnknown_16k_128k_Backgrnd_8k)]			14.2.58a
133		+ ts_RB_SendRB_SetUp_DCH_16K_128K_8k_8k _PS(p_CellId, tsc_RAB_DefPS, tsc_RAB2_DefPS, tcv_ActTime)			
134		+ ts_SetCellCfg (p_CellId, cell_DCH_2_PS_Call)			
135		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_128k_128kPS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background/ unknown/ UL:128 DL:128Kbps Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_128k (tsc_Sfc16, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:128 DL:128kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_128k_2048kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To21_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To21 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_128k_2048kPS_20(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To37_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0To37 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_128k_384kPS_10 (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) </p> Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:128 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_10_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_10_TC,c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_2_PS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:128 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_128k_384kPS_20(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:128 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To17 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ?	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:128 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_144k_144kPS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background / unknown / UL:144 DL:144 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_144k (tsc_Sfc16, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_29_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_336_TFS_29_DL_2 0_TC, c_PowerOffsetInfoHigher64 k) , c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_30_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To11 (c_DCH_148_TFS_UL, c_DCH_336_TFS_30_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:144 DL:144kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_64kPS_10_CC (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) </p> Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background/unknown/ UL:32 DL:64Kbps (TTI 10ms). Coding Type : CC Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_CC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_CC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:32 DL:64kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_64kPS_10_TC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background/ unknown/ UL:32 DL:64Kbps (TTI 10ms). Coding Type : TC Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_1 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:32 DL:64kbps 10ms TTI , TC/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_64kPS_20_CC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background / unknown/UL:32 DL:64Kbps (TTI 20ms) Coding Type : CC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_20_CC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_20_CC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background / unknown / UL:32 DL:64kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_64kPS_20_TC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background/ unknown/ UL:32 DL:64Kbps (TTI 20ms). Coding Type : TC Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_25_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_20_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_20_TC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background / unknown / UL:32 DL:64kbps/PS , 20 ms TTI TC RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_8kPS_20_CC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:32 DL:8 kbps. 20 ms TTI CC Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_CC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_CC), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_2 0_CC,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_2 0_CC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown/ UL:32 DL:8kbps/PS 20 ms TTI CC RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_8kPS_20_TC (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:8 kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Inf o(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_TC,c_PowerOffsetInfoBel ow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_TC), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Inf o(p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_2 0_TC,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_TFS_23_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown / UL:32 DL:8kbps/PS 20ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_384k_2048kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:384 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To21_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To21 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_1 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To11 (c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_1 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:384 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_384k_2048kPS_20(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:384 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To37_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0To37 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To17 (c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:384 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_384k_384kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_1 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_1 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To11 (c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_1 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_384k_384kPS_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To17 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To17 (c_DCH_148_TFS_UL, c_DCH_336_TFS_34_UL_2 0_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:384 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_128k_PS (p_CellId : INTEGER; p_UL_TrChConfig: CphyTrchConfigReq; p_DL_TrChConfig: CphyTrchConfigReq; p_UL_TrCHInfo, p_DL_TrCHInfo: TrCHInfo; p_UL_TrLogMapping, p_DL_TrLogMapping: TrCH_LogCHMappingList1; p_ActTime: ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to modify physical channel DPCH1and connect DCH1and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH1 to the DCH1 transport channel. used for RLC testing. Default : SS_Def Comments : SS is in TM mode different from the mode of UE. Transport channel configuration is parameter (of type: CphyTrchConfigReq); DPCH is 64 kbps physical channel. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, c_DL_DPCH_128K (p_DL_CommonInformation) ,p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_TrChCfgInfo(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, p_DL_TrChConfig)		3.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), p_DL_TrCHInfo, p_DL_TrLogMapping,p_Act Time)		4.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_RE Q	ca_TrChCfgInfo(p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, p_UL_TrChConfig)		2.
12		CPHY?CPHY_TrCH_Config_C NF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
13		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), p_UL_TrCHInfo, p_UL_TrLogMapping,p_Act Time)		4.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
14		CMAC ?	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		CMAC_Config_CNF			
16		[px_RAT = tdd] [TRUE]			
Detailed Comments : 1. configure DPCH1 supporting 64 kspb data rate. 2. connect uplink DCH1 and DCH5 to DPCH1. 3. connect downlink DCH1 and DCH5 to DPCH1. 4. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_144kPS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background / unknown / UL:64 DL:144 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_144k (tsc_Sfc16, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_29_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_336_TFS_29_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:64 DL:144kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_768kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_63_DL_1 0_TC,c_PowerOffsetInfoHi gher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To17 (c_DCH_148_TFS_DL, c_DCH_336_TFS_63_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_1 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_1 0_TC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_2048kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To21_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC,c_PowerOffsetInfoHi gher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To21 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_2048kPS_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTl Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc4, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To37_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC1, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0To37 (c_DCH_148_TFS_DL, c_DCH_656_TFS_35_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_256kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_31_DL_1 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_336_TFS_31_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_256kPS_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To13_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_31_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To13 (c_DCH_148_TFS_DL, c_DCH_336_TFS_31_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_384kPS_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_1 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_1 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 10 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_384kPS_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_256k_384k (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To17_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To17 (c_DCH_148_TFS_DL, c_DCH_336_TFS_32_DL_2 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH/ 20 ms TTI 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_8kPS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background / unknown / UL:64 DL:8 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc128, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_TC ,c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc _CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_TC), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:64 DL:8kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_64k_8k_CC_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for Interactive or Background / unknown / UL:64 DL:8 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc128, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To_3_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_CC ,c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_2_0_To3 (c_DCH_148_TFS_DL, c_DCH_336_TFS_23_DL_4 0_CC), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_2_PS,p _ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting Interactive or Background/ unknown / UL:64 DL:8kbps/PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyStreamUnknown_0k_128k_tc55 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#2) to the DCH2 transport channel respectively. Used for steaming / unknown / UL:0 DL:128 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc16, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o_tc55 (p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, c_DCH_320_TFS_20_DL_4 0_TC ,c_PowerOffsetInfoHigher6 4k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11_t c55 (c_DCH_148_TFS_DL, c_DCH_320_TFS_20_DL_4 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS_ CS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info _tc54 (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_576_TFS_2 ,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3_tc 54 (c_DCH_148_TFS_UL, c_DCH_576_TFS_2), c_TrLogMappingUL_2_PS_ CS ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting streaming / unknown / UL:0 DL:128 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH2 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyStreamUnknown_0k_128k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:0 DL:128 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc16, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To11_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_320_TFS_20_DL_4 0_TC ,c_PowerOffsetInfoHigher6 4k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To11 (c_DCH_148_TFS_DL, c_DCH_320_TFS_20_DL_4 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_576_TFS_2,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_576_TFS_2), c_TrLogMappingUL_2,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting streaming / unknown / UL:0 DL:128 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyStreamUnknown_0k_384k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:0 DL:384 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc8, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To15_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_320_TFS_22_DL_4 0_TC,c_PowerOffsetInfoHi gher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To15 (c_DCH_148_TFS_DL, c_DCH_320_TFS_22_DL_4 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2,p_Ac tTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_576_TFS_2 ,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_576_TFS_2), c_TrLogMappingUL_2 ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting streaming / unknown / UL:0 DL:384kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyStreamUnknown_0k_64k(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for steaming / unknown / UL:0 DL:64 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_320_TFS_18_DL_4 0_TC,c_PowerOffsetInfoHi gher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL, c_DCH_320_TFS_18_DL_4 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_576_TFS_2 ,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_576_TFS_2), c_TrLogMappingUL_2,p_Ac tTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting stramingl / unknown / UL:0 DL:64 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyStreamUnknown_0k_64k_tc54(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH2 transport channel respectively. Used for steaming / unknown / UL:0 DL:64 kbps. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_64k (tsc_Sfc32, p_DL_CommonInformation) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info _tc54 (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_320_TFS_18_DL_4 0_TC,c_PowerOffsetInfoHi gher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To9_tc 54 (c_DCH_148_TFS_DL, c_DCH_320_TFS_18_DL_4 0_TC,c_PowerOffsetInfoHi gher64k), c_TrLogMappingDL_2_PS_ CS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info _tc54 (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_576_TFS_2 ,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfo_UL_2_0To3_tc 54 (c_DCH_148_TFS_UL, c_DCH_576_TFS_2), c_TrLogMappingUL_2_PS_ CS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting stramingl / unknown / UL:0 DL:64 kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_64k_CC_10 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 10 ms, CC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_CC), c_TrLogMappingUL_TM3_AM1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_64k_CC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms, CC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To71_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_CC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_64k_TC_10 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 10 ms, TC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_64k_TC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32 DL:64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms, TC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To71_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_TC), c_TrLogMappingUL_TM3_AM1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_64k_128k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:128 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_I nfo (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_8k_CC_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 10 ms, CC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_CC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_8k_CC_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms, CC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To71_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To71 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_CC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_8k_TC_10 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 10 ms, TC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To71_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To71 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_40_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_10_TC), c_TrLogMappingUL_TM3_AM1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_32k_8k_TC_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 32DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms , TC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_4 0_TC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_4 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To71_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To71 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_64k_256k_10 (</p> <p style="padding-left: 40px;">p_CellId : INTEGER;</p> <p style="padding-left: 40px;">p_ActTime : ActivationTime;</p> <p style="padding-left: 40px;">p_DL_CommonInformation : DL_CommonInformation;</p> <p style="padding-left: 40px;">p_UL_DPCH_Info : UL_DPCH_Info</p> <p style="padding-left: 40px;">)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p style="padding-left: 40px;">Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_31_DL_10_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_31_DL_10_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_64k_256k_20 (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info </p>)					
Group : RB_Steps/RB_Configuration/					
Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p>					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To167_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_31_DL_20_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To167(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_31_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1 ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_64k_384k_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To215_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_32_DL_20_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To215 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_32_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_128k_2048k_10 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 10 ms, TC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc4, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To263_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_656_TFS_35_DL_10_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To263(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_656_TFS_35_DL_10_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_28_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_28_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_128k_2048k_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 128 DL:2048 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms, TC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc4, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To455_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_656_TFS_35_DL_20_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To455(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_656_TFS_35_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_28_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_28_UL_20_TC), c_TrLogMappingUL_TM3_A M1 ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_StreamUnknown_57_6k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:57.6 DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_I nfo (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_1, c_PowerOffsetInfoBelow64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To119(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_1, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM4 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_1 ,activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_1), c_TrLogMappingUL_TM4 ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:57.6 DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_StreamUnknown_0_64k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:0 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_18_DL_40_TC, c_PowerOffsetInfoBelow64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To119(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_18_DL_40_TC, c_PowerOffsetInfoBelow64k), c_TrLogMappingDL_TM4,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47(c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2), c_TrLogMappingUL_TM4, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:0 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_StreamUnknown_0_128k (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:0 DL:128 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To143_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_20_DL_40_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To143(c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_20_DL_40_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM4 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2 ,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47(c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2), c_TrLogMappingUL_TM4,p_ ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:0 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyInteractBackg_64k_128k_StreamingUnknown_0k_128k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Interactive or background / UL:64 DL:128 kbps / PS RAB + Streaming / unknown / UL:0 DL:128 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To59_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC, c_DCH_320_TFS_20_DL_4 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To59 (c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC, c_DCH_320_TFS_20_DL_4 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_AM1_T M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_20_TC, c_DCH_576_TFS_2		2.
11		CPHY?CPHY_TrCH_Config_CNF	,activationCFN :p_ActTime) ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_20_TC, c_DCH_576_TFS_2		3.
13		CMAC ? CMAC_Config_CNF), c_TrLogMappingUL_AM1_TM1,p_ActTime) ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively.</p> <p>Used for Interactive or background / UL:64 DL:128 kbps / PS RAB + Streaming / unknown / UL:0 DL:128 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 kbps / CS RAB / 20 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_40_4, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_40_4,c_PowerOffsetInfoBelow64k), c_TrLogMappingDL_TM4,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Inf o (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_40_4,p_ ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_640_TFS_40_4), c_TrLogMappingUL_TM4,p_ ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI 2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_64k_128k_20(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_27_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_24_UL_2 0_TC,activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_TM1_A M1,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_64k_128k_40(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_24_UL_2 0_TC,activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_TM1_A M1 ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_128k_128k_40(p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_27_DL_20_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_27_DL_20_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_28_UL_2 0_TC,activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_28_UL_2 0_TC), c_TrLogMappingUL_TM1_A M1 ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:128 DL: 128 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_InteractBackg_0k_384k_40 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc8, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To191_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_22_DL_40_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To215 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_320_TFS_22_DL_40_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_576_TFS_2), c_TrLogMappingUL_TM3_AM1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyInteractBackg_64k_128k_StreamingUnknown_0k_64k (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Interactive or background / UL:64 DL:128 kbps / PS RAB + Streaming / unknown / UL:0 DL:128 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc8, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To49_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC, c_DCH_320_TFS_18_DL_4 0_TC, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To49 (c_DCH_148_TFS_DL, c_DCH_336_TFS_27_DL_2 0_TC, c_DCH_320_TFS_18_DL_4 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_AM1_T M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_20_TC, c_DCH_576_TFS_2,activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_20_TC, c_DCH_576_TFS_2), c_TrLogMappingUL_AM1_T M1,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively.</p> <p>Used for Interactive or background / UL:64 DL:128 kbps / PS RAB + Streaming / unknown / UL:0 DL:128 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_64k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:64 DL: 64 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_24_UL_2 0_TC ,(activationCFN :p_ActTime))		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_3_0To19 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_24_UL_2 0_TC), c_TrLogMappingUL_TM1_A M1,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL:64 DL: 64 kbps / PS RAB / 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_4DCH_ModifySpeech12_2k_AMR (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to reconfigure physical channel DPCH1 and connect DCH1, DCH2, DCH3 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1), DTCH(subflow#2), DTCH(subflow#3) to the DCH1, DCH2, DCH3 transport channel respectively. Used for 10.2 kbps speech. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_AMR_NoSc (tsc_Sfc128, p_DL_CommonInformation), p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_4_DCH_102_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_148_TFS_DL, c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx(c_PowerOffsetInfoBelow64k), p_ActTime)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoDL_12_2k_AMR (c_TFCS_Cmpl0_1_8_15_22_59_60_61_68_75_82_119_Tx (c_PowerOffsetInfoBelow64 k)), c_TrLogMappingDL_4DCC H_3DTCH, p_ActTime)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY!CPHY_TrCH_Config_RE Q	ca_4_DCH_102_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_81_TFS_6 , c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_148_TFS_UL, c_TFCS_Cmpl0_1_8_15_22 _59_60_61_68_75_82_119 _Rx,p_ActTime)		2.
12		CPHY?CPHY_TrCH_Config_C NF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoUL_12_2k_AMR (c_TFCS_Cmpl0_1_8_15_2 2_59_60_61_68_75_82_11 9_Rx), c_TrLogMappingUL_4DCC H_3DTCH ,p_ActTime)		
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting conversational/speech/UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps/CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1, DCH2, DCH3 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1, DTCH2 to DCH2, DTCH3 to DCH3 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_8k_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info ; p_TransportChInfo_SS : CommonOrDedicatedTFS) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:32kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_DL_Info _1 (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, p_TransportChInfo_SS, c_PowerOffsetInfoBelow64 k ,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To3 (c_DCH_148_TFS_DL, p_TransportChInfo_SS, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_2_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, p_TransportChInfo_SS, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, p_TransportChInfo_SS), c_TrLogMappingUL_2_PS, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background /UL:16 DL: 16 kbps/PS 40ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_16k_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:32kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_DL_Info (p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, c_DCH_336_TFS_23b_DL_40_TC, p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_2_0To5 (c_DCH_148_TFS_DL, c_DCH_336_TFS_40_TC_2 ,c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_2_PS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_TFS_40_TC_2 , p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_TFS_40_TC_2), c_TrLogMappingUL_2_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background /UL:16 DL: 16 kbps/PS 40ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_8k_8k_TC_40 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_4_0_TC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To239 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_DL_4_0_TC, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_4_0_1_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To239 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_4_0_1_TC), c_TrLogMappingUL_TM3_A M1 , p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_16k_16k_TC_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 16 kbps DL:16 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 40 ms , TC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To359_DL_I nfo (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_40_T C, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To359 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_40_T C, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To359_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To359 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23_UL_40_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL:16 DL: 16 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_32k_32k_TC_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 40 ms , TC) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To541_DL_I nfo (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23c_DL_ 40, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To541 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23c_DL_ 40, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To541_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23c_UL_40, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To541 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_23c_UL_40), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 40 ms , TC)</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_64k_64k_TC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms , TC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To599_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To599 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_25_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_AM1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To599_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To599 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_AM1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms , TC)</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_64k_128k_TC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 64 kbps DL: 128 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To599_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_27_DL_20_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To599 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_27_DL_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To599_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To599 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 64 kbps DL: 128 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 20 ms)</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_InteractBackg_0k_0k_TC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 0 DL:0 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To23_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL_RM192, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To23 (c_DCH_148_TFS_DL_RM192, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To23_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To23 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_20_CC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 0 DL:0 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_InteractBackg_8k_8k_TC_40 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 8 DL:8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_38b_40_TC, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To47 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_38b_40_TC, c_PowerOffsetInfoBelow64k), c_TrLogMappingDL_TM3_AM1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To47_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_38b_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To47 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_38b_40_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 8 DL: 8 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_AMR_InteractBackg_0k_0k_CC_20 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:UL: (12.2 7.95 5.9 4.75) DL: UL: (12.2 7.95 5.9 4.75) / Interactive or background / UL: 0 DL:0 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info_14_2_38e (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL_RM192, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, c_PowerOffsetInfoBelow64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_5_0To119_14_2_38e (c_DCH_148_TFS_DL_RM192, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, c_PowerOffsetInfoBelow64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info_14_2_38e (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_20_CC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119_14_2_38e(c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_336_TFS_20_CC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:UL: (12.2 7.95 5.9 4.75) DL: UL: (12.2 7.95 5.9 4.75) / Interactive or background / UL: 0 DL:0 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_InteractBackg_64k_384k_10 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL: 64 DL: 384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_256k_384k (tsc_Sfc8, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To143_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_32_DL_10_TC, c_PowerOffsetInfoHigher64k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_DL_5_0To143 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_32_DL_10_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_24_UL_20_TC), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational Speech/UL:12.2 kbps DL:12.2 kbps/ Interactive or background / UL:64 DL: 384 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_8k_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To7 (c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_38b_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_38b_40_TC), c_TrLogMappingUL_TM1_A M1 ,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_8k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 40 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To7 (c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_38b_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_38b_40_TC), c_TrLogMappingUL_TM1_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 40 ms TTI + Interactive or background / UL: 8 DL: 8 kbps / PS RAB 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_16k_64k_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 16 DL: 64 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1,c_TrChCo nfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To19_O rder1 (c_DCH_148_TFS_DL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To11_UL_Info_1 (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_51b_UL_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To11 (c_DCH_148_TFS_UL, c_DCH_640_TFS_20_2, c_DCH_336_TFS_51b_UL_40_TC), c_TrLogMappingUL_TM1_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 16 kbps DL: 64 kbps / PS RAB 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_3DCH_ModifyConvUnknown_64k_InteractBackg_16k_64k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 DL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 16 DL: 64 kbps / PS RAB / Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_Order1 _DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoDL_3_0To19 (c_DCH_148_TFS_DL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_25_DL_2 0_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM1_A M1 ,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To11_UL_Info_1 (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_51b_UL_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoUL_3_0To11 (c_DCH_148_TFS_UL, c_DCH_640_TFS_40_4, c_DCH_336_TFS_51b_UL_40_TC), c_TrLogMappingUL_TM1_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2) to the DCH1,DCH2 transport channel respectively. Used for Conversational / Unknown / UL:64 kbps / CS RAB 20 ms TTI + Interactive or background / UL: 16 kbps DL: 64 kbps / PS RAB 2. connect DCH1,DCH2 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2 to DCH1, DCH2 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_FirstSCCPCH_PCH_PCCH_Cfg (p_CellId : INTEGER) Group : RB_Steps/RB_Configuration/ Objective : To configure the second secondary CCPCH (tsc_S_CCPCH2), then connect PCH to the secondary CCPCH . (34.108 cl. 4.2.1), finally to map PCCH to PCH. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_InfoPCH_RAB_StandAlone (p_CellId, tsc_S_CCPCH1, tsc_S_CCPCH_2ndScrCode, 4, (tcv_TmpCellInfo.powersCCPCH1))		s-CCPCH2
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_PCH_Info2 (p_CellId, tsc_S_CCPCH1)		connect PCH and FACH to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMACH_CfgInfo (p_CellId, tsc_S_CCPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfoPCH, c_TrLogMappingPCH_NoFACH)		map PCCH to PCH,
8		CMAC ? CMAC_Config_CNF	ca_CMACH_CfgCnf(p_CellId, tsc_S_CCPCH1)		
9		CPHY!CPHY_RL_Setup_REQ	ca_PICH_Info(p_CellId, c_PichInfo, (tcv_TmpCellInfo.powerPICH) ,1)		PICH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_PICH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractBackg_32k_PS_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL:32 DL:32kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc64, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_DL_Info (p_CellId, tsc_DL_DPCH1, c_DCH_148_TFS_DL, c_DCH_336_3_TFS,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_DL_2_0To5 (c_DCH_148_TFS_DL, c_DCH_336_3_TFS, c_PowerOffsetInfoBelow64k), c_TrLogMappingDL_2_PS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To5_UL_Info (p_CellId , tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_336_3_TFS,p_ActTime)		2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_UL_2_0To5 (c_DCH_148_TFS_UL, c_DCH_336_3_TFS), c_TrLogMappingUL_2_PS ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background / unknown / UL:32 DL:32kbps/PS 20ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifySpeech_12_2k_AMR_ConvUnknown_64k_20 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 kbps / CS RAB / 20 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_20_2, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoDL_5_0To239 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_20_2, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM4,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_20_2,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoUL_5_0To239 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_20_2), c_TrLogMappingUL_TM4,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI 2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifySpeech_12_2k_AMR_ConvUnknown_64k_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 40 ms TTI Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo(p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_40_4, c_PowerOffsetInfoHigher64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoDL_5_0To239 (c_DCH_148_TFS_DL, c_DCH_81_TFS_6_DL, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_40_4, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_TM4,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To239_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_40_4,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoUL_5_0To239 (c_DCH_148_TFS_UL, c_DCH_81_TFS_6, c_DCH_103_TFS_5, c_DCH_60_TFS, c_DCH_640_TFS_40_4), c_TrLogMappingUL_TM4,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH4 to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB / 20 ms TTI 2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
<p>Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_InteractBackg_32k_32k_TC_40 (</p> <p>p_CellId : INTEGER;</p> <p>p_ActTime : ActivationTime;</p> <p>p_DL_CommonInformation : DL_CommonInformation;</p> <p>p_UL_DPCH_Info : UL_DPCH_Info</p> <p>)</p> <p>Group : RB_Steps/RB_Configuration/</p> <p>Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL: 12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTl 40 ms , TC)</p> <p>Default : RRC_Def1</p> <p>Comments :</p> <p>Description :</p>					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23c_DL_40, c_PowerOffsetInfoBelow64 k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23c_DL_40, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_TM3_A M1,p_ActTime)		3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23c_UL_40, activationCFN :p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_336_TFS_23c_UL_40), c_TrLogMappingUL_TM3_A M1, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
<p>Detailed Comments : 1.to configure physical channel DPCH1and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. Used for ConversationalSpeech/UL:(12.2 7.95 5.9 4.75) kbps DL:(12.2 7.95 5.9 4.75) kbps/ Interactive or background / UL: 32 kbps DL: 32 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH (TTI 40 ms , TC)</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractiveBackGround_8k_PS_40 (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL: 64 kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_Info (tsc_Sfc128, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_DL_Info _56tc (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL, c_DCH_340_TFS_40_TC, c_PowerOffsetInfoBelow64 k,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_DL_2_0To3 (c_DCH_148_TFS_DL, c_DCH_340_TFS_40_TC, c_PowerOffsetInfoBelow64 k), c_TrLogMappingDL_2_Multi plex_PS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo (p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info,p_ActTi me)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To3_UL_Info (p_CellId, tsc_UL_DPCH1, c_DCH_148_TFS_UL, c_DCH_340_TFS_40_TC,p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_UL_2_0To3 (c_DCH_148_TFS_UL, c_DCH_340_TFS_40_TC), c_TrLogMappingUL_2_Multi plex_PS,p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for transporting interactive or background /UL:8 DL: 8 kbps/PS 40ms TTl, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_ModifyInteractiveBackGround_64k_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 transport channel respectively. Used for interactive or background / unknown/ UL: 64 DL: 64 kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL_RM1 92, c_DCH_340_TFS_20_TC, c_PowerOffsetInfoHigher64 k ,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_DL_2_0To9 (c_DCH_148_TFS_DL_RM1 92, c_DCH_340_TFS_20_TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_2_Multi plex_PS,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info ,p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_Info (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_UL, c_DCH_340_TFS_20_TC,p _ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CN F	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		3.
12		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_340_TFS_20_TC), c_TrLogMappingUL_2_Multi plex_PS ,p_ActTime)		
13		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1for Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH TC. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB10_ToRB12_TM_Cfg_Segmented Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : RB10, RB11, RB12 for radio beaer subflow#1, subflow#2, subflow#3 Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB10, 81, {uLogicalChannelIdentity tsc_UL_DTCH1, dLlogicalChannelIdentity tsc_DL_DTCH1}))		cofigure radio bearers : RB10 for AMR subflow #1
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB10)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB11, 103, {uLogicalChannelIdentity tsc_UL_DTCH2, dLlogicalChannelIdentity tsc_DL_DTCH2}))		cofigure radio bearers : RB11 for AMR subflow #2
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB11)		
5		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB12, 60, {uLogicalChannelIdentity tsc_UL_DTCH3, dLlogicalChannelIdentity tsc_DL_DTCH3}))		cofigure radio bearers : RB12 for AMR subflow #3
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB12)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB10_ToRB12_TM_RB20_RB22_AM_Cfg Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : setup radio bearers : RB10, RB11, RB12, RB20 and RB22 for radio beaer subflow#1, subflow#2, subflow#3 Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB10, 81, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 for AMR subflow #1
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB10)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB11, 103, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2})		cofigure radio bearers : RB11 for AMR subflow #2
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB11)		
5		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated, tsc_RB12, 60, {uLogicalChannelIdentity tsc_UL_DTCH3, dLogicalChannelIdentity tsc_DL_DTCH3})		cofigure radio bearers : RB12 for AMR subflow #3
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB12)		
7		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH4, dLogicalChannelIdentity tsc_DL_DTCH4},320)		cofigure radio bearers : RB20 (AM + DTCH)
8		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB20)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB22, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH5, dLogicalChannelIdentity tsc_DL_DTCH5},320)		SS configur ation of the radio bearer informati on : RB22 (AM + DTCH)
10		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB22)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_3DCH_ModifyInteractiveBackGround_8k_8k_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Configuration/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 and DTCH(subflow#2) to the DCH2 transport channel respectively. Used for interactive or background / unknown/ UL: 64 kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To15_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL_RM1 92, c_DCH_656_TFS_DL_40_T C, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoHigher64 k ,p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_DL_3_0To15 (c_DCH_148_TFS_DL_RM1 92, c_DCH_656_TFS_DL_40_T C, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_3_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info(p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_UL_20_TC, c_DCH_336_TFS_38b_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMACEReconfigInfo(tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_UL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_336_TFS_UL_20_TC, c_DCH_336_TFS_38b_40_TC), c_TrLogMappingUL_3_PS, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMACECfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1 for transporting interactive or background /UL:16 DL: 16 kbps/PS 40ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1-4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_5DCH_ModifyConvSpeech_12_2k_InteractiveBackground_64k_64k_TC_20 (<p> p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info </p>)					
Group : RB_Steps/RB_Configuration/					
Objective : to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively. <p>Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 64 DL: 64 kbps / PS RAB + Interactive or background / UL: 64 DL: 64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p>					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_64k (tsc_Sfc32, p_DL_CommonInformation, OMIT) ,p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_DL_Info (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_340_TFS_20_TC, c_PowerOffsetInfoHigher64k,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoDL_5_0To119 (c_DCH_148_TFS_DL, c_DCH_81_TFS_DL, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_340_TFS_20_TC, c_PowerOffsetInfoHigher64k), c_TrLogMappingDL_TM3_A M1_Multiplex,p_ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_5_DCH_0_To119_UL_Info(p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_340_TFS_20_TC, activationCFN : p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfoUL_5_0To119 (c_DCH_148_TFS_UL, c_DCH_81_TFS, c_DCH_103_TFS, c_DCH_60_TFS, c_DCH_340_TFS_20_TC), c_TrLogMappingUL_TM3_A M1_Multiplex, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14	[px_RAT = tdd]				
15	[TRUE]			I	
<p>Detailed Comments : 1.to configure physical channel DPCH1 and connect DCH1,DCH2,DCH3,DCH4 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH(subflow#1),DTCH(subflow#2), DTCH(subflow#3) , DTCH(subflow#4) to the DCH1,DCH2,DCH3 and DCH4 transport channel respectively.</p> <p>Used for ConversationalSpeech/UL:12.2 kbps DL: 12.2 kbps/ Interactive or background / UL: 64 DL: 64 kbps / PS RAB + Interactive or background / UL: 64 DL: 64 kbps / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH</p> <p>2. connect DCH1,DCH2, DCH3, DCH4 and DCH5 to DPCH1.</p> <p>3. map logical channels: DCCH1–4 to DCH5, and DTCH1, DTCH2,DTCH3, DTCH4 to DCH1, DCH2,DCH3,DCH4 respectively for both uplink and downlink and send relevant transport channel configuration information to MAC.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_Modify_DPCH_AssociatedWith_DSCH(p_CellId : INTEGER; p_DL_DPCHInfo:DL_DPCHInfo;p_TFCS:TFCS) Group : RB_Steps/RB_Configuration/ Objective : Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfoActNow (p_CellId, tsc_DL_DPCH1, p_DL_DPCHInfo)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_DCH_DSCH_148_DL_Info (p_CellId, tsc_DL_DPCH1, p_TFCS)		2.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoActNow (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_3_4 (p_TFCS), c_TrLogMappingDL_4DCC H)		3.
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfoActNow(p_CellId, tsc_UL_DPCH1, cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.ul_ScramblingCode))		4.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_2_DCH_0_To9_UL_InfoActNow (p_CellId , tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_20_TC)		5.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrCHInfo_UL_2_0To9 (c_DCH_148_TFS_UL, c_DCH_336_TFS_24_UL_2_0_TC), c_TrLogMappingUL_2_PS)		6.
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
15		[px_RAT = tdd]			
16		[TRUE]		I	
Detailed Comments : 1. Modify the DL DPCH . 2. For DL DPCH reconfigure the Trasport channel to include the DSCH + DCH TFCI. 3.For DL DPCH reconfigure theMAC to include the DSCH + DCH TFCI. 4. Modify UL DPCH to include Interactive or background / UL:64 PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH. 5. Modify UL Trasport channel to include Interactive or background / UL:64 / PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH. 6.Modify UL MAC to include Interactive or background / UL:64 PS RAB + UL:3.4 DL: 3.4 kbps SRBs for DCCH.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_DSCH_Cfg (p_Payloadsize: INTEGER) Group : RB_Steps/RB_Configuration/ Objective : setup radio bearers : RB20. default values from 34.108 cl. 6.10.2.4.4 and 6.10.2.4.3.3 Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},p_Payload size)		cofigure radio bearers : RB20 (AM + DTCH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB20)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_PDSCH_Cfg (p_CellId : INTEGER;p_pdsch_CodeMapping : PDSCH_CodeMapping; p_DL_TrChConfig: CphyTrchConfigReq; p_DL_TrCHInfo: TrCHInfo;p_DL_TrLogMapping: TrCH_LogCHMappingList1) Group : RB_Steps/RB_Configuration/ Objective : Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_pDSCH_Info (p_CellId, tscv_TmpCellInfo.priScrmCo de, p_pdsch_CodeMapping, tsc_DL_TxPower_PDSCH)		PDSCH ===== =====
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_DL_PDSCH1)		2nd paramet er to be checked
5		CPHY!CPHY_TrCH_Config_REQ	ca_TrChCfgInfo(p_CellId, tsc_DL_PDSCH1,c_TrChC onfigTypeDCH_NoSHO, p_DL_TrChConfig)		DSCH connect ed to PDSCH
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_DL_PDSCH1)		
7		CMAC!CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now(p_CellId, tsc_DL_PDSCH1, c_UE_Info (OMIT, px_DSCH_RNTI) , p_DL_TrCHInfo, p_DL_TrLogMapping)		mapping DTCH to DSCH.
8		CMAC?CMAC_Config_CNF	ca_CMAC_CfgCnf (tsc_CellDedicated, tsc_DL_PDSCH1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_3DCH_ModifyInteractiveBackGround_16k_128k_8k_8k_PS (p_CellId : INTEGER; p_ActTime : ActivationTime; p_DL_CommonInformation : DL_CommonInformation; p_UL_DPCH_Info : UL_DPCH_Info) Group : RB_Steps/RB_Setup/ Objective : to configure physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel and map DTCH(subflow#1) to the DCH1 and DTCH(subflow#2) to the DCH2 transport channel respectively. Used for interactive or background / unknown/ UL: 64 DL: 64 kbps, 20ms TTI TC. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[px_RAT = fdd]			
2		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, cd_DL_DPCH_Info_128k (tsc_Sfc16, p_DL_CommonInformation, OMIT),p_ActTime)		1.
3		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
4		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To19_DL_Inf o (p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_No SHO, c_DCH_148_TFS_DL_RM1 92, c_DCH_656_TFS_DL128k_ 40_TC, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoHigher64 k ,activationCFN :p_ActTime)		2.
5		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_DL_3_0To19 (c_DCH_148_TFS_DL_RM1 92, c_DCH_656_TFS_DL128k_ 40_TC, c_DCH_336_TFS_38b_40_ TC, c_PowerOffsetInfoHigher64 k), c_TrLogMappingDL_3_PS,p _ActTime)		3.
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_Cell Dedicated, tsc_DL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
9		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
10		CPHY!CPHY_TrCH_Config_REQ	ca_3_DCH_0_To7_UL_Info(p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, c_DCH_148_TFS_UL, c_DCH_336_TFS_UL_20_TC, c_DCH_336_TFS_38b_40_TC, p_ActTime)		2.
11		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		
12		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrCHInfo_UL_3_0To7 (c_DCH_148_TFS_UL, c_DCH_336_TFS_UL_20_TC, c_DCH_336_TFS_38b_40_TC), c_TrLogMappingUL_3_PS, p_ActTime)		3.
13		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated, tsc_UL_DPCH1)		
14		[px_RAT = tdd]			
15		[TRUE]		I	
Detailed Comments : 1. configure DPCH1 for transporting interactive or background /UL:16 DL: 16 kbps/PS 40ms TTI, TC. RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH. 2. connect DCH1 and DCH5 to DPCH1. 3. map logical channels: DCCH1-4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_DCH_16K_128K_8k_8k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM2_PS_ No_Pdcp (useT315, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_AM0 To7(c_PowerOffsetInfoBelow64k) , c_UL_AddReconfTransChInfoListAM2 (c_DCH_336_TFS_UL_20_TC_UE, c_DCH_336_TFS_38b_UL_40_TC_UE), c_DL_CommonTransChInfo_AM_0_19, c_DL_AddReconfTransChInfoListAM2 (c_DCH_656_TFS_DL128k_40_TC_UE, c_DCH_336_TFS_38b_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc16, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.ul_ScramblingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_3DCH_ModifyInteractiveBackGroun d_16k_128k_8k_8k_PS (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_22_AM_Cfg_640			
6		+ ts_RRC_ReceiveRB_SetupCmpl(p_CellId , cell_DCH_2_PS_Call)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUpStreamUnknown14_4k_NoSc (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for streaming unknown14.4 and to reconfigure the SS accordingly. this is same as ts_RB_SendRB_SetUpStreamUnknown14_4k except for Secondary scrambling code as omit Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(cs_IntegrityCheckInfo0, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_1 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0 _To3 , c_UL_AddReconfTransChInfoListTM_1 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc128, OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd128_8), cb_UL_DPCH_Info (tsc_Sf64, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode), OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ ts_2DCH_ModifyStreamUnknown14_4_NoS c (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_8) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88, tcv_TmpCellInfo.ul_ScramblingCode), c_DCH_576_TFS_2)			
5		+ts_SS_RB10_TM_Cfg (576)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId, cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_128k_2048k_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_RRC_Security (p_CellId, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, FALSE, ps_domain)			2.
5		+ ts_CalculateActTime (p_CellId)			
6		+ts_SendRB_SetUpConvSpeech_Interact Backg_128k_2048k_10TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
7		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_128k_2048k_10TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_28_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_263, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_656_TFS_35_DL_1 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_128k_2048k_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_128k_2048k_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_128k_2048k_20TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_128k_2048k_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_28_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_455, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_656_TFS_35_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_128k_2048k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_10TTI_CS_PS(p_CellId , p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_10TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To47(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_1 0_CC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_64k_CC_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_20TTI_CS_PS(p_CellId , p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_CC_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To71(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_2 0_CC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_64k_CC_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_10TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
3.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_10TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To47(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_1 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_25_DL_ 20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_64k_TC_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_20TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 3.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_64k_TC_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To71(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_64k_TC_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_CC_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_CC_10TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
3.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_CC_10TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To47(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_1 0_CC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_47, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_8k_CC_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvSpeech_InteractBackg_32k_8k_CC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvSpeech_InteractBackg_32k_8k_CC_20TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvSpeech_InteractBackg_32k_8k_CC_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To71(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_2 0_CC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_47, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_8k_CC_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_10TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_10TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To47(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_1 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_47, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_8k_TC_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiverRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_20TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_32k_8k_TC_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To71(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_47, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_32k_8k_TC_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_128k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_128k_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_128k_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_PS(c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_64k_128k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k_20_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain. (Transport and Physical channel Information for combined CS and PS domains is provided here)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k_20_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_167, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_31_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_64k_256k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_256k_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_31_DL_1 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_64k_256k_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_384k_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_384k_20TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.					
(Tranport and Physical channel Information for combined CS and PS domains is provided here)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_384k_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_PDCP_twrw512 (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_215, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_32_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_64k_384k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg_twrw512 (320)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_InteractBackg_64k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1.
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			2.
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_64k_CS_PS(p_CellId,p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2. RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListAM_DCH4(c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4(c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBack g_64k_64k_ChC31(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_StreamUnknown_0_128k (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4 (c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM4_ 0To47 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListTM4 (c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM4_0_143, c_DL_AddReconfTransChIn foListTM4(c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_320_TFS_20_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_Scrambl		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3		AM ? RLC_AM_DATA_CNF	ingCode) ,		
4		+ts_5DCH_ModifyConvSpeech_StreamUnkn own_0_128k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (OMIT))		
5		tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
6	TSP	+ts_RB10_To_13_TM_CS_Config (
		81,103, 60, 320) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_StreamUnknown_0_64k (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4 (c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM4_ 0To47 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListTM4 (c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM4_0_119, c_DL_AddReconfTransChIn foListTM4(c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_320_TFS_18_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_Scrambl		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3		AM ? RLC_AM_DATA_CNF	ingCode) ,		
4		+ts_5DCH_ModifyConvSpeech_StreamUnkn own_0_64k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (OMIT))		
5		tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
6	TSP	+ts_RB10_To_13_TM_CS_Config (
		81,103, 60, 320) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_StreamUnknown_57_6k (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4 (c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM4_ 0To119 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListTM4 (c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_576_TFS_UE_1), c_DL_CommonTransChInfo _TM4_0_119, c_DL_AddReconfTransChIn foListTM4(c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_576_TFS_UE_1), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode) ,		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	OMIT)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_StreamUnkn own_57_6k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_RB10_To_13_TM_CS_Config (
		81,103, 60, 576) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_28_8k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARER for conversational 64k with TTI 20 and to reconfigure the SS accordingly.					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0 _To5 , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_576_TFS_3_UE (180)), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64,OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_92, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
2		AM ! RLC_AM_DATA_REQ			
3		AM ? RLC_AM_DATA_CNF			
4		+ ts_2DCH_ModifyConvUnknown_28_8 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_32k_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for conversational 32k with TTI 20 and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_1 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0_To3, c_UL_AddReconfTransChInfoListTM_1 (c_DCH_640_TFS_20_1_UE), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc64, OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_80, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		tcv_Spr dFct + tcv_PuncLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_2DCH_ModifyConvUnknown (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_80, tcv_TmpCellInfo.uL_ScramblingCode), c_DCH_640_TFS_20_1_UE, c_DCH_640_TFS_20_1 , tsc_Sfc64)			
5		+ts_SS_RB10_TM_Cfg_Segmented (640)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_32k_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for conversational 32k with TTI 40 and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_Seg_F also (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0 _To3 , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_640_TFS_40_2_U E), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64,OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_80, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_2DCH_ModifyConvUnknown (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_80, tcv_TmpCellInfo.uL_ScramblingCode), c_DCH_640_TFS_40_2_UE , c_DCH_640_TFS_40_2, tsc_Sfc64)			
5		+ts_SS_RB10_TM_Cfg_Segmented (640)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for conversational 64k with TTI 40 and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_Seg_F else (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0_To_3(c_PowerOffsetInfoHigher64k) , c_UL_AddReconfTransChInfoListTM_1 (c_DCH_640_TFS_40_4_U E), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc32, OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		tcv_Spr dFct + tcv_PuncLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_2DCH_ModifyConvUnknown_64k_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode) , c_DCH_640_TFS_40_4, tsc_Sfc32)			
5		+ts_SS_RB10_TM_Cfg_Segmented (640)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_ConvUnknown_64k_20TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : To setup a RADIO BEARER Cell_DCH_Speech and to reconfigure the SS accordingly. This Step is used by RLC test cases. See TS 34.018 clause 6.10.2.4.1.50 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM2_Seg_F else(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM2_0To7(c_PowerOffsetInfoHigher64k), c_UL_AddReconfTransChInfoListTM2(c_DCH_640_TFS_20_2_UE, c_DCH_640_TFS_20_2_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM2 , c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc16,OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_RB10_To_11_TM_CS_Config(640, 640)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_ConvUnknown_64k_40TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : To setup a RADIO BEARER Cell_DCH_Speech and to reconfigure the SS accordingly. This Step is used by RLC test cases. See TS 34.018 clause 6.10.2.4.1.50 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM2_Seg_F else(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM2_0To7(c_PowerOffsetInfoHigher64k), c_UL_AddReconfTransChInfoListTM2(c_DCH_640_TFS_40_4_UE, c_DCH_640_TFS_40_4_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM2 , c_DL_InformationPerRL(tcv_TmpCellInfo.priScrmCode, tsc_Sfc16,OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_ConvUnknown_64k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_RB10_To_11_TM_CS_Config(640, 640)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_128k_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To send the RB setup message for CS nad PS Domain for the configuration ConvUnknown_64k / InteractBackg_128k / 20TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvUnknown_64k_In teractBackg_128k_20TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_128k_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To send the RB setup message for the configuration ConvUnknown_64k / InteractBackg_128k / 20TTI. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_28_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_128k_128k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiverRB_SetupCmpl (
		p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_128k_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvUnknown_64k_In teractBackg_128k_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_128k_40TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_28_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_128k_128k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_128k_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_128k_20TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_128k_20TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_64k_128k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiverRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_128k_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1.
2		+ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			2.
5		+ts_SendRB_SetUpConvUnknown_64k_In teractBackg_64k_128k_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_128k_40TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_64k_128k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvUnknown_64k_In teractBackg_64k_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvUnknown_64k_InteractBackg_64k_40TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 ,OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_64k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg(320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId: INTEGER; p_RAB_Id : BITSTRING;p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER Cell_DCH_64kCS_RAB_SRB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See ts 34.108 clause 6.10.2.4.1.13 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (cs_IntegrityCheckInfo0, tcv_RRC_Ti , p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_64k (c_ReEstTimerT314, p_RAB_Id,c_RLC_InfoTM_ Def_Seg_False), c_UL_CommTrChInfoTM_6 4k, c_UL_AddReconfTransChIn foListTM_64k, c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM_64k, c_DL_InformationPerRL(tcv_TmpCellInfo.priScrmCo de , tsc_Sfc32 ,OMIT) , c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_64k_ CS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_ CS, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_Modify(p_CellId, c_DCH_640_148_UL_Info(p_ActTime), c_DCH_640_148_DL_Info(p_ActTime), c_TrChInfoUL_640_148, c_TrChInfoDL_640_148, c_TrLogMappingUL_4DCCH_1DTCH, c_TrLogMappingDL_4DCCH_1DTCH, p_ActTime, cd_DL_DPCH_64K_CS_ChC31(c_DL_CommonInformationRB_SetUp (tsc_DL_DPCH1_SFP_64k_CS) , OMIT), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_CS, pl0_88, tcv_TmpCellInfo.ul_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented(640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_DCH_64kPS_RAB_SRB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_6 4k_No_Pdcp (useT315, p_RAB_Id, c_RLC_InfoAM_Def_PS), c_UL_CommTrChInfoDCH_ PS_64k, c_UL_AddReconfTransChIn foListDCH_PS_64k, c_DL_CommonTransChInfo DCH (c_TFCS_Cmpl0_1_2_3_4_5 _6_7_8_9_Rx), c_DL_AddReconfTransChIn foListDCH_PS_64k, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_64k_ PS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_ PS , pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
3		AM ? RLC_AM_DATA_CNF			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ts_SS_2DCH_Modify(p_CellId, cb_DCH_336_148_UL_Info (p_ActTime) , cb_DCH_336_148_DL_Info (p_ActTime) , cb_TrChInfoUL_336_148, cb_TrChInfoDL_336_148, cb_TrLogMappingUL_4DCCH_1DTCH_PS, cb_TrLogMappingDL_4DCCH_1DTCH_PS, p_ActTime, cd_DL_DPCH_64K_PS_ChC31 (c_DL_CommonInformationRB_SetUp (tsc_DL_DPCH1_SFP_64k_PS), OMIT) , cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_PS, pl0_96, tcv_TmpCellInfo.ul_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg(320)			payload= RLC payload + RLC header
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_128k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfoDCH_ PS_64k, c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_28_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_27_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_128k_128kPS(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_128k_2048kPS_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_28_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_21, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_128k_2048kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl(p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_128k_2048kPS_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_28_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_37, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_128k_2048kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_128k_384kPS_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_28_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_11, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_128k_384kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_128k_384kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_28_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_17, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_128k_384kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_144k_144kPS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To11(c_PowerOffsetInfoHig her64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_30_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_11, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_29_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_84 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_144k_144kPS(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_84 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_64kPS_10_CC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfoAM0T o3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 10_CC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_25_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_6 4kPS_10_CC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_64kPS_10_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfoAM0T o3 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_25_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_6 4kPS_10_TC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_64kPS_20_CC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 20_CC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_25_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_6 4kPS_20_CC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_PS_Cfg (320) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_64kPS_20_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_25_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_6 4kPS_20_TC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_8kPS_10_CC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfoAM0T o3(c_PowerOffsetInfoBelo w64k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 10_CC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_CC_UE) , c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_2DCH_ModifyInteractBackg_32k_8kPS_ 10_CC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_8kPS_10_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfoAM0T o3 (c_PowerOffsetInfoBelow6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
2		AM ! RLC_AM_DATA_REQ			
3		AM ? RLC_AM_DATA_CNF			
4		+ts_2DCH_ModifyInteractBackg_32k_8kPS_10_TC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_8kPS_20_CC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 20_CC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_CC_UE) , c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_8k PS_20_CC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_8kPS_20_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_23_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_8k PS_20_TC(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_384k_2048kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To11(c_PowerOffsetInfoHig her64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_34_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_21, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_384k_2048kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_384k_2048kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To17(c_PowerOffsetInfoHig her64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_34_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_37, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_384k_2048kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_384k_384kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To11(c_PowerOffsetInfoHig her64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_34_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_11, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_384k_384kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_384k_384kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To17 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_34_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_17, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_384k_384kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf4, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_144kPS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_11, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_29_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_1 44kPS(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_2048kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_21, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 , OMIT) , c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_2 048kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_PS_Cfg (640) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_2048kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_37, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_65 6_TFS_35_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc4 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_2 048kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_PS_Cfg (640) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_256kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_31_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_2 56kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_256kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_13, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_31_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_2 56kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl(p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_384kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_11, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_3 84kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_384kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_17, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_32_DL_20_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_3 84kPS_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_768kPS_10 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_17, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_63_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_7 68kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_768kPS_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 10_TC_UE), c_DL_CommonTransChInfo _AM_0_17, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_63_DL_10_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_7 68kPS_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_8kPS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_8k PS(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_8k_CC_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_24_UL_ 20_TC_UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_23_DL_40_CC_UE) ', c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) ', cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode) ', OMIT)))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_8k _CC_PS(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpInteractBackg_64k_128k_StreamUnknown_0k_128k(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUp_StreamUnknown_0k_128k _tc55(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpInteractBackg_64k_12 8k_StreamUnknown_0k_128k_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpInteractBackg_64k_128k_StreamUnknown_0k_128k_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP_sdu4 (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM1 _TM1_0To19(c_PowerOffse tInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH1 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _AM1_TM1_0_59, c_DL_AddReconfTransChIn foListAM_DCH1 (c_DCH_320_TFS_20_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyInteractBackg_64k_128k_StreamingUnknown_0k_128k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpInteractBackg_64k_128k_StreamUnknown_0k_64k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUp_StreamUnknown_0k_64k_t c54(p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpInteractBackg_64k_128k_StreamUnknown_0k_64k_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpInteractBackg_64k_128k_StreamUnknown_0k_64k_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP_sdu4 (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM1 _TM1_0To19 (c_PowerOffsetInfoHigher64 k) , c_UL_AddReconfTransChIn foListAM_DCH1 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _AM1_TM1_0_49, c_DL_AddReconfTransChIn foListAM_DCH1 (c_DCH_320_TFS_18_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyInteractBackg_64k_128k_StreamingUnknown_0k_64k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_10_2k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 10.2k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM3(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_1 2_2k, c_UL_AddReconfTransChInfoListTM_3 (c_DCH_65_TFS_UE, c_DCH_99_TFS_UE, c_DCH_40_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChInfoListTM3 (c_DCH_65_TFS_DL_UE, c_DCH_99_TFS_UE, c_DCH_40_TFS_UE), c_DL_InformationPerRL(tcv_TmpCellInfo.priScrmCode, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		tcv_Spr dFct + tcv_PuncLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_4DCH_ModifySpeech102 (p_CellId , p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , c_UL_DPCH (tsc_PIO_96, tsc_Sf64))			
5		+ ts_RB10_To_RB12_TM_ConfigSpeech (65, 99, 40)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_ConvUnknown_0_384k_40TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM4_0To47(c_PowerOffsetInfoBelow64k), c_UL_AddReconfTransChInfoListTM4(c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo_TM4_0_191, c_DL_AddReconfTransChInfoListTM4 (c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_320_TFS_22_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc8 , OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		c_DL_CommonTransChInfoSameAsUL
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_InteractBackg_0k_384k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_RB10_To_13_TM_CS_Config(81, 103, 60, 320)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_ConvUnknown_64k_20TTI_DiffRM_DCH5 (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoTM4_0To47(c_PowerOffsetInfoBelow64k), c_UL_AddReconfTransChInfoListTM4(c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_640_TFS_20_2_UE), c_DL_CommonTransChInfoSameAsUL, c_DL_AddReconfTransChInfoListTM4_DiffRM_DCH5 (c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_640_TFS_20_2_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc32 , OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_20_DiffRM_DCH5(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_RB10_To_13_TM_CS_Config(81, 103, 60, 640)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_ConvUnknown_64k_40TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_TM4_0To47(c_PowerOffsetInfo Below64k), c_UL_AddReconfTransChInfoListTM4(c_DCH_81_TFS_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_640_TFS_40_4_UE), c_DL_CommonTransChInfo_TM4_0_47, c_DL_AddReconfTransChInfoListTM4 (c_DCH_81_TFS_DL_UE, c_DCH_103_TFS_UE, c_DCH_60_TFS_UE, c_DCH_640_TFS_40_4_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc32 , OMIT), c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.ul_ScramblingCode) , OMIT))		c_DL_CommonTransChInfoSameAsUL
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifySpeech_12_2k_ConvUnknown_64k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.ul_ScramblingCode))			
5		+ts_RB10_To_13_TM_CS_Config(81, 103, 60, 640)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_4_75k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 4.75k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_2(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_2 (c_DCH_42_TFS_UE, c_DCH_53_TFS_UE, c_DCH_148_TFS_17_UE), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2(c_DCH_42_TFS _DL_UE, c_DCH_53_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc256 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd256_4) , cb_UL_DPCH_Info (tsc_Sf128, pl0_92, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF			
4		+ ts_3DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4) , cb_UL_DPCH_Info (tsc_Sf128, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode), c_DCH_148_TFS_17, c_DCH_42_TFS, c_DCH_42_TFS_DL, c_DCH_53_TFS , tsc_Sfc256)			
5		+ts_RB10_To_RB11_TM_ConfigSpeech (42, 53)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_5_15k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 5.15k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_2(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_2 (c_DCH_49_TFS_UE, c_DCH_54_TFS_UE, c_DCH_148_TFS_17_UE), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2(c_DCH_49_TFS_DL_UE, c_DCH_54_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc256 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd256_4) , cb_UL_DPCH_Info (tsc_Sf128, pl0_84, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_3DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4) , cb_UL_DPCH_Info (tsc_Sf128, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode), c_DCH_148_TFS_17, c_DCH_49_TFS, c_DCH_49_TFS_DL, c_DCH_54_TFS,tsc_Sfc256)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_RB10_To_RB11_TM_ConfigSpeech (49, 54)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_5_9k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 5.9k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM3(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_3 (c_DCH_55_TFS_UE, c_DCH_63_TFS_UE, c_DCH_60_TFS_UE_0B,c_ DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3(c_DCH_55_TFS _DL_UE, c_DCH_63_TFS_UE,c_DC H_60_TFS_UE_0B), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUpSpeech (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_4DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUpSpeech (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode),c_DC H_148_TFS_UL, c_DCH_55_TFS,c_DCH_55_TFS_DL, c_DCH_63_TFS, c_DCH_60_TFS_0B, tsc_Sfc128)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_RB10_To_RB12_TM_ConfigSpeech (55, 63,60)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_6_7k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARER for SPEECH 6.7 k and to reconfigure the SS accordingly.					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_2(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_2 (c_DCH_58_TFS_UE, c_DCH_76_TFS_UE, c_DCH_148_TFS_UE_UL) , c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2(c_DCH_58_TFS _DL_UE, c_DCH_76_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_3DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode),c_DC H_148_TFS_UL, c_DCH_58_TFS, c_DCH_58_TFS_DL, c_DCH_76_TFS , tsc_Sfc128)			
5		+ts_RB10_To_RB11_TM_ConfigSpeech (58, 76)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_7_4k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 7.4k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_2(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_2 (c_DCH_61_TFS_UE, c_DCH_87_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2(c_DCH_61_TFS_DL_UE, c_DCH_87_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF			
4		+ ts_3DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode), c_DCH_148_TFS_UL, c_DCH_61_TFS, c_DCH_61_TFS_DL, c_DCH_87_TFS , tsc_Sfc128)			
5		+ts_RB10_To_RB11_TM_ConfigSpeech (61, 87)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_7_95k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 7.95k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_2 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTmSp eech(c_PowerOffsetInfoBel ow64k), c_UL_AddReconfTransChIn foListTM_2 (c_DCH_75_TFS_UE, c_DCH_84_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2(c_DCH_75_TFS_DL_UE, c_DCH_84_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode), OMIT)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF			
4		+ ts_3DCH_ModifySpeech (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.ul_ScramblingCode), c_DCH_148_TFS_UL, c_DCH_75_TFS, c_DCH_75_TFS_DL, c_DCH_84_TFS, tsc_Sfc128)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_RB10_To_RB11_TM_ConfigSpeech (75, 84)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_0k_128k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_Seg_F else (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfo_TM0 To3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM_0_11, c_DL_AddReconfTransChIn foListTM1(c_DCH_320_TF S_20_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_ModifyStreamUnknown_0k_128k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_0k_128k_tc55 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_DCH2 _Seg_False (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfo_TM0 To3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListTM_DCH2 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM_0_11, c_DL_AddReconfTransChIn foListTM_DCH2(c_DCH_3 20_TFS_20_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyStreamUnknown_0k_128k_tc55(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_57_6kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_0k_384k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_Seg_F alse(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfo_TM0 To3 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM_0_15, c_DL_AddReconfTransChIn foListTM1(c_DCH_320_TF S_22_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_ModifyStreamUnknown_0k_384k(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_0k_64k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_Seg_F alse (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfo_TM0 To3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM_0_9, c_DL_AddReconfTransChIn foListTM1(c_DCH_320_TF S_18_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_ModifyStreamUnknown_0k_64k (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_57_6kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_0k_64k_tc54 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1_DCH2 _Seg_False (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfo_TM0 To3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListTM_DCH2 (c_DCH_576_TFS_2_UE), c_DL_CommonTransChInfo _TM_0_9, c_DL_AddReconfTransChIn foListTM_DCH2 (c_DCH_320_TFS_18_DL_ 40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyStreamUnknown_0k_64k_tc54(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf64, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_57_6kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_128k_0k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM0T o11(c_PowerOffsetInfoHig her64k) , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_320_TFS_21_UL_ 40_TC_UE), c_DL_CommonTransChInfo _TM_0_3, c_DL_AddReconfTransChIn foListTM1(c_DCH_576_TF S_2_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_2DCH_ModifyStreamUnknown_128k_0k (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_8) , cb_UL_DPCH_Info (tsc_Sf8, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_StreamUnknown_64k_0k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1 (c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM0T o9 (c_PowerOffsetInfoHigher6 4k), c_UL_AddReconfTransChIn foListTM_1 (c_DCH_320_TFS_19_UL_ 40_TC_UE), c_DL_CommonTransChInfo _TM_0_3, c_DL_AddReconfTransChIn foListTM1(c_DCH_576_TF S_2_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_8) , cb_UL_DPCH_Info (tsc_Sf16, pl1, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_2DCH_ModifyStreamUnknown_64k_0k (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_8) , cb_UL_DPCH_Info (tsc_Sf16,pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_57_6k_CS (p_CellId: INTEGER; p_RAB_Id : BITSTRING;p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER Cell_DCH_57_6kCS_RAB_SRB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.17 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM_57_6k(c_ReEstTimerT314, p_RAB_Id, c_RLC_InfoTM_Def_Seg_F alse), c_UL_CommTrChInfoTM_5 7_6k, c_UL_AddReconfTransChIn foListTM_57_6k, c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT) , c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_Strea ming) , cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_Strea ming, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode), OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ts_SS_2DCH_Modify (p_CellId, c_DCH_576_148_UL_Info(p_ActTime), c_DCH_576_148_DL_Info_1(p_ActTime), c_TrChInfoUL_576_148, c_TrChInfoDL_576_148_1, c_TrLogMappingUL_4DCCH_1DTCH, c_TrLogMappingDL_4DCCH_1DTCH,p_ActTime, cd_DL_DPCH_64K_CS_ChC31 (c_DL_CommonInformationRB_SetUp (tsc_DL_DPCH1_SFP_Streaming), OMIT), cb_UL_DPCH_Info (tsc_UL_DPCH_SF_Streaming, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			payload size= RLC payload size + RLC header
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_57_6kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_Speech_DiffRM_DCH5 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER Cell_DCH_Speech and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.018 clause 6.10.2.4.1.4 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti , p_ActTime , cell_DCH, OMIT , {c_RAB_InfoSetupTM_12_2 k (c_ReEstTimerT314, p_RAB_Id)}, c_UL_CommTrChInfoTM_1 2_2k, c_UL_AddReconfTransChIn foListTM_12_2k, c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3_RM192(c_DCH_ 81_TFS_DL_UE, c_DCH_103_TFS_UE ,c_DCH_60_TFS_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de , tsc_Sfc128 , OMIT) , c_DL_CommonInformation RB_SetUp_DTX_fixed (tsc_DL_DPCH1_SFP_Spe ech), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_Spee ch, pl0_84, tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_4DCH_Modify_1 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_DL_DPCH1_SFP_Speech) , cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_Speech, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_SS_RB10_ToRB12_TM_Cfg_Segment ed			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_Speech (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER Cell_DCH_Speech and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.018 clause 6.10.2.4.1.4 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti , p_ActTime , cell_DCH, OMIT , {c_RAB_InfoSetupTM_12_2 k (c_ReEstTimerT314, p_RAB_Id)}, c_UL_CommTrChInfoTM_1 2_2k, c_UL_AddReconfTransChIn foListTM_12_2k, c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3(c_DCH_81_TFS _DL_UE, c_DCH_103_TFS_UE ,c_DCH_60_TFS_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de , tsc_Sfc128 , OMIT) , c_DL_CommonInformation RB_SetUp_DTX_fixed (tsc_DL_DPCH1_SFP_Spe ech), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_Spee ch, pl0_84, tcv_TmpCellInfo.uL_Scrambl ingCode), OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_4DCH_Modify_ChC127 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_DL_DPCH1_SFP_Speech) , cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_Speech, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ts_SS_RB10_ToRB12_TM_Cfg_Segment ed			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpStreamUnknown28_8k (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for streaming unknown 28.8 and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM1(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_0 _To5 , c_UL_AddReconfTransChIn foListTM_1 (c_DCH_576_TFS_3_UE (155)), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM_1, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode), OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_2DCH_ModifyStreamUnknown28_8_NoS c (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_96, tcv_TmpCellInfo.ul_ScramblingCode), c_DCH_576_TFS_3 (155))			
5		+ts_SS_RB10_TM_Cfg_Segmented (576)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId, cell_DCH_57_6kCS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_64k_128kPS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : To setup a RADIO BEARER Cell_DCH_64kPS_RAB_SRB and to reconfigure the SS accordingly. This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_6 4k_No_Pdcp (useT315, p_RAB_Id, c_RLC_InfoAM_Def_PS), c_UL_CommTrChInfoDCH_ PS_64k, c_UL_AddReconfTransChIn foListDCH_PS_64k, c_DL_CommonTransChInfo DCH (c_TFCS_Cmpl0_1_2_3_4_5 _6_7_8_9_Rx), c_DL_AddReconfTransChIn foListDCH_PS_64k_128k, c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_64k_1 28k_PS(p_CellId, cb_DCH_336_148_UL_Info(p_ActTime), c_DCH_336_8_148_DL_Info(p_ActTime), cb_TrChInfoUL_336_148, c_TrChInfoDL_336_8_148(c_PowerOffsetIn foHigher64k), cb_TrLogMappingUL_4DCCH_1DTCH_PS, cb_TrLogMappingDL_4DCCH_1DTCH_PS, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_96 , tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_SS_RB20_AM_PS_Cfg(320)			payload= RLC payload + RLC header
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_12_2k_AMR_DiffRM_DCH5 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 10.2k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM3(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_1 2_2k_AMR, c_UL_AddReconfTransChIn foListTM_3 (c_DCH_81_TFS_UE_6, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3_RM192 (c_DCH_81_TFS_UE_6_D L, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp_DTX_fixed (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_4DCH_ModifySpeech12_2k_AMR_DiffR M_DCH5 (p_CellId , p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ ts_RB10_To_RB12_TM_ConfigSpeech (81, 103, 60)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 10.2k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM3(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_1 2_2k_AMR, c_UL_AddReconfTransChIn foListTM_3 (c_DCH_81_TFS_UE_6, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3 (c_DCH_81_TFS_UE_6_D L, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfd128, OMIT), c_DL_CommonInformation RB_SetUp_DTX_fixed (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_4DCH_ModifySpeech12_2k_AMR (p_CellId , p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ ts_RB10_To_RB12_TM_ConfigSpeech (81, 103, 60)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_10_2k_AMR_DiffRM_DCH5 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 10.2k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM3(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_1 2_2k_AMR, c_UL_AddReconfTransChIn foListTM_3 (c_DCH_65_TFS_UE_6, c_DCH_99_TFS_UE_5, c_DCH_40_TFS_UE, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM3_RM192 (c_DCH_65_TFS_UE_6_D L, c_DCH_99_TFS_UE_5, c_DCH_40_TFS_UE), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_4DCH_ModifySpeech10_2k_AMR_DiffR M_DCH5 (p_CellId , p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.ul_ScramblingCode))			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5	TSP	+ ts_RB10_To_RB12_TM_ConfigSpeech (65, 99, 40)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_7_4k_AMR_DiffRM_DCH5 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for SPEECH 10.2k and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListTM2_1(c_ReEstTimerT314, p_RAB_Id), c_UL_CommTrChInfoTM_7 _4k_AMR, c_UL_AddReconfTransChIn foListTM_2 (c_DCH_61_TFS_UE_6, c_DCH_87_TFS_UE_5, c_DCH_148_TFS_UE_UL), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM2_DiffRM_DCH5 (c_DCH_61_TFS_UE_6_D L, c_DCH_87_TFS_UE_5), c_DL_InformationPerRL(tcv _TmpCellInfo.priScrmCode, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		tcv_Spr dFct + tcv_Pun cLimit => values ? same for uplink and downlink ? FreqInfo ?
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ ts_3DCH_ModifySpeech7_4k_AMR_DiffRM _DCH5 (p_CellId , p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ ts_RB10_To_RB11_TM_ConfigSpeech (61, 87)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_Speech)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_8k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime ; p_TransChInfo_UE : DedicatedTransChTFS ; p_TransChInfo_SS :CommonOrDedicatedTFS) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP_RlcSizeList1 (useT315, p_RAB_Id), c_UL_CommTrChInfoAM0T o3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (p_TransChInfo_UE), c_DL_CommTrChInfo_AM_ 0To3, c_DL_AddReconfTransChIn foListAM_3_4k(p_TransChI nfo_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl1, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT)))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_ModifyInteractBackg_8k_PS (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl1, tcv_TmpCellInfo.uL_ScramblingCode) , p_TransChInfo_SS)			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_16k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP_RlcSizeList1 (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_40_TC_ UE_23b), c_DL_CommTrChInfo_AM_ 0To5, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_40_TC_UE_23b), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4	TSP	+ts_SS_2DCH_ModifyInteractBackg_16k_P S(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_8k_TC_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1. ts_SendRB_SetUpDCH_Speech_12_2k_AMR
2		+ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			2.
5		+ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_8k_TC_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_8k_TC_40TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To239(c_PowerOff setInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_CommTrChInfo_TM3 _AM1_0To239, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl1 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_8k_8k_TC_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiverRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_16k_TC_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments : Conversational / speech / UL: (12.2 7.95 5.9 4.75) DL: (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1. ts_Send RB_Set UpDCH _Speech _12_2k_ AMR
2		+ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_12_2k_A MR_InteractBackg_16k_TC_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_16k_TC_40TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To359 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_ UE_23b), c_DL_CommTrChInfo_TM3 _AM1_0To359, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_ UE_23b), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_16k_16k_TC_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_32k_TC_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments : Conversational / speech / (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1. ts_Send RB_Set UpDCH _Speech _12_2k_ AMR
2		+ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_12_2k_A MR_InteractBackg_32k_TC_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_32k_TC_40TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To541(c_PowerOff setInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_4 _UE), c_DL_CommTrChInfo_TM3 _AM1_0To541, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_4 _UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl1 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_32k_32k_TC_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl1 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_64k_TC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments : Conversational / speech / UL : (12.2 7.95 5.9 4.75) DL: (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId, tsc_RAB_DefCS, p_ActTime)			1. ts_SendRB_SetUpDCH_Speech_12_2k_AMR
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_64k_TC_20TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_64k_TC_20TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To599 (c_PowerOffsetInfoHigher6 4k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommTrChInfo_TM3 _AM1_0To599, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_64k_64k_TC_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiverRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_64k_128k_TC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments : Conversational / speech / (12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1. ts_Send RB_Set UpDCH _Speech _12_2k_ AMR
2		+ts_SendRB_SetUpSpeech_12_2k_AMR (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_12_2k_A MR_InteractBackg_64k_128k_TC_20TTI_C S_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_64k_128k_TC_20TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To599 (c_PowerOffsetInfoHigher6 4k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommTrChInfo_TM3 _AM1_0To599, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_27_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_64k_128k_TC_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvSpeech_InteractBackg_0k_TC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech_DiffRM_DCH 5 (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvSpeech_Interact Backg_0k_TC_20TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvSpeech_InteractBackg_0k_TC_20TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (useT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To23(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38a_UL_ 20_CC_UE), c_DL_CommTrChInfo_TM3 _AM1_0To23, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38a_DL_ 20_CC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp_DTX_fixed(tsc_Sfd128) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_Inter actBackg_0k_0k_TC_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_Sfd128) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_8k_TC_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractBackg_8k_TC_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_8k_TC_40TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To47(c_PowerOffs etInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38b_UL_ 40_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_47, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38b_DL_ 40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_Inter actBackg_8k_8k_TC_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_0k_CC_20TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpSpeech_12_2k_AMR_DiffR M_DCH5 (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_12_2k_A MR_InteractBackg_0k_CC_20TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_AMR_InteractBackg_0k_CC_20TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (useT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119_14_2_38e(c _PowerOffsetInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38a_UL_ 20_CC_UE), c_DL_CommTrChInfo_TM3 _AM1_0To119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_38a_DL_ 20_CC_UE) , c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128 , OMIT), c_DL_CommonInformation RB_SetUp_DTX_fixed(tsc_Sfd128) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_AMR _InteractBackg_0k_0k_CC_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp_DTX_f ixed (tsc_Sfd128) , cb_UL_DPCH_Info (tsc_Sf64, pl0_84, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_384k_10TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUpConvSpeech_InteractB ackg_64k_384k_10TTI_CS_PS(p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain. (Tranport and Physical channel Information for combined CS and PS domains is provided here)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_InteractBackg_64k_384k_10TTI_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_PDCP_twrw512 (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM3_AM1_0_143, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_32_DL_1 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc8 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_Inter actBackg_64k_384k_10(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd8) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg_twrw512 (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_64k_20(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvUnknown_64k_I nteractBackg_64k_20_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_64k_20_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To19_Order1(c_Po werOffsetInfoHigher64k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_24_UL_2 0_TC_UE), c_DL_CommonTransChInfo _TM1_AM1_0_19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_64k_20_Order1 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf8, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg(320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_8k_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvUnknown_64k_I nteractBackg_8k_20_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_8k_20_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To7 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_23_UL_4 0_TC_UE), c_DL_CommTrChInfo_TM1 _AM1_0To7, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_InteractBackg_8k_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg(320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_8k_40 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvUnknown_64k_I nteractBackg_8k_40_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_8k_40_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To7 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_23_UL_4 0_TC_UE), c_DL_CommTrChInfo_TM1 _AM1_0To7, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_23_DL_4 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_InteractBackg_8k_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg(320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_16k_64k_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_64k_CS_Segmented (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvUnknown_64k_I nteractBackg_16k_64k_20_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_16k_64k_20_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To11 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_51b_UL_ 40_TC_UE), c_DL_CommTrChInfo_TM1 _AM1_0To19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_64 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_16k_64k_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_64 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_16k_64k_40 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpConvUnknown_64k_40TTI (p_CellId, tsc_RAB_DefCS, tcv_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_64kCS_RAB_SRB)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_SendRB_SetUp_ConvUnknown_64k_I nteractBackg_16k_64k_40_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			2.
6		+ ts_SetCellCfg (p_CellId, cell_Two_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain.					
2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_ConvUnknown_64k_InteractBackg_16k_64k_40_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_2 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM1 _AM1_0To11 (c_PowerOffsetInfoBelow6 4k) , c_UL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_51b_UL_ 40_TC_UE), c_DL_CommTrChInfo_TM1 _AM1_0To19_Order1, c_DL_AddReconfTransChIn foListAM_DCH2 (c_DCH_336_TFS_25_DL_2 0_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc16 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_64 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_3DCH_ModifyConvUnknown_64k_Inter actBackg_16k_64k_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd16) , cb_UL_DPCH_Info (tsc_Sf16, pl0_64 , tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_PS_Cfg (320) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_Two_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_2SCCPCH_32k (p_CellId: INTEGER; p_RAB_Id : BITSTRING) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARERcell_FACH_2SCCPCH_StandAlonePCH_PS and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM (
2		AM ! RLC_AM_DATA_REQ			
3		+ ts_RRC_Delay (80)			
4		+ts_CMAC_New_RNTI_Reconf_2sccpch (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI,tcv_TmpCellInfo.cRN TI)			
5		+ ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_FACH_2SCCPCH_StandAlonePCH_P S)	cas_RB_SetUpAM (
			tsc_CellDedicated, tsc_RB2, cbs_108_RB_SetUpFACH_ PS (
			tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, tcv_TmpCellInfo.cRNTI)		

Detailed Comments :

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_2SCCPCH_32k_2a (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING;p_ActTime: ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARERcell_FACH_2SCCPCH_StandAlonePCH_PS and to reconfigure the SS accordingly.					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cs_RB_SetUpFACH_PS_2 SCCPCH_2a (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, p_RAB_Id2, tcv_TmpCellInfo.cRNTI))		
2		(tcv_TmpCellInfo.cRNTI := '1010101010101010'B)			
3		+ ts_SaveCellInfo (p_CellId)			
4		AM ! RLC_AM_DATA_REQ			
5		+ ts_RRC_Delay (80)			
6		+ts_CMAC_New_RNTI_Reconf_2SCCPCH_2a (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI,tcv_TmpCellInfo.cRNTI)			
7		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_3SCCPCH_32k (p_CellId: INTEGER; p_RAB_Id : BITSTRING;p_ActTime: ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARER cell_FACH_3_SCCPCH_4_FACH .					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP1	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cbs_108_RB_SetUpFACH_PS (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, tcv_TmpCellInfo.cRNTI))		
2		(tcv_TmpCellInfo.cRNTI := '1010101010101010'B)			
3		+ ts_SaveCellInfo (p_CellId)			
4		AM ! RLC_AM_DATA_REQ			
5		+ ts_RRC_Delay (80)			
6		+ts_CMAC_New_RNTI_Reconf_3SCCPCH (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI)			
7		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , tcv_TmpCellInfo.cellConfig)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_3SCCPCH_32k_2a (p_CellId: INTEGER; p_RAB_Id , p_RAB_Id2: BITSTRING;p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_FACH_3_SCCPCH_4_FACH . Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		(tcv_TmpCellInfo.cRNTI := '1010101010101010'B)			
3		+ ts_SaveCellInfo (p_CellId)			
4		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cs_RB_SetUpFACH_PS_2 SCCPCH_2a (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, p_RAB_Id2, tcv_TmpCellInfo.cRNTI))		
5		+ ts_RRC_Delay (80)			
6		+ts_CMAC_New_RNTI_Reconf_3SCCPCH_2a (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI)			
7	TSP1	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , tcv_TmpCellInfo.cellConfig)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_1SCCPCH_32k (p_CellId: INTEGER; p_RAB_Id : BITSTRING;p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_RRC_Delay (80)			
3		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cbs_108_RB_SetUpFACH_PS (
			tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, tcv_TmpCellInfo.cRNTI		
))		
4		+ ts_RRC_Delay (80)			
5		+ts_CMAC_New_RNTI_Reconf (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI,tcv_TmpCellInfo.cRNTI)			after the RAB setup message SS reconfiguration to use the new CRNTI.
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_FACH_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_3SCCPCH_32k_1_2a (p_CellId: INTEGER; p_RAB_Id ,p_RAB_Id2: BITSTRING;p_ActTime: ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARER cell_FACH_3_SCCPCH_3_FACH_CTCH and to reconfigure the SS accordingly.					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cs_RB_SetUpFACH_PS_2 SCCPCH_2a (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, p_RAB_Id2, tcv_TmpCellInfo.cRNTI))		
2		(tcv_TmpCellInfo.cRNTI := '1010101010101010'B)			
3		+ ts_SaveCellInfo (p_CellId)			
4		AM ! RLC_AM_DATA_REQ			
5		+ ts_RRC_Delay (80)			
6		+ts_CMAC_New_RNTI_Reconf_3SCCPCH_CTCH_2a (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI)			
7		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUp_FACH_3SCCPCH_32k_1 (p_CellId: INTEGER; p_RAB_Id : BITSTRING;p_ActTime: ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective : To setup a RADIO BEARER cell_FACH_3_SCCPCH_3_FACH_CTCH and to reconfigure the SS accordingly.					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP	+ ts_SetTmpCellInfo (p_CellId)	cas_RB_SetUpAM (tsc_CellDedicated, tsc_RB2, cbs_108_RB_SetUpFACH_PS (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_RAB_Id, tcv_TmpCellInfo.cRNTI))		cell_FACH_3_SCCPCH need to be used
2		(tcv_TmpCellInfo.cRNTI := '1010101010101010'B)			
3		+ ts_SaveCellInfo (p_CellId)			
4		AM ! RLC_AM_DATA_REQ			
5		+ ts_RRC_Delay (80)			
6		+ts_CMAC_New_RNTI_Reconf_3SCCPCH_CTCH (FALSE, p_CellId, tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI)			
7		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_FACH_3_SCCPCH_3_FACH_CTCH)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32kPS_20_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER for Interactive / Background 32k with TTI 20 and to reconfigure the SS accordingly Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To5(c_PowerOffsetInfoBelow64k) , c_UL_AddReconfTransChInfoListAM1 (c_DCH_336_3_TFS_UE), c_DL_CommonTransChInfo_AM0To5, c_DL_AddReconfTransChInfoListAM_3_4k(c_DCH_336_3_TFS_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc64 , OMIT), c_DL_CommonInformationRB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_P S_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_PS_Cfg (320) + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_12_2k_AMR_ConvUnknown_64k_20TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_TM4 _0To239(c_PowerOffsetInf oHigher64k), c_UL_AddReconfTransChIn foListTM4(c_DCH_81_TFS _UE_6, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_640_TFS_20_2_U E), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM4 (c_DCH_81_TFS_UE_6_D L, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_640_TFS_20_2_U E), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifySpeech_12_2k_AMR_ConvUnknown_64k_20(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_RB10_To_13_TM_CS_Config(81, 103, 60, 640)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpSpeech_12_2k_AMR_ConvUnknown_64k_40TTI (p_CellId: INTEGER; p_RAB_Id, p_RAB_Id2 : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListTM4(c_ReEstTimerT314, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_TM4 _0To239(c_PowerOffsetInf oHigher64k), c_UL_AddReconfTransChIn foListTM4(c_DCH_81_TFS _UE_6, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_640_TFS_40_4_U E), c_DL_CommonTransChInfo SameAsUL, c_DL_AddReconfTransChIn foListTM4 (c_DCH_81_TFS_UE_6_D L, c_DCH_103_TFS_UE_5, c_DCH_60_TFS_UE, c_DCH_640_TFS_40_4_U E), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifySpeech_12_2k_AMR_ConvUnknown_64k_40(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_72, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_RB10_To_13_TM_CS_Config(81, 103, 60, 640)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_InteractBackg_32k_TC_40TTI (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments : Conversational / speech / UL 12.2 DL 12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			1.
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			2.
5		+ts_SendRB_SetUpConvSpeech_12_2k_In teractBackg_32k_TC_40TTI_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpConvSpeech_12_2k_InteractBackg_32k_TC_40TTI_CS_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119(c_PowerOff setInfoBelow64k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_4 _UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_336_TFS_40_TC_4 _UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl1 , tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_5DCH_ModifyConvSpeech_12_2k_Inter actBackg_32k_32k_TC_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl1 , tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_DCH_8k_PS_40_TC (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM2_No_ PdcP (c_ReEstTimerT315, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfoAM0T o3(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_340_TFS_40_TC_ UE), c_DL_CommonTransChInfo _AM_0_3, c_DL_AddReconfTransChIn foListAM_3_4k (c_DCH_340_TFS_40_TC_ UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc128, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl1, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractiveBackGroun d_8k_PS_40 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd128_4) , cb_UL_DPCH_Info (tsc_Sf64, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_22_AM_Cfg			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_2AM_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_DCH_64k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : RB Setup for Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM2_No_P dcp_sdu4 (useT315, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoHigh er64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_340_TFS_20_TC_ UE), c_DL_CommTrChInfo_AM_ 0To9, c_DL_AddReconfTransChIn foListAM_3_4k_RM192 (c_DCH_340_TFS_20_TC_ UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_92, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractiveBackGroun d_64k_PS (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (
5		tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_92, tcv_TmpCellInfo.uL_ScramblingCode))			
6		+ts_SS_RB20_AM_22_AM_Cfg + ts_RRC_ReceiveRB_SetupCmpl (
		p_CellId , cell_DCH_2AM_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_DCH_8k_8k_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_RAB_Id2 : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM2_PS_ No_Pdcp (useT315, p_RAB_Id, p_RAB_Id2), c_UL_CommTrChInfo_AM0 To7(c_PowerOffsetInfoBelow64k) , c_UL_AddReconfTransChInfoListAM2 (c_DCH_336_TFS_UL_20_TC_UE, c_DCH_336_TFS_38b_UL_40_TC_UE), c_DL_CommonTransChInfo_AM_0_15, c_DL_AddReconfTransChInfoListAM2 (c_DCH_656_TFS_DL_40_TC_UE, c_DCH_336_TFS_38b_DL_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCode, tsc_Sfc32, OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.ul_ScramblingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_3DCH_ModifyInteractiveBackGroun d_8k_8k_PS (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf32, pl1, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_22_AM_Cfg_640			
6		+ ts_RRC_ReceiveRB_SetupCmpl(p_CellId , cell_DCH_2_PS_Call)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_ConvSpeech_12_2k_InteractiveBackground_64k_64k_TC_20 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime)					
Group : RB_Steps/RB_Setup/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_SendRB_SetUpDCH_Speech (p_CellId, tsc_RAB_DefCS, p_ActTime)			1.
3		+ ts_SetCellCfg (p_CellId, cell_DCH_Speech)			
4		+ ts_CalculateActTime (p_CellId)			
5		+ts_RB_SendRB_SetUp_ConvSpeech_12_2k_InteractiveBackground_64k_64k_TC_20_CS_PS (p_CellId, p_RAB_Id, p_ActTime)			3.
6		+ ts_SetCellCfg (p_CellId, cell_Four_DTCH_CS_PS)			cell_DC H_Speech_2_P S
Detailed Comments : 1. RB Setup procedure for CS domain. 2.RB Setup procedure for PS domain.(Tranport and Physical channel Information for combined CS and PS domains is provided here.)					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SendRB_SetUp_ConvSpeech_12_2k_InteractiveBackground_64k_64k_TC_20_CS_PS(p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : RB_Steps/RB_Setup/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListAM_DCH_4 _No_Pdcp (c_ReEstTimerT315, p_RAB_Id), c_UL_CommTrChInfo_TM3 _AM1_0To119 (c_PowerOffsetInfoHigher6 4k) , c_UL_AddReconfTransChIn foListAM_DCH4 (c_DCH_340_TFS_20_TC_ UE), c_DL_CommonTransChInfo _TM3_AM1_0_119, c_DL_AddReconfTransChIn foListAM_DCH4 (c_DCH_340_TFS_20_TC_ UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc32 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76 , tcv_TmpCellInfo.ul_Scrambl ingCode) , OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3	TSP	AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		cell_DC H_Spee ch_2_P S
4		+ts_5DCH_ModifyConvSpeech_12_2k_Inter activeBackground_64k_64k_TC_20 (p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd32) , cb_UL_DPCH_Info (tsc_Sf16, pl0_76, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB10_ToRB12_TM_RB20_RB22_ AM_Cfg			
6		+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_Four_DTCH_CS_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_SendRB_SetUpDCH_DSCH_64k_384k_10TTI_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_DSCH_64k_384k_RAB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CPHY_ActTime (p_CellId, tsc_DL_PDSCH1, 1)			
3		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp_DSCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_DSCH_64k (useT315, p_RAB_Id, c_RLC_InfoAM_Def), c_UL_CommTrChInfoDCH_PS_64k, c_UL_AddReconfTransChInfoListDCH_PS_64k, c_DL_CommonTransChInfoDCH (c_TFCS_DCH_0_To_1_DSCH_0_To_5_Rx), c_DL_AddReconfTransChInfoList_DCH_DSCH (c_DSCH_354_TFS_10_TC_UE), c_DL_InformationPerRL_DSCH (c_pdsch_CodeMapping_0To5 (tcv_TmpCellInfo.priScrmCode), tcv_TmpCellInfo.priScrmCode , tsc_DL_DPCH1_ChC_64k_PS, tsc_DL_DPCH1_2ndScrC) ,		cas_RB_SetUpA_M_WithCnf(tsc_CellDedicated, tsc_RB2, tsc_Mui, cbs_108_RB_SetUp64k_384k_10TTI_PS (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, p_RAB_Id, tcv_TmpCellInfo.priScrmCode, tcv_TmpCellInfo_dsch.uL_ScramblingCode))

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		AM ? RLC_AM_DATA_CNF	c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_256k_PS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_PS, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode), OMIT, c_DL_PDSCH_Information (c_pdsch_CodeMapping_0To5 (tcv_TmpCellInfo.priScrmCode)), px_DSCH_RNTI)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
5		+ts_SS_PDSCH_Cfg (p_CellId, c_pdsch_CodeMapping_0To5 (tcv_TmpCellInfo.priScrmCode), c_DSCH_TFS_Info (c_DSCH_354_TFS_10_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_5_Tx (c_PowerOffsetInfoHigher64k)), c_TrChInfoDSCH (c_DSCH_354_TFS_10_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_5_Tx (c_PowerOffsetInfoHigher64k)), c_TrLogMappingDSCH)			
6		+ts_SS_Modify_DPCH_AssociatedWith_DSCH (p_CellId, cb_DL_DPCH_256K_PS (c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4), tsc_Sfc256), c_TFCS_DCH_0_To_1_DSCH_0_To_5_Tx (c_PowerOffsetInfoHigher64k))			
7		+ts_SS_RB20_AM_DSCH_Cfg (320)			payload= RLC payload + RLC header
8	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_DSCH_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_SendRB_SetUpDCH_DSCH_64k_384k_20TTI_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_DSCH_64k_384k_RAB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CPHY_ActTime (p_CellId, tsc_DL_PDSCH1, 2)			
3		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp_DSCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_DSCH_64k (useT315, p_RAB_Id, c_RLC_InfoAM_Def), c_UL_CommTrChInfoDCH_PS_64k, c_UL_AddReconfTransChInfoListDCH_PS_64k, c_DL_CommonTransChInfoDCH (c_TFCS_DCH_0_To_1_DSCH_0_To_8_Rx), c_DL_AddReconfTransChInfoList_DCH_DSCH (c_DSCH_354_TFS_20_TC_UE), c_DL_InformationPerRL_DSCH (c_pdsch_CodeMapping_0To8 (tcv_TmpCellInfo.priScrmCode), tcv_TmpCellInfo.priScrmCode , tsc_DL_DPCH1_ChC_64k_PS, tsc_DL_DPCH1_2ndScrC)		cas_RB_SetUpA_M_WithCnf(tsc_CellDedicated, tsc_RB2, tsc_Mui, cbs_108_RB_SetUp64k_384k_20TTI_PS (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, p_RAB_Id, tcv_TmpCellInfo.priScrmCode, tcv_TmpCellInfo_dsch.uL_ScramblingCode)

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		AM ? RLC_AM_DATA_CNF	c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_256k_PS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_PS, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode), OMIT, c_DL_PDSCH_Information (c_pdsch_CodeMapping_0To8 (tcv_TmpCellInfo.priScrmCode)) , px_DSCH_RNTI)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
5		+ts_SS_PDSCH_Cfg (p_CellId, c_pdsch_CodeMapping_0To8 (tcv_TmpCellInfo.priScrmCode), c_DSCH_TFS_Info (c_DSCH_354_TFS_20_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_8_Tx (c_PowerOffsetInfoHigher64k)), c_TrChInfoDSCH (c_DSCH_354_TFS_20_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_8_Tx (c_PowerOffsetInfoHigher64k)), c_TrLogMappingDSCH)			
6		+ts_SS_Modify_DPCH_AssociatedWith_DSCH (p_CellId, cb_DL_DPCH_256K_PS (c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4) , tsc_Sfc256) , c_TFCS_DCH_0_To_1_DSCH_0_To_8_Tx (c_PowerOffsetInfoHigher64k))			
7		+ts_SS_RB20_AM_DSCH_Cfg (320)			payload= RLC payload + RLC header
8	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_DSCH_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_SendRB_SetUpDCH_DSCH_64k_2048k_10TTI_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_DSCH_64k_384k_RAB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CPHY_ActTime (p_CellId, tsc_DL_PDSCH1, 1)			
3		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp_DSCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_DSCH_64k (useT315, p_RAB_Id, c_RLC_InfoAM_Def), c_UL_CommTrChInfoDCH_PS_64k, c_UL_AddReconfTransChInfoListDCH_PS_64k, c_DL_CommonTransChInfoDCH (c_TFCS_DCH_0_To_1_DSCH_0_To_10_Rx), c_DL_AddReconfTransChInfoList_DCH_DSCH (c_DSCH_674_TFS_10_TC_UE), c_DL_InformationPerRL_DSCH (c_pdsch_CodeMapping_0To10 (tcv_TmpCellInfo.priScrmCode), tcv_TmpCellInfo.priScrmCode , tsc_DL_DPCH1_ChC_64k_PS, tsc_DL_DPCH1_2ndScrC)		cas_RB_SetUpA_M_WithCnf(tsc_CellDedicated, tsc_RB2, tsc_Mui, cbs_108_RB_SetUp64k_2048k_10TTI_PS (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, p_RAB_Id, tcv_TmpCellInfo.priScrmCode, tcv_TmpCellInfo_dsch.uL_ScramblingCode)

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		AM ? RLC_AM_DATA_CNF	c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_256k_PS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_PS, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode), OMIT, c_DL_PDSCH_Information (c_pdsch_CodeMapping_0To10 (tcv_TmpCellInfo.priScrmCode)), px_DSCH_RNTI)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
5		+ts_SS_PDSCH_Cfg (p_CellId, c_pdsch_CodeMapping_0To10 (tcv_TmpCellInfo.priScrmCode), c_DSCH_TFS_Info (c_DSCH_674_TFS_10_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_10_Tx (c_PowerOffsetInfoHigher64k)), c_TrChInfoDSCH (c_DSCH_674_TFS_10_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_10_Tx (c_PowerOffsetInfoHigher64k)), c_TrLogMappingDSCH)			+ts_SS_PDSCH_2048k_10TTI_Cfg (p_CellId)
6		+ts_SS_Modify_DPCH_AssociatedWith_DSCH (p_CellId, cb_DL_DPCH_256K_PS (c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4), tsc_Sfc256), c_TFCS_DCH_0_To_1_DSCH_0_To_10_Tx (c_PowerOffsetInfoHigher64k))			+ts_SS_DPCH_2048k_10TTI_Modify (p_CellId)
7		+ts_SS_RB20_AM_DSCH_Cfg (320)			payload= RLC payload + RLC header
8	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_DSCH_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_SendRB_SetUpDCH_DSCH_64k_2048k_20TTI_PS (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime: ActivationTime) Group : RB_Steps/RB_Setup/ Objective : To setup a RADIO BEARER cell_DSCH_64k_384k_RAB and to reconfigure the SS accordingly. Default : RRC_Def1 Comments : This Step is used by RLC test cases. See TS 34.108 clause 6.10.2.4.1.26 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CPHY_ActTime (p_CellId, tsc_DL_PDSCH1, 2)			
3		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp_DSCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, c_RAB_InfoListDCH_PS_DSCH_64k (useT315, p_RAB_Id, c_RLC_InfoAM_Def), c_UL_CommTrChInfoDCH_PS_64k, c_UL_AddReconfTransChInfoListDCH_PS_64k, c_DL_CommonTransChInfoDCH (c_TFCS_DCH_0_To_1_DSCH_0_To_18_Rx), c_DL_AddReconfTransChInfoList_DCH_DSCH (c_DSCH_674_TFS_20_TC_UE), c_DL_InformationPerRL_DSCH (c_pdsch_CodeMapping_0To18 (tcv_TmpCellInfo.priScrmCode), tcv_TmpCellInfo.priScrmCode , tsc_DL_DPCH1_ChC_64k_PS, tsc_DL_DPCH1_2ndScrC)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		AM ? RLC_AM_DATA_CNF	c_DL_CommonInformation RB_SetUp (tsc_DL_DPCH1_SFP_256k_PS), cb_UL_DPCH_Info (tsc_UL_DPDCH_SF_64k_PS, pl0_96, tcv_TmpCellInfo.uL_ScramblingCode), OMIT, c_DL_PDSCH_Information (c_pdsch_CodeMapping_0To18 (tcv_TmpCellInfo.priScrmCode)), px_DSCH_RNTI)) car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
5		+ts_SS_PDSCH_Cfg (p_CellId, c_pdsch_CodeMapping_0To10 (tcv_TmpCellInfo.priScrmCode), c_DSCH_TFS_Info (c_DSCH_674_TFS_20_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_18_Tx (c_PowerOffsetInfoHigher64k)), c_TrChInfoDSCH (c_DSCH_674_TFS_20_TC, c_TFCS_DCH_0_To_1_DSCH_0_To_18_Tx (c_PowerOffsetInfoHigher64k)), c_TrLogMappingDSCH)			
6		+ts_SS_Modify_DPCH_AssociatedWith_DSCH (p_CellId, cb_DL_DPCH_256K_PS (c_DL_CommonInformationRB_SetUp (tsc_Sfd256_4), tsc_Sfc256), c_TFCS_DCH_0_To_1_DSCH_0_To_18_Tx (c_PowerOffsetInfoHigher64k))			
7		+ts_SS_RB20_AM_DSCH_Cfg (320)			payload= RLC payload + RLC header
8	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_DSCH_PS)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_9_tc_14_2_58a(p_Data_String : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB20_Special_3 (c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_10_11, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 40,1)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB20_Special_3(c_ TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_2_10_12, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 40, 1)			Subtest 2 Steps 11–17
3		+ts_RB_SubTest_RAB_SRB_RB20_Special_3(c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_3_10_13, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1912,60), OMIT, OMIT, OMIT), 40, 1)			Subtest 3 Steps 11–17
4		+ts_RB_SubTest_RAB_SRB_RB20_Special_ 3(c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_4_10_14, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT, OMIT), 40, 1)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ ts_RB_SubTest_RAB_SRB_RB22 (c_TFC_Allowed_0_1_2_4_6, c_TFC_Allowed_0_5_10_15, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT, OMIT), 40)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB20_RB22_2_Special (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_5_6_10_16, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 632, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40,1)			Subtest 6 Steps 11–17
7		+ ts_RB_SubTest_RAB_SRB_RB20_RB22_2_Special (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_5_7_10_17, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 1272, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40, 1)			Subtest 7 Steps 11–17
8		+ ts_RB_SubTest_RAB_SRB_RB20_RB22_2_Special (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_5_8_10_18, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 1912, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40, 1)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB20_R B22_Special (c_TFC_Allowed_0_1_2_3_4_5_6_7 , c_TFC_Allowed_0_1_5_9_10_19, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 2552, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40, 1)			Subtest 9 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_TFC_Restriction_FACH (p_cellId:INTEGER;p_TFC_UL,p_TFC_DL : TFC_Subset) Group : RB_Steps/RB_Subtests/ Objective : to request SS to send the data on the RABs in the same frame. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)]			
2		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_S_CCPCH3, c_TFC_Restriction_DL(p_TFC_DL))		
3		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_S_CCPCH3)		
4		+It_UL_Restrict			
5		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)]			
6		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_S_CCPCH2, c_TFC_Restriction_DL(p_TFC_DL))		
7		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_S_CCPCH2)		
8		+It_UL_Restrict			
9		[tcv_TmpCellInfo.cellConfig = cell_FACH_PS]			
10		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_S_CCPCH1, c_TFC_Restriction_DL(p_TFC_DL))		
11		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_S_CCPCH1)		
12		+It_UL_Restrict			
13		[TRUE]		(F)	
		It_UL_Restrict			
14		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_PRACH1, c_TFC_Restriction_UL(p_TFC_UL))		
15		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_PRACH1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB20_FACH (p_Data : BITSTRING; p_TFC_UL,p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB20 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated , tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength))			
4		+ts_SS_TFC_Restriction_FACH (tsc_CellA, p_TFC_UL, p_TFC_DL)			
5		AM ! RLC_AM_TestDataReq START t_Dly	cas_RLC_AM_DataReq (tsc_CellDedicated , tsc_RB20, c_TrD_Data(tcv_RB_Data1))		Step 14
6		AM ? RLC_AM_TestDataInd CANCEL t_Dly	car_RLC_AM_DataInd (tsc_CellDedicated , tsc_RB20, c_TrD_Data(tcv_RB_Data1))	(P)	Step 15
7		+ ts_TC_OpenUE_TestLoop(tsc_CellDedicated)			Step 16-17
8		?TIMEOUT t_Dly		(F)	
9		+ ts_TC_OpenUE_TestLoop(tsc_CellA)			Step 16-17
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_RB_SubTest_RB24_FACH (p_Data : BITSTRING; p_TFC_UL,p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength : INTEGER)						
Group : RB_Steps/RB_Subtests/						
Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB20 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123–1 clause 14.1.1						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1		AM ! RLC_AM_DATA_REQ	cas_TranportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11	
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12–13	
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength))				
4		+ts_SS_TFC_Restriction_FACH (tsc_CellA, p_TFC_UL, p_TFC_DL)				
5		AM ! RLC_AM_TestDataReq START t_Dly	cas_RLC_AM_DataReq (tsc_CellDedicated, tsc_RB24, c_TrD_Data(tcv_RB_Data1))	(P)	Step 14	
6		AM ? RLC_AM_TestDataInd CANCEL t_Dly	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB24, c_TrD_Data(tcv_RB_Data1))		Step 15	
7		+ ts_TC_OpenUE_TestLoop(tsc_CellDedicated)			(F)	Step 16–17
8		?TIMEOUT t_Dly				
9		+ ts_TC_OpenUE_TestLoop(tsc_CellDedicated)		Step 16–17		
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_Exit_Testcase Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
2		+ ts_TC_DeactivateRB_TestMode (tsc_CellDedicated)			Steps 20-21
3		+ po_ConnectionAndSS_Rel (tsc_CellA)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_Prepare_DataToBeReceived (p_Data : BITSTRING; p_ULSDULength, p_DLTBSLength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : UE shall send back the same RLC SDU. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Len:=0,tcv_testdata2_len:=0)			
2		(tcv_RB_testdata1:="B, tcv_RB_testdata2 :="B, tcv_RB_testdata3 :="B)			
3		(tcv_Len := (p_ULSDULength – p_DLTBSLength))			
4		(tcv_RB_testdata1:=o_GetMostSignificantBits (p_Data, p_DLTBSLength))			
5		[tcv_Len >0]			
6		REPEAT It_Add UNTIL [tcv_Len<=p_DLTBSLength]			
7		(tcv_RB_testdata2:=o_BitstringConcat(tcv_RB_testdata2, o_GetMostSignificantBits (tcv_RB_testdata1, tcv_Len), tcv_testdata2_len, tcv_Len))			
8		(tcv_RB_testdata3 := o_BitstringConcat(tcv_RB_testdata1, tcv_RB_testdata2, p_DLTBSLength, (tcv_testdata2_len+tcv_Len)))			
9		[TRUE]			
10		[tcv_Len <=0]			
11		(tcv_RB_testdata3 := o_GetMostSignificantBits (tcv_RB_testdata1 , p_ULSDULength))			
12		[TRUE]			
		It_Add			
13		[tcv_Len>p_DLTBSLength]			
14		(tcv_RB_testdata2:=o_BitstringConcat(tcv_RB_testdata2, tcv_RB_testdata1, tcv_testdata2_len, p_DLTBSLength))			
15		(tcv_testdata2_len := tcv_testdata2_len +p_DLTBSLength)			
16		(tcv_Len :=tcv_Len – p_DLTBSLength)			
17		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrl (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize))			CMAC Restriction
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- -----
7		(tcv_max_Timer:=(p_max_tti * 12) + (p_max_tti/10))			
8		+ts_ReceiveData_RB10 (tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			12 times max tti among the RABs + 10% of max tti Step 14b
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB10(tcv_RB_Data1)			for TTCN Delay Step 15a.1
12		+ts_Simultaneous_Data_SRB_RB10(tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			the SS also checks the returned data during this time.
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 and SRB2 RLC SDUs. UE shall send back the same RLC SDU on the same 3 RBs and SRB2. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB10_RB11 (tcv_RB_Data1,tcv_RB_Data2,p_RAB _Tx_Info)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			
11		+ts_ReceiveFirstSDU_RB10_RB 11 (tcv_RB_Data1,tcv_RB_Data2)			
					for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_Simultaneous_Data_SRB_R B10_RB11(tcv_RB_Data1, tcv_RB_Data2, p_RAB_Tx_Info)			Step 16–17
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 and SRB2 RLC SDUs. UE shall send back the same RLC SDU on the same 3 RBs and SRB2. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize), tcv_RB_Data3 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[2].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB10_RB11_RB12 (tcv_RB_Data1,tcv_RB_Data2,tcv_R B_Data3,p_RAB_Tx_Info)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
10		[tcv_result=TRUE]			Step 14b

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB10_RB11_RB12 (tcv_RB_Data1,tcv_RB_Data2,tcv_RB_Data3)			for TTCN Delay Step 15a.1 the SS also checks the returned data during this time.
12		+ts_Simultaneous_Data_SRB_RB10_RB11_RB12 (tcv_RB_Data1,tcv_RB_Data2,tcv_RB_Data3,p_RAB_Tx_Info)			
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	Step 16–17
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB13 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 , RB12 and RB13 an RLC SDU. UE shall send back the same RLC SDU on the same 4 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12–13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize), tcv_RB_Data3 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[2].sduSize), tcv_RB_Data4 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[3].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB10_RB11_RB12_ RB13 (tcv_RB_Data1,tcv_RB_Data2,tcv_R B_Data3,tcv_RB_Data4,p_RAB_Tx_In fo ,p_ReceiveFactor)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB13(tcv_RB_Data1,tcv_RB_Data2,tcv_RB_Data3,tcv_RB_Data4)			for TTCN Delay Step 15a.1 the SS also checks the returned data during this time.
12		+ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB13 (tcv_RB_Data1,tcv_RB_Data2,tcv_RB_Data3,tcv_RB_Data4,p_RAB_Tx_Info,p_ReceiveFactor)			
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
14		[tcv_result=FALSE]		(I)	Step 16–17
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12–13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize), tcv_RB_Data3 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[2].sduSize), tcv_RB_Data4 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[3].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- -----
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB10_RB11_RB12_ RB20 (tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, tcv_RB_Data4, p_RAB_Tx_Info)			12 times max tti among the RABs + 10% of max tti
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB20 (tcv_RB_Data1,tcv_RB_Data2,tcv_RB_Data3,tcv_RB_Data4)			Step 16–17
12		+ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB20(tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, tcv_RB_Data4, p_RAB_Tx_Info)			
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20_Special (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize), tcv_RB_Data3 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[2].sduSize))			
4		+ts_RB_Prepare_DataToBeReceived(p_RAB_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_Setup_RB_IE4.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[3].sduSize)			
5		(tcv_RB_Data4 := tcv_RB_testdata3)			
6		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			Step 14a
7		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
8		[tcv_result=TRUE]			
9		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
					12 times max tti among the RABs + 10% of max tti

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		+ts_ReceiveData_RB10_RB11_RB12_RB20_Special (tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, tcv_RB_Data4, p_RAB_Tx_Info, p_ReceiveFactor)			Step 14b
11		+ts_SendDataInContinuousTTI(p_RAB_Tx_Info)			
12		[tcv_result=TRUE]			
13		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB20 (tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, tcv_RB_Data4)			for TTCN Delay Step 15a.1
14		+ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB20_Special(tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, tcv_RB_Data4, p_RAB_Tx_Info, p_ReceiveFactor)			
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
18		[tcv_result=FALSE]		(I)	
19		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB13 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti, p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, and RB13 an RLC SDU. UE shall send back the same RLC SDU on the same 2 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
8		+ts_ReceiveData_RB10_RB13 (tcv_RB_Data1,tcv_RB_Data2 ,p_RAB_Tx_Info ,p_ReceiveFactor)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			
11		+ts_ReceiveFirstSDU_RB10_RB13 (tcv_RB_Data1,tcv_RB_Data2)			
					for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_Simultaneous_Data_SRB_R B10_RB13 (tcv_RB_Data1,tcv_RB_Data2 ,p_RAB_Tx_Info, p_ReceiveFactor)			the SS also checks the returned data during this time. Step 16-17
13		+ ts_TC_OpenUE_TestLoop (
14		tsc_CellDedicated)			
15		[tcv_result=FALSE]		(I)	
16		+ ts_TC_OpenUE_TestLoop (
17		tsc_CellDedicated)			
		[tcv_result=FALSE]		(I)	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB20 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
8		+ts_ReceiveData_RB10_RB20 (tcv_RB_Data1,tcv_RB_Data2,p_RAB_Tx_Info)			12 times max tti among the RABs + 10% of max tti
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			
11		+ts_ReceiveFirstSDU_RB10_RB20 (tcv_RB_Data1,tcv_RB_Data2)			for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_Simultaneous_Data_SRB_R B10_RB20 (tcv_RB_Data1,tcv_RB_Data2, p_RAB_Tx_Info)			Step 16–17
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB20_Special (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		+ts_RB_Prepare_DataToBeReceived(p_RAB_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupR B_IE1.rLC_SDU_Size) , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize)			
4		(tcv_RB_Data1 := tcv_RB_testdata3)			
5		+ts_RB_Prepare_DataToBeReceived(p_RA B_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_Set upRB_IE4.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[1].sduSize)			
6		(tcv_RB_Data2 := tcv_RB_testdata3)			
7		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
8		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
9		[tcv_result=TRUE]			Timer Value ----- -----
10		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
11		+ts_ReceiveData_RB10_RB20_S pecial (tcv_RB_Data1,tcv_RB_Data2,p_ RAB_Tx_Info,p_ReceiveFactor)			12 times max tti among the RABs + 10% of max tti Step 14b

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
13		[tcv_result=TRUE]			
14		+ts_ReceiveFirstSDU_RB10 _RB20 (tcv_RB_Data1,tcv_RB_Da ta2)			for TTCN Delay Step 15a.1
15		+ts_Simultaneous_Data_S RB_RB10_RB20_Special (tcv_RB_Data1,tcv_RB_D ata2,p_RAB_Tx_Info,p_Re ceiveFactor)			
16		+ ts_TC_OpenUE_TestLoo p (tsc_CellDedicated)			Step 16-17
17		[tcv_result=FALSE]		(I)	
18		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
19		[tcv_result=FALSE]		(I)	
20		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB20_Special_2 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor:INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize))			
4		+ts_RB_Prepare_DataToBeReceived(p_RAB_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_Setup_RB_1E2.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[1].sduSize)			
5		(tcv_RB_Data2 := tcv_RB_testdata3)			
6		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			Step 14a
7		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
8		[tcv_result=TRUE]			
9		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
10		+ts_ReceiveData_RB10_RB20_Special (tcv_RB_Data1,tcv_RB_Data2,p_RAB_Tx_Info,p_ReceiveFactor)			
11		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
12		[tcv_result=TRUE]			12 times max tti among the RABs + 10% of max tti Step 14b

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ts_ReceiveFirstSDU_RB10_ RB20 (tcv_RB_Data1,tcv_RB_Data 2)			for TTCN Delay Step 15a.1
14		+ts_Simultaneous_Data_SR B_RB10_RB20_Special (tcv_RB_Data1,tcv_RB_Dat a2,p_RAB_Tx_Info,p_Recei veFactor)			
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
18		[tcv_result=FALSE]		(I)	
19		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments : Created based in ts_RB_SubTest_RAB_SRB_RB10_RB20_Special_1, ts_RB_SubTest_RAB_SRB_RB10_RB20_Special_2 but calutates the data expected in RB10 (tcv_RB_Data1) based in the test data size using "ts_RB_Prepare_DataToBeReceived" rather than through "o_GetMostSignificantBits " (1280 bits)					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB13 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti ,p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrl (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize))			CMAC Restriction
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB13 (tcv_RB_Data1 ,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu,p_ReceiveFactor)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB13(tcv_RB_Data1)			for TTCN Delay Step 15a.1
12		+ts_Simultaneous_Data_SRB_RB13(tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu,p_ReceiveFactor)			the SS also checks the returned data during this time.
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16–17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize))			CMAC Restriction
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- -----
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB20 (tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			12 times max tti among the RABs + 10% of max tti Step 14b
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB20(tcv_RB_Data1)			for TTCN Delay Step 15a.1
12		+ts_Simultaneous_Data_SRB_RB20(tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			the SS also checks the returned data during this time.
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20_Special (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		+ts_RB_Prepare_DataToBeReceived(p_RAB_T x_Info.testData,BIT_TO_INT(p_TestLoopMode Setup.IB_SetupRB_IE4.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[0].sduSize)			
4		(tcv_RB_Data1 := tcv_RB_testdata3)			
5		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			CMAC Restricti on
6		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
7		[tcv_result=TRUE]			
8		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
9		+ts_ReceiveData_RB20_Special (tcv_RB_Data1,p_RAB_Tx_Info.rbT xInfoList.[0].nomOfSdu,p_ReceiveFa ctor)			12 times max tti among the RABs + 10% of max tti
10		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
11		[tcv_result=TRUE]			
12		+ts_ReceiveFirstSDU_RB20(tc v_RB_Data1)			for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ts_Simultaneous_Data_SRB _RB20_Special(tcv_RB_Data 1,p_RAB_Tx_Info.rbTxInfoLis t.[0].nomOfSdu,p_ReceiveFac tor)			
14		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
15		[tcv_result=FALSE]		(I)	
16		+ ts_TC_OpenUE_TestLoop (
17		tsc_CellDedicated)			
18		[tcv_result=FALSE]		(I)	
		+ ts_TC_OpenUE_TestLoop (
		tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20_Special_1 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12–13
3		+ts_RB_Prepare_DataToBeReceived(p_RAB_T x_Info.testData,BIT_TO_INT(p_TestLoopMode Setup.IB_SetupRB_IE2.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[0].sduSize)			
4		(tcv_RB_Data1 := tcv_RB_testdata3)			
5		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			CMAC Restricti on
6		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
7		[tcv_result=TRUE]			
8		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
9		+ts_ReceiveData_RB20_Special (tcv_RB_Data1,p_RAB_Tx_Info.rbT xInfoList.[0].nomOfSdu,p_ReceiveFa ctor)			12 times max tti among the RABs + 10% of max tti
10		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
11		[tcv_result=TRUE]			
12		+ts_ReceiveFirstSDU_RB20(tc v_RB_Data1)			for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ts_Simultaneous_Data_SRB _RB20_Special(tcv_RB_Data 1,p_RAB_Tx_Info.rbTxInfoLis t.[0].nomOfSdu,p_ReceiveFac tor)			
14		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
15		[tcv_result=FALSE]		(I)	
16		+ ts_TC_OpenUE_TestLoop (
17		tsc_CellDedicated)			
18		[tcv_result=FALSE]		(I)	
		+ ts_TC_OpenUE_TestLoop (
		tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20_Special_3 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		+ts_RB_Prepare_DataToBeReceived(p_RAB_T x_Info.testData,BIT_TO_INT(p_TestLoopMode Setup.IB_SetupRB_IE1.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[0].sduSize)			
4		(tcv_RB_Data1 := tcv_RB_testdata3)			
5		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			CMAC Restricti on
6		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
7		[tcv_result=TRUE]			
8		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			Timer Value ----- -----
9		+ts_ReceiveData_RB20_Special (tcv_RB_Data1,p_RAB_Tx_Info.rbT xInfoList.[0].nomOfSdu,p_ReceiveFa ctor)			12 times max tti among the RABs + 10% of max tti
10		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
11		[tcv_result=TRUE]			
12		+ts_ReceiveFirstSDU_RB20(tc v_RB_Data1)			for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ts_Simultaneous_Data_SRB _RB20_Special(tcv_RB_Data 1,p_RAB_Tx_Info.rbTxInfoLis t.[0].nomOfSdu,p_ReceiveFac tor)			
14		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
15		[tcv_result=FALSE]		(I)	
16		+ ts_TC_OpenUE_TestLoop (
17		tsc_CellDedicated)			
18		[tcv_result=FALSE]		(I)	
		+ ts_TC_OpenUE_TestLoop (
		tsc_CellDedicated)			
Detailed Comments : An analogous test step based on ts_RB_SubTest_RAB_SRB_RB20_Special" which handles two RAB instead of 4.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB10 (p_Data : BITSTRING; p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC triAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))	(P)	Step 14
5		TM ! RLC_TR_TestDataReq			Step 15
6		TM ? RLC_TR_TestDataInd			
7		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB10_RB11 (p_Data : BITSTRING; p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength1, p_DataLength2 : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 and RB11 an RLC SDU. UE shall send back the same RLC SDU on the same 2 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))	(F)	Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength1) , tcv_RB_Data2 := o_GetMostSignificantBits (p_Data , p_DataLength2))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_Bind_TestData_TTI (tsc_CellDedicated, 2)			
6		[tcv_Bind_TestData = TRUE]			
7		+lt_SendAndReceiveData			
8		[tcv_Bind_TestData = FALSE]			
9		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
		lt_SendAndReceiveData			
10		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 14
11		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB11, c_TrD_Data (tcv_RB_Data2))		Step 14
12	TBP1 1	TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 15
13	TBP1 2	TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (tcv_RB_Data2))	(P)	Step 15
14		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15	TBP2 1	TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (tcv_RB_Data2))	(P)	Step 15
16	TBP2 2	TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 15
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB10_RB11_RB12 (p_Data : BITSTRING; p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength1, p_DataLength2, p_DataLength3 : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))	(F)	Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data, p_DataLength1), tcv_RB_Data2 := o_GetMostSignificantBits (p_Data, p_DataLength2), tcv_RB_Data3 := o_GetMostSignificantBits (p_Data, p_DataLength3))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_Bind_TestData_TTI (tsc_CellDedicated, 3)			Step 16-17
6		[tcv_Bind_TestData = TRUE]			
7		+It_SendAndReceiveData			
8		[tcv_Bind_TestData = FALSE]			
9		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
		It_SendAndReceiveData			
10		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 14
11		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB11, c_TrD_Data (tcv_RB_Data2))		Step 14
12		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB12, c_TrD_Data (tcv_RB_Data3))		Step 14
13		(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_ReceiveRB12 := FALSE, tcv_Res := FALSE)			
14		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
16		lt_Receive3			
16		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 15
17		[tcv_ReceiveRB10 = FALSE]			
18	TBP1	(tcv_ReceiveRB10 := TRUE)		(P)	
19		+ lt_CheckRes			
20		[tcv_ReceiveRB10 = TRUE]			
21	TBF1	(tcv_Res := TRUE)		(F)	
22		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (tcv_RB_Data2))		Step 15
23		[tcv_ReceiveRB11 = FALSE]			
24	TBP2	(tcv_ReceiveRB11 := TRUE)		(P)	
25		+ lt_CheckRes			
26		[tcv_ReceiveRB11 = TRUE]			
27	TBF2	(tcv_Res := TRUE)		(F)	
28		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (tcv_RB_Data3))		Step 15
29		[tcv_ReceiveRB12 = FALSE]			
30	TBP3	(tcv_ReceiveRB12 := TRUE)		(P)	
31		+ lt_CheckRes			
32		[tcv_ReceiveRB12 = TRUE]			
33	TBF3	(tcv_Res := TRUE)		(F)	
34		lt_CheckRes			
34		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE) AND (tcv_ReceiveRB12 = TRUE)]			
35		(tcv_Res := TRUE)			
36		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB10_TM_CS (p_Data : BITSTRING; p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength, p_NoOfSDU : INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		TM ! RLC_TR_TestDataReq	cas_RLC_DataReq (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_Data1))		Step 14
6		[p_NoOfSDU = 2]			
7		+ts_Receive2SDUsAccrossTTI(tcv_RB_Data1,p_DataLength, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupRB_IE1.rLC_SDU_Size))			
8		+ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
9		[p_NoOfSDU = 3]			
10		+ts_Receive3SDUsAccrossTTI(tcv_RB_Data1,p_DataLength, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupRB_IE1.rLC_SDU_Size))			
11		+ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
12		[p_NoOfSDU = 4]			
13		+ts_Receive4SDUsAccrossTTI(tcv_RB_Data1,p_DataLength, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupRB_IE1.rLC_SDU_Size))			
14		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB20 (p_Data : BITSTRING; p_TFC_UL,p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_DataLength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB20 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_Data , p_DataLength))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		AM ! RLC_AM_TestDataReq START t_Dly	cas_RLC_AM_DataReq (tsc_CellDedicated, tsc_RB20, c_TrD_Data(tcv_RB_Data1))		Step 14
6		AM ? RLC_AM_TestDataInd CANCEL t_Dly	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(tcv_RB_Data1))	(P)	Step 15
7		+ ts_TC_OpenUE_TestLoop(tsc_CellDedicated)			Step 16-17
8		?TIMEOUT t_Dly		(F)	
9		+ ts_TC_OpenUE_TestLoop(tsc_CellDedicated)			Step 16-17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RB20_AM (p_Data : BITSTRING; p_TFC_UL,p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_ULSDULength, p_DLSDULength : INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB20 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123–1 clause 14.1.1					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TBP1	AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))	(P) <	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		(tcv_RB_testdata2:=o_BitstringConcat(tcv_RB_testdata2, o_GetMostSignificantBits(tcv_RB_testdata1, tcv_Len), tcv_testdata2_len, tcv_Len))			
16		(tcv_RB_testdata3 := o_BitstringConcat(tcv_RB_testdata1, tcv_RB_testdata2, p_DLSDULength, (tcv_testdata2_len+tcv_Len)))			
17		[tcv_Len <=0]			
18		(tcv_RB_testdata3 := o_GetMostSignificantBits(tcv_RB_Data1 , p_ULSDULength))			
19		It_Add			
20		[tcv_Len>p_DLSDULength]			
21		(tcv_RB_testdata2:=o_BitstringConcat(tcv_RB_testdata2, tcv_RB_testdata1, tcv_testdata2_len, p_DLSDULength))			
22		(tcv_testdata2_len := tcv_testdata2_len +p_DLSDULength)			
23		(tcv_Len :=tcv_Len – p_DLSDULength)			
		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTests_TC_14_2_41(p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_15_16, c_TFC_Allowed_0_1_15_16, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (c_TFC_Allowed_0_1_2_3_15_17, c_TFC_Allowed_0_2_15_17, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_15_18, c_TFC_Allowed_0_3_15_18, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11–17
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_15_16_18_19, c_TFC_Allowed_0_3_4_15_19, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT), 20)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_15_17_18_20 , c_TFC_Allowed_0_3_5_15_20, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,60)), 20)			Subtest 5 Steps 11-17
6		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_6_15_21, c_TFC_Allowed_0_3_6_15_21, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 20)			Subtest 6 Steps 11-17
7		+ts_RB_SubTest_RAB_SRB_RB10_RB 20 (c_TFC_Allowed_0_1_2_3_6_7_15_16_ 21_22, c_TFC_Allowed_0_3_7_15_22, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT), 20)			Subtest 7 Steps 11-17
8		+ts_RB_SubTest_RAB_SRB_RB10_R B11_RB12_RB20(c_TFC_Allowed_0_1_2_3_6_8_15_17_ 21_23, c_TFC_Allowed_0_3_8_15_23, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,632,60)), 20)			Subtest 8 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB20_S pecial (c_TFC_Allowed_0_1_2_3_9_15_24 , c_TFC_Allowed_0_3_9_15_24, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 9 Steps 11–17
10		+ts_RB_SubTest_RAB_SRB_RB10 _RB20_Special (c_TFC_Allowed_0_1_2_3_9_10_1 5_16_24_25, c_TFC_Allowed_0_3_10_15_25, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT), 20, 1)			Subtest 10 Steps 11–17
11		+ts_RB_SubTest_RAB_SRB_RB 10_RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_9_11_ 15_17_24_26, c_TFC_Allowed_0_3_11_15_26, cb_UE_TestLoopMode1LB_Setu p4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60) , c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,1272,6 0)), 20, 1)			Subtest 11 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ ts_RB_SubTest_RAB_SRB_RB 20_Special (c_TFC_Allowed_0_1_2_3_12_ 15_27, c_TFC_Allowed_0_3_12_15_2 7, cb_UE_TestLoopMode1LB_Set up4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 12 Steps 11-17
13		+ts_RB_SubTest_RAB_SRB_ RB10_RB20_Special (c_TFC_Allowed_0_1_2_3_12 _13_15_16_27_28, c_TFC_Allowed_0_3_13_15_ 28, cb_UE_TestLoopMode1LB_S etup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT), 20, 1)			Subtest 13 Steps 11-17
14		+ts_RB_SubTest_RAB_SRB _RB10_RB11_RB12_RB20 _Special(c_TFC_Allowed_0_1_2_3_1 2_14_15_17_27_29, c_TFC_Allowed_0_3_14_15 _29, cb_UE_TestLoopMode1LB_ Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,10 3,60), c_RB_Tx_Info (tsc_RB12,60,60),			Subtest 14 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
		c_RB_Tx_Info(tsc_RB20,25 52,60)), 20, 1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10 (p_data : BITSTRING;p_no_of_sdus: INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			
2		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_data))	(P)	
3		(tcv_Receive_RB10 := 1) CANCEL t_Dly			
4		REPEAT It_Receive UNTIL [tcv_Receive_RB10 = p_no_of_sdus]			
5		?TIMEOUT t_Dly		(I)	
6		+ts_Exit_Testcase			
7		It_Receive TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data))		
8		(tcv_Receive_RB10:=tcv_Receive_RB10+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11 (p_data1,p_data2: BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]			
4		It_Receive3 TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB11:= tcv_count_RB11 + 1)			
9		+ It_CheckRes			
10		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]			
11		(tcv_Res := TRUE)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11_RB12 (p_data1,p_data2,p_data3 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11_RB12 (p_data1,p_data2,p_data3)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]			
4		It_Receive3			
5		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
6		(tcv_count_RB10:= tcv_count_RB10 + 1)			
7		+ It_CheckRes			
8		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15
9		(tcv_count_RB11:= tcv_count_RB11 + 1)			
10		+ It_CheckRes			
11		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15
12		(tcv_count_RB12:= tcv_count_RB12 + 1)			
13		+ It_CheckRes			
14		It_CheckRes			
15		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu)]			
16		(tcv_Res := TRUE)			
17		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11_RB12_RB13 (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor : INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB13 (p_data1,p_data2,p_data3,p_data4)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
4		It_Receive4			
5		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step 15
6		(tcv_count_RB10:= tcv_count_RB10 + 1)	tsc_CellDedicated,		
7		+ It_CheckRes	tsc_RB10, c_TrD_Data (
8		TM ? RLC_TR_TestDataInd	p_data1))		
9		(tcv_count_RB11:= tcv_count_RB11 + 1)	car_RLC_DataInd (Step 15
10		+ It_CheckRes	tsc_CellDedicated,		
11		TM ? RLC_TR_TestDataInd	tsc_RB11, c_TrD_Data (
12		(tcv_count_RB12:= tcv_count_RB12 + 1)	p_data2))		
13		+ It_CheckRes	car_RLC_DataInd (Step 15
14		TM ? RLC_TR_TestDataInd	tsc_CellDedicated,tsc_RB12		
15		(tcv_count_RB13:= tcv_count_RB13 + 1)	, c_TrD_Data (p_data3))		
16		+ It_CheckRes	car_RLC_DataInd (Step 15
17		It_CheckRes	tsc_CellDedicated,tsc_RB13		
18		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB13 =(p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu * p_ReceiveFactor))]	car_RLC_DataInd (
17		(tcv_Res := TRUE)	tsc_CellDedicated,tsc_RB13		
18		[TRUE]	, c_TrD_Data (p_data4))		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11_RB12_RB20 (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB20 (p_data1,p_data2,p_data3,p_data4)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
4		It_Receive4			
4		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB11:= tcv_count_RB11 + 1)			
9		+ It_CheckRes			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15
11		(tcv_count_RB12:= tcv_count_RB12 + 1)			
12		+ It_CheckRes			
13		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data4))		Step 15
14		(tcv_count_RB20:= tcv_count_RB20 + 1)			
15		+ It_CheckRes			
16		It_CheckRes			
16		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu)]			
17		(tcv_Res := TRUE)			
18		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11_RB12_RB20_Special (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor : INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11_RB12_RB20 (p_data1,p_data2,p_data3,p_data4)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
4		It_Receive4			
4		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB11:= tcv_count_RB11 + 1)			
9		+ It_CheckRes			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15
11		(tcv_count_RB12:= tcv_count_RB12 + 1)			
12		+ It_CheckRes			
13		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data4))		Step 15
14		(tcv_count_RB20:= tcv_count_RB20 + 1)			
15		+ It_CheckRes			
16		It_CheckRes			
16		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB20 =(p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu * p_ReceiveFactor))]			
17		(tcv_Res := TRUE)			
18		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB13 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB13 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]			
4		It_Receive2 TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		TM? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB13:= tcv_count_RB13 + 1)			
9		+ It_CheckRes			
10		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB13 =(p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu*p_ReceiveFactor))]			
11		(tcv_Res := TRUE)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB20 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info:RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB20 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]			
4		It_Receive2 TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB20:= tcv_count_RB20 + 1)			
9		+ It_CheckRes			
10		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]			
11		(tcv_Res := TRUE)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB20_Special (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB20 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]			
4		It_Receive2 TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB20:= tcv_count_RB20 + 1)			
9		+ It_CheckRes			
10		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB20 =(p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu*p_ReceiveFactor))]			
11		(tcv_Res := TRUE)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB13 (p_data : BITSTRING;p_no_of_sdus,p_ReceiveFactor: INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			
2		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data(p_data))	(P)	
3		(tcv_Receive_RB13:= 1) CANCEL t_Dly			
4		REPEAT It_Receive UNTIL [tcv_Receive_RB13 = (p_no_of_sdus*p_ReceiveFactor)]			
5		?TIMEOUT t_Dly		(I)	
6		+ts_Exit_Testcase			
7		It_Receive TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data))		
8		(tcv_Receive_RB13:=tcv_Receive_RB13+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB20 (p_data : BITSTRING;p_no_of_sdus: INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			
2		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(p_data))	(P)	
3		(tcv_count_RB20 := 1) CANCEL t_Dly			
4		REPEAT It_Receive UNTIL [tcv_count_RB20 = p_no_of_sdus]			
5		?TIMEOUT t_Dly		(I)	
6		+ts_Exit_Testcase			
7		It_Receive AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data))		
8		(tcv_count_RB20:=tcv_count_RB20+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB20_Special (p_data : BITSTRING;p_no_of_sdus,p_ReceiveFactor: INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			
2		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(p_data))	(P)	
3		(tcv_count_RB20 := 1) CANCEL t_Dly			
4		REPEAT It_Receive UNTIL [tcv_count_RB20 = p_no_of_sdus*p_ReceiveFactor]			
5		?TIMEOUT t_Dly		(I)	
6		+ts_Exit_Testcase			
7		It_Receive AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data))		
8		(tcv_count_RB20:=tcv_count_RB20+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB10_RB11 (p_data1,p_data2: BITSTRING)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default :					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_ Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_Res := FALSE, tcv_count_RB10 := 0, tcv_count_RB11 := 0)	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15
2		START t_Dly(tcv_max_Timer)			
3		TM ? RLC_TR_TestDataInd			
4		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)			
5		+It_CheckStatus			
6		[tcv_Res = FALSE]			
7		-> Get_Data			
8		[tcv_Res = TRUE]			
9		CANCEL t_Dly			
10		TM ? RLC_TR_TestDataInd			
11		(tcv_count_RB11:= tcv_count_RB11 + 1,tcv_ReceiveRB11 := TRUE)			
12		+It_CheckStatus			
13		[tcv_Res = FALSE]			
14		-> Get_Data			
15		[tcv_Res =TRUE]			
16		CANCEL t_Dly			
17		? TIMEOUT t_Dly			
18		+ts_Exit_Testcase			
19		It_CheckStatus			
20		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE)]			
21		(tcv_Res := TRUE)			
21	[TRUE]				
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_ReceiveFirstSDU_RB10_RB11_RB12 (p_data1,p_data2,p_data3 : BITSTRING)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default :						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_ Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_ReceiveRB12 := FALSE, tcv_Res := FALSE, tcv_count_RB10 := 0, tcv_count_RB11 := 0, tcv_count_RB12 := 0)	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15	
2		START t_Dly(tcv_max_Timer)				
3		TM ? RLC_TR_TestDataInd				
4		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)				
5		+It_CheckStatus				
6		[tcv_Res = FALSE]				
7		-> Get_Data				
8		[tcv_Res = TRUE]				
9		CANCEL t_Dly				
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))	(P)	Step 15	
11		(tcv_count_RB11:= tcv_count_RB11 + 1, tcv_ReceiveRB11 := TRUE)				
12		+It_CheckStatus				
13		[tcv_Res = FALSE]				
14		-> Get_Data				
15		[tcv_Res =TRUE]				
16		CANCEL t_Dly				
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))	(P)	Step 15	
18		(tcv_count_RB12:= tcv_count_RB12 + 1, tcv_ReceiveRB12 := TRUE)				
19		+It_CheckStatus				
20		[tcv_Res = FALSE]				
21		-> Get_Data				
22		[tcv_Res =TRUE]				
23		CANCEL t_Dly				
24		? TIMEOUT t_Dly		(I)		
25		+ts_Exit_Testcase				
		It_CheckStatus				
26		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE) AND (tcv_ReceiveRB12 = TRUE)]				
27		(tcv_Res := TRUE)				
28	[TRUE]					
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_Receive2SDUsAccrossTTI (p_Data : BITSTRING; p_DataLength, p_ULSDULength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_ReceiveFirstSDU := FALSE, tcv_ReceiveSecondSDU := FALSE, tcv_Res := FALSE)			
2		(tcv_RB_testdata1:= o_BitstringXtract (p_Data, p_DataLength,p_ULSDULength,0))			Fisrt SDU MSB 576 bits of test data
3		(tcv_RB_testdata2:= o_BitstringXtract(p_Data,p_DataLength, p_ULSDULength,p_ULSDULength))			Third SDU LSB 576 bits of test data
4		START t_Dly((40*16) + (4*16))			12 TTI to stat Transmit ting + 4 TTI to actually tansmit + 10% tolerenc e
5	Get_ Data	TM ? RLC_TR_TestDataInd [(tcv_ReceiveFirstSDU = FALSE) AND (tcv_ReceiveSecondSDU = FALSE)]CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_Data))	(P)	Step 15 UE uses UL _TFC3 and could return data in the same TTI
6		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata1))	(P)	Step 15 Step 15 UE uses UL_TFC 1and UE couldn't return data in the same TTI
7		(tcv_ReceiveFirstSDU := TRUE)			
8		+lt_CheckStatus			
9		[tcv_Res = FALSE]			
10		-> Get_Data			
11		[tcv_Res = TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata2))	(P)	Step 15
13		TM ? RLC_TR_TestDataInd			
14		(tcv_ReceiveSecondSDU := TRUE)			
15		+lt_CheckStatus			
16		[tcv_Res = FALSE]			
17		-> Get_Data			
18		[tcv_Res =TRUE]			
19		CANCEL t_Dly			
20		? TIMEOUT t_Dly		(I)	
21		+ts_Exit_Testcase			
		lt_CheckStatus			
22		[(tcv_ReceiveFirstSDU = TRUE) AND (tcv_ReceiveSecondSDU = TRUE)]			
23		(tcv_Res := TRUE)			
24		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Receive3SDUsAccrossTTI (p_Data : BITSTRING; p_DataLength, p_ULSDULength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_ReceiveFirstSDU := FALSE, tcv_ReceiveSecondSDU := FALSE, tcv_ReceiveThirdSDU := FALSE, tcv_Res := FALSE)			
2		(tcv_RB_testdata1:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,0))			Fisrt SDU MSB 576 bits of test data
3		(tcv_RB_testdata2:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,p_ULSDULength))			Second SDU 576 bits between bit 577 and bit 1152 from MSB of test data
4		(tcv_RB_testdata3 := o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,(p_ULSDULength*2)))			Third SDU LSB 576 bits of test data
5		START t_Dly((40*16) + (4*16))			12 TTI to stat Transmit ting + 4 TTI to actually tansmit + 10% tolerenc e
6	Get_ Data	TM ? RLC_TR_TestDataInd [(tcv_ReceiveFirstSDU = FALSE) AND (tcv_ReceiveSecondSDU = FALSE)AND (tcv_ReceiveThirdSDU = FALSE)] CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_Data))	(P)	Step 15 UE uses UL _TFC3 and could return data in the same TTI

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata1))	(P)	Step 15 Step 15 UE uses UL_TFC 1and UE couldn't return data in the same TTI
8		(tcv_ReceiveFirstSDU := TRUE)			
9		+It_CheckStatus			
10		[tcv_Res = FALSE]			
11		-> Get_Data			
12		[tcv_Res = TRUE]			
13		CANCEL t_Dly			
14		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata2))	(P)	Step 15
15		(tcv_ReceiveSecondSDU := TRUE)			
16		+It_CheckStatus			
17		[tcv_Res = FALSE]			
18		-> Get_Data			
19		[tcv_Res =TRUE]			
20		CANCEL t_Dly			
21		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB10 , c_TrD_Data (tcv_RB_testdata3))	(P)	Step 15
22		(tcv_ReceiveThirdSDU := TRUE)			
23		+It_CheckStatus			
24		[tcv_Res = FALSE]			
25		-> Get_Data			
26		[tcv_Res =TRUE]			
27		CANCEL t_Dly			
28		? TIMEOUT t_Dly		(I)	
29		+ts_Exit_Testcase			
		It_CheckStatus			
30		[(tcv_ReceiveFirstSDU = TRUE) AND (tcv_ReceiveSecondSDU = TRUE) AND (tcv_ReceiveThirdSDU = TRUE)]			
31		(tcv_Res := TRUE)			
32		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Receive4SDUsAccrossTTI (p_Data : BITSTRING; p_DataLength, p_ULSDULength : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_ReceiveFirstSDU := FALSE, tcv_ReceiveSecondSDU := FALSE, tcv_ReceiveThirdSDU := FALSE, tcv_ReceiveFourthSDU := FALSE, tcv_Res := FALSE)			
2		(tcv_RB_testdata1:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,0))			Fisrt SDU MSB 576 bits of test data
3		(tcv_RB_testdata2:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,p_ULSDULength))			Second SDU 576 bits between bit 577 and bit 1152 from MSB of test data
4		(tcv_RB_testdata3:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,(p_ULSDULength*2)))			Third SDU 576 bits between bit 1153 and bit 1728 from MSB of test data
5		(tcv_RB_Data4:= o_BitstringXtract(p_Data, p_DataLength , p_ULSDULength,(p_ULSDULength*3)))			Fourth SDU LSB 576 bits of test data
6		START t_Dly((40*16) + (4*16))			12 TTI to stat Transmit ting + 4 TTI to actually tansmit + 10% tolerenc e

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7	Get_ Data	TM ? RLC_TR_TestDataInd [(tcv_ReceiveFirstSDU = FALSE) AND (tcv_ReceiveSecondSDU = FALSE)AND (tcv_ReceiveThirdSDU = FALSE)AND (tcv_ReceiveFourthSDU = FALSE)] CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_Data))	(P)	Step 15 UE uses UL _TFC4 and could return data in the same TTI
8		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata1))	(P)	Step 15 Step 15 UE uses UL_TFC 1and UE couldn't return data in the same TTI
9		(tcv_ReceiveFirstSDU := TRUE)			
10		+It_CheckStatus			
11		[tcv_Res = FALSE]			
12		-> Get_Data			
13		[tcv_Res = TRUE]			
14		CANCEL t_Dly			
15		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (tcv_RB_testdata2))	(P)	Step 15
16		(tcv_ReceiveSecondSDU := TRUE)			
17		+It_CheckStatus			
18		[tcv_Res = FALSE]			
19		-> Get_Data			
20		[tcv_Res =TRUE]			
21		CANCEL t_Dly			
22		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB10 , c_TrD_Data (tcv_RB_testdata3))	(P)	Step 15
23		(tcv_ReceiveThirdSDU := TRUE)			
24		+It_CheckStatus			
25		[tcv_Res = FALSE]			
26		-> Get_Data			
27		[tcv_Res =TRUE]			
28		CANCEL t_Dly			
29		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB10 , c_TrD_Data (tcv_RB_Data4))	(P)	Step 15
30		(tcv_ReceiveFourthSDU := TRUE)			
31		+It_CheckStatus			
32		[tcv_Res = FALSE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
33		→ Get_Data			
34		[tcv_Res =TRUE]			
35		CANCEL t_Dly			
36		? TIMEOUT t_Dly			
37		+ts_Exit_Testcase			
		It_CheckStatus			
38		[(tcv_ReceiveFirstSDU = TRUE) AND (tcv_ReceiveSecondSDU = TRUE) AND (tcv_ReceiveThirdSDU = TRUE) AND (tcv_ReceiveFourthSDU = TRUE)]			
39		(tcv_Res := TRUE)			
40		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB10_RB11_RB12_RB13 (p_data1,p_data2,p_data3,p_data4 : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_ReceiveRB12 := FALSE, tcv_ReceiveRB13 := FALSE, tcv_Res := FALSE, tcv_count_RB10 := 0, tcv_count_RB11 := 0, tcv_count_RB12 := 0, tcv_count_RB13 := 0)			
2		START t_Dly(tcv_max_Timer)			
3		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15
4		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)			
5		+lt_CheckStatus			
6		[tcv_Res = FALSE]			
7		-> Get_Data			
8		[tcv_Res = TRUE]			
9		CANCEL t_Dly			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))	(P)	Step 15
11		(tcv_count_RB11:= tcv_count_RB11 + 1, tcv_ReceiveRB11 := TRUE)			
12		+lt_CheckStatus			
13		[tcv_Res = FALSE]			
14		-> Get_Data			
15		[tcv_Res = TRUE]			
16		CANCEL t_Dly			
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB12, c_TrD_Data (p_data3))	(P)	Step 15
18		(tcv_count_RB12:= tcv_count_RB12 + 1, tcv_ReceiveRB12 := TRUE)			
19		+lt_CheckStatus			
20		[tcv_Res = FALSE]			
21		-> Get_Data			
22		[tcv_Res = TRUE]			
23		CANCEL t_Dly			
24		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data4))	(P)	Step 15
25		(tcv_count_RB13:= tcv_count_RB13 + 1, tcv_ReceiveRB13 := TRUE)			
26		+lt_CheckStatus			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
27		[tcv_Res = FALSE]			
28		-> Get_Data			
29		[tcv_Res =TRUE]			
30		CANCEL t_Dly			
31		? TIMEOUT t_Dly			
32		+ts_Exit_Testcase			
		It_CheckStatus			
33		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE) AND (tcv_ReceiveRB12 = TRUE) AND (tcv_ReceiveRB13 = TRUE)]			
34		(tcv_Res := TRUE)			
35		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB10_RB11_RB12_RB20 (p_data1,p_data2,p_data3,p_data4 : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_ReceiveRB12 := FALSE, tcv_ReceiveRB20 := FALSE, tcv_Res := FALSE, tcv_count_RB10 := 0, tcv_count_RB11 := 0, tcv_count_RB12 := 0, tcv_count_RB20 := 0)			
2		START t_Dly(tcv_max_Timer)			
3		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data4))	(P)	Step 15
4		(tcv_count_RB20:= tcv_count_RB20 + 1, tcv_ReceiveRB20 := TRUE)			
5		+lt_CheckStatus			
6		[tcv_Res = FALSE]			
7		-> Get_Data			
8		[tcv_Res =TRUE]			
9		CANCEL t_Dly			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15
11		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)			
12		+lt_CheckStatus			
13		[tcv_Res = FALSE]			
14		-> Get_Data			
15		[tcv_Res = TRUE]			
16		CANCEL t_Dly			
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))	(P)	Step 15
18		(tcv_count_RB11:= tcv_count_RB11 + 1,tcv_ReceiveRB11 := TRUE)			
19		+lt_CheckStatus			
20		[tcv_Res = FALSE]			
21		-> Get_Data			
22		[tcv_Res =TRUE]			
23		CANCEL t_Dly			
24		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))	(P)	Step 15
25		(tcv_count_RB12:= tcv_count_RB12 + 1, tcv_ReceiveRB12 := TRUE)			
26		+lt_CheckStatus			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
27		[tcv_Res = FALSE]			
28		-> Get_Data			
29		[tcv_Res =TRUE]			
30		CANCEL t_Dly			
31		? TIMEOUT t_Dly			
32		+ts_Exit_Testcase			
		It_CheckStatus			
33		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE) AND (tcv_ReceiveRB12 = TRUE) AND (tcv_ReceiveRB20 = TRUE)]			
34		(tcv_Res := TRUE)			
35		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_ReceiveFirstSDU_RB10_RB13 (p_data1,p_data2 : BITSTRING)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB13 := FALSE, tcv_Res := FALSE,tcv_count_RB10 := 0,tcv_count_RB13 := 0)	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15a	
2		START t_Dly(tcv_max_Timer)				
3		TM ? RLC_TR_TestDataInd				
4		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)				
5		+It_CheckStatus				
6		[tcv_Res = FALSE]				
7		-> Get_Data				
8		[tcv_Res = TRUE]				
9		CANCEL t_Dly				
10		TM? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data2))	(P)	Step 15a	
11		(tcv_count_RB13:= tcv_count_RB13 + 1,tcv_ReceiveRB13 := TRUE)				
12		+It_CheckStatus				
13		[tcv_Res = FALSE]				
14		-> Get_Data				
15		[tcv_Res =TRUE]				
16		CANCEL t_Dly				
17		? TIMEOUT t_Dly				(I)
18		+ts_Exit_Testcase				
19		It_CheckStatus				
20		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB13 = TRUE)]				
21		(tcv_Res := TRUE)				
	[TRUE]					
Detailed Comments :						

Test Step Dynamic Behaviour						
Test Step Name : ts_ReceiveFirstSDU_RB10_RB20 (p_data1,p_data2 : BITSTRING)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_ Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB20 := FALSE, tcv_Res := FALSE,tcv_count_RB10 := 0,tcv_count_RB20 := 0)	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data2))	(P)	Step 15a	
2		START t_Dly(tcv_max_Timer)				
3		AM? RLC_AM_TestDataInd				
4		(tcv_count_RB20:= tcv_count_RB20 + 1,tcv_ReceiveRB20 := TRUE)				
5		+It_CheckStatus				
6		[tcv_Res = FALSE]				
7		-> Get_Data				
8		[tcv_Res =TRUE]				
9		CANCEL t_Dly				
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15a	
11		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)				
12		+It_CheckStatus				
13		[tcv_Res = FALSE]				
14		-> Get_Data				
15		[tcv_Res = TRUE]				
16		CANCEL t_Dly				
17		? TIMEOUT t_Dly				(I)
18		+ts_Exit_Testcase				
19		It_CheckStatus				
20		[(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB20 = TRUE)]				
21		(tcv_Res := TRUE)				
	[TRUE]					
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB20 (p_data : BITSTRING)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			for TTCN Delay Step 15a.1
2		AM ? RLC_AM_TestDataInd CANCEL t_Dly	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(p_data))	(P)	15b.1
3		(tcv_count_RB20:=1)			
4		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB22 (p_data : BITSTRING)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			for TTCN Delay Step 15a.1
2		AM ? RLC_AM_TestDataInd CANCEL t_Dly	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data(p_data))	(P)	15b.1
3		(tcv_count_RB22:=1)			
4		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendDataInContineousTTI (p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_SendContinuousData_REQ	cas_CRLC_SendContinuousData_REQ (tsc_CellDedicated , p_RAB_Tx_Info)		
2		CRLC ? CRLC_SendContinuousData_CNF	car_CRLC_SendContinuousData_CNF_Success (tsc_CellDedicated)		
3		(tcv_result := TRUE)			
4		CRLC ? CRLC_SendContinuousData_CNF	car_CRLC_SendContinuousData_CNF_Failure (tsc_CellDedicated)		
5		(tcv_result := FALSE)			
Detailed Comments : (tcv_result:=o_SendContinuousData (p_RAB_Tx_Info))					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10 (p_data : BITSTRING;p_no_of_sdus: INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (tsc_CellDedicated, tsc_RB2, cs_MeasurementControlDef Periodic (tcv_CellIndInfo.dl_Integrity CheckInfo , tcv_RRC_Ti , tcv_TmpCellInfo.priScrmCo de))	(P)	15a.2
2		(tcv_Res:=FALSE)			
3		START t_Dly(1000)			
4		AM ? RLC_AM_DATA_IND CANCEL t_Dly	car_MeasurementReport(tsc_CellDedicated, tsc_RB2, cr_MeasurementReportAny)		15b
5		REPEAT It_Receive UNTIL [tcv_Receive_RB10 = p_no_of_sdus]			
6		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_data))		15b.1
7		(tcv_Receive_RB10:=tcv_Receive_RB10+1)			
8		-> Get_Report			
9		? TIMEOUT t_Dly			
10		It_Receive			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data))		
11		(tcv_Receive_RB10:=tcv_Receive_RB10+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res := FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND CANCEL t_Dly	cs_MeasurementControlDef		
5		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]	Periodic (
6		TM ? RLC_TR_TestDataInd	tcv_CellIndInfo.dl_Integrity		
7		(tcv_count_RB10 := tcv_count_RB10 + 1)	CheckInfo ,		
8		-->Get_Report	tcv_RRC_Ti ,		
9		TM ? RLC_TR_TestDataInd	tcv_TmpCellInfo.priScrmCo		
10		(tcv_count_RB11 := tcv_count_RB11 + 1)	de))		
11		-->Get_Report	car_MeasurementReport(
12		?TIMEOUT t_Dly	tsc_CellDedicated,		
13		+ts_Exit_Testcase	tsc_RB2,		
14		It_Receive3	cr_MeasurementReportAny		
15		TM ? RLC_TR_TestDataInd)		
16		(tcv_count_RB10:= tcv_count_RB10 + 1)	car_RLC_DataInd (
		+ It_CheckRes	tsc_CellDedicated,		
			tsc_RB10, c_TrD_Data (
			p_data1))		
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB11, c_TrD_Data (
			p_data2))		
				(I)	Step 15b.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b.1
18		(tcv_count_RB11:= tcv_count_RB11 + 1)			
19		+ It_CheckRes			
		It_CheckRes			
20		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]			
21		(tcv_Res := TRUE)			
22		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11_RB12 (p_data1,p_data2,p_data3 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res:=FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND CANCEL t_Dly	cs_MeasurementControlDef Periodic (
5		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]	tcv_CellIndInfo.dl_Integrity CheckInfo ,		
6		TM ? RLC_TR_TestDataInd	tcv_RRC_Ti ,		
7		(tcv_count_RB10 := tcv_count_RB10 +1)	tcv_TmpCellInfo.priScrmCo de))		
8		->Get_Report	car_MeasurementReport(
9		TM ? RLC_TR_TestDataInd	tsc_CellDedicated,		
10		(tcv_count_RB11 := tcv_count_RB11 +1)	tsc_RB2,		
11		->Get_Report	cr_MeasurementReportAny		
12		TM ? RLC_TR_TestDataInd)		
13		(tcv_count_RB12 := tcv_count_RB12 +1)	car_RLC_DataInd (
14		->Get_Report	tsc_CellDedicated,		
15		?TIMEOUT t_Dly	tsc_RB10, c_TrD_Data (
16		+ts_Exit_Testcase	p_data1))		
		It_Receive3	car_RLC_DataInd ((I)	Step 15b
			tsc_CellDedicated,		
			tsc_RB11, c_TrD_Data (
			p_data2))		
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB12, c_TrD_Data (
			p_data3))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15b.1
18		(tcv_count_RB10:= tcv_count_RB10 + 1)			
19		+ It_CheckRes			
20		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b.1
21		(tcv_count_RB11:= tcv_count_RB11 + 1)			
22		+ It_CheckRes			
23		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15b.1
24		(tcv_count_RB12:= tcv_count_RB12 + 1)			
25		+ It_CheckRes			
26		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu)]			
27		(tcv_Res := TRUE)			
28		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB13 (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo ;p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (tsc_CellDedicated, tsc_RB2, cs_MeasurementControlDef Periodic (tcv_CellIndInfo.dl_Integrity CheckInfo , tcv_RRC_Ti , tcv_TmpCellInfo.priScrmCode))	(P)	15a.2
2		(tcv_Res:=FALSE)			
3		START t_Dly(1000)			
4		AM ? RLC_AM_DATA_IND	car_MeasurementReport(tsc_CellDedicated, tsc_RB2, cr_MeasurementReportAny)		15b
5		CANCEL t_Dly			
6		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15b
8		(tcv_count_RB10 := tcv_count_RB10 +1)			
9		->Get_Report			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b
11		(tcv_count_RB11 := tcv_count_RB11 +1)			
12		->Get_Report			
13		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB12, c_TrD_Data (p_data3))		Step 15b
14		(tcv_count_RB12 := tcv_count_RB12 +1)			
15		->Get_Report			
16		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data4))		Step 15b
17		(tcv_count_RB13 := tcv_count_RB13 +1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		→Get_Report		(I)	Step 15b.1
19		?TIMEOUT t_Dly			
20		It_Receive4 TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		
21		(tcv_count_RB10:= tcv_count_RB10 + 1)			
22		+ It_CheckRes			
23		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b.1
24		(tcv_count_RB11:= tcv_count_RB11 + 1)			
25		+ It_CheckRes			
26		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15b.1
27		(tcv_count_RB12:= tcv_count_RB12 + 1)			
28		+ It_CheckRes			
29		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB13 , c_TrD_Data (p_data4))		Step 15b.1
30		(tcv_count_RB13:= tcv_count_RB13 + 1)			
31		+ It_CheckRes			
32		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB13 =(p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu * p_ReceiveFactor))]			
33		(tcv_Res := TRUE)			
34		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB20 (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
			tsc_CellDedicated,		
			tsc_RB2,		
			cs_MeasurementControlDef		
			Periodic (
			tcv_CellIndInfo.dl_Integrity		
			CheckInfo ,		
			tcv_RRC_Ti ,		
			tcv_TmpCellInfo.priScrmCo		
			de))		
2		(tcv_Res:=FALSE)			
3		START t_Dly(1000)			
4		AM ? RLC_AM_DATA_IND	car_MeasurementReport(15b
			tsc_CellDedicated,		
			tsc_RB2,		
			cr_MeasurementReportAny		
)		
5		CANCEL t_Dly			
6		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step 15b
			tsc_CellDedicated,		
			tsc_RB10, c_TrD_Data (
			p_data1))		
8		(tcv_count_RB10 := tcv_count_RB10 +1)			
9		->Get_Report			
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step 15b
			tsc_CellDedicated,		
			tsc_RB11, c_TrD_Data (
			p_data2))		
11		(tcv_count_RB11 := tcv_count_RB11 +1)			
12		->Get_Report			
13		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step 15b
			tsc_CellDedicated,		
			tsc_RB12, c_TrD_Data (
			p_data3))		
14		(tcv_count_RB12 := tcv_count_RB12 +1)			
15		->Get_Report			
16		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (Step 15b
			tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data4))		
17		(tcv_count_RB20 := tcv_count_RB20 +1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		->Get_Report		(I)	
19		?TIMEOUT t_Dly			
20		+ts_Exit_Testcase			
		It_Receive4			
21		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15b.1
22		(tcv_count_RB10:= tcv_count_RB10 + 1)			
23		+ It_CheckRes			
24		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b.1
25		(tcv_count_RB11:= tcv_count_RB11 + 1)			
26		+ It_CheckRes			
27		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))		Step 15b.1
28		(tcv_count_RB12:= tcv_count_RB12 + 1)			
29		+ It_CheckRes			
30		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data4))		Step 15b.1
31		(tcv_count_RB20:= tcv_count_RB20 + 1)			
32		+ It_CheckRes			
		It_CheckRes			
33		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu)]			
34		(tcv_Res := TRUE)			
35		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11_RB12_RB20_Special (p_data1,p_data2,p_data3,p_data4 : BITSTRING; p_RAB_Tx_Info: RabTxInfo ;p_ReceiveFactor :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res:=FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND	cs_MeasurementControlDef		
5		CANCEL t_Dly	Periodic (
6		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]	tcv_CellIndInfo.dl_Integrity		
7		TM ? RLC_TR_TestDataInd	CheckInfo ,		
8		(tcv_count_RB10 := tcv_count_RB10 +1)	tcv_RRC_Ti ,		
9		->Get_Report	tcv_TmpCellInfo.priScrmCo		
10		TM ? RLC_TR_TestDataInd	de))		
11		(tcv_count_RB11 := tcv_count_RB11 +1)	car_MeasurementReport(
12		->Get_Report	tsc_CellDedicated,		
13		TM ? RLC_TR_TestDataInd	tsc_RB2,		
14		(tcv_count_RB12 := tcv_count_RB12 +1)	cr_MeasurementReportAny		
15		->Get_Report)		
16		AM ? RLC_AM_TestDataInd	car_RLC_DataInd (
17		(tcv_count_RB20 := tcv_count_RB20 +1)	tsc_CellDedicated,		
			tsc_RB10, c_TrD_Data (
			p_data1))		
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB11, c_TrD_Data (
			p_data2))		
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB12, c_TrD_Data (
			p_data3))		
			car_RLC_AM_DataInd (
			tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data4))		

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Test Step Dynamic Behaviour						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
18		->Get_Report		(I)	Step 15b.1	
19		?TIMEOUT t_Dly				
20		+ts_Exit_Testcase				
		lt_Receive4				
21		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))			
22		(tcv_count_RB10:= tcv_count_RB10 + 1)				
23		+ lt_CheckRes				
24		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))			
25		(tcv_count_RB11:= tcv_count_RB11 + 1)				
26		+ lt_CheckRes				
27		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated,tsc_RB12 , c_TrD_Data (p_data3))			
28		(tcv_count_RB12:= tcv_count_RB12 + 1)		Step 15b.1		
29		+ lt_CheckRes				
30		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data4))			
31		(tcv_count_RB20:= tcv_count_RB20 + 1)				
32		+ lt_CheckRes		Step 15b.1		
		lt_CheckRes				
33		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB12 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu) AND (tcv_count_RB20 =(p_RAB_Tx_Info.rbTxInfoList.[3].nomOfSdu * p_ReceiveFactor))]				
34		(tcv_Res := TRUE)				
35		[TRUE]				
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB13 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res:=FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND	cs_MeasurementControlDef		
5		CANCEL t_Dly	Periodic (
6		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]	tcv_CellIndInfo.dl_Integrity		
7		TM ? RLC_TR_TestDataInd	CheckInfo ,		
8		(tcv_count_RB10 := tcv_count_RB10 + 1)	tcv_RRC_Ti ,		
9		->Get_Report	tcv_TmpCellInfo.priScrmCo		
10		TM ? RLC_TR_TestDataInd	de))		
11		(tcv_count_RB13 := tcv_count_RB13 + 1)	car_MeasurementReport(
12		->Get_Report	tsc_CellDedicated,		
13		?TIMEOUT t_Dly	tsc_RB2,		
14		It_Receive2	cr_MeasurementReportAny		
15		(tcv_count_RB10:= tcv_count_RB10 + 1))		
16		+ It_CheckRes	car_RLC_DataInd (
			tsc_CellDedicated,		Step 15b
			tsc_RB10, c_TrD_Data (Step 15b
			p_data1))		Step 15b.1
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB13, c_TrD_Data (
			p_data2))		
			car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB10, c_TrD_Data (
			p_data1))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17		TM? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data (p_data2))		Step 15b.1
18		(tcv_count_RB13:= tcv_count_RB13 + 1)			
19		+ It_CheckRes			
		It_CheckRes			
20		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB13 = (p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu * p_ReceiveFactor))]			
21		(tcv_Res := TRUE)			
22		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour	
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Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB20 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo)
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Group : RB_Steps/RB_Subtests/

Objective :	
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Default	: RRC_Def1
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Comments	:
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Description :	
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Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (15a.2
			tsc_CellDedicated,		
			tsc_RB2,		
			cs_MeasurementControlDef		
			Periodic (
			tcv_CellIndInfo.dl_Integrity		
			CheckInfo ,		
			tcv_RRC_Ti ,		
			tcv_TmpCellInfo.priScrmCo		
			de))		
2		(tcv_Res := FALSE)			
3		START t_Dly(1000)			
4	Get_Report	AM ? RLC_AM_DATA_IND	car_MeasurementReport((P)	15b
			tsc_CellDedicated,		
			tsc_RB2,		
			cr_MeasurementReportAny		
)		
5		CANCEL t_Dly			
6		REPEAT It_Receive2 UNTIL [tcv_Res =			
		TRUE]			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step
			tsc_CellDedicated,		15b
			tsc_RB10, c_TrD_Data (
			p_data1))		
8		(tcv_count_RB10 := tcv_count_RB10 +1)			
9		->Get_Report			
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (Step
			tsc_CellDedicated,		15b
			tsc_RB20, c_TrD_Data (
			p_data2))		
11		(tcv_count_RB20 := tcv_count_RB20 +1)			
12		->Get_Report			
13		?TIMEOUT t_Dly		(I)	
14		+ts_Exit_Testcase			
		It_Receive2			
15		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (Step
			tsc_CellDedicated,		15b.1
			tsc_RB10, c_TrD_Data (
			p_data1))		
16		(tcv_count_RB10:= tcv_count_RB10 + 1)			
17		+ It_CheckRes			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data2))		Step 15b.1
19		(tcv_count_RB20:= tcv_count_RB20 + 1)			
20		+ It_CheckRes			
		It_CheckRes			
21		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]			
22		(tcv_Res := TRUE)			
23		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB20_Special (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res := FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND	cs_MeasurementControlDef Periodic (
5		CANCEL t_Dly	tcv_CellIndInfo.dl_Integrity CheckInfo ,		
6		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]	tcv_RRC_Ti ,		
7		TM ? RLC_TR_TestDataInd	tcv_TmpCellInfo.priScrmCo de))		
8		(tcv_count_RB10 := tcv_count_RB10 +1)	car_MeasurementReport(
9		->Get_Report	tsc_CellDedicated,		
10		AM ? RLC_AM_TestDataInd	tsc_RB2,		
11		(tcv_count_RB20 := tcv_count_RB20 +1)	cr_MeasurementReportAny		
12		->Get_Report)		
13		?TIMEOUT t_Dly	car_RLC_DataInd (Step 15b
14		+ts_Exit_Testcase	tsc_CellDedicated,		
15		It_Receive2	tsc_RB10, c_TrD_Data ((I)	Step 15b.1
16		TM ? RLC_TR_TestDataInd	p_data1))		
17		(tcv_count_RB10:= tcv_count_RB10 + 1)	car_RLC_AM_DataInd (
		+ It_CheckRes	tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data2))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data2))		Step 15b.1
19		(tcv_count_RB20:= tcv_count_RB20 + 1)			
20		+ It_CheckRes			
		It_CheckRes			
21		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB20 = (p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu * p_ReceiveFactor))]			
22		(tcv_Res := TRUE)			
23		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB13 (p_data : BITSTRING;p_no_of_sdus ,p_ReceiveFactor: INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
			tsc_CellDedicated,		
			tsc_RB2,		
			cs_MeasurementControlDef		
			Periodic (
			tcv_CellIndInfo.dl_Integrity		
			CheckInfo ,		
			tcv_RRC_Ti ,		
			tcv_TmpCellInfo.priScrmCo		
			de))		
2		START t_Dly(1000)			
3		AM ? RLC_AM_DATA_IND CANCEL t_Dly	car_MeasurementReport(15b
			tsc_CellDedicated,		
			tsc_RB2,		
			cr_MeasurementReportAny		
)		
4		REPEAT It_Receive UNTIL			
		[tcv_Receive_RB13 =			
		(p_no_of_sdus*p_ReceiveFactor)]			
5		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (15b.1
			tsc_CellDedicated,		
			tsc_RB13,		
			c_TrD_Data(p_data))		
6		(tcv_Receive_RB13:=tcv_Receive_RB13+1)			
7		-> Get_Report			
8		? TIMEOUT t_Dly			
		It_Receive		(I)	
9		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (
			tsc_CellDedicated,		
			tsc_RB13, c_TrD_Data (
			p_data))		
10		(tcv_Receive_RB13:=tcv_Receive_RB13+1)			
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_Simultaneous_Data_SRB_RB20 (p_data : BITSTRING;p_no_of_sdus: INTEGER)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (tsc_CellDedicated, tsc_RB2, cs_MeasurementControlDef Periodic (tcv_CellIndInfo.dl_Integrity CheckInfo , tcv_RRC_Ti , tcv_TmpCellInfo.priScrmCo de))	(P)	15a.2	
2		START t_Dly(1000)				
3		AM ? RLC_AM_DATA_IND CANCEL t_Dly	car_MeasurementReport(tsc_CellDedicated, tsc_RB2, cr_MeasurementReportAny)		15b	
4		REPEAT It_Receive UNTIL [tcv_count_RB20 = p_no_of_sdus]				
5		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(p_data))		15b.1	
6		(tcv_count_RB20:=tcv_count_RB20+1)				
7		-> Get_Report				
8		? TIMEOUT t_Dly			(I)	
9		+ts_Exit_Testcase				
		It_Receive				
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data))			
11		(tcv_count_RB20:=tcv_count_RB20+1)				
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB20_Special (p_data : BITSTRING;p_no_of_sdus , p_ReceiveFactor: INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (tsc_CellDedicated, tsc_RB2, cs_MeasurementControlDef Periodic (tcv_CellIndInfo.dl_Integrity CheckInfo , tcv_RRC_Ti , tcv_TmpCellInfo.priScrmCo de))	(P)	15a.2
2		START t_Dly(1000)			
3		AM ? RLC_AM_DATA_IND CANCEL t_Dly	car_MeasurementReport(tsc_CellDedicated, tsc_RB2, cr_MeasurementReportAny)		15b
4		REPEAT It_Receive UNTIL [(tcv_count_RB20 = p_no_of_sdus*p_ReceiveFactor)]			
5		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data(p_data))		15b.1
6		(tcv_count_RB20:=tcv_count_RB20+1)			
7		-> Get_Report			
8		? TIMEOUT t_Dly			
9		+ts_Exit_Testcase			
		It_Receive			
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data))		(I)
11		(tcv_count_RB20:=tcv_count_RB20+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_11_To_20_TC_14_2_43_2 (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_9_11_15_17_24_26, c_TFC_Allowed_0_2_3_11_27_38, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,1272,60)), 20, 1) 2			Subtest 11 Steps 11–17
2		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_12_27_39, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT, OMIT), 20, 1) 3			Subtest 12 Steps 11–17
3		+ts_RB_SubTest_RAB_SRB_RB10_RB20_Special(c_TFC_Allowed_0_1_2_3_12_13_15_16_27_28, c_TFC_Allowed_0_1_3_13_27_40, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT), 20, 1)			Subtest 13 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ts_RB_SubTest_RAB_SRB_RB10_RB11_R B12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17_27 _29, c_TFC_Allowed_0_2_3_14_27_41, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,2552,60)), 20, 1)			Subtest 14 Steps 11–17
5		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_15_27_42, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,3832,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 15 Steps 11–17
6		+ts_RB_SubTest_RAB_SRB_RB10_RB20 _Special(c_TFC_Allowed_0_1_2_3_12_13_15_16 _27_28, c_TFC_Allowed_0_1_3_16_27_43, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,3832,60), OMIT, OMIT), 20, 1)			Subtest 16 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17_27_29, c_TFC_Allowed_0_2_3_17_27_44, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,3832,60)), 20, 1)			Subtest 17 Steps 11–17
8		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_18_27_45, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,5112,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 18 Steps 11–17
9		+ts_RB_SubTest_RAB_SRB_RB10_RB20_Special(c_TFC_Allowed_0_1_2_3_12_13_15_16_27_28, c_TFC_Allowed_0_1_3_19_27_46, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,5112,60), OMIT, OMIT), 20, 1)			Subtest 19 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		+ts_RB_SubTest_RAB_SRB_RB10 _RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_ 15_17_27_29, c_TFC_Allowed_0_2_3_20_27_47, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,5112,60)), 20, 1)			Subtest 20 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_To_10_TC_14_2_43_2(p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_15_16, c_TFC_Allowed_0_1_27_28, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (c_TFC_Allowed_0_1_2_3_15_17, c_TFC_Allowed_0_2_27_29, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_15_18, c_TFC_Allowed_0_3_27_30, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11–17
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_15_16_18_19, c_TFC_Allowed_0_1_3_4_27_31, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT), 20)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_15_17_18_20 , c_TFC_Allowed_0_2_3_5_27_32, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,60)), 20)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_6_15_21, c_TFC_Allowed_0_3_6_27_33, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 20)			Subtest 6 Steps 11–17
7		+ts_RB_SubTest_RAB_SRB_RB10_RB 20 (c_TFC_Allowed_0_1_2_3_6_7_15_16_ 21_22, c_TFC_Allowed_0_1_3_7_27_34, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT), 20)			Subtest 7 Steps 11–17
8		+ts_RB_SubTest_RAB_SRB_RB10_R B11_RB12_RB20(c_TFC_Allowed_0_1_2_3_6_8_15_17_ 21_23, c_TFC_Allowed_0_2_3_8_27_35, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,632,60)), 20)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB20_S pecial (c_TFC_Allowed_0_1_2_3_9_15_24 , c_TFC_Allowed_0_3_9_27_36, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 9 Steps 11–17
10		+ts_RB_SubTest_RAB_SRB_RB10 _RB20_Special(c_TFC_Allowed_0_1_2_3_9_10_1 5_16_24_25, c_TFC_Allowed_0_1_3_10_27_37, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 952, tsc_RB20),c_RAB_Tx_Info(tsc_RB_TestData_2688, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT), 20, 1)			Subtest 10 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_21_To_26_TC_14_2_43_2 (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_21_27_48, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,6392,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 21 Steps 11-17
2		+ts_RB_SubTest_RAB_SRB_RB10_RB20_Special(c_TFC_Allowed_0_1_2_3_12_13_15_16_27_28 , c_TFC_Allowed_0_1_3_22_27_49, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,6392,60), OMIT, OMIT), 20, 1)			Subtest 22 Steps 11-17
3		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17_27_29, c_TFC_Allowed_0_2_3_23_27_50, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,6392,60)), 20, 1)			Subtest 23 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_24_27_51, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,7672,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 24 Steps 11-17
5		+ts_RB_SubTest_RAB_SRB_RB10_RB20_ Special(c_TFC_Allowed_0_1_2_3_12_13_15_16_2 7_28, c_TFC_Allowed_0_1_3_25_27_52, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,7672,60), OMIT, OMIT), 20, 1)			Subtest 25 Steps 11-17
6		+ts_RB_SubTest_RAB_SRB_RB10_RB11_ _RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17 _27_29, c_TFC_Allowed_0_2_3_26_27_53, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,7672,60)), 20, 1)			Subtest 26 Steps 11-17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB10_RB11_RB20 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize), tcv_RB_Data3 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[2].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- ----- 12 times max tti among the RABs + 10% of max tti
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB10_RB11_RB20 (tcv_RB_Data1, tcv_RB_Data2, tcv_RB_Data3, p_RAB_Tx_Info)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			for TTCN Delay Step 15a.1
11		+ts_ReceiveFirstSDU_RB10_RB 11_RB20 (tcv_RB_Data1,tcv_RB_Data2,tc v_RB_Data3)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_Simultaneous_Data_SRB_R B10_RB11_RB20(tcv_RB_Dat a1, tcv_RB_Data2, tcv_RB_Data3, p_RAB_Tx_Info)			
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16–17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB10_RB11_RB20 (p_data1,p_data2,p_data3: BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB10_RB11_RB20 (p_data1,p_data2,p_data3)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]			
4		It_Receive4			
4		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB10:= tcv_count_RB10 + 1)			
6		+ It_CheckRes			
7		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB11:= tcv_count_RB11 + 1)			
9		+ It_CheckRes			
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data3))		Step 15
11		(tcv_count_RB20:= tcv_count_RB20 + 1)			
12		+ It_CheckRes			
13		It_CheckRes			
13		[(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu)]			
14		(tcv_Res := TRUE)			
15		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB10_RB11_RB20 (p_data1,p_data2,p_data3 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res:=FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND	cs_MeasurementControlDef Periodic (
5		CANCEL t_Dly	tcv_CellIndInfo.dl_Integrity CheckInfo ,		
6		REPEAT It_Receive4 UNTIL [tcv_Res = TRUE]	tcv_RRC_Ti ,		
7		TM ? RLC_TR_TestDataInd	tcv_TmpCellInfo.priScrmCo de))		
8		(tcv_count_RB10 := tcv_count_RB10 +1)	car_MeasurementReport(
9		->Get_Report	tsc_CellDedicated,		
10		TM ? RLC_TR_TestDataInd	tsc_RB2,		
11		(tcv_count_RB11 := tcv_count_RB11 +1)	cr_MeasurementReportAny)		
12		->Get_Report	car_RLC_DataInd (Step 15b
13		AM ? RLC_AM_TestDataInd	tsc_CellDedicated,		
14		(tcv_count_RB20 := tcv_count_RB20 +1)	tsc_RB10, c_TrD_Data ((I)	Step 15b
15		->Get_Report	p_data1))		
16		?TIMEOUT t_Dly	car_RLC_DataInd (
17		+ts_Exit_Testcase	tsc_CellDedicated,		
		It_Receive4	tsc_RB11, c_TrD_Data (
			p_data2))		
			car_RLC_AM_DataInd (
			tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data3))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))		Step 15b.1
19		(tcv_count_RB10:= tcv_count_RB10 + 1)			
20		+ It_CheckRes			
21		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))		Step 15b.1
22		(tcv_count_RB11:= tcv_count_RB11 + 1)			
23		+ It_CheckRes			
24		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data3))		Step 15b.1
25		(tcv_count_RB20:= tcv_count_RB20 + 1)			
26		+ It_CheckRes			
27		It_CheckRes [(tcv_count_RB10 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB11 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu) AND (tcv_count_RB20 =p_RAB_Tx_Info.rbTxInfoList.[2].nomOfSdu)]			
28		(tcv_Res := TRUE)			
29		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_ReceiveFirstSDU_RB10_RB11_RB20 (p_data1,p_data2,p_data3 : BITSTRING)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default : RRC_Def1						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_ Data	(tcv_ReceiveRB10 := FALSE, tcv_ReceiveRB11 := FALSE, tcv_ReceiveRB20 := FALSE, tcv_Res := FALSE, tcv_count_RB10 := 0, tcv_count_RB11 := 0, tcv_count_RB20 := 0)	car_RLC_AM_DataInd (tsc_CellDedicated,tsc_RB20 , c_TrD_Data (p_data3))	(P)	Step 15	
2		START t_Dly(tcv_max_Timer)				
3		AM ? RLC_AM_TestDataInd				
4		(tcv_count_RB20:= tcv_count_RB20 + 1, tcv_ReceiveRB20 := TRUE)				
5		+It_CheckStatus				
6		[tcv_Res = FALSE]				
7		-> Get_Data				
8		[tcv_Res =TRUE]				
9		CANCEL t_Dly				
10		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data (p_data1))	(P)	Step 15	
11		(tcv_count_RB10:= tcv_count_RB10 + 1, tcv_ReceiveRB10 := TRUE)				
12		+It_CheckStatus				
13		[tcv_Res = FALSE]				
14		-> Get_Data				
15		[tcv_Res = TRUE]				
16		CANCEL t_Dly				
17		TM ? RLC_TR_TestDataInd	car_RLC_DataInd (tsc_CellDedicated, tsc_RB11, c_TrD_Data (p_data2))	(P)	Step 15	
18		(tcv_count_RB11:= tcv_count_RB11 + 1,tcv_ReceiveRB11 := TRUE)				
19		+It_CheckStatus				
20		[tcv_Res = FALSE]				
21		-> Get_Data				
22		[tcv_Res =TRUE]				
23		CANCEL t_Dly				
24		? TIMEOUT t_Dly		(I)		
25		+ts_Exit_Testcase				
26		It_CheckStatus [(tcv_ReceiveRB10 = TRUE) AND (tcv_ReceiveRB11 = TRUE) AND (tcv_ReceiveRB20 = TRUE)]				

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
27		(tcv_Res := TRUE)			
28		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_11_To_17_TC_14_2_43_1 (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_9_11_15_17_24_26, c_TFC_Allowed_0_2_3_9_11_18_29, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,1272,60)), 20)			Subtest 11 Steps 11-17
2		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_12_18_30, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 12 Steps 11-17
3		+ts_RB_SubTest_RAB_SRB_RB10_RB20_Special(c_TFC_Allowed_0_1_2_3_12_13_15_16_27_28, c_TFC_Allowed_0_1_3_12_13_18_31, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT), 20, 1)			Subtest 13 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		+ts_RB_SubTest_RAB_SRB_RB10_RB11_R B12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17_27 _29, c_TFC_Allowed_0_2_3_12_14_18_32, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,2552,60)), 20, 1)			Subtest 14 Steps 11–17
5		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_12_15_27, c_TFC_Allowed_0_3_15_18_33, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,3832,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 15 Steps 11–17
6		+ts_RB_SubTest_RAB_SRB_RB10_RB20 _Special(c_TFC_Allowed_0_1_2_3_12_13_15_16 _27_28, c_TFC_Allowed_0_1_3_15_16_18_34, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,3832,60), OMIT, OMIT), 20, 1)			Subtest 16 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_12_14_15_17_27_29, c_TFC_Allowed_0_2_3_15_17_18_35, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 632, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,3832,60)), 20, 1)			Subtest 17 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_To_10_TC_14_2_43_1(p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_15_16, c_TFC_Allowed_0_1_18_19, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (c_TFC_Allowed_0_1_2_3_15_17, c_TFC_Allowed_0_2_18_20, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_15_18, c_TFC_Allowed_0_3_18_21, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11–17
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_15_16_18_19, c_TFC_Allowed_0_1_3_4_18_22, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT), 20)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_15_17_18_20 , c_TFC_Allowed_0_2_3_5_18_23, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,60)), 20)			Subtest 5 Steps 11-17
6		+ ts_RB_SubTest_RAB_SRB_RB20_Special (c_TFC_Allowed_0_1_2_3_6_15_21, c_TFC_Allowed_0_3_6_18_24, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 20, 1)			Subtest 6 Steps 11-17
7		+ts_RB_SubTest_RAB_SRB_RB10_RB20_Special(c_TFC_Allowed_0_1_2_3_6_7_15_16_21_22, c_TFC_Allowed_0_1_3_6_7_18_25, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT), 20, 1)			Subtest 7 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		+ts_RB_SubTest_RAB_SRB_RB10_R B11_RB12_RB20_Special(c_TFC_Allowed_0_1_2_3_6_8_15_17 _21_23, c_TFC_Allowed_0_2_3_6_8_18_26, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info (tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,632,60)), 20, 1)			Subtest 8 Steps 11-17
9		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_9_15_24 , c_TFC_Allowed_0_3_9_18_27, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 20)			Subtest 9 Steps 11-17
10		+ts_RB_SubTest_RAB_SRB_RB10 _RB20(c_TFC_Allowed_0_1_2_3_9_10_1 5_16_24_25, c_TFC_Allowed_0_1_3_9_10_18_ 28, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 1272, tsc_RB20) ,c_RAB_Tx_Info(tsc_RB_TestData_2688, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT), 20)			Subtest 10 Steps 11-17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_5_tc_14_2_38_b (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_6_7, c_TFC_Allowed_0_1_6_7, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1. Test Data on RB10 and Measurment control Report on SRB.
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (c_TFC_Allowed_0_1_2_3_6_8, c_TFC_Allowed_0_2_6_8, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2. Test Data on RB10, RB11, RB12 and Measurment control Report on SRB.
3		+ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_6_9, c_TFC_Allowed_0_3_6_9, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Subtest 3. Test Data on RB20 and Measurment control Report on SRB.
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_6_7_9_10, c_TFC_Allowed_0_3_4_6_10, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Subtest 3. Test Data on RB10, RB20 and Measurment control Report on SRB.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20 (c_TFC_Allowed_0_1_2_3_5_6_8_9_11, c_TFC_Allowed_0_3_5_6_11, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,30)), 40)			Subtest 5. Test Data on RB10, RB11, Rb12, RB20 and Measurm ent control Report on SRB.
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_5_tc_14_2_38_e (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_6_7, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1. Test Data on RB10 and Measurment control Report on SRB.
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_8, c_TFC_Allowed_0_2_6_8, cb_UE_TestLoopMode1LB_Setup4 (42,tsc_RB10, 53, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,42,60), c_RB_Tx_Info(tsc_RB11,53,60), OMIT, OMIT), 20)			Subtest 2. Test Data on RB10, RB11 and Measurment control Report on SRB.
3		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_9, c_TFC_Allowed_0_3_6_9, cb_UE_TestLoopMode1LB_Setup4 (55,tsc_RB10, 63, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,55,60), c_RB_Tx_Info(tsc_RB11,63,60), OMIT, OMIT), 20)			Subtest 3. Test Data on RB10, RB11 and Measurment control Report on SRB.
4		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_10, c_TFC_Allowed_0_3_4_6_10, cb_UE_TestLoopMode1LB_Setup4 (75,tsc_RB10, 84, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,75,60), c_RB_Tx_Info(tsc_RB11,84,60), OMIT, OMIT), 20)			Subtest 4. Test Data on RB10, RB11 and Measurment control Report on SRB.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12(c_TFC_Allowed_0_1_2_3_4_5_6_11, c_TFC_Allowed_0_3_5_6_11, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,0, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 5. Test Data on RB10, RB11, RB12 and Measurm ent control Report on SRB.
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_TC_14_2_51_1(p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_5_10_11, c_TFC_Allowed_0_1_10_11, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_5_10_12, c_TFC_Allowed_0_1_2_10_12, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_3_5_10_13, c_TFC_Allowed_0_1_3_10_13, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,952,60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11–17
4		+ ts_RB_SubTest_RAB_SRB_RB20(c_TFC_Allowed_0_1_4_5_10_14, c_TFC_Allowed_0_1_4_10_14, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 20)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10 (c_TFC_Allowed_0_1_5_10_15, c_TFC_Allowed_0_1_5_10_15, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,1280,60), OMIT, OMIT, OMIT), 20)			Subtest 5 Steps 11–17
6		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_5_6_10_11_15_16 , c_TFC_Allowed_0_1_6_10_16, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT), 20)			Subtest 6 Steps 11–17
7		+ts_RB_SubTest_RAB_SRB_RB10_RB 20 (c_TFC_Allowed_0_1_2_5_7_10_12_1 5_17, c_TFC_Allowed_0_1_7_10_17, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT), 20)			Subtest 7 Steps 11–17
8		+ts_RB_SubTest_RAB_SRB_RB10_R B20 (c_TFC_Allowed_0_1_3_5_8_10_13_ 15_18, c_TFC_Allowed_0_1_8_10_18, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,952,60), OMIT, OMIT), 20)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ts_RB_SubTest_RAB_SRB_RB10_ RB20 (c_TFC_Allowed_0_1_4_5_9_10_14 _15_19, c_TFC_Allowed_0_1_9_10_19, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT), 20)			Subtest 9 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_9_tc_14_2_51b_1 (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_3_6_7, c_TFC_Allowed_0_1_10_11, c_UE_TestLoopMode1_LB_Setup2 (1280,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Subtest 1 Steps 11–17
2		+ ts_RB_SubTest_RAB_SRB_RB20_Special_1 (c_TFC_Allowed_0_1_2_3_6_8, c_TFC_Allowed_0_1_2_10_12, c_UE_TestLoopMode1_LB_Setup2 (1280,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,30), OMIT, OMIT, OMIT), 40, 1)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20_Special_1 (c_TFC_Allowed_0_1_2_3_6_8, c_TFC_Allowed_0_1_3_10_13, c_UE_TestLoopMode1_LB_Setup2 (1280,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,952,30), OMIT, OMIT, OMIT), 40, 1)			Subtest 3 Steps 11–17
4		+ ts_RB_SubTest_RAB_SRB_RB20_Special_1 (c_TFC_Allowed_0_1_2_3_6_8, c_TFC_Allowed_0_1_4_10_14, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,30), OMIT, OMIT, OMIT), 40, 1)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ ts_RB_SubTest_RAB_SRB_RB10 (c_TFC_Allowed_0_1_3_6_9, c_TFC_Allowed_0_5_10_15, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,1280,60), OMIT, OMIT, OMIT), 20)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_3_4_6_7_9_10, c_TFC_Allowed_0_1_5_6_10_16, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Subtest 6 Steps 11–17
7		+ ts_RB_SubTest_RAB_SRB_RB10_RB2 0_Special_2 (c_TFC_Allowed_0_1_2_3_4_5_6_8_9 _11, c_TFC_Allowed_0_1_5_7_10_17, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,632,30), OMIT, OMIT), 40, 1)			Subtest 7 Steps 11–17
8		+ ts_RB_SubTest_RAB_SRB_RB10_RB 20_Special_2 (c_TFC_Allowed_0_1_2_3_4_5_6_8_ 9_11, c_TFC_Allowed_0_1_5_8_10_18, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,952,30), OMIT, OMIT), 40, 1)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB10_R B20_Special_2 (c_TFC_Allowed_0_1_2_3_4_5_6_8 _9_11, c_TFC_Allowed_0_1_5_9_10_19, c_UE_TestLoopMode1_LB_Setup2 (640,tsc_RB10, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,1280,60), c_RB_Tx_Info(tsc_RB20,1272,30), OMIT, OMIT), 40, 1)			Subtest 9 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_TC_14_2_38_c (p_Data_String:BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_15_16, c_TFC_Allowed_0_1_15_16, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12 (c_TFC_Allowed_0_1_2_3_15_17, c_TFC_Allowed_0_2_15_17, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_15_18, c_TFC_Allowed_0_3_15_18, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Subtest 3 Steps 11–17
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_15_16_18_19, c_TFC_Allowed_0_3_4_15_19, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_15_17_18_20 , c_TFC_Allowed_0_3_5_15_20, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,30)), 40)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_6_15_21, c_TFC_Allowed_0_3_6_15_21, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,30), OMIT, OMIT, OMIT), 40)			Subtest 6 Steps 11–17
7		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_6_7_15_16_21_22, c_TFC_Allowed_0_3_7_15_22, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,632,30), OMIT, OMIT), 40)			Subtest 7 Steps 11–17
8		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_6_8_15_17_21_23, c_TFC_Allowed_0_3_8_15_23, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,632,30)), 40)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_9_15_24 , c_TFC_Allowed_0_3_9_15_24, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,952,30), OMIT, OMIT, OMIT), 40)			Subtest 9 Steps 11–17
10		+ts_RB_SubTest_RAB_SRB_RB10 _RB20(c_TFC_Allowed_0_1_2_3_4_9_10 _15_16_24_25, c_TFC_Allowed_0_3_10_15_25, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,952,30), OMIT, OMIT), 40)			Subtest 10 Steps 11–17
11		+ts_RB_SubTest_RAB_SRB_RB 10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_9_1 1_15_17_24_26, c_TFC_Allowed_0_3_11_15_26, cb_UE_TestLoopMode1LB_Setu p4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60) , c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,952,30)) , 40)			Subtest 11 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ ts_RB_SubTest_RAB_SRB_RB 20 (c_TFC_Allowed_0_1_2_3_12_ 15_27, c_TFC_Allowed_0_3_12_15_2 7, cb_UE_TestLoopMode1LB_Set up4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,30), OMIT, OMIT, OMIT), 40)			Subtest 12 Steps 11–17
13		+ts_RB_SubTest_RAB_SRB_ RB10_RB20(c_TFC_Allowed_0_1_2_3_4_ 12_13_15_16_27_28, c_TFC_Allowed_0_3_13_15_ 28, cb_UE_TestLoopMode1LB_S etup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,1272,30), OMIT, OMIT), 40)			Subtest 13 Steps 11–17
14		+ts_RB_SubTest_RAB_SRB _RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5 _12_14_15_17_27_29, c_TFC_Allowed_0_3_14_15 _29, cb_UE_TestLoopMode1LB_ Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20) , c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,10 3,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,12			Subtest 14 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
		72,30)), 40)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_5_tc_14_2_57(p_Data_String : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_5_6, c_TFC_Allowed_0_1_5_6, c_UE_TestLoopMode1_LB_Setup2 (312,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11–17
2		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_5_7, c_TFC_Allowed_0_1_2_5_7, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 632, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20, 632, 60), OMIT, OMIT, OMIT), 20)			Subtest 2 Steps 11–17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_3_5_8, c_TFC_Allowed_0_1_3_5_8, c_UE_TestLoopMode1_LB_Setup2 (952,tsc_RB20, 952, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20, 952, 60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11–17
4		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5_9, c_UE_TestLoopMode1_LB_Setup2 (1272,tsc_RB20, 1272, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20, 1272, 60), OMIT, OMIT, OMIT), 20)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ ts_RB_SubTest_RAB_SRB_RB22 (c_TFC_Allowed_0_1_4_5_9, c_TFC_Allowed_0_1_4_5_9, c_UE_TestLoopMode1_LB_Setup2 (1272,tsc_RB20, 1272, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB22, 1272, 60), OMIT, OMIT, OMIT), 20)			Subtest 5 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB22 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup; p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10 an RLC SDU. UE shall send back the same RLC SDU. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize))			CMAC Restriction
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- -----
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB22 (tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			12 times max tti among the RABs + 10% of max tti
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14b
10		[tcv_result=TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_ReceiveFirstSDU_RB22(tcv_RB_Data1)			for TTCN Delay Step 15a.1
12		+ts_Simultaneous_Data_SRB_RB22(tcv_RB_Data1,p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu)			the SS also checks the returned data during this time.
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16-17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB22 (p_data : BITSTRING;p_no_of_sdus: INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			
2		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data(p_data))	(P)	
3		(tcv_count_RB22 := 1) CANCEL t_Dly			
4		REPEAT It_Receive UNTIL [tcv_count_RB22 = p_no_of_sdus]			
5		?TIMEOUT t_Dly		(I)	
6		+ts_Exit_Testcase			
7		It_Receive AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data))		
8		(tcv_count_RB22:=tcv_count_RB22+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB22 (p_data : BITSTRING;p_no_of_sdus: INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective :					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl (tsc_CellDedicated, tsc_RB2, cs_MeasurementControlDef Periodic (tcv_CellIndInfo.dl_Integrity CheckInfo , tcv_RRC_Ti , tcv_TmpCellInfo.priScrmCo de))	(P)	15a.2
2		START t_Dly(1000)			
3		AM ? RLC_AM_DATA_IND CANCEL t_Dly	car_MeasurementReport(tsc_CellDedicated, tsc_RB2, cr_MeasurementReportAny)		15b
4		REPEAT It_Receive UNTIL [tcv_count_RB22 = p_no_of_sdus]			
5		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data(p_data))		15b.1
6		(tcv_count_RB22:=tcv_count_RB22+1)			
7		-> Get_Report			
8		? TIMEOUT t_Dly			
9		+ts_Exit_Testcase			
		It_Receive			
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data))		
11		(tcv_count_RB22:=tcv_count_RB22+1)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_to_7_tc_14_2_58(p_Data_String : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_8_9, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 40)			Subtest 1 Steps 11–17
2		+ts_RB_SubTest_RAB_SRB_RB20_Special_3(c_ TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_2_8_10, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 40, 1)			Subtest 2 Steps 11–17
3		+ts_RB_SubTest_RAB_SRB_RB20_Special_3(c_TFC_Allowed_0_1_2_4_5, c_TFC_Allowed_0_1_3_8_11, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,2552,60), OMIT, OMIT, OMIT), 40, 1)			Subtest 3 Steps 11–17
4		+ ts_RB_SubTest_RAB_SRB_RB22 (c_TFC_Allowed_0_1_2_4_6, c_TFC_Allowed_0_4_8_12, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT, OMIT), 40)			Subtest 4 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ ts_RB_SubTest_RAB_SRB_RB20_RB22 (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_4_5_8_13, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 632, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB20_RB22_ Special (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_4_6_8_14, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 1272, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40, 1)			Subtest 6 Steps 11–17
7		+ ts_RB_SubTest_RAB_SRB_RB20_RB2 2_Special (c_TFC_Allowed_0_1_2_3_4_5_6_7, c_TFC_Allowed_0_1_4_7_8_15, c_UE_TestLoopMode1_LB_Setup2 (632,tsc_RB20, 312, tsc_RB22), c_RAB_Tx_Info (p_Data_String, 2, c_RB_Tx_Info(tsc_RB20, 2552, 60), c_RB_Tx_Info(tsc_RB22, 312, 30), OMIT, OMIT), 40, 1)			Subtest 7 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20_RB22 (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti :INTEGER) Group : RB_Steps/RB_Subtests/ Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 and SRB2 RLC SDUs. UE shall send back the same RLC SDU on the same 3 RBs and SRB2. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1 Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombC trlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatComb Ctrl (tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		(tcv_RB_Data1 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize), tcv_RB_Data2 := o_GetMostSignificantBits (p_RAB_Tx_Info.testData , p_RAB_Tx_Info.rbTxInfoList.[1].sduSize))			
4		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
5		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			Step 14a
6		[tcv_result=TRUE]			Timer Value ----- -----
7		(tcv_max_Timer:=(p_max_tti * 12) +(p_max_tti/10))			
8		+ts_ReceiveData_RB20_RB22 (tcv_RB_Data1,tcv_RB_Data2,p_RAB _Tx_Info)			
9		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			12 times max tti among the RABs + 10% of max tti Step 14b
10		[tcv_result=TRUE]			
11		+ts_ReceiveFirstSDU_RB20_RB 22(tcv_RB_Data1,tcv_RB_Data2)			
					for TTCN Delay Step 15a.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_Simultaneous_Data_SRB_R B20_RB22(tcv_RB_Data1, tcv_RB_Data2, p_RAB_Tx_Info)			
13		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			Step 16–17
14		[tcv_result=FALSE]		(I)	
15		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
16		[tcv_result=FALSE]		(I)	
17		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB20_RB22 (p_data1,p_data2: BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB20_RB22 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]			
4		It_Receive3			
5		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (Step 15
6		(tcv_count_RB20:= tcv_count_RB20 + 1)	tsc_CellDedicated,		
7		+ It_CheckRes	tsc_RB20, c_TrD_Data (
8		AM ? RLC_AM_TestDataInd	p_data1))		
9		(tcv_count_RB22:= tcv_count_RB22 + 1)	car_RLC_AM_DataInd (Step 15
10		+ It_CheckRes	tsc_CellDedicated,		
11		It_CheckRes	tsc_RB22, c_TrD_Data (
12		[(tcv_count_RB20 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB22 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]	p_data2))		
13		(tcv_Res := TRUE)			
14		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_ReceiveFirstSDU_RB20_RB22 (p_data1,p_data2: BITSTRING)						
Group : RB_Steps/RB_Subtests/						
Objective :						
Default :						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	Get_ Data	(tcv_ReceiveRB20 := FALSE, tcv_ReceiveRB22 := FALSE, tcv_Res := FALSE, tcv_count_RB20 := 0, tcv_count_RB22 := 0)	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data1))	(P)	Step 15	
2		START t_Dly(tcv_max_Timer)				
3		AM ? RLC_AM_TestDataInd				
4		(tcv_count_RB20:= tcv_count_RB20 + 1, tcv_ReceiveRB20 := TRUE)				
5		+It_CheckStatus				
6		[tcv_Res = FALSE]				
7		-> Get_Data				
8		[tcv_Res = TRUE]				
9		CANCEL t_Dly				
10		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data2))	(P)	Step 15	
11		(tcv_count_RB22:= tcv_count_RB22 + 1,tcv_ReceiveRB22 := TRUE)				
12		+It_CheckStatus				
13		[tcv_Res = FALSE]				
14		-> Get_Data				
15		[tcv_Res =TRUE]				
16		CANCEL t_Dly				
17		? TIMEOUT t_Dly				(I)
18		+ts_Exit_Testcase				
		It_CheckStatus				
19		[(tcv_ReceiveRB20 = TRUE) AND (tcv_ReceiveRB22 = TRUE)]				
20		(tcv_Res := TRUE)				
21	[TRUE]					
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB20_RB22 (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res := FALSE)	tsc_CellDedicated,		Step 15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND CANCEL t_Dly	cs_MeasurementControlDef		
5		REPEAT It_Receive3 UNTIL [tcv_Res = TRUE]	Periodic (
6		AM ? RLC_AM_TestDataInd	tcv_CellIndInfo.dl_Integrity		
7		(tcv_count_RB20 := tcv_count_RB20 + 1)	CheckInfo ,		
8		->Get_Report	tcv_RRC_Ti ,		
9		AM ? RLC_AM_TestDataInd	tcv_TmpCellInfo.priScrmCode))		
10		(tcv_count_RB22 := tcv_count_RB22 + 1)	car_MeasurementReport(
11		->Get_Report	tsc_CellDedicated,		
12		?TIMEOUT t_Dly	tsc_RB2,		
13		+ts_Exit_Testcase	cr_MeasurementReportAny		
14		It_Receive3)		
15		(tcv_count_RB20:= tcv_count_RB20 + 1)	car_RLC_AM_DataInd (
16		+ It_CheckRes	tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data1))		
			car_RLC_AM_DataInd (
			tsc_CellDedicated,		
			tsc_RB22, c_TrD_Data (
			p_data2))		
				(I)	Step 15b.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17		AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data2))		Step 15b.1
18		(tcv_count_RB22:= tcv_count_RB22 + 1)			
19		+ It_CheckRes			
		It_CheckRes			
20		[(tcv_count_RB20 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB22 =p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu)]			
21		(tcv_Res := TRUE)			
22		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB_SubTest_RAB_SRB_RB20_RB22_Special (p_TFC_UL, p_TFC_DL : TFC_Subset; p_TestLoopModeSetup : UE_TestLoopMode1LB_Setup;p_RAB_Tx_Info: RabTxInfo;p_max_tti,p_ReceiveFactor:INTEGER)					
Group : RB_Steps/RB_Subtests/					
Objective : SS limits the UE allowed uplink transport format combinations, SS closes the test loop, then SS transmit on RB10, RB11 and RB12 an RLC SDU. UE shall send back the same RLC SDU on the same 3 RBs. Refer to steps 11 to 17 of TS 34.123-1 clause 14.1.1					
Default : RRC_Def1					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		AM ! RLC_AM_DATA_REQ	cas_TransportFormatCombCtrlAM (tsc_CellDedicated, tsc_RB2, cbs_TransportFormatCombCtrl (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, p_TFC_UL))		Step 11
2		+ ts_TC_CloseUE_TestLoop (tsc_CellDedicated, tsc_UE_TestLoopMode1, p_TestLoopModeSetup)			Steps 12-13
3		+ts_RB_Prepare_DataToBeReceived(p_RAB_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupRB_IE1.rLC_SDU_Size) , p_RAB_Tx_Info.rbTxInfoList.[0].sduSize)			
4		(tcv_RB_Data1 := tcv_RB_testdata3)			
5		+ts_RB_Prepare_DataToBeReceived(p_RAB_Tx_Info.testData, BIT_TO_INT(p_TestLoopModeSetup.IB_SetupRB_IE2.rLC_SDU_Size), p_RAB_Tx_Info.rbTxInfoList.[1].sduSize)			
6		(tcv_RB_Data2 := tcv_RB_testdata3)			
7		+ts_SS_TFC_Restriction (tsc_CellDedicated, p_TFC_UL, p_TFC_DL)			
8		+ts_SendDataInContinuousTTI(p_RAB_Tx_Info)			Step 14a
9		[tcv_result=TRUE]			Timer Value ----- -----
10		(tcv_max_Timer:=(p_max_tti * 12) + (p_max_tti/10))			
11		+ts_ReceiveData_RB20_RB22_Special (tcv_RB_Data1,tcv_RB_Data2,p_RAB_Tx_Info,p_ReceiveFactor)			12 times max tti among the RABs + 10% of max tti Step 14b

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ts_SendDataInContineousTTI(p_RAB_Tx_Info)			
13		[tcv_result=TRUE]			
14		+ts_ReceiveFirstSDU_RB20 _RB22(tcv_RB_Data1, tcv_RB_Data2)			for TTCN Delay Step 15a.1
15		+ts_Simultaneous_Data_S RB_RB20_RB22_Special (tcv_RB_Data1,tcv_RB_D ata2,p_RAB_Tx_Info,p_Re ceiveFactor)			
16		+ ts_TC_OpenUE_TestLoo p (tsc_CellDedicated)			Step 16-17
17		[tcv_result=FALSE]		(I)	
18		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
19		[tcv_result=FALSE]		(I)	
20		+ ts_TC_OpenUE_TestLoop (tsc_CellDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveData_RB20_RB22_Special (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor:INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_ReceiveFirstSDU_RB20_RB22 (p_data1,p_data2)			
2		(tcv_Res := FALSE)			
3		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]			
4		It_Receive2 AM ? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB20, c_TrD_Data (p_data1))		Step 15
5		(tcv_count_RB20:= tcv_count_RB20 + 1)			
6		+ It_CheckRes			
7		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data2))		Step 15
8		(tcv_count_RB22:= tcv_count_RB22 + 1)			
9		+ It_CheckRes			
10		It_CheckRes [(tcv_count_RB20 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB22 =(p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu*p_ReceiveFactor))]			
11		(tcv_Res := TRUE)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Simultaneous_Data_SRB_RB20_RB22_Special (p_data1,p_data2 : BITSTRING; p_RAB_Tx_Info: RabTxInfo;p_ReceiveFactor : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	Get_Report	AM ! RLC_AM_DATA_REQ	cas_MeasurementControl ((P)	15a.2
2		(tcv_Res := FALSE)	tsc_CellDedicated,		15b
3		START t_Dly(1000)	tsc_RB2,		
4		AM ? RLC_AM_DATA_IND	cs_MeasurementControlDef		
5		CANCEL t_Dly	Periodic (
6		REPEAT It_Receive2 UNTIL [tcv_Res = TRUE]	tcv_CellIndInfo.dl_Integrity		
7		AM ? RLC_AM_TestDataInd	CheckInfo ,		
8		(tcv_count_RB20 := tcv_count_RB20 + 1)	tcv_RRC_Ti ,		
9		->Get_Report	tcv_TmpCellInfo.priScrmCo		
10		AM ? RLC_AM_TestDataInd	de))		
11		(tcv_count_RB22 := tcv_count_RB22 + 1)	car_MeasurementReport(
12		->Get_Report	tsc_CellDedicated,		
13		?TIMEOUT t_Dly	tsc_RB2,		
14		+ts_Exit_Testcase	cr_MeasurementReportAny		
15		It_Receive2)		
16		(tcv_count_RB20:= tcv_count_RB20 + 1)	car_RLC_AM_DataInd (
17		+ It_CheckRes	tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data1))		
			car_RLC_AM_DataInd (
			tsc_CellDedicated,		
			tsc_RB22, c_TrD_Data (
			p_data2))		
			car_RLC_AM_DataInd (
			tsc_CellDedicated,		
			tsc_RB20, c_TrD_Data (
			p_data1))		
				(I)	Step 15b.1

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		AM? RLC_AM_TestDataInd	car_RLC_AM_DataInd (tsc_CellDedicated, tsc_RB22, c_TrD_Data (p_data2))		Step 15b.1
19		(tcv_count_RB22 := tcv_count_RB22 + 1)			
20		+ It_CheckRes			
		It_CheckRes			
21		[(tcv_count_RB20 = p_RAB_Tx_Info.rbTxInfoList.[0].nomOfSdu) AND (tcv_count_RB22 = (p_RAB_Tx_Info.rbTxInfoList.[1].nomOfSdu * p_ReceiveFactor))]			
22		(tcv_Res := TRUE)			
23		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_14_TC_14_2_40 (p_Data_String: BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_15_16, c_TFC_Allowed_0_1_15_16, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1 Steps 11-17
2		+ ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12(c_TFC_Allowed_0_1_2_3_15_17, c_TFC_Allowed_0_2_15_17, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 2 Steps 11-17
3		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_15_18, c_TFC_Allowed_0_3_15_18, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT, OMIT), 20)			Subtest 3 Steps 11-17
4		+ts_RB_SubTest_RAB_SRB_RB10_RB20 (c_TFC_Allowed_0_1_2_3_4_15_16_18_19, c_TFC_Allowed_0_3_4_15_19, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20),c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,60), OMIT, OMIT), 20)			Subtest 4 Steps 11-17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_5_15_17_18_20 , c_TFC_Allowed_0_3_5_15_20, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20) ,c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,60)), 20)			Subtest 5 Steps 11–17
6		+ ts_RB_SubTest_RAB_SRB_RB20 (c_TFC_Allowed_0_1_2_3_6_15_21, c_TFC_Allowed_0_3_6_15_21, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT, OMIT), 20)			Subtest 6 Steps 11–17
7		+ ts_RB_SubTest_RAB_SRB_RB10_RB20(c_TFC_Allowed_0_1_2_3_6_7_15_16_21_22, c_TFC_Allowed_0_3_7_15_22, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,632,60), OMIT, OMIT), 20)			Subtest 7 Steps 11–17
8		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_6_8_15_17_21_23, c_TFC_Allowed_0_3_8_15_23, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,632, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info (tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12, 60,60), c_RB_Tx_Info (tsc_RB20,632,60)), 20)			Subtest 8 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ ts_RB_SubTest_RAB_SRB_RB20(c_TFC_Allowed_0_1_2_3_9_15_24, c_TFC_Allowed_0_3_9_15_24, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,952,60), OMIT, OMIT, OMIT), 20)			Subtest 9 Steps 11–17
10		+ ts_RB_SubTest_RAB_SRB_RB10_ RB20(c_TFC_Allowed_0_1_2_3_9_10_1 5_16_24_25, c_TFC_Allowed_0_3_10_15_25, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,952,60), OMIT, OMIT), 20)			Subtest 10 Steps 11–17
11		+ ts_RB_SubTest_RAB_SRB_RB1 0_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_9_11_ 15_17_24_26, c_TFC_Allowed_0_3_11_15_26, cb_UE_TestLoopMode1LB_Setu p4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,952, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info (tsc_RB11,103,60), c_RB_Tx_Info (tsc_RB12,60,60), c_RB_Tx_Info (tsc_RB20,952,60)), 20)			Subtest 11 Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		+ ts_RB_SubTest_RAB_SRB_RB 20(c_TFC_Allowed_0_1_2_3_12_ 15_27, c_TFC_Allowed_0_3_12_15_2 7, cb_UE_TestLoopMode1LB_Set up4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,1272,60), OMIT, OMIT, OMIT), 20)			Subtest 12 Steps 11–17
13		+ ts_RB_SubTest_RAB_SRB_R B10_RB20(c_TFC_Allowed_0_1_2_3_12_ _13_15_16_27_28, c_TFC_Allowed_0_3_13_15_ 28, cb_UE_TestLoopMode1LB_S etup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,1272,60),OMIT, OMIT), 20)			Subtest 13 Steps 11–17
14		+ ts_RB_SubTest_RAB_SRB_ RB10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_1 2_14_15_17_27_29, c_TFC_Allowed_0_3_14_15_ _29, cb_UE_TestLoopMode1LB_ Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,1272, tsc_RB20) , c_RAB_Tx_Info (p_Data_String, 4, c_RB_Tx_Info (tsc_RB10,81,60), c_RB_Tx_Info (tsc_RB11,103,60),c_RB_T x_Info (tsc_RB12,60,60), c_RB_Tx_Info (tsc_RB20,1272,60)), 20)			Subtest 14 Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Subtests_1_14_TC_14_2_38f (p_Data_String: BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RB_SubTest_RAB_SRB_RB10(c_TFC_Allowed_0_1_2_3_4_5_6_12_13, c_TFC_Allowed_0_1_12_13, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12,312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 1, c_RB_Tx_Info(tsc_RB10,39,60), OMIT, OMIT, OMIT), 20)			Subtest 1. Test Data on RB10 and Measurm ent control Report on SRB.
2		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_12_14, c_TFC_Allowed_0_2_12_14, cb_UE_TestLoopMode1LB_Setup4 (42,tsc_RB10, 53, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,42,60), c_RB_Tx_Info(tsc_RB11,53,60), OMIT, OMIT), 20)			Subtest 2 Test Data on RB10 , RB11 and Measurm ent control Report on SRB.
3		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_12_15, c_TFC_Allowed_0_3_12_15, cb_UE_TestLoopMode1LB_Setup4 (55,tsc_RB10, 63, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,55,60), c_RB_Tx_Info(tsc_RB11,63,60), OMIT, OMIT), 20)			Subtest 3 Test Data on RB10 , RB11 and Measurm ent control Report on SRB.
4		+ts_RB_SubTest_RAB_SRB_RB10_RB11(c_TFC_Allowed_0_1_2_3_4_5_6_12_16 , c_TFC_Allowed_0_4_12_16, cb_UE_TestLoopMode1LB_Setup4 (75,tsc_RB10, 84, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,75,60), c_RB_Tx_Info(tsc_RB11,84,60), OMIT, OMIT), 20)			Subtest 4 Test Data on RB10 , RB11 and Measurm ent control Report on SRB.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
5		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB12(c_TFC_Allowed_0_1_2_3_4_5_6_12_17, c_TFC_Allowed_0_5_12_17, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60), c_RB_Tx_Info(tsc_RB12,60,60), OMIT), 20)			Subtest 5 Test Data on RB10 , RB11, RB12 and Measurm ent control Report on SRB.
6		+ts_RB_SubTest_RAB_SRB_RB20(c_TFC_Allowed_0_1_2_3_4_5_6_12_18, c_TFC_Allowed_0_6_12_18, cb_UE_TestLoopMode1LB_Setup4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 1, c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT, OMIT), 40)			Steps 11–17 Subtest 6. Test Data on RB20 and Measurm ent control Report on SRB.
7		+ ts_RB_SubTest_RAB_SRB_RB10_RB2 0(c_TFC_Allowed_0_1_2_3_4_5_6_7_ 12_13_18_19, c_TFC_Allowed_0_6_7_12_19, cb_UE_TestLoopMode1LB_Setup4 (39,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 2, c_RB_Tx_Info(tsc_RB10,39,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT, OMIT), 40)			Steps 11–17 Subtest 7. Test Data on RB10, RB20 and Measurm ent control Report on SRB.
					Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB20(c_TFC_Allowed_0_1_2_3_4_5_6_8_12_14_18_20, c_TFC_Allowed_0_6_8_12_20, cb_UE_TestLoopMode1LB_Setup4 (42,tsc_RB10, 53, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,42,60), c_RB_Tx_Info(tsc_RB11,53,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT), 40)			Subtest 8 Test Data on RB10, RB11, RB20 and Measurm ent control Report on SRB.
9		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB20(c_TFC_Allowed_0_1_2_3_4_5_6_9_12_15_18_21, c_TFC_Allowed_0_6_9_12_21, cb_UE_TestLoopMode1LB_Setup4 (55,tsc_RB10, 63, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,55,60), c_RB_Tx_Info(tsc_RB11,63,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT), 40)			Steps 11–17 Subtest 9 Test Data on RB10, RB11, RB20 and Measurm ent control Report on SRB.
10		+ts_RB_SubTest_RAB_SRB_RB10_RB11_RB20(c_TFC_Allowed_0_1_2_3_4_5_6_10_12_16_18_22, c_TFC_Allowed_0_6_10_12_22, cb_UE_TestLoopMode1LB_Setup4 (75,tsc_RB10, 84, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info(p_Data_String, 3, c_RB_Tx_Info(tsc_RB10,75,60), c_RB_Tx_Info(tsc_RB11,84,60), c_RB_Tx_Info(tsc_RB20,312,30), OMIT), 40)			Steps 11–17 Subtest 10 Test Data on RB10 , RB11, RB20 and Measurm ent control Report on SRB. Steps 11–17

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		+ts_RB_SubTest_RAB_SRB_RB 10_RB11_RB12_RB20(c_TFC_Allowed_0_1_2_3_4_5_6 _11_12_17_18_23, c_TFC_Allowed_0_6_11_12_23, cb_UE_TestLoopMode1LB_Setu p4 (81,tsc_RB10, 103, tsc_RB11, 60, tsc_RB12, 312, tsc_RB20), c_RAB_Tx_Info (p_Data_String, 4, c_RB_Tx_Info(tsc_RB10,81,60), c_RB_Tx_Info(tsc_RB11,103,60) , c_RB_Tx_Info(tsc_RB12,60,60), c_RB_Tx_Info(tsc_RB20,312,30)), 40)			Subtest 11 Test Data on RB10, RB11, RB12, RB20 and Measurm ent control Report on SRB. Steps 11–17
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Bind_TestData_TTI (p_cellId , p_NoOfRBs : INTEGER) Group : RB_Steps/RB_Subtests/ Objective : To configure the SS to send the data on the TM RBS in one TTI Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Bind_TestData_TTI_REQ	cas_CRLC_Bind_TestData_TTI_REQ (p_cellId , p_NoOfRBs)		
2		CRLC ? CRLC_Bind_TestData_TTI_CNF	car_CRLC_Bind_TestData_TTI_CNF_Success (p_cellId)		
3		(tcv_Bind_TestData := TRUE)			
4		CRLC ? CRLC_Bind_TestData_TTI_CNF	car_CRLC_Bind_TestData_TTI_CNF_Failure (p_cellId)		
5		(tcv_Bind_TestData := FALSE)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB10 (p_data : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			for TTCN Delay Step 15a.1
2		(tcv_Receive_RB10 := 0)			
3		TM ? RLC_TR_TestDataInd CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB10, c_TrD_Data(p_data))		15b.1
4		(tcv_Receive_RB10:=tcv_Receive_RB10+1)			
5		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReceiveFirstSDU_RB13 (p_data : BITSTRING) Group : RB_Steps/RB_Subtests/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly (tcv_max_Timer)			for TTCN Delay Step 15a.1
2		(tcv_Receive_RB13 := 0)			
3		TM ? RLC_TR_TestDataInd CANCEL t_Dly	car_RLC_DataInd (tsc_CellDedicated, tsc_RB13, c_TrD_Data(p_data))		15b.1
4		(tcv_Receive_RB13:=tcv_Receive_RB13+1)			
5		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_3SCCPCH_CTCH_2a(p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : NewTestSteps/ Objective : econfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti] CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH3, c_UE_Info (tcv_TmpCellInfo.uRNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS_2a)		map CCCH, BCCH, DTCH U-RNTI are required.
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
7		[NOT p_urnti]			
8		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_2_ DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
9		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
10		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH3, c_UE_Info (OMIT, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS_2a)		map CCCH, BCCH, DTCH C-RNTI are required.
11		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH3)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_2SCCPCH_2a(p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : NewTestSteps/ Objective : Reconfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti] CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH1, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS_2a)		SS has valid U-RNTI, C-RNTI is not valid
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
7		[NOT p_urnti]			
8		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_2_DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
9		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
10		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS_2a)		
11		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_3SCCPCH_2a (p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : NewTestSteps/ Objective : Reconfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti]			
6		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1)]			
		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH3, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS_2a)		SS has valid U-RNTI, C-RNTI is not valid
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH3)		
8		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
9		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_SCCPCH2_P S, c_TrLogMappingFACH_BC CH_DCCH_CCCH_SCCP CH2_PS_2a)		SS has valid U-RNTI, C-RNTI is not valid
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
11		[NOT p_urnti]			
12		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_2_DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
15		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH3, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BCCH_DCCH_CCCH_PS_2a)		
16		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH3)		
17		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
18		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_2_DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
19		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
20		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_SCCPCH2_P S, c_TrLogMappingFACH_BCCH_DCCH_CCCH_SCCPCH2_PS_2a)		
21		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
22		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_3SCCPCH (p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : NewTestSteps/ Objective : Reconfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
4		[p_urnti]			
5		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1)]			
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH3, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS)		SS has valid U-RNTI, C-RNTI is not valid
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH3)		
8		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)]			
9		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_BCCH_C CCH_DCCH_SCCPCH2_P S, c_TrLogMappingFACH_BC CH_DCCH_CCCH_SCCP CH2_PS)		SS has valid U-RNTI, C-RNTI is not valid
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
11		[NOT p_urnti]			
12		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DT CH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
15		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH3, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_PS, c_TrLogMappingFACH_BC CH_DCCH_CCCH_PS)		
16		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH3)		
17		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)]			
18		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DT CH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
19		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
20		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_BCCH_C CCH_DCCH_SCCPCH2_P S, c_TrLogMappingFACH_BC CH_DCCH_CCCH_SCCP CH2_PS)		
21		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
22		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendModifiedSIB5_withoutSIB6_3SCCPCH (p_CellId: INTEGER)					
Group : NewTestSteps/					
Objective : To broadcast default system information.					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			Fetch record corresponding to current cell
2		[px_RAT = fdd]			
3		+ts_SendSIB5(cb_SIB5_Def_3SCCPCH(tcv_TmpCellInfo), p_CellId, tsc_Now)			
4		+ts_SendSB1_DefSchedul(tcv_SB1, p_CellId, tsc_Now)			
5		+ts_SendMIB(tcv_MIB, p_CellId, tsc_Now)			
6		+ts_SendPage1_ModifySI(p_CellId, tcv_MIB.mib_ValueTag)			
7	ERR1	[px_RAT = tdd]			
8	ERR2	[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendDefSysInfo_withoutSIB6_3SCCPCH (p_CellId: INTEGER) Group : NewTestSteps/ Objective : To broadcast default system information. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			Fetch record corresponding to current cell
2					
3		+ ts_UTRAN_GERAN_ParaInit(p_CellId)			
4		+ts_CellDependentPara(p_CellId)			
5		+ts_InitializeSIB2AndSIB18(tcv_TmpCellInfo)			
6		+ ts_InitializeSIB11_SIB12 (p_CellId)			
7		+ts_InitializeSB			
8		[px_RAT = fdd]			
9		+It_FillNoneScheduledBlocks			
10		+ts_SendNoSegDefSchedul(p_CellId)			
11		+ts_SendSIB1 (cb_SIB1_Def(tcv_TmpCellInfo), p_CellId, tsc_Now)			
12		+ts_SendSIB2 (tcv_SIB2 , p_CellId, tsc_Now)			
13		+ts_SendSIB3(tcv_SIB3, p_CellId, tsc_Now)			
14		+ts_SendSIB4(tcv_SIB4, p_CellId, tsc_Now)			
15		+ts_SendSIB5(cd_SIB5_NoSib6 (tcv_TmpCellInfo), p_CellId, tsc_Now)			
16		+ts_SendSIB7(c_SIB7_Def, p_CellId, tsc_Now)			
17		+ts_SendSIB11_RAB(tcv_SIB11, p_CellId, tsc_Now)			
18		+ts_SendSIB12_RAB(tcv_SIB12, p_CellId, tsc_Now)			
19		+ts_SendSIB18_RAB (tcv_SIB18, p_CellId, tsc_Now)			
20		+ts_SendSB1_DefSchedul(tcv_SB1, p_CellId, tsc_Now)			
21	ERR1	[px_RAT = tdd]		I	
22	ERR2	[TRUE]		I	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		It_FillNoneScheduledBlocks			
24		+ts_Scheduling(p_CellId, 6, 3, tsc_Now)			pos = 3
		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
25		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
26		+ts_Scheduling(p_CellId, 6, 5, tsc_Now)			pos = 5
27		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
29		+ts_Scheduling(p_CellId, 6, 6, tsc_Now)			pos = 6
30		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
31		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
32		+ts_Scheduling(p_CellId, 6, 7, tsc_Now)			pos = 7
33		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
34		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB10_TM_Cfg_Segmented (p_PayLoad: INTEGER)					
Group : NewTestSteps/					
Objective : setup radio bearers : RB10. default values from 34.123–1Table 7.2/3A					
Default : SS_Def					
Comments : CRLC is configured with cellId –1 (tsc_CellDedicated)					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info_Segmented (tsc_CellDedicated , tsc_RB10, p_PayLoad, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 (TM + DTCH) For use in RAB test cases for Non Segment ed RLC TM configur ation where local SS is configur ed for segment ated operatio n,
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated , tsc_RB10)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB11_RAB(p_SIB: SysInfoType11; p_CellId : INTEGER; p_Timing: INTEGER) Group : NewTestSteps/ Objective : To deliver the SIBType11 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB11 : p_SIB)))			1.
2		[tcv_Segs.segCount >3]		I	2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul1)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+It_CompleteSIB(systemInformationBlockType11)			4.
10		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul2)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
22		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 2, tcv_Segs.seg1))		4.
25		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul3)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
37		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 3, tcv_Segs.seg1))		4.
40		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg3) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType11, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType11, 2, tcv_Segs.seg3))		9.
46		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
47		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
48		[TRUE]			
49		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB11 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB11 (current assumption). 3. Send the scheduling info to SS. one segment: REP=64, POS=29. 4. Construct the system information message containing first segment of SIB11 and send it to SS. 5. Send the scheduling info to SS. one segment: REP=64, POS=30. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB11 and send it to SS. 8. Send the scheduling info segment to SS. one segment: REP=64, POS=31. 9. Construct the system information message containing the third segment of SIB11 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB12_RAB (p_SIB: SysInfoType12; p_CellId : INTEGER; p_Timing: INTEGER) Group : NewTestSteps/ Objective : To deliver the SIBType12 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB12 : p_SIB)))			1.
2		[tcv_Segs.segCount >3]		I	2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul1)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+It_CompleteSIB(systemInformationBl ockType12)			4.
10		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config _CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_C onfig_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul2)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
22		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 2, tcv_Segs.seg1))		4.
25		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul3)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
37		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 3, tcv_Segs.seg1))		4.
40		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg2) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType12, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType12, 2, tcv_Segs.seg3))		9.
46		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
47		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
48		[TRUE]			
49		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB12 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB12 (current assumption). 3. Send the scheduling info segment to SS. one segment: REP=64, POS=13 . 4. Construct the system information message containing the first segment of SIB12 and send it to SS. 5. Send the scheduling info segment to SS. one segment: REP=64, POS=14. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB12 and send it to SS. 8. Send the scheduling info to SS. one segment: REP=64, POS=15 . 9. Construct the system information message containing the third segment of SIB12 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB18_RAB (p_SIB: SysInfoType18; p_CellId : INTEGER; p_Timing: INTEGER) Group : NewTestSteps/ Objective : To deliver the SIB18 to SS Default : InitOtherwiseFail Comments : SIB18 is concatenated with SIB7, default scheduling described in 3GPP TS 34.123-3 clause 8.4.3 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB18 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB18 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB7 : tcv_SIB7))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) > 201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 18, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg2, systemInformationBlockType 18, tcv_Segs.seg1))		4.
7		(tcv_SB1.sib_ReferenceList.[3].sib_Type.sysInfoType18 := (tcv_SB1.sib_ReferenceList.[3].sib_Type.sysInfoType18) MOD 4 + 1)			
8		+ts_ChangeSB1_ValueTag			
Detailed Comments : 1. save the new value of SIB18, unaligned PER encoding of the SIB7 and SIB18. 2. The concatenated SIB7 and SIB18 is too long. 3. Send the scheduling info to SS. one segment: REP=64, POS=18. 4. Construct the system information message containing SIB18 + SIB7 and sent to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_InitializeSB Group : NewTestSteps/ Objective : Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SB1 := c_SB1_Def_witoutSIB6)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendRB_SetUpDCH_32k_PS1 (p_CellId: INTEGER; p_RAB_Id : BITSTRING; p_ActTime : ActivationTime) Group : NewTestSteps/ Objective : Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		AM ! RLC_AM_DATA_REQ	cas_RB_SetUpAM_WithCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui, cs_RRC_RB_SetUp(tcv_CellIndInfo.dl_Integrity CheckInfo, tcv_RRC_Ti, p_ActTime, cell_DCH, OMIT, cb_RAB_InfoListAM1_No_ PdcP_RlcSizeList1 (useT315, p_RAB_Id), c_UL_CommTrChInfo_AM0 To9(c_PowerOffsetInfoBelo w64k) , c_UL_AddReconfTransChIn foListAM1 (c_DCH_336_TFS_40_TC_ UE), c_DL_CommonTransChInfo _AM_0_9, c_DL_AddReconfTransChIn foListAM_3_4k(c_DCH_33 6_TFS_40_TC_UE), c_DL_InformationPerRL (tcv_TmpCellInfo.priScrmCo de, tsc_Sfc64 , OMIT), c_DL_CommonInformation RB_SetUp(tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_Scrambl ingCode) , OMIT))		
3		AM ? RLC_AM_DATA_CNF	car_AM_DataMuiCnf (tsc_CellDedicated, tsc_RB2, tsc_Mui)		
4		+ts_SS_2DCH_ModifyInteractBackg_32k_P S_NoSc(p_CellId, p_ActTime, c_DL_CommonInformationRB_SetUp (tsc_Sfd64) , cb_UL_DPCH_Info (tsc_Sf32, pl0_88, tcv_TmpCellInfo.uL_ScramblingCode))			
5		+ts_SS_RB20_AM_PS_Cfg (320)			
6	TSP	+ ts_RRC_ReceiveRB_SetupCmpl (p_CellId , cell_DCH_64kPS_RAB_SRB)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : po_ConnectionAndSS_Rel_14_2_7a (p_CellId : INTEGER) Group : NewTestSteps/ Objective : To release the existing RRC connection and release the channels that are configured in the SS. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[tcv_TmpCellInfo.cellConfig <> cell_NotConfigured]			
3		+ It_Send_RRC_ConnectionRelease			
4		+ts_RRC_Delay(tsc_DelayAfterRRC_ConnRel)			
5		+ ts_SS_Rel_14_2_7a (p_CellId)			
6		[tcv_TmpCellInfo.cellConfig = cell_NotConfigured]		I	0.
7		It_Send_RRC_ConnectionRelease [(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_NoDPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoCon n)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCon n)]			3.
8		[TRUE]			4.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)]			1.
10		UM!RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1, cs_108_RRC_ConnRelDCC H(tcv_CellIndInfo.dl_Integri tyCheckInfo, tcv_RRC_Ti, OMIT))		
11		AM?RLC_AM_DATA_IND	car_RRC_ConnRelCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_ConnRelCm pl (tcv_RRC_Ti))	(P)	
12		[TRUE]			2.
13		(tcv_N308 := 1, tcv_K := 1)			Maximu m number of retransm issions of the RRC CONNE CTION RELEAS E COMPL ETE message
14		UM!RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1, cs_108_RRC_ConnRelDCC H(tcv_CellIndInfo.dl_Integri tyCheckInfo, tcv_RRC_Ti, tcv_N308))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		UM?RLC_UM_DATA_IND	car_RRC_ConnRelCmplUM(tsc_CellDedicated, tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	UE sends RRC Connect ion Release Comple te for N308 times
16		REPEAT It_RptRcvConnRel UNTIL [tcv_K = (tcv_N308+1)]			
17		It_RptRcvConnRel			
18	TSF2	START t_Dly			
19		? TIMEOUT t_Dly (tcv_K := tcv_N308 + 1)		(F)	
20	TSP2	UM?RLC_UM_DATA_IND (tcv_K := tcv_K+1) CANCEL t_Dly	car_RRC_ConnRelCmplUM (tsc_CellDedicated , tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	To stop the loop Retrans mission
Detailed Comments : 0. The cell has not been configured, it shall not be released 1. cell_FACH state 2. cell_DCH state 3. No RRC connection is established 4. An RRC connection is established					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_Rel_14_2_7a (p_CellId : INTEGER) Group : NewTestSteps/ Objective : To release all channels that are configured in the SS. Default : SS_Def Comments : uses "ts_SS_RelDPCH_14_2_7a" instead of "ts_SS_RelDPCH" Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn)]			
3		+ ts_SS_RelDPCH_14_2_7a (p_CellId)			1.
4		+ lt_ReleaseCommonCh			
5		+ lt_Release_BCCH			
6		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
7		[(tcv_TmpCellInfo.cellConfig = cell_NoDPCH)]			
8		+ lt_ReleaseCommonCh			
9		+ lt_Release_BCCH			
10		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
11		[(tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn)]			
12		+ lt_RelSRB1_4			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
14		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
15		+ It_ReleaseCommonCh			
16		+ It_Release_BCCH			
17		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
18		[(tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn)]			
19		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			1.
20		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
21		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_DCCH_FACH_MAC)			
22		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
23		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
24		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
25		+ It_ReleaseCommonCh			
26		+ It_Release_BCCH			
27		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
28		[(tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn)]			
29		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			1.
30		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
31		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
32		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
33		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
34		+ ts_CRLC_Rel (p_CellId, tsc_RB_CCCH_FACH_MAC)			
35		+ It_ReleaseCommonCh			
36		+ It_Release_BCCH			
37		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
38		[(tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn)]			
39		+ It_RelSRB1_4			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
40		+ ts_CRLC_Rel (p_CellId , tsc_RB30)			
41		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
42		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
43		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
44		+ It_ReleaseCommonCh			
45		+ It_Release_BCCH			
46		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
47		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH)]			
48		+ It_RelSRB1_4			
49		+ ts_CRLC_Rel (p_CellId , tsc_RB_2ndCCCH)			
50		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
51		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
52		+ It_ReleaseCommonCh			
53		+ ts_CMAC_Rel (p_CellId, tsc_PRACH2)			
54		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH2)			
55		+ ts_SS_StopRL (p_CellId , tsc_PRACH2)			
56		+ ts_SS_StopRL (p_CellId , tsc_AICH2)			
57		+ It_Release_BCCH			
58		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
59		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH)]			
60		+ It_RelSRB1_4			
61		+ ts_CRLC_Rel (p_CellId , tsc_RB30)			
62		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
63		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
64		+ ts_CRLC_Rel (p_CellId , tsc_RB31)			
65		+ ts_CRLC_Rel (p_CellId , tsc_RB_2ndPCCH)			
66		+ It_ReleaseCommonCh			
67		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
68		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
69		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
70		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
71		+ It_Release_BCCH			
72		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
73		[(tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated)]			
74		+ It_ReleaseCommonCh			
75		+ It_Release_BCCH			
76		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
77		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH)]			
78		+ It_RelSRB1_4			
79		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
80		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
81		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH2)			
82		+ ts_CRLC_Rel (p_CellId , tsc_RB0)			2.
83		+ ts_CMACH_Rel (p_CellId, tsc_PRACH1)			
84		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
85		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
86		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
87		+ ts_CMACH_Rel (p_CellId, tsc_S_CCPCH1)			
88		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
89		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
90		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
91		+ ts_CMACH_Rel (p_CellId, tsc_S_CCPCH2)			
92		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH2)			
93		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
94		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
95		+ It_ReleaseCommonCh			
96		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
97		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPC H2)			
98		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
99		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
100		+ It_Release_BCCH			
101		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
102		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)]			
103		+ It_RelSRB1_4			
104		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
105		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB22)			
106		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB29)			
107		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
108		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			2.
109		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
110		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			
111		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
112		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
113		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
114		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
115		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
116		+ ts_CPHY_TrChRelNonD ch (p_CellId , tsc_S_CCPCH1)			
117		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
118		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
119		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
120		+ ts_CPHY_TrChR elNonDch (p_CellId , tsc_S_CCPCH2)			
121		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH 2)			
122		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPC H3)			
123		+ ts_CPHY_Tr ChRelNonDc h (p_CellId , tsc_S_CCP CH3)			
124		+ ts_SS_Sto pRL (p_CellId , tsc_S_CC PCH3)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
125		+ It_Releas e_BCCH			
126		+ ts_Set CellCfg (p_Celll d, cell_No tConfig ured)			
127		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)]			
128		+ It_RelSRB1_4			
129		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
130		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB30)			
131		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB29)			
132		+ ts_CRLC_Rel (p_Cellld , tsc_RB_BCCH_FACH)			
133		+ ts_CRLC_Rel (p_Cellld , tsc_RB_BCCH_FACH_RAB)			
134		+ ts_CRLC_Rel (p_Cellld , tsc_RB_PCCH)			
135		+ ts_CRLC_Rel (p_Cellld, tsc_RB0)			2.
136		+ ts_CMAC_Rel (p_Cellld, tsc_PRACH1)			
137		+ts_CPHY_TrChRelNonDch (p_Cellld , tsc_PRACH1)			
138		+ ts_SS_StopRL (p_Cellld , tsc_PRACH1)			
139		+ ts_SS_StopRL (p_Cellld , tsc_AICH1)			
140		+ ts_CMAC_Rel (p_Cellld, tsc_S_CCPCH1)			
141		+ ts_CPHY_TrChRelNonD ch (p_Cellld , tsc_S_CCPCH1)			
142		+ ts_SS_StopRL (p_Cellld , tsc_S_CCPCH1)			
143		+ ts_SS_StopRL (p_Cellld , tsc_PICH1)			
144		+ ts_CMAC_Rel (p_Cellld, tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
145		+ts_CPHY_TrChRelNonDch(p_CellId , tsc_S_CCPCH2)			
146		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH 2)			
147		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPC H3)			
148		+ ts_CPHY_TrChRelNonDch(p_CellId , tsc_S_CCP CH3)			
149		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH3)			
150		+ It_Release_BCCH			
151		+ ts_SetCellCfg (p_CellId , cell_No tConfig ured)			
152		[tcv_TmpCellInfo.cellConfig = cell_Two_DTCH]			
153		+ It_RelSRB1_4			
154		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
155		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
156		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
157		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
158		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
159		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
160		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
161		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
162		+ It_ReleaseCommonCh			
163		+ It_Release_BCCH			
164		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
165		[tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS]			
166		+ It_RelSRB1_4			
167		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
168		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
169		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
170		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB13)			
171		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
172		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
173		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
174		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
175		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
176		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
177		+ It_ReleaseCommonCh			
178		+ It_Release_BCCH			
179		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
180		[(tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS)]			
181		+ It_RelSRB1_4			
182		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
183		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
184		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
185		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
186		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
187		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
188		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
189		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
190		+ It_ReleaseCommonCh			
191		+ It_Release_BCCH			
192		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
193		[(tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS)]			
194		+ It_RelSRB1_4			
195		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
196		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
197		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
198		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
199		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
200		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
201		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
202		+ts_CPHY_TrChRelDCH_NoSH O (p_CellId , tsc_UL_DPCH1)			
203		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
204		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
205		+ It_ReleaseCommonCh			2.
206		+ It_Release_BCCH			
207		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
208		[(tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_PS)]			
209		+ It_RelSRB1_4			
210		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
211		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
212		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
213		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
214		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
215		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
216		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
217		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_PDSCH1)			
218		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_DL_PDSCH1)			
219		+ ts_SS_StopRL (p_CellId , tsc_DL_PDSCH1)			
220		+ It_ReleaseCommonCh			
221		+ It_Release_BCCH			
222		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
223		[(tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS)]			
224		+ It_RelSRB1_4			
225		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
226		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
227		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
228		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
229		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
230		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
231		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
232		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
233		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
234		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
235		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_PDSCH1)			
236		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_DL_PDSCH1)			
237		+ ts_SS_StopRL (p_CellId , tsc_DL_PDSCH1)			
238		+ It_ReleaseCommonCh			
239		+ It_Release_BCCH			
240		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
241		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH)]			
242		+ lt_RelSRB1_4			
243		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
244		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
245		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
246		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH2)			
247		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
248		+ ts_CMAL_Rel (p_CellId, tsc_PRACH1)			
249		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
250		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
251		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
252		+ ts_CMAL_Rel (p_CellId, tsc_S_CCPCH1)			
253		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
254		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
255		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
256		+ ts_CMAL_Rel (p_CellId, tsc_S_CCPCH2)			
257		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH2)			
258		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
259		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
260		+ lt_ReleaseComm onCh			
261		+ ts_CMAL_Rel (p_CellId, tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
262		+ ts_CPHY_Tr ChRelNonDc h (p_CellId ,			
263		tsc_S_CCP CH2)			
264		+ ts_SS_Sto pRL (p_CellId , tsc_S_CC PCH2)			
265		+ ts_SS_S topRL (p_CellId ,			
266		tsc_PIC H2)			
267		+ lt_Relea se_BC CH			
268		+ ts_Se tCellC fg (p_Cell Id, cell_N otCo nfigur ed)			
269		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_N oConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_ NoConn) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
270		+ lt_RelSRB1_4			
271		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
272		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB22)			
273		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB23)			
274		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
275		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB29)			
276		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
277		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
276		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			2.
277		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			
278		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
279		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
280		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
281		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
282		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
283		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
284		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
285		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
286		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
287		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPC H2)			
288		+ ts_SS_StopRL (p_CellId , , tsc_S_CCPCH2)			
289		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH3)			
290		+ ts_CPHY_TrChRelNonDch (p_CellId , , tsc_S_CCPCH3)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
291		+ ts_SS_ StopRL (p_Celll d, tsc_S_ CCPC H3)			
292		+ lt_Rel ease_ BCC H			
293		+ ts_ Set Cell Cfg (p_C ellId,			
		cell_ Not Con figur ed)			
294		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_N oConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)]			
295		+ lt_RelSRB1_4			
296		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
297		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
298		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB30)			
299		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB29)			
300		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
301		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			
302		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
303		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
304		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
305		+ts_CPHY_TrChRelNonDch (p_CellId, tsc_PRACH1)			
306		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
307		+ ts_SS_StopRL (
308		p_CellId , tsc_AICH1)			
309		+ ts_CMAC_Rel			
		(p_CellId,			
		tsc_S_CCPCH1)			
		+			
		ts_CPHY_TrChRelNon			
		Dch (p_CellId ,			
		tsc_S_CCPCH1)			
310		+ ts_SS_StopRL (
		p_CellId ,			
		tsc_S_CCPCH1)			
311		+ ts_SS_StopRL (
		p_CellId ,			
		tsc_PICH1)			
312		+ ts_CMAC_Rel			
		(p_CellId,			
		tsc_S_CCPCH2)			
313		+ts_CPHY_TrC			
		hRelNonDch(
		p_CellId ,			
		tsc_S_CCPCH			
		2)			
314		+			
		ts_SS_StopR			
		L (p_CellId ,			
		tsc_S_CCPC			
		H2)			
315		+			
		ts_CMAC_R			
		el (p_CellId,			
		tsc_S_CCP			
		CH3)			
316		+			
		ts_CPHY_			
		TrChRelNo			
		nDch(
		p_CellId ,			
		tsc_S_CC			
		PCH3)			
317		+			
		ts_SS_S			
		topRL (
		p_CellId ,			
		tsc_S_C			
		CPCH3)			
318		+			
		It_Relea			
		se_BC			
		CH			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
319		+ ts_Se tCellC fg (p_Cell Id, cell_N otCo nfigur ed)			
320		[TRUE] It_Release_BCCH			
321		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH)			
322		+ ts_CMAC_Rel (p_CellId , tsc_P_CCPCH)			
323		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_P_CCPCH)			
324		+ ts_SS_StopRL (p_CellId , tsc_S_SCH)			
325		+ ts_SS_StopRL (p_CellId , tsc_P_SCH)			
326		+ ts_SS_StopRL (p_CellId , tsc_P_CCPCH)			
327		+ ts_SS_StopRL (p_CellId , tsc_P_CPICH)			
		It_ReleaseCommonCh			
328		+ ts_CRLC_Rel (p_CellId , tsc_RB0)			2.
329		+ ts_CMAC_Rel (p_CellId , tsc_PRACH1)			
330		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
331		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
332		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
333		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			3.
334		+ ts_CMAC_Rel (p_CellId , tsc_S_CCPCH1)			
335		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
336		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
337		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
		It_RelSRB1_4			
338		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB1)			1.
339		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB2)			
340		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB3)			
341		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB4)			
Detailed Comments : 1. Release DPCH 2. Release PRACH 3. Release S-CCPCH					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RelDPCH_14_2_7a (p_CellId : INTEGER) Group : NewTestSteps/ Objective : To release the DPCH channel. Default : SS_Def Comments : The following channels need to be removed: physical channels: DPCH; transport channels: DCH logical channels: DCCH; and signalling radio bearer: signalling bearers on DCH radio access bearer on DCH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis)]			
3		+ It_RelSRB1_4			
4		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB10)			
5		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB11)			
6		+ ts_CMALC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
7		+ ts_CMALC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
8		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
9		+ ts_CPHY_TrChRelDCH_NoSHO(p_CellId , tsc_UL_DPCH1)			
10		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
11		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
12	ERR	[TRUE]		I	
		It_ReleaseRLC_RB			
13		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis)]			
14		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB_AM_15_RLC)			
15		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis)]			
16		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB_AM_7_RLC)			
17		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis)]			
18		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB_UM_15_RLC)			
19		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis)]			
20		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB_UM_7_RLC)			
		It_RelSRB1_4			
21		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB1)			1.
22		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB2)			
23		+ ts_CRRLC_Rel (tsc_CellDedicated, tsc_RB3)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
24		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_22_AM_Cfg_640 Group : NewTestSteps/ Objective : setup radio bearers : RB20 and 22 mapped on AM Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},640)		configure radio bearers :
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated, tsc_RB20)		RB20 (AM + DTCH)
3		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB22, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2},320)		SS configuration of the radio bearer information :
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_CellDedicated, tsc_RB22)		RB22 (AM + DTCH)
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSysInfo_2SCCPCH (p_CellId: INTEGER; p_SIB5: SysInfoType5) Group : NewTestSteps/ Objective : To broadcast system information for 2SCCPCH Default : InitOtherwiseFail Comments : broadcast MIB, SB1, SIB1, SIB2, SIB3, SIB4, SIB5, SIB7, SIB11, SIB12, SIB18 for test cases using two PRACH or two SCCPCH Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			Fetch record corresponding to current cell
2					
3		+ ts_UTRAN_GERAN_ParaInit(p_CellId)			
4		+ts_CellDependentPara(p_CellId)			
5		+ ts_InitializeSIB11_SIB12 (p_CellId)			
6		[px_RAT = fdd]			
7		(tcv_SB1 :=			
8		c_SB1_BMC_2PRACH_2SCCPCH,			
9		tcv_MIB :=			
10		c_MIB_MulRatOrBMC(tcv_TmpCellInfo))			
11		+ts_SendNoSegBMC(p_CellId)			
12		+ts_SendSIB1_MulRatOrBMC (
13		cb_SIB1_Def(tcv_TmpCellInfo),			
14		p_CellId, tsc_Now)			
15		+ts_SendSIB2_MulRatOrBMC(c_SIB2_Def (tcv_TmpCellInfo),			
16		p_CellId, tsc_Now)			
17		+ts_SendSIB3_MulRatOrBMC(tcv_SIB3,			
18		p_CellId, tsc_Now)			
19	ERR1	[px_RAT = tdd]		I	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf_2sccpch(p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : NewTestSteps/ Objective : Reconfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti] CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH1, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS)		SS has valid U-RNTI, C-RNTI is not valid
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
7		[NOT p_urnti]			
8		[tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH]			
9		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
11		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH2, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS)		
12		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH2)		
13		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendNoSegBMC(p_CellId : INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the system information message with NoSegment to SS on the frames on which there is no any SIB/MIB/SB scheduled Default : InitOtherwiseFail Comments : Current scheduling assumption: Repetition period: 128 frames; Not scheduled positions: frames : 6, 28, 92, 102, 106, 108, 110 (SIB_POS: 3, 14, 46, 51, 53, 54, 55). Above unscheduled positions can be used for other SIBs later, if so this test Step shall be modified accordingly. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_Scheduling(p_CellId, 7, 3, tsc_Now)			pos = 3
2		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
3		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
4		+ts_Scheduling(p_CellId, 7, 14, tsc_Now)			pos = 14
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
7		+ts_Scheduling(p_CellId, 7, 46, tsc_Now)			pos = 46
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
10		+ts_Scheduling(p_CellId, 7, 51, tsc_Now)			pos = 51
11		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
13		+ts_Scheduling(p_CellId, 7, 53, tsc_Now)			pos = 53
14		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
16		+ts_Scheduling(p_CellId, 7, 54, tsc_Now)			pos = 54
17		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
18		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
19		+ts_Scheduling(p_CellId, 7, 55, tsc_Now)			pos = 55

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		CMAC?CMAC_S YSINFO_Config_ CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DA TA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSB1_MulRatOrBMC(p_SB: SysInfoTypeSB1; p_CellId : INTEGER; p_Timing : INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SysInfoTypeSB1 to SS Default : InitOtherwiseFail Comments : for two S-CCPCH or two PRACH (MBC) configuration or multi-Rat handover configuration. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sB1 : p_SB)))			1.
2		[tcv_Segs.segCount >2]		I	2.
3		[tcv_Segs.segCount <=2]			
4		[tcv_Segs.segCount = 1]			
5		+ts_Scheduling(p_CellId, 4, 1, p_Timing)			3.
6		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
7		+lt_CompleteSIB(schedulingBlock1)			4.
8		+ts_Scheduling(p_CellId, 4, 2, p_Timing)			5.
9		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
11		+ts_ChangeSB1_ValueTag			
12		(tcv_MIB.sibSb_ReferenceList.[0]. scheduling := c_SB1_Schedul1, tcv_SB1_ValueTagChanged := FALSE)			
13		[tcv_Segs.segCount = 2]			
14		+ts_Scheduling(p_CellId, 4, 1, p_Timing)			3.
15		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
16		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(scheduling Block1, 2, tcv_Segs.seg1))		4.
17		+ts_Scheduling(p_CellId, 4, 2, p_Timing)			5.
18		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
19		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
20		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(sche dulingBlock1, 1, tcv_Segs.seg2))		7.
21		+ts_ChangeSB1_ValueTag			
22		(tcv_MIB.sibSb_ReferenceList.[0].scheduling := c_SB1_Schedul2, tcv_SB1_ValueTagChanged := FALSE)			
23		[TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(scheduling Block1, 1, tcv_Segs.seg2))		7.
25		+ts_ChangeSB1_ValueTag			
26		(tcv_MIB.sibSb_ReferenceList.[0].scheduling := c_SB1_Schedul2, tcv_SB1_ValueTagChanged := FALSE)			
27		It_CompleteSIB(p_SIBType : SIB_Type)			
28		[LENGTH_OF(tcv_Segs.seg1) = 226] TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
29		[LENGTH_OF(tcv_Segs.seg1) <> 226]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SB1 then segmentation. 2. The result of segmentation shall be less than three segments for the SB1 (current assumption). 3. Send the scheduling info for first segment to SS.(one segment; REP=16; POS=1). 4. Construct the first system information message containing first segment of SB1 and send it to SS. 5. Send the scheduling info for second segment to SS.(one segment; REP=16; POS=2). 6. Send no segment system information message to SS. 7. Construct the second system information message containing second segment of SB1 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB11_MulRatOrBMC(p_SIB: SysInfoType11; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIBType11 to SS Default : InitOtherwiseFail Comments : maximum two segments, scheduled according 3GPP TS 34.123-3 clause 8.4.4.1 and 8.4.4.2 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation (o_SIB_PER_Encoding (sIB11 : p_SIB)))		I	1.
2		[tcv_Segs.segCount >3]			2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul1_MulRatOrBMC)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 7, 29, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+lt_CompleteSIB(systemInformationBlockType11)			4.
10		+ts_Scheduling(p_CellId, 7, 30, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 7, 31, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul2_MulRatOrBMC)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 7, 29, p_Timing)			3.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 2, tcv_Segs.seg1))		4.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		+ts_Scheduling(p_CellId, 7, 31, p_Timing)			8.
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
25		+ts_Scheduling(p_CellId, 7, 30, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[1].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[1].scheduling := c_SIB11_Schedul3_MulRatOrBMC)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 7, 29, p_Timing)			3.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 3, tcv_Segs.seg1))		4.
37		+ts_Scheduling(p_CellId, 7, 30, p_Timing)			5.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
40		+ts_Scheduling(p_CellId, 7, 31, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg2) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType11, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType11, 2, tcv_Segs.seg3))		9.
46		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
47		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
48		[TRUE]			
49		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB11 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB11 (current assumption). 3. Send the scheduling info for first segment to SS. one segment: REP=128, POS=29. 4. Construct the first system information message containing first segment of SIB11 and send it to SS. 5. Send the scheduling info for second segment to SS. one segment: REP=128, POS=30. 6. Send no segment system information message to SS. 7. Construct the second system information message containing second segment of SIB11 and send it to SS. 8. Send the scheduling info for third segment to SS. one segment: REP=128, POS=31. 9. Construct the third system information message containing third segment of SIB11 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB12_BMC(p_SIB: SysInfoType12; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIBType12 to SS Default : InitOtherwiseFail Comments : for BMC test cases using two PRACH or two SCCPCH Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB12 : p_SIB)))			1.
2		[tcv_Segs.segCount >3]		I	2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul1_BMC)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 7, 61, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+lt_CompleteSIB(systemInformationBlockType12)			4.
10		+ts_Scheduling(p_CellId, 7, 62, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 7, 63, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul2_BMC)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 7, 61, p_Timing)			3.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 2, tcv_Segs.seg1))		4.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		+ts_Scheduling(p_CellId, 7, 63, p_Timing)			8.
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
25		+ts_Scheduling(p_CellId, 7, 62, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB12_Schedul3_BMC)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 7, 61, p_Timing)			3.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 3, tcv_Segs.seg1))		4.
37		+ts_Scheduling(p_CellId, 7, 62, p_Timing)			5.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
40		+ts_Scheduling(p_CellId, 7, 63, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg3) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType12, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType12, 2, tcv_Segs.seg3))		9.
46		[TRUE]			
47		It_CompleteSIB(p_SIBType : SIB_Type)			
48		[LENGTH_OF(tcv_Segs.seg1) = 226]			
48		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
49		[TRUE]			
50		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB12 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB12 (current assumption). 3. Send the scheduling info segment to SS. one segment: REP=128, POS=61 . 4. Construct the system information message containing the first segment of SIB12 and send it to SS. 5. Send the scheduling info to SS. (one segment: REP=128, POS=62). 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB12 and send it to SS. 8. Send the scheduling info to SS. (one segment: REP=128, POS=63). 9. Construct the system information message containing the third segment of SIB12 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB18_MulRatOrBMC (p_SIB: SysInfoType18; p_CellId : INTEGER; p_Timing: INTEGER)					
Group : RRCM_SysInfo/RRCM_MRAT_BMC/					
Objective : To deliver the SIB18 to SS					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB18 : p_SIB)))		I	1.
2		[tcv_Segs.segCount <>1]			2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 7, 6, p_Timing)			
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		+lt_CompleteSIB(systemInformationBlockTy pe18)			4.
7		(tcv_SB1.sib_ReferenceList.[3].sib_Type.s ysInfoType18 := (tcv_SB1.sib_ReferenceList.[3].sib_Type.s ysInfoType18) MOD 4 + 1)			
8		+ts_ChangeSB1_ValueTag			
		lt_CompleteSIB(p_SIBType : SIB_Type)			
9		[LENGTH_OF(tcv_Segs.seg1) = 226]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
11		[TRUE]			
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB7 then segmentation. 2. The result of segmentation shall be one segment for the SIB18 (current assumption). 3. Send the scheduling info to SS. one segment: REP=128, POS=6. 4. Construct the system information message containing SIB18 and sent to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB1_MulRatOrBMC(p_SIB: SysInfoType1; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIBType1 to SS Default : InitOtherwiseFail Comments : system information on air changes immediately if p_Timing => 512. change of system information on air starts at the frame number = p_Timing. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB1 : p_SIB)))			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 7, 5, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		4.
6		+It_CompleteSIB(systemInformationBlockType1)			4.
7		(tcv_MIB.sibSb_ReferenceList.[1].sibSb_Type.sysInfoType1 := (tcv_MIB.sibSb_ReferenceList.[1].sibSb_Type.sysInfoType1) MOD 256 + 1)			
8		+ts_ChangeMIB_ValueTag			
		It_CompleteSIB(p_SIBType : SIB_Type)			
9		[LENGTH_OF(tcv_Segs.seg1) = 226]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
11		[TRUE]			
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB1 then segmentation. 2. The result of segmentation shall be one segment for the SIB1 (current assumption). 3. Send the scheduling info for first segment to SS. one segment: REP=128, POS=5. 4. Construct the first system information message containing segment of SIB1 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB2_MulRatOrBMC (p_SIB: SysInfoType2; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIB2 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB2 : p_SIB)))			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 7, 7, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		4.
6		+It_CompleteSIB(systemInformationBlockType2)			4.
7		(tcv_MIB.sibSb_ReferenceList.[2].sibSb_Type.sysInfoType2 := (tcv_MIB.sibSb_ReferenceList.[2].sibSb_Type.sysInfoType2) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
		It_CompleteSIB(p_SIBType : SIB_Type)			
9		[LENGTH_OF(tcv_Segs.seg1) = 226]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
11		[TRUE]			
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB2 then segmentation. 2. The result of segmentation shall be one segment for the SIB2 (current assumption). 3. Send the scheduling info to SS. one segment, REP=128, POS=7. 4. Construct the system information message containing SIB2 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB3_MulRatOrBMC (p_SIB: SysInfoType3; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIB3 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB3 : p_SIB)))			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		4.
6		+It_CompleteSIB(systemInformationBlockType3)			4.
7		(tcv_MIB.sibSb_ReferenceList.[3].sibSb_Type.sysInfoType3 := (tcv_MIB.sibSb_ReferenceList.[3].sibSb_Type.sysInfoType3) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
		It_CompleteSIB(p_SIBType : SIB_Type)			
9		[LENGTH_OF(tcv_Segs.seg1) = 226]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
11		[TRUE]			
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB3 then segmentation. 2. The result of segmentation shall be one segment for the SIB3 (current assumption). 3. Send the scheduling info to SS. one segment; REP=64, POS=13. 4. Construct the system information message containing SIB3 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB4_MulRatOrBMC (p_SIB: SysInfoType4; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIB4 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB4 : p_SIB)))			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		4.
6		+It_CompleteSIB(systemInformationBlockType4)			4.
7		(tcv_MIB.sibSb_ReferenceList.[4].sibSb_Type.sysInfoType4 := (tcv_MIB.sibSb_ReferenceList.[4].sibSb_Type.sysInfoType4) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
		It_CompleteSIB(p_SIBType : SIB_Type)			
9		[LENGTH_OF(tcv_Segs.seg1) = 226]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
11		[TRUE]			
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB4 then segmentation. 2. The result of segmentation shall be one segment for the SIB4 (current assumption). 3. Send the scheduling info to SS. one segment; REP=64, POS=15. 4. Construct the system information message containing SIB4 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB5_BMC(p_SIB: SysInfoType5; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIBType5 to SS Default : InitOtherwiseFail Comments : Maximum number of segments is 8, default scheduling described in 3GPP TS 34.123-3 clause 8.4.4.1 for BMC test cases using two PRACH or two SCCPCH Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB5 : p_SIB)))		I	1.
2		[tcv_Segs.segCount >8]			2.
3		[tcv_Segs.segCount <=8]			
4		+It_SendNoSegments			
5		[tcv_Segs.segCount = 1]			
6		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul1_MulRatOrBMC)			
7		+ts_ChangeMIB_ValueTag			
8		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
9		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
10		+It_CompleteSIB(systemInformationBlockType5)			4.
11		[tcv_Segs.segCount = 2]			
12		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul2_MulRatOrBMC)			
13		+ts_ChangeMIB_ValueTag			
14		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
15		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
16		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 2, tcv_Segs.seg1))		4.
17		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
18		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
19		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
20		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 1, tcv_Segs.seg2))		6.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21		[TRUE]			
22		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType5, 1, tcv_Segs.seg2))		6.
23		[tcv_Segs.segCount = 3]			
24		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul3_MulRatOrBMC)			
25		+ts_ChangeMIB_ValueTag			
26		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
27		CMAC?CMAC_SYSINFO_Config_C NF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInf ormationBlockType5, 3, tcv_Segs.seg1))		4.
29		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
30		CMAC?CMAC_SYSINFO_Confi g_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
31		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(sys temInformationBlockType5, 1, tcv_Segs.seg2))		6.
32		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
33		CMAC?CMAC_SYSINFO_ Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
34		[LENGTH_OF(tcv_Segs.s eg3) <= 214]			
35		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType5, 2, tcv_Segs.seg3))		8.
36		[TRUE]			
37		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType5, 2, tcv_Segs.seg3))		8.
38		[tcv_Segs.segCount = 4]			
39		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul4_MulRatOrBMC)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
40		+ts_ChangeMIB_ValueTag			
41		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
42		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 4, tcv_Segs.seg1))		4.
44		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
45		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
46		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		6.
47		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
48		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
49		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 2, tcv_Segs.seg3))		8.
50		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
51		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
52		[LENGTH_OF(tcv_Segs.seg4) <= 214]			
53		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
54		[TRUE]			
55		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
56		[tcv_Segs.segCount = 5]			
57		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul5_BMC)			
58		+ts_ChangeMIB_ValueTag			
59		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
60		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
61		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 5, tcv_Segs.seg1))		4.
62		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
63		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
64		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		6.
65		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
66		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
67		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 2, tcv_Segs.seg3))		8.
68		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
69		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
70		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
71		+ts_Scheduling(p_CellId, 7, 35, p_Timing)			11.
72		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
73		[LENGTH_OF(tcv_Segs.seg5) <= 214]			
74		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 4, tcv_Segs.seg5))		12.
75		[TRUE]			
76		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 4, tcv_Segs.seg5))		12.
77		[tcv_Segs.segCount = 6]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
78		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul6_BMC)			
79		+ts_ChangeMIB_ValueTag			
80		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
81		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
82		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 6, tcv_Segs.seg1))		4.
83		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
84		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
85		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		6.
86		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
87		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
88		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 2, tcv_Segs.seg3))		8.
89		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
90		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
91		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
92		+ts_Scheduling(p_CellId, 7, 35, p_Timing)			11.
93		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
94		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 4, tcv_Segs.seg5))		12.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
95		+ts_Scheduling(p_CellId, 7, 37, p_Timing)			13.
96		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
97		[LENGTH_OF(tcv_Segs.seg6) <= 214]			
98		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 5, tcv_Segs.seg6))		14.
99		[TRUE]			
100		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 5, tcv_Segs.seg6))		14.
101		[tcv_Segs.segCount = 7]			
102		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul7_BMC)			
103		+ts_ChangeMIB_ValueTag			
104		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
105		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
106		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 6, tcv_Segs.seg1))		4.
107		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
108		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
109		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		6.
110		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
111		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
112		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 2, tcv_Segs.seg3))		8.
113		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
114		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
115		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
116		+ts_Scheduling(p_CellId, 7, 35, p_Timing)			11.
117		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
118		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 4, tcv_Segs.seg5))		12.
119		+ts_Scheduling(p_CellId, 7, 37, p_Timing)			13.
120		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
121		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 5, tcv_Segs.seg6))		14.
122		+ts_Scheduling(p_CellId, 7, 38, p_Timing)			15.
123		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
124		[LENGTH_OF(tcv_Segs.seg7) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
125		TM! RLC _TR _DA TA_ RE Q	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType5, 6, tcv_Segs.seg7))		16.
126		[TRU E]			
127		TM! RLC _TR _DA TA_ RE Q	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType5, 6, tcv_Segs.seg7))		16.
128		[tcv_Segs.segCount = 8]			
129		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Ty pe.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul8_BMC)			
130		+ts_ChangeMIB_ValueTag			
131		+ts_Scheduling(p_CellId, 7, 19, p_Timing)			3.
132		CMAC?CMAC_SYSINFO_Config_CN F	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
133		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInf ormationBlockType5, 6, tcv_Segs.seg1))		4.
134		+ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
135		CMAC?CMAC_SYSINFO_Confi g_CNf	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
136		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(sys temInformationBlockType5, 1, tcv_Segs.seg2))		6.
137		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
138		CMAC?CMAC_SYSINFO_ Config_CNf	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
139		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(sys temInformationBlockType5, 2, tcv_Segs.seg3))		8.
140		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
141		CMAC?CMAC_SYSIN FO_Config_CNf	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
142		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 3, tcv_Segs.seg4))		10.
143		+ts_Scheduling(p_CellId, 7, 35, p_Timing)			11.
144		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
145		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 4, tcv_Segs.seg5))		12.
146		+ts_Scheduling(p_CellId, 7, 37, p_Timing)			13.
147		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
148		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 5, tcv_Segs.seg6))		14.
149		+ts_Scheduling(p_CellId, 7, 38, p_Timing)			15.
150		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
151		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 6, tcv_Segs.seg7))		16.
152		+ts_Scheduling(p_CellId, 7, 39, p_Timing)			17.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
153			ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
154					
155			ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType5, 7, tcv_Segs.seg8))		18.
156					
157			ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType5, 7, tcv_Segs.seg8))		18.
158		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
159		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
160		[TRUE]			
161		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
162		It_SendNoSegments +ts_Scheduling(p_CellId, 7, 21, p_Timing)			5.
163		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
164		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
165		+ts_Scheduling(p_CellId, 7, 22, p_Timing)			7.
166		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
167		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
168		+ts_Scheduling(p_CellId, 7, 23, p_Timing)			9.
169		CMAC?CMAC_SYSINFO_Config_C NF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
170		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
171		+ts_Scheduling(p_CellId, 7, 35, p_Timing)			11.
172		CMAC?CMAC_SYSINFO_Confi g_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
173		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
174		+ts_Scheduling(p_CellId, 7, 37, p_Timing)			13.
175		CMAC?CMAC_SYSINFO_ Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
176		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
177		+ts_Scheduling(p_CellId, 7, 38, p_Timing)			15.
178		CMAC?CMAC_SYSIN FO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
179		TM!RLC_TR_DATA_ REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.
180		+ts_Scheduling(p_ CellId, 7, 39, p_Timing)			17.
181		CMAC?CMAC_S YSINFO_Config_ CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
182		TM!RLC_TR_DA TA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		19.

Detailed Comments :

1. Unaligned PER encoding of the SIB5 then segmentation.
2. The segmentation shall result in one to eight segments for the SIB5 (current assumption).
3. Send the scheduling info to SS.(one segment; REP=128; POS=19).
4. Construct the system information message containing the first segment of SIB5 and send it to SS.
5. Send the scheduling info to SS.(one segment; REP=128; POS=21).
6. Construct the system information message containing the second segment of SIB5 and send it to SS.
7. Send the scheduling info to SS.(one segment; REP=128; POS=22).
8. Construct the system information message containing the third segment of SIB5 and send it to SS.
9. Send the scheduling info to SS.(one segment; REP=128; POS=23).
10. Construct the system information message containing the forth segment of SIB5 and send it to SS.
11. Send the scheduling info to SS.(one segment; REP=128; POS=35).
12. Construct the system information message containing the fifth segment of SIB5 and send it to SS.

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Test Step Dynamic Behaviour	
Detailed Comments :	<p>13. Send the scheduling info to SS.(one segment; REP=128; POS=37).</p> <p>14. Construct the system information message containing the sixth segment of SIB5 and send it to SS.</p> <p>15. Send the scheduling info to SS.(one segment; REP=128; POS=38).</p> <p>16. Construct the system information message containing the seventh segment of SIB5 and send it to SS.</p> <p>17. Send the scheduling info to SS.(one segment; REP=128; POS=39).</p> <p>18. Construct the system information message containing the eighth segment of SIB5 and send it to SS.</p> <p>19. Send no segment system information message to SS when the number of segments is less than 8.</p>

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB7_MulRatOrBMC(p_SIB: SysInfoType7; p_CellId : INTEGER; p_Timing: INTEGER) Group : RRCM_SysInfo/RRCM_MRAT_BMC/ Objective : To deliver the SIB7 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (SIB7 : p_SIB)))			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 5, 11, p_Timing)			
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		4.
6		+lt_CompleteSIB(systemInformationBlockType7)			
7		lt_CompleteSIB(p_SIBType : SIB_Type)			
8		[LENGTH_OF(tcv_Segs.seg1) = 226]			4.
		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		
9		[TRUE]			
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB7 then segmentation. 2. The result of segmentation shall be one segment for the SIB7 (current assumption). 3. Send the scheduling info to SS. one segment: REP=32, POS=11. 4. Construct the system information message containing SIB7 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSysInfoWithSpecialSIB5_And6 (p_CellId: INTEGER; p_SIB5: SysInfoType5; p_SIB6: SysInfoType6) Group : RRCM_SysInfo/ Objective : To broadcast default system information except the contents of SIB5 and SIB6 are specified by the caller Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ts_UTRAN_GERAN_ParaInit(p_CellId)			
3		+ts_InitializeSIB2AndSIB18(tcv_TmpCellInfo)			
4		+ ts_InitializeSIB11_SIB12 (p_CellId)			
5		[px_RAT = fdd]			
6		+ts_SendNoSegDefSchedul(p_CellId)			
7		+ts_SendSIB1(cb_SIB1_Def(tcv_TmpCellInfo), p_CellId, tsc_Now)			
8		+ts_CellDependentPara(p_CellId)			
9		+ts_SendSIB2(tcv_SIB2 , p_CellId, tsc_Now)			
10		+ts_SendSIB3(tcv_SIB3, p_CellId, tsc_Now)			
11		+ts_SendSIB4(tcv_SIB4, p_CellId, tsc_Now)			
12		+ts_SendSIB5(p_SIB5, p_CellId, tsc_Now)			
13		+ts_SendSIB6(p_SIB6, p_CellId, tsc_Now)			
14		+ts_SendSIB7(c_SIB7_Def, p_CellId, tsc_Now)			
15		+ts_SendSIB11(tcv_SIB11, p_CellId, tsc_Now)			
16		+ts_SendSIB12(tcv_SIB12, p_CellId, tsc_Now)			
17		+ts_SendSIB18(tcv_SIB18 , p_CellId, tsc_Now)			
18		+ts_SendSB1_DefSchedul(tcv_SB1, p_CellId, tsc_Now)			
19		+ts_SendMIB(tcv_MIB, p_CellId, tsc_Now)			
20	ERR1	[px_RAT = tdd]		I	
21	ERR2	[TRUE]		I	
Detailed Comments : 1.cd_SIB5_DefBMC and cd_SIB6_DefBMC can be used for BMC and CBS testing					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB_PCCH2_Cfg (p_CellId: INTEGER) Group : RRCM_SS_Steps/ Objective : to setup radio bearer (RB_PCCH) used for paging message sending Default : SS_Def Comments : To be used in cell configuration cell_FACH_2SCCPCH_StandAlonePCH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_PCCH_Info(p_CellId, tsc_RB_PCCH2, {dLogicalChannelIdentity tsc_PCCH1})		configure radio bearer(RB_PCCH2) on TM + PCCH + PCH, used for sending paging message
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB_PCCH2)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2FACH_CCCH_DCCH_BCCH_DTCH_Cfg (p_CellId : INTEGER) Group : RRCM_SS_Steps/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH2), then connect 2 FACH's to the secondary CCPCH . Finally to map DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH1) for FACH. DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH (for BCCH_FACH) mapping to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info (p_CellId, tsc_S_CCPCH2, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH1_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH2)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_2_FACH_InfoActNow (p_CellId, tsc_S_CCPCH2)		connect FACH1 and 2 to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH2)		
7		CMAC ! CMAC_Config_REQ	ca_CMACH_CfgInfo (p_CellId, tsc_S_CCPCH2, c_UE_Info (tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoFACH_PS, c_TrLogMappingFACH_PS)		map PCCH to PCH, and map CCCH, BCCH, DTCH and DCCH's to FACH. C-RNTI and U-RNTI are required.
8		CMAC ? CMAC_Config_CNF	ca_CMACH_CfgCnf(p_CellId , tsc_S_CCPCH2)		
9	ERR1	[px_RAT = tdd]		I	
10	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_SecondSCCPCH_PCH_PCCH_Cfg (p_CellId : INTEGER) Group : RRCM_SS_Steps/ Objective : To configure the second secondary CCPCH (tsc_S_CCPCH1), then connect PCH to the secondary CCPCH . (34.108 cl. 4.2.1), finally to map PCCH to PCH. Used for the configuration cell_FACH_2SCCPCH_StandAlonePCH. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_InfoPCH_StandAlone (p_CellId, tsc_S_CCPCH1, tsc_S_CCPCH_2ndScrCode, 4, (tcv_TmpCellInfo.powersCCPCH1))		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_PCH_Info2 (p_CellId, tsc_S_CCPCH1)		connect PCH and FACH to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (p_CellId, tsc_S_CCPCH1, c_UE_Info(OMIT, OMIT), c_TrCHInfoPCH, c_TrLogMappingPCH2)		map PCCH to PCH, and map CTCH to FACH. C-RNTI and U-RNTI are not needed (noDCC H/TCH mapped on this S-CCPCH)
8		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId, tsc_S_CCPCH1)		
9		CPHY!CPHY_RL_Setup_REQ	ca_PICH_Info (p_CellId, c_PichInfo, (tcv_TmpCellInfo.powerPICH), tsc_S_CCPCH1)		PICH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_PICH1)		
11	ERR1	[px_RAT = tdd]		I	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_CreateCell2_SCCPCH_StandAlonePCH (p_CellId : INTEGER) Group : RRCM_SS_Steps/ Objective : To setup a baseline cell which consists of the following: physical channels: p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH1, s-CCPCH2 and PRACH; transport channels: BCH, FACH, PCH, RACH; logical channels: BCCH, CCCH, PCCH, DCCH; and signalling radio bearer RB0(CCCH), RB1(UM DCCH), RB2(AM DCCH), RB3(AM DCCH for NAS high priority), RB4(AM DCCH for NAS low priority) on FACH and RACH, RB-3(TM BCCH FACH) on FACH, RB20(AM DTCH) on FACH and RACH, RB-1 (TM, BCCH) on BCH, RB-19 (TM, PCCH, PCH, s-CCPCH2) Default : InitOtherwiseFail Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) This step shall be used for single cell test cases only. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SS_CellCfg (p_CellId)			
2		+ts_SS_BCH_SCH_CPICH_Cfg(p_CellId)			
3		+ ts_SS_SecondSCCPCH_PCH_PCCH_Cfg (p_CellId)			
4		+ ts_SS_2FACH_CCCH_DCCH_BCCH_DTC H_Cfg (p_CellId)			
5		+ts_SS_RACH_CCCH_DCCH_DTCH_Cf g (p_CellId)			
6		+ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
7		+ts_SS_RB_PCCH2_Cfg(p_CellId)			
8		+ts_SS_RB0_Cfg(p_CellId)			
9		+ts_SS_RB1_ToRB4_Cfg			
10		+ts_SS_RB_BCCH_FACH_Cfg(p _CellId)			
11		+ts_SS_RB20_AM_PS_Cfg (320)			
12		+ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandA lonePCH_NoConn)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_TFC_Restriction (p_cellId:INTEGER;p_TFC_UL,p_TFC_DL : TFC_Subset) Group : RRCM_SS_Steps/ Objective : to request SS to send the data on the RABs in the same frame. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_DL_DPCH1,c_TFC_Restriction_DL(p_TFC_DL))		
2		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_DL_DPCH1)		
3		CMAC ! CMAC_Restriction_REQ	ca_CMAC_RestrictReq(p_cellId,tsc_UL_DPCH1,c_TFC_Restriction_UL(p_TFC_UL))		
4		CMAC ? CMAC_Restriction_CNF	ca_CMAC_RestrictCnf(p_cellId,tsc_UL_DPCH1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_GMM_DetachOnSwitchOff (p_CellId : INTEGER) Group : L3M_MM_GMM_Steps/ Objective : Turn off UE and execute GMM Detach procedure for properly detach PS or combined PS/CS services on the cell referenced by p_CellId. Additionally, if Attach Flag is set, and the UE is in Operation Mode A, then IMSI DETACH INDICATION shall be send by the UE. Default : NAS_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RRC_Delay(5000)			Wait before switching off (e.g. in case ATT flag has been previously changed) to allow UE to re-read new SysInfos
2		[pc_SwitchOnOff]			UE can actually be switched off
3		+ts_SetTmpCellInfo (p_CellId)			Get CellInfo to be used later
4		+It_Init_RRC_RelStatus			
5		+ts_MMI_UE_SwitchOff			
6		+ts_RRC_ConnEst(p_CellId, est_MO, detach)			
7		[(tcv_UE_OpMode = opModeA) AND (tcv_TmpCellInfo.nmo = tsc_NMO_I)]			If UE is in operation mode A and network mode of operation is I, then run combined PS/CS procedures.
8		+It_Detach_NMO_I			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		+ts_RRC_ConnRel_AfterSwitchOff(p_CellId, tcv_RRC_RelStatus)			
10		[(tcv_UE_OpMode = opModeA) AND (tcv_TmpCellInfo.nmo = tsc_NMO_II)]			If UE is in operatio n mode A and network mode of operatio n is II, then run first CS and PS independ ently
11		+It_Detach_NMO_II			SS shall accept either PS detach first or IMSI detach first, since this is not clear in the core spec
12		+ts_RRC_ConnRel_AfterSwitchOff(p_CellId, tcv_RRC_RelStatus)			
13		[tcv_UE_OpMode = opModeC]			If UE is in operatio n mode C, then run GMM procedur e (for PS only detach).
14		+It_Detach_POnly			
15		+ts_RRC_ConnRel_AfterSwitchOff(p_CellId, tcv_RRC_RelStatus)			
16		[TRUE]			UE power supply must be removed
17		+ts_MMI_UE_PwrOff It_Init_RRC_RelStatus			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_No Conn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCP			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
19		CH_StandAlonePCH_2a_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated)] (tcv_RRC_RelStatus := cell_Fach_Dch)			Get CellInfo to be used later
20		[TRUE]			
21		(tcv_RRC_RelStatus := cell_Dch)			Get CellInfo to be used later
22		It_Detach_NMO_I Dc ? RRC_DataInd	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_DetachReq (c_DetachType('1'B, '011'B), c_MobileIdPTMSI (tcv_AssignedPTMSI), c_PTMSI_Signature_tlv (tcv_Assigned_PTMSI_Sig)))	(P)	DETAC H REQUE ST – 'power switched off, GPRS/I MSI combine d detach'
23		It_Detach_POnly Dc ? RRC_DataInd	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_DetachReq (c_DetachType('1'B, '001'B), c_MobileIdPTMSI (tcv_AssignedPTMSI), c_PTMSI_Signature_tlv (tcv_Assigned_PTMSI_Sig)))	(P)	DETAC H REQUE ST – 'power switched off, PS detach'
24		It_Detach_NMO_II Dc ? RRC_DataInd	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_DetachReq (c_DetachType('1'B, '001'B), c_MobileIdPTMSI (tcv_AssignedPTMSI), c_PTMSI_Signature_tlv (tcv_Assigned_PTMSI_Sig)))	(P)	DETAC H REQUE ST – 'power switched off, GPRS detach'
25		[tcv_TmpCellInfo.attFlag = tsc_AttOn]			If ATTflag is ON

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
26		Dc ? RRC_DataInd	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, c_IMSI_DetachInd)	(P)	IMSI DETAC H INDICA TION
27		[TRUE]			
28		Dc ? RRC_DataInd	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, c_IMSI_DetachInd)	(P)	IMSI DETAC H INDICA TION
29		Dc ? RRC_DataInd	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_DetachReq (c_DetachType('1'B, '001'B), c_MobileIdPTMSI (tcv_AssignedPTMSI), c_PTMSI_Signature_tlv (tcv_Assigned_PTMSI_Sig)))	(P)	DETAC H REQUE ST - 'power switched off, GPRS detach'
Detailed Comments : See 3GPP 24.008 / 4.7.4					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_ConnRel_AfterSwitchOff (p_CellId: INTEGER; p_RRC_RelStatus : RRC_Rel_Status) Group : L3M_RRC_Steps/ Objective : To release RRC connection to be used in cases where the UE has been switched off (adapted from test step ts_RRC_ConnRel) Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_RRC_Delay (tsc_DelayBeforeRRC_ConnRel)			
3		+ It_Send_RRC_ConnectionRelease			
4		+ It_RestartCRLC_ForNextConnection			
5		+ ts_SS_ResetSecurityKey			
		It_RestartCRLC_ForNextConnection			
6		[tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB]			
7		+ ts_CRLC_RelReconfSRB (p_CellId)			
8		+ ts_SetCellCfg (p_CellId, cell_DCH_StandAloneSRB_NoConn)			
9		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS)]			
10		+ ts_CRLC_RelReconfSRB (p_CellId)			
11		+ ts_SetCellCfg (p_CellId, cell_FACH_NoConn)			
12		[tcv_TmpCellInfo.cellConfig = cell_FACH_BMC]			
13		+ ts_CRLC_RelReconfSRB (p_CellId)			
14		+ ts_SetCellCfg (p_CellId, cell_FACH_BMC_NoConn)			
15		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH]			
16		+ ts_CRLC_RelReconfSRB (p_CellId)			
17		+ ts_SetCellCfg (p_CellId, cell_FACH_2_PRACH_NoConn)			
18		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH]			
19		+ ts_CRLC_RelReconfSRB (p_CellId)			
20		+ ts_SetCellCfg (p_CellId, cell_FACH_2_SCCPCH_NoConn)			
21		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			
22		+ ts_CRLC_RelReconfSRB (p_CellId)			
23		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH_NoConn)			
24		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1]			
25		+ ts_CRLC_RelReconfSRB (p_CellId)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
26		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg1_No Conn)			
27		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2]			
28		+ ts_CRLC_RelReconfSRB (p_CellId)			
29		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg2_No Conn)			
30		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH]			
31		+ ts_CRLC_RelReconfSRB (p_CellId)			
32		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_CTCH_No Conn)			
33		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a) OR(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)]			
34		+ ts_CRLC_RelReconfSRB (p_CellId)			
35		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH_2a_N oConn)			
36		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1)]			
37		+ ts_CRLC_RelReconfSRB (p_CellId)			
38		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_ NoConn)			
39		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
40		+ ts_CRLC_RelReconfSRB (p_CellId)			
41		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_ NoConn)			
42		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)]			
43		+ ts_CRLC_RelReconfSRB (p_CellId)			
44		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_ NoConn)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
45		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call)]			
46		+ ts_SS_ReconfigRAB_ToSRB (p_CellId)			
47		+ ts_SetCellCfg (p_CellId, cell_DCH_StandAloneSRB_NoConn)			
48	ERR1	[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_StandAlonePCH_NoConn)]		I	1.
49	ERR2	[TRUE]		I	
		It_Send_RRC_ConnectionRelease			
50		[p_RRC_RelStatus= cell_Dch]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
51		(tcv_N308 := 1, tcv_K := 1)			Maximum number of retransmissions of the RRC CONNECTION RELEASE COMPLETE message
52		UM ! RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1,		
			cs_108_RRC_ConnRelDCCH(tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, tcv_N308))		
53		START t_Dly(1000)			
54		? TIMEOUT t_Dly		(P)	UE may not send RRC CONNECTION RELEASE COMPLETE
55	TSP1	UM ? RLC_UM_DATA_IND CANCEL t_Dly	car_RRC_ConnRelCmplUM (tsc_CellDedicated, tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	
56		REPEAT It_RptRcvConnRel UNTIL [tcv_K = (tcv_N308+1)]			UE sends RRC Connection Release Complete for N308 times
57		[p_RRC_RelStatus = cell_Fach_Dcch]			
58		UM ! RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1,		
			cs_108_RRC_ConnRelDCCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, OMIT))		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
59	TSP2	START t_Dly(1000)		(P)	UE may not send RRC CONNECTION RELEASE COMPLETE
60		? TIMEOUT t_Dly			
61		AM ? RLC_AM_DATA_IND CANCEL t_Dly			
62		+ ts_RRC_Delay (tsc_DelayAfterRRC_ConnRel)			
63	TSF2	[p_RRC_RelStatus = cell_Fach_Ccch]	car_RRC_ConnRelCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti)) cas_RRC_ConnRelCCCH(p_CellId, tsc_RB0, cs_108_RRC_ConnRelCCC H (c_U_RNTI, tcv_RRC_Ti))	(P)	Program ming error
64		UM!RLC_UM_DATA_REQ			
65		[TRUE]			
66		It_RptRcvConnRel			
67	TSF2	START t_Dly		(P)	To stop the loop
68		? TIMEOUT t_Dly (tcv_K := tcv_N308 + 1)			
69	TSP2	UM?RLC_UM_DATA_IND (tcv_K := tcv_K+1) CANCEL t_Dly	car_RRC_ConnRelCmplUM (tsc_CellDedicated , tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	Retrans mission
Detailed Comments : Note: Test step copied from original test step ts_RRC_ConnRel and then adapted to consider UEs which do not send RRC CONNECTION RELEASE COMPLETE after switch off.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_PagType1_TMSI_PTMSI_Cau (p_CellId: INTEGER; p_Tmsi:OCTETSTRING; p_PagCause: PagingCause)					
Group : L3M_RRC_Steps/					
Objective : To send PAGING TYPE 1 with TIMSI for CS and PTMSI for PS and with a given paging cause					
Default : RRC_Def1					
Comments : SS CMAC shall be configured before sending the PAGING TYPE 1 message.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RRC_Delay(tsc_WaitBeforePaging)			Give delay before paging type1
2		+ts_CMACE_Pag1_Cfg(p_CellId)			
3		[tcv_CN_Domain = cs_domain]			
4		TM ! RLC_TR_DATA_REQ	cas_PagingType1 (p_CellId, tsc_RB_PCCH, cs_RRC_PagingType1_TMSI (p_PagCause, o_ConvertTMSI(p_Tmsi), tcv_CN_Domain))		
5		[tcv_CN_Domain = ps_domain]			
6		TM ! RLC_TR_DATA_REQ	cas_PagingType1 (p_CellId, tsc_RB_PCCH, cs_RRC_PagingType1_PTM SI (p_PagCause, o_ConvertPTMSI (p_Tmsi), tcv_CN_Domain))		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB10_TM_Cfg (p_PayLoad: INTEGER) Group : L3M_SS_ConfigSteps/ Objective : setup radio bearers : RB10. default values from 34.123-1Table 7.2/3A Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_Info (tsc_CellDedicated , tsc_RB10, p_PayLoad, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1})		cofigure radio bearers : RB10 (TM + DTCH) note: set UE to AM mode correspondingly
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated , tsc_RB10)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_22_AM_Cfg Group : L3M_SS_ConfigSteps/ Objective : setup radio bearers : RB20 and 22 mapped on AM Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},320)		configure radio bearers :
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB20)		RB20 (AM + DTCH)
3		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB22, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH2, dLogicalChannelIdentity tsc_DL_DTCH2},320)		SS configur ation of the radio bearer informati on : RB22 (AM + DTCH)
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(tsc_Cell Dedicated, tsc_RB22)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CalculateActTime (p_CellId: INTEGER) Group : BasicM_General_Steps/ Objective : To calculate the activation time with the tti value corresponding to the actual SS configuration. Default : SS_Def Comments : The tti value passed as parameter to ts_CPHY_ActTime is equal to tti/10 (e.g. tti 40 -> 4) Based on 34.108 on SRB tti. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB)OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig =cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig =cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig =cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB)OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS)OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS)OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS)OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS)OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS_Init)OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS_Init)OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS_Init)OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS_Init)]			An RRC connecti on is establish ed. Use DCCH on DL DPCH1
3		+ ts_CPHY_ActTime (p_CellId, tsc_DL_DPCH1, 4)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn)]			No RRC connection is established Use CCCH configuration
5		+ ts_CPHY_ActTime (p_CellId, tsc_S_CCPCH1, 1)			
6		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			One RRC connection established on FACH
7		+ ts_CPHY_ActTime (p_CellId, tsc_S_CCPCH1, 1)			
8		[(tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn)]			One RRC connection established on FACH BMC
9		+ ts_CPHY_ActTime (p_CellId, tsc_S_CCPCH1, 1)			
10		[(tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS)]			
11		+ ts_CPHY_ActTime (p_CellId, tsc_DL_DPCH1, 2)			
12		[(tcv_TmpCellInfo.cellConfig = cell_DCH_HS_DSCH)]			
13		+ ts_CPHY_ActTime (p_CellId, tsc_DL_DPCH1, 4)			
14		[(tcv_TmpCellInfo.cellConfig = cell_NotConfigured)]		I	
15		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CountConfiguredCell Group : BasicM_General_Steps/ Objective : Assign tcv_NumCfgCell to the number of cells already configured. Default : SS_Def Comments : When a cell is configured, the corresponding receod.cellConfig is set to a different value than cell_NotConfigured Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_NumCfgCell := 0)			
2		+ It_CheckOneCell (tcv_CellInfoA)			
3		+ It_CheckOneCell (tcv_CellInfoB)			
4		+ It_CheckOneCell (tcv_CellInfoC)			
5		+ It_CheckOneCell (tcv_CellInfoD)			
6		+ It_CheckOneCell (tcv_CellInfoE)			
7		+ It_CheckOneCell (tcv_CellInfoF)			
8		+ It_CheckOneCell (tcv_CellInfoG)			
9		+ It_CheckOneCell (tcv_CellInfoH)			
		It_CheckOneCell (p_CellInfo : CellInfoCfg)			
10		[p_CellInfo.cellConfig <> cell_NotConfigured]			
11		(tcv_NumCfgCell := tcv_NumCfgCell + 1)			
12		[p_CellInfo.cellConfig = cell_NotConfigured]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_InitVariables Group : BasicM_General_Steps/ Objective : Initialisation of the test case variables tcv_CellInfoX, tcv_SF_Pilot and tcv_SF512. Default : SS_Def Comments : tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoD, tcv_CellInfoE and tcv_CellInfoF : contains the cell information used to configure SS. tcv_SF_Pilot and tcv_SF512 are assigned based on the capability of the UE. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ It_CalculateFrequencyInfo			
2		+It_Init_SSInfo_CellA			
3		+It_Init_SSInfo_CellB			
4		+It_Init_SSInfo_CellC			
5		+It_Init_SSInfo_CellD			
6		+It_Init_SSInfo_CellE			
7		+It_Init_SSInfo_CellF			
8		+It_Init_SSInfo_CellG			
9		+It_Init_SSInfo_CellH			
		It_CalculateFrequencyInfo			
10		+ It_CalculateFreqMid			
11		+ It_CalculateFreqHigh			
12		+ It_CalculateFreqLow			
		It_CalculateFreqMid			
13		+ It_CheckFreqSeperation(px_UARFCN_D_Mid, px_UARFCN_U_Mid)			
14		[NOT tcv_Res]			
15		(tcv_FreqInfoMid := c_FreqInfo (px_UARFCN_U_Mid, px_UARFCN_D_Mid))			
16		[tcv_Res]			
17		(tcv_FreqInfoMid := c_FreqInfo (OMIT, px_UARFCN_D_Mid))			Uplink UARFCN is OMITTED when the distance of 190 MHz is used (distance of 950 in UARFCN)
		It_CalculateFreqHigh			
18		+ It_CheckFreqSeperation(px_UARFCN_D_High, px_UARFCN_U_High)			
19		[NOT tcv_Res]			
20		(tcv_FreqInfoHigh := c_FreqInfo (px_UARFCN_U_High, px_UARFCN_D_High))			
21		[tcv_Res]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		(tcv_FreqInfoHigh := c_FreqInfo (OMIT, px_UARFCN_D_High))			Uplink UARFCN is OMITTED when the distance of 190 MHz is used (distance of 950 in UARFCN)
23		It_CalculateFreqLow + It_CheckFreqSeperation(px_UARFCN_D_Low, px_UARFCN_U_Low)			
24		[NOT tcv_Res]			
25		(tcv_FreqInfoLow := c_FreqInfo (px_UARFCN_U_Low, px_UARFCN_D_Low))			
26		[tcv_Res]			
27		(tcv_FreqInfoLow := c_FreqInfo (OMIT, px_UARFCN_D_Low))			Uplink UARFCN is OMITTED when the distance of 190 MHz is used (distance of 950 in UARFCN)
28		It_CheckFreqSeperation(p_UARFCN_D, p_UARFCN_U:INTEGER) [px_FDD_OperationBand = 1]			Operation Band 1 under test
29		[((p_UARFCN_D - p_UARFCN_U) = 950)]			Default separation
30		(tcv_Res := TRUE)			
31		[TRUE]			Non default separation
32		(tcv_Res := FALSE)			
33		[px_FDD_OperationBand = 2]			Operation Band 2 under test

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
34		[((p_UARFCN_D - p_UARFCN_U) = 400)]			Default separation
35		(tcv_Res := TRUE)			
36		[TRUE]			Non default separation
37		(tcv_Res := FALSE)			
38		[px_FDD_OperationBand = 3]			Operation Band 3 under test
39		[((p_UARFCN_D - p_UARFCN_U) = 475)]			Default separation
40		(tcv_Res := TRUE)			
41		[TRUE]			Non default separation
42		(tcv_Res := FALSE)			
43		[(px_FDD_OperationBand = 5) OR (px_FDD_OperationBand = 6)]			Operation Band 5 Or 6 under test
44		[((p_UARFCN_D - p_UARFCN_U) = 225)]			Default separation
45		(tcv_Res := TRUE)			
46		[TRUE]			Non default separation
47		(tcv_Res := FALSE)			
48		[TRUE]		I	
49		It_Init_SSInfo_CellA (tcv_CellInfoA := c_CellInfoDef (tsc_CellA, px_PriScrmCode, tsc_URA_IdCellA, px_TCellA, tsc_SFN_OffsetA, tcv_FreqInfoMid , px_UL_ScramblingCode))			1.
50		It_Init_SSInfo_CellB (tcv_CellInfoB := c_CellInfoDef (tsc_CellB, ((px_PriScrmCode + 50) MOD 512) , tsc_URA_IdCellB, px_TCellB, tsc_SFN_OffsetB, tcv_FreqInfoMid , ((px_UL_ScramblingCode +1000) MOD 16777216))) It_Init_SSInfo_CellC			2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
51		(tcv_CellInfoC := c_CellInfoDef (tsc_CellC, ((px_PriScrmCode + 100) MOD 512), tsc_URA_IdCellC, px_TCellC, tsc_SFN_OffsetC, tcv_FreqInfoMid , ((px_UL_ScramblingCode +2000) MOD 16777216)))			3.
52		It_Init_SSInfo_CellD (tcv_CellInfoD := c_CellInfoDef (tsc_CellD, ((px_PriScrmCode + 150) MOD 512), tsc_URA_IdCellD, px_TCellD, tsc_SFN_OffsetD, tcv_FreqInfoHigh , ((px_UL_ScramblingCode +3000) MOD 16777216)))			4.
53		It_Init_SSInfo_CellE (tcv_CellInfoE := c_CellInfoDef (tsc_CellE, ((px_PriScrmCode + 200) MOD 512), tsc_URA_IdCellE, px_TCellE, tsc_SFN_OffsetE, tcv_FreqInfoHigh , ((px_UL_ScramblingCode +4000) MOD 16777216)))			5.
54		It_Init_SSInfo_CellF (tcv_CellInfoF := c_CellInfoDef (tsc_CellF, ((px_PriScrmCode + 250) MOD 512), tsc_URA_IdCellF, px_TCellF, tsc_SFN_OffsetF, tcv_FreqInfoHigh , ((px_UL_ScramblingCode +5000) MOD 16777216)))			6.
55		It_Init_SSInfo_CellG (tcv_CellInfoG := c_CellInfoDef (tsc_CellG, ((px_PriScrmCode + 300) MOD 512), tsc_URA_IdCellG, px_TCellG, tsc_SFN_OffsetG, tcv_FreqInfoMid , ((px_UL_ScramblingCode +6000) MOD 16777216)))			
56		It_Init_SSInfo_CellH (tcv_CellInfoH := c_CellInfoDef (tsc_CellH, ((px_PriScrmCode + 350) MOD 512), tsc_URA_IdCellH, px_TCellH, tsc_SFN_OffsetH, tcv_FreqInfoMid , ((px_UL_ScramblingCode +7000) MOD 16777216)))			.
Detailed Comments : 1. The primary scrambling code of cell A is equal to the pixed value 2. The primary scrambling code of cell B is equal to the pixed value + 50 3. The primary scrambling code of cell B is equal to the pixed value + 100 4. Cell A and D do not have the same frequency, the primary scrambling code of cell D is equal to the one of cell A 5. Cell B and E do not have the same frequency, the primary scrambling code of cell E is equal to the one of cell B 6. Cell C and F do not have the same frequency, the primary scrambling code of cell F is equal to the one of cell C					

Test Step Dynamic Behaviour					
Test Step Name : ts_NAS_Delay(p_Dly: INTEGER) Group : BasicM_General_Steps/ Objective : Realization of a Delay Default : NAS_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly(p_Dly)			
2		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_Delay (p_Dly: INTEGER) Group : BasicM_General_Steps/ Objective : Realization of a Delay Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		START t_Dly(p_Dly)			
2		?TIMEOUT t_Dly			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SaveCellInfo (p_CellId : INTEGER) Group : BasicM_General_Steps/ Objective : To save in the variable dedicated to the cell p_CellID, the value contained in tcv_TmpCellInfo. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[p_CellId = tsc_CellA]			
2		(tcv_CellInfoA := tcv_TmpCellInfo)			
3		[p_CellId = tsc_CellB]			
4		(tcv_CellInfoB:= tcv_TmpCellInfo)			
5		[p_CellId = tsc_CellC]			
6		(tcv_CellInfoC:= tcv_TmpCellInfo)			
7		[p_CellId = tsc_CellD]			
8		(tcv_CellInfoD:= tcv_TmpCellInfo)			
9		[p_CellId = tsc_CellE]			
10		(tcv_CellInfoE := tcv_TmpCellInfo)			
11		[p_CellId = tsc_CellF]			
12		(tcv_CellInfoF := tcv_TmpCellInfo)			
13		[p_CellId = tsc_CellG]			
14		(tcv_CellInfoG := tcv_TmpCellInfo)			
15		[p_CellId = tsc_CellH]			
16		(tcv_CellInfoH := tcv_TmpCellInfo)			
17		[TRUE]		I	program ming error
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SetCellCfg (p_CellId : INTEGER ; p_CellConfig : RB_ConfigType) Group : BasicM_General_Steps/ Objective : To assign the field cellConfig of the cell given as parameter to the value 'p_CellConfig'. (tcv_CellInfo[p_CellId] := p_CellConfig) Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		(tcv_TmpCellInfo.cellConfig := p_CellConfig)			
3		+ ts_SaveCellInfo (p_CellId)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SetTmpCellInfo (p_CellId : INTEGER) Group : BasicM_General_Steps/ Objective : To Set global variable tcv_TmpCellInfo to the table corresponding to given cell Default : SS_Def Comments : This Step helps the programmer when he/she needs to access cell information in a generic test Step. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[p_CellId = tsc_CellA]			
2		(tcv_TmpCellInfo := tcv_CellInfoA)			
3		[p_CellId = tsc_CellB]			
4		(tcv_TmpCellInfo := tcv_CellInfoB)			
5		[p_CellId = tsc_CellC]			
6		(tcv_TmpCellInfo := tcv_CellInfoC)			
7		[p_CellId = tsc_CellD]			
8		(tcv_TmpCellInfo := tcv_CellInfoD)			
9		[p_CellId = tsc_CellE]			
10		(tcv_TmpCellInfo := tcv_CellInfoE)			
11		[p_CellId = tsc_CellF]			
12		(tcv_TmpCellInfo := tcv_CellInfoF)			
13		[p_CellId = tsc_CellG]			
14		(tcv_TmpCellInfo := tcv_CellInfoG)			
15		[p_CellId = tsc_CellH]			
16		(tcv_TmpCellInfo := tcv_CellInfoH)			
17	ERR	[TRUE]		I	Fatal error
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_GMM_Authentication (p_CellId : INTEGER) Group : BasicM_MM_GMM_Steps/ Objective : Generate authentication paramters and run the GMM Authentication procedure Default : NAS_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_GMM_AuthenticationInit			Compute all relevant authentication parameters.
2		Dc ! RRC_DataReq	ca_PS_DataReq(tsc_CellDedicated , tsc_RB3, cs_AuthAndCiphReq (c_GMM_AuthRAND(tcv_AuthRAND), c_GMM_KeySeq_tv(tcv_PS_KeySeq), c_GMM_AuthAUTN(tcv_AuthAUTN)))		AUTHENTICATION AND CIPHERING REQUEST using relevant PS keys computed before.
3		Dc ? RRC_DataInd (tcv_TmpAuthAndCiphRspPDU := RRC_DataInd.msg, tcv_AuthRsp := tcv_TmpAuthAndCiphRspPDU.authRsp.value, tcv_AuthRspExt := tcv_TmpAuthAndCiphRspPDU.authRspExt)	car_PS_UplinkDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_AuthAndCiphRsp (c_AuthRspAny_tv,c_AuthCiphRspExtAny))		AUTHENTICATION AND CIPHERING RESPONSE including both Authentication Response parameters (RES and RESext)
4		(tcv_Res := o_AuthRspChk(tcv_AuthRsp, tcv_AuthRspExt, tcv_AuthK, tcv_AuthRAND, TRUE))			Verify that the received Authentication Response parameters match expected response.
5	TSF1	[tcv_Res = FALSE]		(F)	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
6	TSP1	[tcv_Res = TRUE]		(P)	AUTHE NTICATI ON AND CIPHER ING RESPO NSE including only one Authenti cation Respons e paramter s (RES) Verify that the received Authenti cation Respons e paramter s match expecte d respons e.
7		Dc ? RRC_DataInd (tcv_TmpAuthAndCiphRspPDU := RRC_DataInd.msg, tcv_AuthRsp := tcv_TmpAuthAndCiphRspPDU.authRsp.value)	car_PS_UplinkDirectTransfe r (tsc_CellDedicated , tsc_RB3, cr_AuthAndCiphRsp (c_AuthRspAny_tv, -))		
8		(tcv_Res := o_AuthRspChk(tcv_AuthRsp, -, tcv_AuthK, tcv_AuthRAND, FALSE))			
9	TSF2	[tcv_Res = FALSE]		(F)	
10	TSP2	[tcv_Res = TRUE]		(P)	AUTHE NTICATI ON AND CIPHER ING Failure, then stop executio n by assignin g INCON CLUSIV E verdict as this might be a USIM issue.
11		Dc ? RRC_DataInd	car_PS_UplinkDirectTransfe r (tsc_CellDedicated , tsc_RB3, cr_AuthAndCiphFailureAny)		
Detailed Comments : See 3GPP 24.008 / 4.7, 3GPP 33.102 / 6.3 and 3GPP 34.108 / 8 (for the computation of authentication paramters for Test USIM) See also the detailed description in test Step ts_MM_Authentication, on which this test Step is based.					

Test Step Dynamic Behaviour					
Test Step Name : ts_GMM_AuthenticationInit Group : BasicM_MM_GMM_Steps/ Objective : Computation of variables related to the Authentication and Key Agreement procedure for PS domain Default : NAS_OtherwiseFail Comments : Based on TS 34.108 cl. 8.1.2 and TS 33.102 cl.s 6.3 and 6.8.1.2 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+It_IncrementCiphKeySeqNum			1. Calculati on of AUTN needed for Authenti cation Request 2. Calculati on of other authenti cation informati on needed (IK, CK, XRES) 3. Calculati on of Kc GSM, using IK and CK
2		+It_AuthCalcAUTN			
3		+It_AuthCalcUMTS_Others			
4		+It_AuthCalcKcGSM			
5		It_IncrementCiphKeySeqNum			
6		[tcv_PS_KeySeq = '000'B]			
7		(tcv_PS_KeySeq := '001'B)			
8		[tcv_PS_KeySeq = '001'B]			
9		(tcv_PS_KeySeq := '010'B)			
10		[tcv_PS_KeySeq = '010'B]			
11		(tcv_PS_KeySeq := '011'B)			
12		[tcv_PS_KeySeq = '011'B]			
13		(tcv_PS_KeySeq := '100'B)			
14		[tcv_PS_KeySeq = '100'B]			
15		(tcv_PS_KeySeq := '101'B)			
16		[tcv_PS_KeySeq = '101'B]			
17		(tcv_PS_KeySeq := '110'B)			
18		[TRUE]			
		(tcv_PS_KeySeq := '000'B)			
		It_AuthCalcAUTN			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
19		(tcv_AuthXDOUT := o_BitstringXOR(tcv_AuthRAND, tcv_AuthK, 128))			XDOUT := RAND XOR K
20		(tcv_AuthCDOUT := o_BitstringConcat(tsv_AuthSQN, tcv_AuthAMF, 48, 16))			CDOUT := SQN AMF
21		(tcv_AuthXDOUT_Half := o_BitstringXtract(tcv_AuthXDOUT, 128, 64, 0))			XDOUT _half := 64 bits of XDOUT starting from offset 0
22		(tcv_AuthAK := o_BitstringXtract(tcv_AuthXDOUT, 128, 48, 24))			AK := 48 bits of XDOUT starting from offset 24
23		(tcv_AuthAUTN_1 := o_BitstringXOR(tsv_AuthSQN, tcv_AuthAK, 48))			AUTN1 := SQN XOR AK
24		(tcv_AuthMAC := o_BitstringXOR(tcv_AuthXDOUT_Half, tcv_AuthCDOUT, 64))			MAC := XDOUT _half XOR CDOUT
25		(tcv_AuthAUTN_2 := o_BitstringConcat(tcv_AuthAMF, tcv_AuthMAC, 16, 64))			AUTN2 := AMF MAC
26		(tcv_AuthAUTN := o_BitstringConcat(tcv_AuthAUTN_1, tcv_AuthAUTN_2, 48, 80))			AUTN := AUTN1 AUTN2
27		It_AuthCalcUMTS_Others (tcv_PS_AuthIK := o_BitstringXtract(tcv_AuthXDOUT, 128, 128, 16))			IK := 128 bits of XDOUT starting from offset 16 (wrappin g)

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
28		(tcv_PS_AuthCK := o_BitstringXtract(tcv_AuthXDOUT, 128, 128, 8))			CK := 128 bits of XDOUT starting from offset 8 (wrapping)
29		(tcv_AuthXRES := o_BitstringXtract(tcv_AuthXDOUT, 128, (tcv_AuthN + 1), 0))			XRES := (n+1) bits of XDOUT starting from offset 0
30		lt_AuthCalcKcGSM (tcv_AuthCK_1 := o_BitstringXtract(tcv_PS_AuthCK, 128, 64, 0))			CK1 := 64 bits of CK starting from offset 0
31		(tcv_AuthCK_2 := o_BitstringXtract(tcv_PS_AuthCK, 128, 64, 64))			CK2 := 64 bits of CK starting from offset 64
32		(tcv_AuthIK_1 := o_BitstringXtract(tcv_PS_AuthIK, 128, 64, 0))			IK1 := 64 bits of IK starting from offset 0
33		(tcv_AuthIK_2 := o_BitstringXtract(tcv_PS_AuthIK, 128, 64, 64))			IK2 := 64 bits of IK starting from offset 64
34		(tcv_AuthCK_XOR := o_BitstringXOR(tcv_AuthCK_1, tcv_AuthCK_2, 64))			CK_XOR := CK1 XOR CK2
35		(tcv_AuthIK_XOR := o_BitstringXOR(tcv_AuthIK_1, tcv_AuthIK_2, 64))			IK_XOR := IK1 XOR IK2

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
36		(tcv_AuthKcGSM := o_BitstringXOR(tcv_AuthCK_XOR, tcv_AuthIK_XOR, 64))			KcGSM := CK_XO R XOR IK_XOR (= CK1 XOR CK2 XOR IK1 XOR IK2)
Detailed Comments : (see ts_MM_AuthenticationInit)					

Test Step Dynamic Behaviour					
Test Step Name : ts_GMM_IdleUpdated (p_CellId : INTEGER) Group : BasicM_MM_GMM_Steps/ Objective : Turn on UE and register for PS or combined PS/CS services. Default : NAS_OtherwiseFail Comments : Initial conditions: – Cell referenced by p_CellId is configured and sending SysInfos on BCCH – UE is switched off with a valid Test USIM inserted Input paramters: – p_CellId referencing the Cell Global paramters used: – The SS will use global authentication paramters and keys which are generated in test Step ts_GMM_Authentication: tcv_AuthRAND, tcv_KeySeq, tcv_AuthAUTN, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM. – The SS will assign to the UE default values for P-TMSI, P-TMSI signature and (in case of combined PS/IMSI attach) TMSI.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_UE_OpMode = opModeA) AND (tcv_TmpCellInfo.nmo = tsc_NMO_I)]			If UE is in operation mode A and network mode of operation is I, then run combined PS/CS procedures.
3		+It_IdleUpdated_NMO_I			
4		(tcv_Use_E_PLMN := FALSE)			Invalidat e the tcv_E_P LMN
5		[(tcv_UE_OpMode = opModeA) AND (tcv_TmpCellInfo.nmo = tsc_NMO_II)]			If UE is in operation mode A and network mode of operation is II, then run first CS and PS procedures indepen dently
6		+It_IdleUpdated_NMO_II			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		(tcv_Use_E_PLMN := FALSE)			Invalidat e the tcv_E_P LMN
8		[tcv_UE_OpMode = opModeC]			If UE is in operatio n mode C, then run GMM procedur e (for PS only attach).
9		+It_GMMOnly_IdleUpdated			
10		(tcv_Use_E_PLMN := FALSE)			Invalidat e the tcv_E_P LMN
11	ERR	[TRUE]		I	Program ming error
12		It_IdleUpdated_NMO_I			
13		+ ts_MMI_UE_SwitchOn			
13		+ts_RRC_ConnEst(p_CellId, est_Reg, registration)			Establis h RRC connecti on
14		[pc_AutomaticAttachSwitchON = TRUE]			Perform combine d CS/PS procedur e
15		+It_AttachRequest			ATTACH REQUE ST
16		+ts_GMM_Authentication (p_CellId)			AUTHE NTICATI ON AND CIPHER ING REQUE ST AUTHE NTICATI ON AND CIPHER ING RESPO NSE

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
17		+lt_SecurityMode			SECURITY MODE COMMAND SECURITY MODE COMPLETE
18		+lt_AttachAccept			ATTACH ACCEPT ATTACH COMPLETE
19		+lt_RRC_ConnRel			RRC connection release
20		[pc_AutomaticAttachSwitchON = FALSE]			First perform Location Update procedure, and then trigger UE via AT command to perform GPRS Attach
21		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cb_LocUpdReqAny(?))		Any Location Update request
22		+ ts_SS_SecurityDownloadStart (cs_domain, tcv_Start)			
23		+ts_MM_Authentication(p_CellId)			Authentication
24		+ts_RRC_Security (p_CellId, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM, TRUE, cs_domain)			
25		+ lt_LocUpdAcc			
26		+lt_RRC_ConnRel			Release RRC connection
27		START t_WaitS (1)			Wait 1 s to allow UE to relax

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
28		?TIMEOUT t_WaitS			
29		START t_WaitS (60)			
30		+ts_AT_TriggerGMM_Attach			trigger UE to initiate GMM Attach after allowing the UE to decode Sys Infos
31		+ts_RRC_ConnEst(p_CellId, est_Reg, registration)			Establish RRC connection
32		Dc ? RRC_DataInd (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_Start := RRC_DataInd.start)CANCEL t_WaitS	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUEST – Extract Attach type requested
33		+ ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
34		+ts_GMM_Authentication (p_CellId)			AUTHENTICATION AND CIPHERING REQUEST AUTHENTICATION AND CIPHERING RESPONSE
35		+lt_SecurityMode			SECURITY MODE COMMAND SECURITY MODE COMPLETE

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
36		+lt_AttachAccept			ATTACH ACCEP T
37		+lt_RRC_ConnRe l			ATTACH COMPL ETE RRC connecti on release
38		? TIMEOUT t_WaitS		F	IF UE doesnt respond to Attach triggere d Fail the UE.
39		lt_IdleUpdated_NMO_II			
40		+ ts_MMI_UE_SwitchOn			
41		+ts_RRC_ConnEst(p_CellId, est_Reg, registration) Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start)	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cb_LocUpdReqAny(?))		Establis h RRC connecti on Any Location Update request
42		(tcv_GMM_AttachExpect := TRUE, tcv_GMM_AttachRec := FALSE)			Set Flags in order to enable default handler to store ATTACH REQUE ST PDU in case it is sent during Location Update procedur e
43		+ ts_SS_SecurityDownloadStart (cs_domain, tcv_Start)			
44		+ts_MM_Authentication(p_CellId)			Authenti cation
45		+ts_RRC_Security (p_CellId, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM, TRUE, cs_domain)			
46		+ lt_LocUpdAcc			
47		+lt_HandleAttachRequest			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
48		+ts_GMM_Authentication (p_CellId)			AUTHE NTICATI ON AND CIPHER ING REQUE ST AUTHE NTICATI ON AND CIPHER ING RESPO NSE
49		+lt_SecurityMode			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
50		+lt_AttachAccept			ATTACH ACCEP T ATTACH COMPL ETE
51		+lt_RRC_ConnRel			RRC connecti on release
52		lt_HandleAttachRequest (tcv_GMM_AttachExpect := FALSE)			Disable NAS default handler for ATTACH REQUE ST
53		[tcv_GMM_AttachRec = TRUE]			ATTACH REQUE ST was received and handled by NAS default handler

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
54		[NOT pc_AutomaticAttachSwitchON]			ATTACH REQUE ST was NOT yet received and the UE does not automati cally attach at switch on
55		+lt_RRC_ConnRel			RRC connecti on release
56		START t_WaitS (1)			Wait 1 s to allow UE to relax
57		?TIMEOUT t_WaitS			
58		START t_WaitS (60)			
59		+ts_AT_TriggerGMM_Attach			Trigger UE to initiate GMM Attach after allowing the UE to decode Sys Infos
60		+ts_RRC_ConnEst(p_CellId, est_Reg, registration)			Establis h RRC connecti on
61		Dc ? RRC_DataInd (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.t ype, tcv_Start := RRC_DataInd.start)CANCEL t_WaitS	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_Iv, c_RAI_Any_v, ?))		ATTACH REQUE ST - Extract Attach type requeste d
62		+ ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
63		? TIMEOUT t_WaitS		F	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
64		[TRUE]			The UE did not send ATTACH REQUEST but it should since it shall automatically call switch attach at switch on
65		START t_WaitS (5)			
66		Dc ? RRC_DataInd (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_Start := RRC_DataInd.start)CANCEL t_WaitS	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUEST – Extract Attach type requested
67		+ ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
68		? TIMEOUT t_WaitS			Now, if this event happens , then the UE didn't send an ATTACH REQUEST yet. We give the UE a last chance: We release the connection and wait for the UE to automatically start a connection and finally send an ATTACH REQUEST

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
69		+lt_RRC_ConnRel			RRC connecti on release
70		START t_WaitS (5)			
71		+ts_RRC_ConnEst(p_CellId, est_Reg, registration)			Establis h RRC connecti on
72		Dc ? RRC_DataInd (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_Start := RRC_DataInd.start)CANCEL t_WaitS	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUE ST - Extract Attach type requeste d
73		+ ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
74		? TIMEOUT t_WaitS		F	Here we finally FAIL the UE! [Note: Actually the timeout will be handled by the default handler of ts_RRC _ConnE st]
75		lt_GMMOnly_IdleUpdated			
76		+ ts_MMI_UE_SwitchOn			
77		+lt_GMMOnly_TriggerAttach +ts_RRC_ConnEst(p_CellId, est_Reg, registration)			Establis h RRC connecti on
78		+lt_AttachRequest			ATTACH REQUE ST

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
79		+ts_GMM_Authentication (p_CellId)			AUTHE NTICATI ON AND CIPHER ING REQUE ST AUTHE NTICATI ON AND CIPHER ING RESPO NSE
80		+lt_SecurityMode			SECURI TY MODE COMMA ND SECURI TY MODE COMPL ETE
81		+lt_AttachAccept			ATTACH ACCEP T ATTACH COMPL ETE
82		+lt_RRC_ConnRel			RRC connecti on release
83		lt_GMMOnly_TriggerAttach [NOT pc_AutomaticAttachSwitchON]			
84		+ts_NAS_Delay(tsc_TWaitSysInfo)			Allow UE to decode Sys Infos
85		+ts_AT_TriggerGMM_Attach			Trigger UE to initiate GMM Attach after allowing the UE to decode Sys Infos

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
86		[TRUE]			Do nothing: UE will automatically attempt PS attach
87		It_AttachRequest Dc ? RRC_DataInd (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_Start := RRC_DataInd.start)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUEST – Extract Attach type requested
88		+ ts_SS_SecurityDownloadStart (ps_domain, tcv_Start)			
89		It_LocUpdAcc			
90		[tcv_Use_E_PLMN = FALSE] Dc!RRC_DataReq	ca_DataReq(tsc_CellDedicated, tsc_RB3, c_LocUpdAcpTMSI(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac))		LOCATION UPDATING ACCEPT
91		Dc?RRC_DataInd	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_TMSI_ReallocCmpl)		TMSI REALLOCATION COMPLETE
92		[TRUE]			[tcv_Use_E_PLMN = TRUE]
93		Dc!RRC_DataReq	ca_DataReq(tsc_CellDedicated, tsc_RB3, c_LocUpdAcpTMSI_E_PLMN (tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_E_PLMN))		LOCATION UPDATING ACCEPT including 'equivalent PLMN list'
94		Dc?RRC_DataInd	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_TMSI_ReallocCmpl)		TMSI REALLOCATION COMPLETE
		It_SecurityMode			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
95		+ ts_RRC_Security (p_CellId, tcv_PS_AuthCK, tcv_PS_AuthIK, tcv_AuthKcGSM, TRUE, ps_domain)			SECURITY MODE COMMAND SECURITY MODE COMPLETE
96		It_AttachAccept [(tcv_UE_OpMode = opModeA) AND (tcv_TmpCellInfo.nmo = tsc_NMO_I)]			if UE is mode A and NMO II
97		(tcv_AssignedTMSI :=px_TMSI_Def, tcv_AssignedPTMSI :=px_PTMSI_Def, tcv_Assigned_PTMSI_Sig := px_PTMSI_SigDef)			Use default values
98		[tcv_Use_E_PLMN = FALSE]			
99		Dc ! RRC_DataReq	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3, cs_AttachAcc(c_GMM_AttachResult('011'B), c_RAI_v(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_TmpCellInfo.rac), c_PTMSI_Signature(tcv_Assigned_PTMSI_Sig), c_MobileIdPTMSI(tcv_AssignedPTMSI), c_GMM_MobileIdTMSI(tcv_AssignedTMSI)))		ATTACH ACCEPT for combined CS/PS – Attach result 'GPRS/IMSI attached' – RAI default – P-TMSI signature – MobileId P-TMSI – default TMSI
100		Dc ? RRC_DataInd	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachComplete)		ATTACH COMPLETE
101		[TRUE]			[tcv_Use_E_PLMN = TRUE]

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
102		Dc ! RRC_DataReq	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3, cs_AttachAccE_PLMN(c_GMM_AttachResult('011'B), c_RAI_v(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_TmpCellInfo.rac), c_PTMSI_Signature(tcv_Assigned_PTMSI_Sig), c_MobileIdPTMSI(tcv_AssignedPTMSI), c_GMM_MobileIdTMSI(tcv_AssignedTMSI), tcv_E_PLMN))		ATTACH ACCEPT for combined CS/PS – Attach result 'GPRS/MSI attached' – RAI default – P-TMSI signature – MobileId P-TMSI – default TMSI – equivalent PLMN list
103		Dc ? RRC_DataInd	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachComplete)		ATTACH COMPLETE
104	[TRUE]				If mode is C or if NMO is II
105		(tcv_AssignedPTMSI := px_PTMSI_Def, tcv_Assigned_PTMSI_Sig := px_PTMSI_SigDef)			Use default values
106		[tcv_Use_E_PLMN = FALSE]			
107		Dc ! RRC_DataReq	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3, cs_AttachAcc(c_GMM_AttachResult('001'B), c_RAI_v(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_TmpCellInfo.rac), c_PTMSI_Signature(tcv_Assigned_PTMSI_Sig), c_MobileIdPTMSI(tcv_AssignedPTMSI), –))		ATTACH ACCEPT for PS only – Attach result 'GPRS attached' – RAI default (RAI-1) – P-TMSI -1 signature – MobileId

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
108		Dc ? RRC_DataInd	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachComplete)		P-TMSI -1 - omit TMSI ATTACH COMPL ETE
109		[TRUE]			[tcv_Us e_E_PL MN = TRUE]
110		Dc ! RRC_DataReq	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3, cs_AttachAccE_PLMN(c_GMM_AttachResult('001' B), c_RAI_v(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_TmpCellInfo.rac), c_PTMSI_Signature (tcv_Assigned_PTMSI_Sig), c_MobileIdPTMSI (tcv_AssignedPTMSI), -, tcv_E_PLMN))		ATTACH ACCEP T for PS only - Attach result 'GPRS attached - RAI default (RAI-1) - P-TMSI -1 signatur e - MobileId P-TMSI -1 - omit TMSI - equivale nt PLMN list
111		Dc ? RRC_DataInd	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachComplete)		ATTACH COMPL ETE
		It_RRC_ConnRel			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
112		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
113		+ ts_RRC_ConnRel (p_CellId, cell_Fach_Dcch)			
114		[tcv_TmpCellInfo.cellConfig <> cell_FACH]			
115		+ ts_RRC_ConnRel (p_CellId, cell_Dch)			
Detailed Comments : See 3GPP 24.008 / 4.7 and also 3GPP 34.108 / 7.2.2 (Registration on PS) See also the detailed description in test Step ts_MM_IdleUpdated, on which this test Step is based.					

Test Step Dynamic Behaviour	
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Test Step Name : ts_IdleUpdated (p_CellId: INTEGER)

Group : BasicM_MM_GMM_Steps/

Objective : To bring the UE into MM and/or GMM state Idle Updated

Default : NAS_OtherwiseFail

Comments : Initial conditions:

- The UE is initially switched off
- The cell referred has been properly configured.

Note-1: The cell settings (like PLMN, LAC and RAC) used during the registration procedure are extracted from cell info record in tcv_CellInfoX

Note-2: If 'equivalent PLMN list' is to be used, please set global variable `tcv_Use_E_PLMN` to TRUE and accordingly initialise `tcv_E_PLMN`

Description :	
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Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[pc_CS AND pc_PS]			both CS and PS supported and for testing
2		+ts_GMM_IdleUpdated (p_CellId)			
3		[pc_CS]			CS supported and for testing
4		+ts_MM_IdleUpdated (p_CellId)			
5		[pc_PS]			PS supported and for testing
6		+ts_GMM_IdleUpdated (p_CellId)			
7	ERR1	[TRUE]		I	

Detailed Comments : Parameter description:

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p_CellId	Cell Identification	INTEGER
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INTEGER

Test Step Dynamic Behaviour					
Test Step Name : ts_MM_Authentication (p_CellId: INTEGER)					
Group : BasicM_MM_GMM_Steps/					
Objective : Normal authentication					
Default : NAS_OtherwiseFail					
Comments : To be used after the synchronization of the authentication in both SS and UE has been achieved.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSF1	+ts_MM_AuthenticationInit	ca_DataReq (tsc_CellDedicated, tsc_RB3, c_AuthReq (tcv_CS_KeySeq, tcv_AuthRAND, c_AUTN(tcv_AuthAUTN)))	(F)	1.
2		Dc!RRC_DataReq			Authenti cation Request
3		Dc?RRC_DataInd (tcv_AuthRspPDU := RRC_DataInd.msg, tcv_AuthRsp := tcv_AuthRspPDU.authRsp, tcv_AuthRspExt := tcv_AuthRspPDU.authRspExt)			Authenti cation Respons e with extensio n
4		(tcv_Res := o_AuthRspChk (tcv_AuthRsp, tcv_AuthRspExt, tcv_AuthK, tcv_AuthRAND, TRUE))			Authenti cation Respons e without extensio n
5		[tcv_Res = FALSE]			
6		[tcv_Res = TRUE]			
7		Dc?RRC_DataInd (tcv_AuthRspPDU := RRC_DataInd.msg, tcv_AuthRsp := tcv_AuthRspPDU.authRsp)			
8		(tcv_Res := o_AuthRspChk (tcv_AuthRsp, tcv_AuthRspExt, tcv_AuthK, tcv_AuthRAND, FALSE))			2.
9		[tcv_Res = FALSE]			
10		[tcv_Res = TRUE]			
11	TSF3	Dc?RRC_DataInd	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_AuthFailAny)	(F)	
Detailed Comments : Normal authentication to be used after the synchronization between SS and UE has been performed successfully. 1. Authentication request is sent by the network. Key Sequence and RAND as provided by the corresponding variables tcv_KeySeq and tcv_AuthRAND. 2. If the UE generates a response, this response may consist of 2 parts which have to be extracted from the received message. 3. The response is checked against an expected response calculated using a test suite operation. 4. If the comparison of expected and calculated response is not ok, then authentication fails. 5. Otherwise authentication is granted. 6. If the UE sends an Authentication Failure the authentication fails. Steps:					

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Test Step Dynamic Behaviour	
Detailed Comments : ...	1. Initialization of the authentication variables (see 34.108 cl. 8.1.2)
	2. Authentication Failure: should not happen because the synchronization has been done already

Test Step Dynamic Behaviour					
Test Step Name : ts_MM_AuthenticationInit Group : BasicM_MM_GMM_Steps/ Objective : Initialization of variables related to authentication. Default : NAS_OtherwiseFail Comments : Based on TS 34.108 cl. 8.1.2 and TS 33.102 cl.s 6.3 and 6.8.1.2 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+It_IncrementCiphKeySeqNum			
2		+It_AuthCalcAUTN			1. Calculati on of AUTN needed for Authenti cation Request
3		+It_AuthCalcUMTS_Others			2. Calculati on of other authenti cation informati on needed (IK, CK, XRES)
4		+It_AuthCalcKcGSM			3. Calculati on of Kc GSM, using IK and CK
5		It_IncrementCiphKeySeqNum			
6		[tcv_CS_KeySeq = '000'B]			
7		(tcv_CS_KeySeq := '001'B)			
8		[tcv_CS_KeySeq = '001'B]			
9		(tcv_CS_KeySeq := '010'B)			
10		[tcv_CS_KeySeq = '010'B]			
11		(tcv_CS_KeySeq := '011'B)			
12		[tcv_CS_KeySeq = '011'B]			
13		(tcv_CS_KeySeq := '100'B)			
14		[tcv_CS_KeySeq = '100'B]			
15		(tcv_CS_KeySeq := '101'B)			
16		[tcv_CS_KeySeq = '101'B]			
17		(tcv_CS_KeySeq := '110'B)			
18		[TRUE]			
19		(tcv_CS_KeySeq := '000'B)			
		It_AuthCalcAUTN			
		(tcv_AuthXDOUT := o_BitstringXOR(tcv_AuthRAND, tcv_AuthK, 128))			XDOUT := RAND XOR K

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		(tcv_AuthCDOU := o_BitstringConcat(tsv_AuthSQN, tcv_AuthAMF, 48, 16))			CDOU := SQN AMF
21		(tcv_AuthXDOUT_Half := o_BitstringXtract(tcv_AuthXDOUT, 128, 64, 0))			XDOUT _half := 64 bits of XDOUT starting from offset 0
22		(tcv_AuthAK := o_BitstringXtract(tcv_AuthXDOUT, 128, 48, 24))			AK := 48 bits of XDOUT starting from offset 24
23		(tcv_AuthAUTN_1 := o_BitstringXOR(tsv_AuthSQN, tcv_AuthAK, 48))			AUTN1 := SQN XOR AK
24		(tcv_AuthMAC := o_BitstringXOR(tcv_AuthXDOUT_Half, tcv_AuthCDOU, 64))			MAC := XDOUT _half XOR CDOU
25		(tcv_AuthAUTN_2 := o_BitstringConcat(tcv_AuthAMF, tcv_AuthMAC, 16, 64))			AUTN2 := AMF MAC
26		(tcv_AuthAUTN := o_BitstringConcat(tcv_AuthAUTN_1, tcv_AuthAUTN_2, 48, 80))			AUTN := AUTN1 AUTN2
27		It_AuthCalcUMTS_Others (tcv_AuthIK := o_BitstringXtract(tcv_AuthXDOUT, 128, 128, 16))			IK := 128 bits of XDOUT starting from offset 16 (wrappin g)

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
28		(tcv_AuthCK := o_BitstringXtract(tcv_AuthXDOUT, 128, 128, 8))			CK := 128 bits of XDOUT starting from offset 8 (wrapping)
29		(tcv_AuthXRES := o_BitstringXtract(tcv_AuthXDOUT, 128, (tcv_AuthN + 1), 0))			XRES := (n+1) bits of XDOUT starting from offset 0
30		lt_AuthCalcKcGSM (tcv_AuthCK_1 := o_BitstringXtract(tcv_AuthCK, 128, 64, 0))			CK1 := 64 bits of CK starting from offset 0
31		(tcv_AuthCK_2 := o_BitstringXtract(tcv_AuthCK, 128, 64, 64))			CK2 := 64 bits of CK starting from offset 64
32		(tcv_AuthIK_1 := o_BitstringXtract(tcv_AuthIK, 128, 64, 0))			IK1 := 64 bits of IK starting from offset 0
33		(tcv_AuthIK_2 := o_BitstringXtract(tcv_AuthIK, 128, 64, 64))			IK2 := 64 bits of IK starting from offset 64
34		(tcv_AuthCK_XOR := o_BitstringXOR(tcv_AuthCK_1, tcv_AuthCK_2, 64))			CK_XOR := CK1 XOR CK2
35		(tcv_AuthIK_XOR := o_BitstringXOR(tcv_AuthIK_1, tcv_AuthIK_2, 64))			IK_XOR := IK1 XOR IK2

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
36		(tcv_AuthKcGSM := o_BitstringXOR(tcv_AuthCK_XOR, tcv_AuthIK_XOR, 64))			KcGSM := CK_XOR R XOR IK_XOR (= CK1 XOR CK2 XOR IK1 XOR IK2)
Detailed Comments : Initialization of the variables needed for authentication. The calculation is done according to the prescription of TS 34.108 cl. 8.1.2. and TS 33.102 cl. 6.8.1.2 The AUTN calculated is used as parameter of the Authentication Request. CK, IK and Kc GSM are used by RRC. XRES is used to check the RES contained in Authentication Response.					

Test Step Dynamic Behaviour					
Test Step Name : ts_MM_IdleUpdated (p_CellId: INTEGER) Group : BasicM_MM_GMM_Steps/ Objective : To bring the UE into MM state Idle Updated – CS mode, general case Default : NAS_OtherwiseFail Comments : Before IdleUpdated can be used a Cell is to be created and System Information must be sent. This test Step covers the general case: the UE has a valid TMSI. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			Fetch SS_CellInfo table corresponding to the cell
2		+ts_MM_PwrOrUSIM_On(tsc_USIM_NeedRmv)			2. Activate the UE
3		START t_Dly (150000)			3. Supervise the reception of the expected Location Updating Request
4		+ ts_RRC_ConnEst (p_CellId, est_Reg, OMIT)			Connect ion Establishment MO
5		Dc?RRC_DataInd (tcv_Start := RRC_DataInd.start) CANCEL t_Dly	car_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cb_LocUpdReqAny(?))		4. Any Location Update request
6		+ ts_SS_SecurityDownloadStart (cs_domain, tcv_Start)			
7		+ts_MM_Authentication(p_CellId)			4.1 Authentication
8		+ts_RRC_Security (p_CellId, tcv_AuthCK, tcv_AuthIK, tcv_AuthKcGSM, TRUE, cs_domain)			
9		[tcv_Use_E_PLMN = FALSE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		Dc!RRC_DataReq	ca_DataReq(tsc_CellDedicated, tsc_RB3, c_LocUpdAcqTMSI(tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac))		4.2 Location Updatin g Accept without 'equivale nt PLMN list'
11		Dc?RRC_DataInd	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_TMSI_ReallocCmpl)		4.3 TMSI reallocati on complete
12		+ It_RRC_ConnRel			Connect ion Release
13		[TRUE]			[tcv_Us e_E_PL MN = TRUE]
14		Dc!RRC_DataReq	ca_DataReq(tsc_CellDedicated, tsc_RB3, c_LocUpdAcqTMSI_E_PLM N (tcv_TmpCellInfo.mcc, tcv_TmpCellInfo.mnc, tcv_TmpCellInfo.lac, tcv_E_PLMN))		4.2 Location Updatin g Accept including 'equivale nt PLMN list'
15		Dc?RRC_DataInd	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_TMSI_ReallocCmpl)		4.3 TMSI reallocati on complete
16		+ It_RRC_ConnRel			Connect ion Release
17		It_RRC_ConnRel (tcv_Use_E_PLMN := FALSE)			De-initia lise the variable tcv_Use _E_PLM N

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Test Step Dynamic Behaviour					
Test Step Name : ts_MM_PwrOrUSIM_Off (p_USIM_Rmvd : BOOLEAN) Group : BasicM_MM_GMM_Steps/ Objective : Deactivation of the UE Default : NAS_OtherwiseFail Comments : Depending upon UE's properties (USIM removal, switching off or powering off) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[(p_USIM_Rmvd) AND (pc_USIM_Rmv)]			SIM needs to be removed
2		+ts_MMI_USIM_Remove			. remove SIM card
3		[pc_SwitchOnOff]			
4		+ts_MMI_UE_SwitchOff			switch off the UE
5		[TRUE]			
6		+ts_MMI_UE_PwrOff			power off the UE
Detailed Comments : There are 2 types of deactivation required for testing: 1. USIM removal or switching off or removal of the power source 2. Switching off or removal of the power source Parameter p_USIM_Rmvd controls the variant to be applied: 1. tsc_USIM_NeedRmv(=TRUE): variant 1 is to be used, the USIM is to be removed if possible					

Test Step Dynamic Behaviour					
Test Step Name : ts_MM_PwrOrUSIM_On (p_USIM_Rmvd : BOOLEAN) Group : BasicM_MM_GMM_Steps/ Objective : Activation of the UE Default : NAS_OtherwiseFail Comments : Depending upon the UE's properties (USIM insertion, switching on or powering on) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[(p_USIM_Rmvd) AND (pc_USIM_Rmv)]			USIM has been removed
2		+ts_MMI_USIM_Insert			insert USIM card
3		[pc_SwitchOnOff]			
4		+ts_MMI_UE_SwitchOn			switch on the UE
5		[(((NOT p_USIM_Rmvd) OR (NOT pc_USIM_Rmv)) AND (NOT pc_SwitchOnOff))]			
6		+ts_MMI_UE_PwrOn			power on the UE
Detailed Comments : There are 2 types of activation required for testing: 1. USIM insertion or switching on or restoration of the power source 2. Switching on or restoration of the power source Parameter p_USIM_Rmvd controls the variant to be applied: 1. tsc_USIM_NeedRmv(=TRUE): variant 1 is to be used, the USIM is to be inserted if possible This test Step is the counterpart to ts_MM_PwrOrUSIM_Off resp ts_MM_IMSI_Detach.					

Test Step Dynamic Behaviour					
Test Step Name : po_ConnectionAndSS_Rel (p_CellId : INTEGER) Group : BasicM_Postambles/ Objective : To release the existing RRC connection and release the channels that are configured in the SS. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[tcv_TmpCellInfo.cellConfig <> cell_NotConfigured]			
3		+ It_Send_RRC_ConnectionRelease			
4		+ ts_SS_Rel (p_CellId)			
5		[tcv_TmpCellInfo.cellConfig = cell_NotConfigured]		I	0.
6		It_Send_RRC_ConnectionRelease [(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_NoDPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCon n) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_No Conn)]			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
7		[TRUE]			4.
8		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCP CH_StandAlonePCH_PS_2a)]			1.
9		UM!RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1, cs_108_RRC_ConnRelDCC H(tcv_CellIndInfo.dl_Integri tyCheckInfo, tcv_RRC_Ti, OMIT))		
10		AM?RLC_AM_DATA_IND	car_RRC_ConnRelCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_ConnRelCm pl (tcv_RRC_Ti))	(P)	
11		+ ts_RRC_Delay (tsc_DelayAfterRRC_ConnRel)			
12		[TRUE]			2.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		(tcv_N308 := 1, tcv_K := 1)			Maximum number of retransmissions of the RRC CONNECTION RELEASE COMPLETE message
14		UM!RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1, cs_108_RRC_ConnRelDCC H(tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, tcv_N308))		
15		UM?RLC_UM_DATA_IND	car_RRC_ConnRelCmplUM(tsc_CellDedicated, tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	
16		REPEAT It_RptRcvConnRel UNTIL [tcv_K = (tcv_N308+1)]			
17		It_RptRcvConnRel			UE sends RRC Connection Release Complete for N308 times
18	TSF2	START t_Dly			
19		? TIMEOUT t_Dly		(F)	
20	TSP2	(tcv_K := tcv_N308 + 1)			
20	TSP2	UM?RLC_UM_DATA_IND (tcv_K := tcv_K+1) CANCEL t_Dly	car_RRC_ConnRelCmplUM (tsc_CellDedicated , tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	To stop the loop Retransmission
Detailed Comments : 0. The cell has not been configured, it shall not be released 1. cell_FACH state 2. cell_DCH state 3. No RRC connection is established 4. An RRC connection is established					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_ConnEst (p_CellId : INTEGER; p_MO_Reg : RegOr_MO; p_EstCause : EstablishmentCause) Group : BasicM_RRC_Steps/ Objective : The generic Step to establish RRC Connection and bring UE to CELL_FACH or CELL_DCH state. Default : RRC_DefConnEst Comments : In this Step , 5 Signalling Radio Bearers with 3.4kbps DL & UL is setup (RB#0, 1, 2, 3,4) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SS_PrepareCellRRC_ConnEst (p_CellId)			
2		+ ts_SetTmpCellInfo (p_CellId)			
3		+It_RcvConnReq			
4		+It_Send_ConnSetUp			
5		+ ts_RRC_ReceiveConnSetupCmpl (p_CellId)			
6		It_RcvConnReq			
7		[p_MO_Reg = est_Reg] TM ? RLC_TR_DATA_IND (tcv_InitialUE_Id := RLC_TR_DATA_IND.tM_message.uL_CCCH_M essage.message.rrcConnectionRequest.initialUE_Identity)	car_RRC_ConnReq (p_CellId, tsc_RB0, cbr_108_RRC_ConnReq (registration))		
8		[(p_MO_Reg = est_MO) OR (p_MO_Reg = est_MT)]			
9		TM ? RLC_TR_DATA_IND (tcv_InitialUE_Id := RLC_TR_DATA_IND.tM_message.uL_CCCH_M essage.message.rrcConnectionRequest.initialUE_Identity)	car_RRC_ConnReq (p_CellId, tsc_RB0, cbr_108_RRC_ConnReq (p_EstCause))		
10		It_Send_ConnSetUp [(tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a_NoConn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_N			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		oConn) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_NoConn) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_NoConn)] UMIRLC_UM_DATA_REQ	cas_RRC_ConnSetup(p_CellId, tsc_RB0, cbs_108_RRC_ConnSetupFACH (tcv_InitialUE_Id, tcv_RRC_Ti, tcv_TmpCellInfo.priScrmCode , tcv_TmpCellInfo.uRNTI , tcv_TmpCellInfo.cRNTI, tcv_TmpCellInfo.uL_ScramblingCode))		
12		[tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn]			
13		+ ts_SetCellCfg (p_CellId, cell_FACH)			1.
14		[tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn]			
15		+ ts_SetCellCfg (p_CellId, cell_FACH_BMC)			1.
16		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn]			
17		+ ts_SetCellCfg (p_CellId, cell_FACH_2_PRACH)			1.
18		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn]			
19		+ ts_SetCellCfg (p_CellId, cell_FACH_2_SCCPCH)			1.
20		[tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn]			
21		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH)			
22		[tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn]			
23		+ ts_SetCellCfg (p_CellId, cell_FACH_MAC_SRB)			
24		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn]			
25		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
26		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_No Conn]			
27		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg2)			
28		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_No Conn]			
29		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_CTCH)			
30		[tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a_N oConn]			
31		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH_2a)			
32		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_ NoConn]			
33		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1)			
34		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_ NoConn]			
35		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)			
36		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_ NoConn]			
37		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)			
38		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn)]			
39		UM!RLC_UM_DATA_REQ	cas_RRC_ConnSetup(p_CellId, tsc_RB0, cbs_108_RRC_ConnSetup DCH (tcv_InitialUE_Id, tcv_RRC_Ti, tcv_TmpCellInfo.priScrmCo de , tcv_TmpCellInfo.uRNTI , tcv_TmpCellInfo.uL_Scrambl ingCode))		
40		[tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
41		+ ts_SetCellCfg (p_CellId, cell_DCH_StandAloneSRB)			
42		[tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn]			
43		+ ts_SetCellCfg (p_CellId, cell_DCH_MAC_SRB)			
44	ERR	[TRUE]		I	2.
Detailed Comments : 1. Update the cell configuration 2. A RRC connection has already been established.					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_ConnRel (p_CellId: INTEGER; p_RRC_RelStatus : RRC_Rel_Status) Group : BasicM_RRC_Steps/ Objective : To bring the UE from state CELL_DCH/ CELL_FACH to idle state by releasing the RRC connection Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_RRC_Delay (tsc_DelayBeforeRRC_ConnRel)			
3		+ It_Send_RRC_ConnectionRelease			
4		+ It_RestartCRLC_ForNextConnection			
5		+ ts_SS_ResetSecurityKey It_RestartCRLC_ForNextConnection			
6		[tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB]			
7		+ ts_CRLC_RelReconfSRB (p_CellId)			
8		+ ts_SetCellCfg (p_CellId, cell_DCH_StandAloneSRB_NoConn)			
9		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS)]			
10		+ ts_CRLC_RelReconfSRB (p_CellId)			
11		+ ts_SetCellCfg (p_CellId, cell_FACH_NoConn)			
12		[tcv_TmpCellInfo.cellConfig = cell_FACH_BMC]			
13		+ ts_CRLC_RelReconfSRB (p_CellId)			
14		+ ts_SetCellCfg (p_CellId, cell_FACH_BMC_NoConn)			
15		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH]			
16		+ ts_CRLC_RelReconfSRB (p_CellId)			
17		+ ts_SetCellCfg (p_CellId, cell_FACH_2_PRACH_NoConn)			
18		[tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH]			
19		+ ts_CRLC_RelReconfSRB (p_CellId)			
20		+ ts_SetCellCfg (p_CellId, cell_FACH_2_SCCPCH_NoConn)			
21		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			
22		+ ts_CRLC_RelReconfSRB (p_CellId)			
23		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH_NoC onn)			
24		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1]			
25		+ ts_CRLC_RelReconfSRB (p_CellId)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
26		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg1_No Conn)			
27		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2]			
28		+ ts_CRLC_RelReconfSRB (p_CellId)			
29		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_Cnfg2_No Conn)			
30		[tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH]			
31		+ ts_CRLC_RelReconfSRB (p_CellId)			
32		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_CTCH_No Conn)			
33		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a) OR(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)]			
34		+ ts_CRLC_RelReconfSRB (p_CellId)			
35		+ ts_SetCellCfg (p_CellId, cell_FACH_2SCCPCH_StandAlonePCH_2a_N oConn)			
36		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1)]			
37		+ ts_CRLC_RelReconfSRB (p_CellId)			
38		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_ NoConn)			
39		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
40		+ ts_CRLC_RelReconfSRB (p_CellId)			
41		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_ NoConn)			
42		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)]			
43		+ ts_CRLC_RelReconfSRB (p_CellId)			
44		+ ts_SetCellCfg (p_CellId, cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_ NoConn)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
45		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS)]			
46		+ ts_SS_ReconfigRAB_ToSRB (p_CellId)			
47		+ ts_SetCellCfg (p_CellId, cell_DCH_StandAloneSRB_NoConn)			
48	ERR1	[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn)]		I	1.
49	ERR2	[TRUE]		I	
		It_Send_RRC_ConnectionRelease			
50		[p_RRC_RelStatus= cell_Dch]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
51		(tcv_N308 := 1, tcv_K := 1)			Maximum number of retransmissions of the RRC CONNECTION RELEASE COMPLETE message
52		UM ! RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1,		
53	TSP1	UM ? RLC_UM_DATA_IND	cs_108_RRC_ConnRelDCCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, tcv_N308)) car_RRC_ConnRelCmplUM (tsc_CellDedicated, tsc_RB1, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	
54		REPEAT It_RptRcvConnRel UNTIL [tcv_K = (tcv_N308+1)]			UE sends RRC Connection Release Complete for N308 times
55		[p_RRC_RelStatus = cell_Fach_Dcch]			
56		UM ! RLC_UM_DATA_REQ	cas_RRC_ConnRelDCCH (tsc_CellDedicated, tsc_RB1,		
57	TSP2	AM ? RLC_AM_DATA_IND	cs_108_RRC_ConnRelDCCH (tcv_CellIndInfo.dl_IntegrityCheckInfo, tcv_RRC_Ti, OMIT)) car_RRC_ConnRelCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_ConnRelCmpl (tcv_RRC_Ti))	(P)	
58		+ ts_RRC_Delay (tsc_DelayAfterRRC_ConnRel)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
59	TSF2	[p_RRC_RelStatus = cell_Fach_Ccch]	cas_RRC_ConnRelCCCH(p_CellId, tsc_RB0, cs_108_RRC_ConnRelCCC H (c_U_RNTI, tcv_RRC_Ti))	I	Program ming error
60		UM!RLC_UM_DATA_REQ			
61		[TRUE]			
62		lt_RptRcvConnRel			
63		START t_Dly			
64	TSP2	? TIMEOUT t_Dly (tcv_K := tcv_N308 + 1)	car_RRC_ConnRelCmplUM (tsc_CellDedicated , tsc_RB1, cbr_108_RRC_ConnRelCm pl (tcv_RRC_Ti))	(F)	To stop the loop Retrans mission
65		UM?RLC_UM_DATA_IND (tcv_K := tcv_K+1) CANCEL t_Dly		(P)	
Detailed Comments : 1. No RRC connection is established					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_ReceiveConnSetupCmpl (p_CellId : INTEGER) Group : BasicM_RRC_Steps/ Objective : To receive RRC CONNECTION SETUP COMPLETE message and download SS security keys according to the received information element. Default : RRC_DefConnEst,RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS)]			
3		START t_WaitMS			
4	TSF1	? TIMEOUT t_WaitMS		(F)	
5	TSP1	AM ? RLC_AM_DATA_IND (tcv_StartList := RLC_AM_DATA_IND.aM_message.uL_DCC H_Message.message.rrcConnectionSetupComplete.startList, tcv_CellIndInfo.cipheringAlgorithmCapability := RLC_AM_DATA_IND.aM_message.uL_DCC H_Message.message.rrcConnectionSetupComplete.ue_RadioAccessCapability.securityCapability.cipheringAlgorithmCap) CANCEL t_WaitMS	car_RRC_ConnSetupCmpl (tsc_CellDedicated, tsc_RB2, cr_108_RRC_ConnSetupCmpl (tcv_RRC_Ti, ?))	(P)	UE capability ie is present in a DCH configuration
6		+ It_GetHFN			
7		[(tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
8		(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)] START t_WaitMS			
9	TSF2	? TIMEOUT t_WaitMS		(F)	
10	TSP2	AM ? RLC_AM_DATA_IND (tcv_StartList := RLC_AM_DATA_IND.aM_message.uL_DCC H_Message.message.rrcConnectionSetupCo mplete.startList) CANCEL t_WaitMS	car_RRC_ConnSetupCmpl (tsc_CellDedicated, tsc_RB2, cr_108_RRC_ConnSetupCmpl (tcv_RRC_Ti, ?))	(P)	UE capability is not present in a FACH configuration
11		+ It_GetHFN			
12		[TRUE] It_GetHFN		I	
13		(tcv_Count := NUMBER_OF_ELEMENTS (tcv_StartList))			
14		[tcv_Count = 1]			
15		+ It_FirstValue			
16		[tcv_Count = 2]			
17		+ It_FirstValue			
18		+ It_SecondValue			
19	ERR1	[TRUE] It_FirstValue		F	
20		[tcv_StartList.[0].cn_DomainIdentity = tsc_CS_Domain]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21	ERR2	(tcv_CellIndInfo.start_CS := tcv_StartList.[0].start_Value)		F	
22		[tcv_StartList.[0].cn_DomainIdentity = tsc_PS_Domain]			
23		(tcv_CellIndInfo.start_PS := tcv_StartList.[0].start_Value)			
24		[TRUE]			
		It_SecondValue			
25		[tcv_StartList.[1].cn_DomainIdentity = tsc_CS_Domain]			
26		(tcv_CellIndInfo.start_CS := tcv_StartList.[1].start_Value)			
27		[tcv_StartList.[1].cn_DomainIdentity = tsc_PS_Domain]			
28	ERR3	(tcv_CellIndInfo.start_PS := tcv_StartList.[1].start_Value)		F	
29		[TRUE]			
Detailed Comments : 1. Download the START value to SS					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_ReceiveRB_SetupCmpl (p_CellId : INTEGER; p_RbType: RB_ConfigType) Group : BasicM_RRC_Steps/ Objective : To receive RADIO BEARER SETUP COMPLETE message and reconfigure SS according to the received information element values. Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		START t_WaitMS			
3		[(p_RbType = cell_DCH_Speech) OR (p_RbType = cell_DCH_64kCS_RAB_SRB) OR (p_RbType = cell_DCH_57_6kCS_RAB_SRB) OR (p_RbType = cell_Two_DTCH) OR (p_RbType = cell_Four_DTCH_CS) OR ((p_RbType = cell_Two_DTCH_PS_CS) AND (tcv_CN_Domain = cs_domain)) OR ((p_RbType = cell_Four_DTCH_PS_CS) AND (tcv_CN_Domain = cs_domain)) OR ((p_RbType = cell_DCH_DSCH_CS_PS) AND (tcv_CN_Domain = cs_domain))]			TM RAB
4		[(tcv_CellIndInfo.cs_cipheringStarted = TRUE) AND(tcv_CellIndInfo.recentSecureDomain = cs_domain)]			
5		+ It_CipheringStartedTM_RAB			
6		[(tcv_CellIndInfo.cs_cipheringStarted = TRUE) AND ((p_RbType = cell_DCH_Speech) OR (p_RbType = cell_DCH_64kCS_RAB_SRB))]			For RAB test cases
7		+ It_CipheringStartedTM_RAB			
8		[tcv_CellIndInfo.cs_cipheringStarted = FALSE]			
9		+ It_CipheringNotStartedTM_RAB			
10		[TRUE]			AM/UM RAB
11		[(tcv_CellIndInfo.ps_cipheringStarted = TRUE) AND(tcv_CellIndInfo.recentSecureDomain = ps_domain)]			
12		+ It_CipheringStartedAM_RAB			
13		[TRUE]			
14		+ It_CipheringNotStartedAM_RAB			
15		It_CipheringStartedTM_RAB + ts_CMAC_DownloadSecurityKey (tcv_AuthCK, OMIT, OMIT, tcv_CN_Domain, tcv_CellIndInfo.start_CS)			
16		+ ts_CMAC_UL_DL_CipherCfg (tcv_CellIndInfo.dL_CipherMode, tcv_ActTime ,notInc)			
17	TSF1	? TIMEOUT t_WaitMS		(F)	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
18	TSP2	AM ?RLC_AM_DATA_IND (tcv_CipherActTime := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.c ount_C_ActivationTime, tcv_CellIndInfo.start_CS := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.st art_Value) CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RB_SetUpCmpl (tcv_RRC_Ti, *, OMIT))	(P)	A new start value is provided
19		+ts_CMAC_DownloadSecurityKey(tcv_Auth CK, OMIT, OMIT, tcv_CN_Domain, tcv_CellIndInfo.start_CS)			
20		+ ts_CMAC_UL_DL_CipherCfg (tcv_CellIndInfo.dL_CipherMode, tcv_CipherActTime, incPerCFN_Cycle)			
		It_CipheringStartedAM_RAB			
21	TSF3	? TIMEOUT t_WaitMS		(F)	
22	TSP4	AM ?RLC_AM_DATA_IND (tcv_CellIndInfo.start_PS := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.start_V alue , tcv_CellIndInfo.uL_CipherMode := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.rb_UL_ CiphActivationTimeInfo) CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RB_SetUpCmpl (tcv_RRC_Ti, OMIT, *))	(P)	A new start value is provided
		+ It_SS_CipheringAM_RAB_UL_DL (tcv_PS_AuthCK)			A RB UL cipher is not present
		It_CipheringNotStartedTM_RAB			
24	TSF5	? TIMEOUT t_WaitMS		(F)	
25	TSP5	AM ?RLC_AM_DATA_IND (tcv_CipherActTime := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.count_ C_ActivationTime, tcv_CellIndInfo.uL_CipherMode := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.rb_UL_ CiphActivationTimeInfo) CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cr_RRC_RB_SetUpCmplNo StartVal (tcv_RRC_Ti, *,*))	(P)	No start value No RB UL cipher
26		+ ts_CMAC_DownloadSecurityKey (OMIT, OMIT, OMIT, tcv_CN_Domain, tcv_CellIndInfo.start_CS)			
27		+ ts_CMAC_UL_CipherCfg (tcv_CellIndInfo.dL_CipherMode, tcv_CipherActTime, notInc)			
28	TSP6	AM ?RLC_AM_DATA_IND (tcv_CipherActTime := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.count_ C_ActivationTime, tcv_CellIndInfo.start_CS := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.start_V alue) CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RB_SetUpCmpl (tcv_RRC_Ti, *, OMIT))	(P)	A new start value is provided

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
29		+ ts_CMAC_DownloadSecurityKey (OMIT, OMIT, OMIT, tcv_CN_Domain, tcv_CellIndInfo.start_CS)			
30		+ ts_CMAC_UL_CipherCfg (tcv_CellIndInfo.dL_CipherMode, tcv_CipherActTime ,notInc)			
		It_CipheringNotStartedAM_RAB			
31	TSF5	? TIMEOUT t_WaitMS		(F)	
32	TSP5	AM ?RLC_AM_DATA_IND CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cr_RRC_RB_SetUpCmplNo StartVal (tcv_RRC_Ti, OMIT,OMIT))	(P)	No start value No RB UL cipher
33		+ It_SS_CipheringAM_RAB_UL_DL(OMIT)			
34	TSP6	AM ?RLC_AM_DATA_IND (tcv_CellIndInfo.start_PS := RLC_AM_DATA_IND.aM_message.uL_DCCH_Me ssage.message.radioBearerSetupComplete.start_V alue) CANCEL t_WaitMS	car_RB_SetUpCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RB_SetUpCmpl (tcv_RRC_Ti, OMIT, OMIT))	(P)	A new start value is provided
35		+ It_SS_CipheringAM_RAB_UL_DL (OMIT)			
		It_SS_CipheringAM_RAB_UL_DL (p_KC : KeyCiphering)			
36		[(p_RbType = cell_DCH_64kPS_RAB_SRB) OR (p_RbType = cell_FACH_PS) OR (p_RbType = cell_Two_DTCH_CS_PS) OR (p_RbType = cell_Four_DTCH_CS_PS) OR (p_RbType = cell_PDCP_AM_RAB)OR (p_RbType= cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (p_RbType = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)OR (p_RbType = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (p_RbType = cell_DCH_DSCH_PS) OR (p_RbType = cell_DCH_DSCH_CS_PS) OR (p_RbType = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			
37		+ It_CRLC_SecurityConfig (tcv_CellIndInfo.start_PS, p_KC)			
38		(tcv_RLC_SeqNumDL_RB20 := 0)			
39		+ ts_CRLC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode , p_RbType ,notInc)			
40		+ ts_CRLC_UL_CipherCfg_RAB (ps_domain, cs_RB_ActTimeInfoList20 (0) ,notInc)			
41		[(p_RbType = cell_DCH_2AM_PS) OR (p_RbType =cell_DCH_2_PS_Call)]			
42		+ It_CRLC_SecurityConfig (tcv_CellIndInfo.start_PS, p_KC)			
43		(tcv_RLC_SeqNumDL_RB20 := 0, tcv_RLC_SeqNumDL_RB22 := 0)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
44		+ ts_CRLC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode , p_RbType ,notInc)			
45		+ ts_CRLC_UL_CipherCfg_RAB (ps_domain,cs_RB_ActTimeInfoList20_22 (0, 0),notInc)			
46		[(p_RbType = cell_PDCP_AM_UM_RAB)]			
47		+ It_CRLC_SecurityConfig (tcv_CellIndInfo.start_PS, p_KC)			
48		(tcv_RLC_SeqNumDL_RB20 := 0, tcv_RLC_SeqNumDL_RB21 := 0)			
49		+ ts_CRLC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode , p_RbType ,notInc)			
50		+ ts_CRLC_UL_CipherCfg_RAB (ps_domain,cs_RB_ActTimeInfoList20_21 (0, 0),notInc)			
51		[(p_RbType = cell_PDCP_UM_RAB)]			
52		+ It_CRLC_SecurityConfig (tcv_CellIndInfo.start_PS, p_KC)			
53		(tcv_RLC_SeqNumDL_RB21 := 0)			
54		+ ts_CRLC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode , p_RbType ,notInc)			
55		+ ts_CRLC_UL_CipherCfg_RAB (ps_domain, cs_RB_ActTimeInfoList21 (0) ,notInc)			
56		[(p_RbType = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (p_RbType= cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (p_RbType = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a) OR (p_RbType = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
57		+ It_CRLC_SecurityConfig (tcv_CellIndInfo.start_PS, p_KC)			
58		(tcv_RLC_SeqNumDL_RB20 := 0, tcv_RLC_SeqNumDL_RB24 := 0)			
59		+ ts_CRLC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode , p_RbType ,notInc)			
60		+ ts_CRLC_UL_CipherCfg_RAB (ps_domain,cs_RB_ActTimeInfoList20_24 (0, 0),notInc)			
61		[TRUE]			
		It_CRLC_SecurityConfig (p_Hfn_LT: HyperFrameNumber ; p_KC_LT: KeyCIPHERing)			for RLC do nothing

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
62		CRLC ! CRLC_SecurityMode_Config_REQ	ca_CRLC_SecurityModeCfg Req (tsc_CellDedicated , tcv_CellIndInfo.recentSecur eDomain, p_Hfn_LT , p_KC_LT, OMIT, OMIT)		Downloa d security keys for RLC. CRLC is configur ed with cellId -1 (tsc_Cell Dedicate d)
63		CRLC ? CRLC_SecurityMode_Config_CNF	ca_CRLC_SecurityModeCfg Cnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_GetRLC_SeqNumSecurity (p_CellId : INTEGER) Group : BasicM_Security_Steps/ Objective : To assign the variables to the current RLC sequence number of its corresponding SRB and RAB if configured. Default : SS_Def_Special Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB1)		
3		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB1 := CRLC_SequenceNumber_CNF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB1)		
4		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB2)		
5		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB2 := CRLC_SequenceNumber_CNF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB2)		
6		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB3)		
7		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB3 := CRLC_SequenceNumber_CNF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB3)		
8		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB4)		
9		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB4 := CRLC_SequenceNumber_CNF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB4)		
10		[(tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR ((tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS)) OR ((tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS)) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		_CTCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH _2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlon ePCH_2a)) AND(tcv_CellIndInfo. recentSecureDomain =ps_domain)] CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
12		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB20 := CRLC_SequenceNumber_CNF. count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
13		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB)AND(tcv_CellIndInfo. recentSecureDomain =ps_domain)]			
14		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB21)		
15		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB21 := CRLC_SequenceNumber_CNF. count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB21)		
16		[(tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) AND(tcv_CellIndInfo. recentSecureDomain =ps_domain)]			
17		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
18		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB20 := CRLC_SequenceNumber_CNF. count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
19		CRLC ! CRLC_SequenceNumber_RE Q	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB22)		
20		CRLC ? CRLC_SequenceNumber_C NF (tcv_RLC_SeqNumDL_RB22 := CRLC_SequenceNumber_C NF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB22)		
21		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB)AND(tcv_CellIndInfo. recentSecureDomain =ps_domain)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		CRLC ! CRLC_SequenceNumber_REQ	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
23		CRLC ? CRLC_SequenceNumber_CNF (tcv_RLC_SeqNumDL_RB20 := CRLC_SequenceNumber_CNF. count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB20)		
24		CRLC ! CRLC_SequenceNumber_RE Q	cas_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB21)		
25		CRLC ? CRLC_SequenceNumber_C NF (tcv_RLC_SeqNumDL_RB21 := CRLC_SequenceNumber_C NF.count_C_LSB_DL)	car_GetRLC_SeqNum (tsc_CellDedicated, tsc_RB21)		
26		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_InitSystemSpecificCap Group : BasicM_Security_Steps/ Objective : If UE supports GSM, this step initialises tcv_UE_SystemSpecificCap based on PICS Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[pc_UMTS_GSM]			IF GSM is supported
2		(tcv_UE_SystemSpecificCap := 0)			
3		+lt_A57			
4		+lt_A56			
5		+lt_A55			
6		+lt_A54			
7		+lt_A53			
8		+lt_A52			
9		+lt_A51			
10		[TRUE]			IF GSM not Supported
11		(tcv_UE_SystemSpecificCap := 0)			
		lt_A57			
12		[pc_MS_ClsmkA5_7='1'B]			A5_7 Supported
13		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+64)			
14		[TRUE]			
		lt_A56			
15		[pc_MS_ClsmkA5_6='1'B]			A5_6 Supported
16		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+32)			
17		[TRUE]			
		lt_A55			
18		[pc_MS_ClsmkA5_5='1'B]			A5_5 Supported
19		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+16)			
20		[TRUE]			
		lt_A54			
21		[pc_MS_ClsmkA5_4='1'B]			A5_4 Supported
22		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+8)			
23		[TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
24		It_A53 [pc_MS_ClsmkA5_3='1'B]			A5_3 Support ed
25		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+4)			
26		[TRUE]			
27		It_A52 [pc_MS_ClsmkA5_2='1'B]			A5_2 Support ed
28		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+2)			
29		[TRUE]			
30		It_A51 [pc_MS_ClsmkA5_1='0'B]			A5_1 Support ed
31		(tcv_UE_SystemSpecificCap := tcv_UE_SystemSpecificCap+1)			
32		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_DownloadSecurityKey (p_KC : KeyCiphering; p_IK : IntegrityKey; p_GSM_ck : GSM_CipheringKey; p_CN_Domain : CN_DomainIdentity; p_HFN : B20) Group : BasicM_Security_Steps/ Objective : To download all security keys to CMAC. Only the keys/Parameters to be downloaded will be passed as parameters, rest will be omitted depending on the PICS. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CMAC ! CMAC_SecurityMode_Config_REQ	ca_CMAC_SecurityModeCf gReq (tsc_CellDedicated , p_CN_Domain, p_HFN , p_KC, p_IK, p_GSM_ck)		Downloa d security keys for MAC
2		CMAC ? CMAC_SecurityMode_Config_CNF	ca_CMAC_SecurityModeCf gCnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAL_DL_CipherCfg (p_CipherMode: CipheringModeCommand; p_ActTime: INTEGER; p_IncrDcr : Increment_Mode)					
Group : BasicM_Security_Steps/					
Objective : Configure ciphering on the MAC layer for DL RBs. This step shall be called when required.					
Default : SS_Def					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CMAC ! CMAC_Ciphering_Activate_REQ	ca_CMAL_DL_CipherActReq (tsc_CellDedicated, tsc_DL_DPCH1, p_CipherMode, p_ActTime, p_IncrDcr)		start, restart or stop ciphering for TM RB
2		CMAC ? CMAC_Ciphering_Activate_CNF	ca_CMAL_CipherActCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAL_UL_CipherCfg (p_CipherMode: CipheringModeCommand; p_ActTime: INTEGER; p_IncrDcr : Increment_Mode)					
Group : BasicM_Security_Steps/					
Objective : Configure ciphering on the MAC layer for UL RBs.					
Default : SS_Def					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CMAC ! CMAL_Ciphering_Activate_REQ	ca_CMAL_UL_CipherActReq (tsc_CellDedicated, tsc_UL_DPCH1, p_CipherMode, p_ActTime , p_IncrDcr)		start, restart or stop ciphering
2		CMAC ? CMAL_Ciphering_Activate_CNF	ca_CMAL_CipherActCnf (tsc_CellDedicated, tsc_UL_DPCH1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_DL_CipherCfgRB (p_CipherMode: CipheringModeCommand; p_RbType : RB_ConfigType ;p_IncMode : RLC_IncMode) Group : BasicM_Security_Steps/ Objective : Configure ciphering for RLC layer for configured AM/UM RAB's Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[(p_RbType = cell_DCH_64kPS_RAB_SRB) OR (p_RbType = cell_FACH_PS) OR (p_RbType = cell_Two_DTCH_CS_PS) OR (p_RbType = cell_Four_DTCH_CS_PS) OR (p_RbType = cell_PDCP_AM_RAB)OR (p_RbType = cell_PDCP_UM_RAB)OR (p_RbType= cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (p_RbType = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)OR (p_RbType = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (p_RbType = cell_DCH_DSCH_PS) OR (p_RbType = cell_DCH_DSCH_CS_PS) OR (p_RbType = cell_FACH_2SCCPCH_StandAlonePCH) OR (p_RbType = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			
2		+ lt_RLC_Activate (tsc_RB20, tcv_RLC_SeqNumDL_RB20)			
3		[(p_RbType = cell_PDCP_UM_RAB)]			
4		+ lt_RLC_Activate (tsc_RB21, tcv_RLC_SeqNumDL_RB21)			
5		[(p_RbType = cell_PDCP_AM_UM_RAB)]			
6		+ lt_RLC_Activate (tsc_RB20, tcv_RLC_SeqNumDL_RB20)			
7		+ lt_RLC_Activate (tsc_RB21, tcv_RLC_SeqNumDL_RB21)			
8		[(p_RbType = cell_DCH_2AM_PS) OR (p_RbType = cell_DCH_2_PS_Call)]			
9		+ lt_RLC_Activate (tsc_RB20, tcv_RLC_SeqNumDL_RB20)			
10		+ lt_RLC_Activate (tsc_RB22, tcv_RLC_SeqNumDL_RB22)			
11		[(p_RbType = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (p_RbType= cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (p_RbType = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a) OR (p_RbType = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
12		+ lt_RLC_Activate (tsc_RB20, tcv_RLC_SeqNumDL_RB20)			
13		+ lt_RLC_Activate (tsc_RB24, tcv_RLC_SeqNumDL_RB24)			
14		[TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		lt_RLC_Activate (p_rblId : INTEGER ; p_SeqNum : RLC_SequenceNumber) CRLC ! CRLC_Ciphering_Activate_REQ	ca_CRLC_DL_CipherActReq (tsc_CellDedicated , ps_domain , p_rblId , p_CipherMode, p_SeqNum , p_IncMode)		configure ciphering for signaling radio bearers
16		CRLC ? CRLC_Ciphering_Activate_CNF	ca_CRLC_CipherActCnf(tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_DL_CipherCfgSRB (p_CipherMode: CipheringModeCommand;p_IncMode : RLC_IncMode)					
Group : BasicM_Security_Steps/					
Objective : Configure ciphering for RLC layer for RB1, RB2, RB3 and RB4					
Default : SS_Def					
Comments : CRLC is configured with cellId -1 (tsc_CellDedicated)					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ lt_RLC_Activate (tsc_RB1, tcv_RLC_SeqNumDL_RB1)			
2		+ lt_RLC_Activate (tsc_RB2 , tcv_RLC_SeqNumDL_RB2+2)			
3		+ lt_RLC_Activate (tsc_RB3 , tcv_RLC_SeqNumDL_RB3)			
4		+ lt_RLC_Activate (tsc_RB4 , tcv_RLC_SeqNumDL_RB4)			
5		lt_RLC_Activate (p_rb : INTEGER ; p_RLC_Seq : RLC_SequenceNumber) CRLC ! CRLC_Ciphering_Activate_REQ	ca_CRLC_DL_CipherActReq (tsc_CellDedicated , tcv_CellIndInfo. recentSecureDomain, p_rb, p_CipherMode, p_RLC_Seq, p_IncMode)		configure ciphering for signaling radio bearers
6		CRLC ? CRLC_Ciphering_Activate_CNF	ca_CRLC_CipherActCnf(tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_DL_Integrity (p_IntegrityProtectionModelInfo : IntegrityProtectionModelInfo) Group : BasicM_Security_Steps/ Objective : To start the integrity protection Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Integrity_Activate_REQ	ca_CRLC_DL_IntegrityActivateReq (tsc_CellDedicated , tcv_CellIndInfo. recentSecureDomain, p_IntegrityProtectionModelInfo)		
2		CRLC ?CRLC_Integrity_Activate_CNF	ca_CRLC_IntegrityActivateCnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_UL_CipherCfg (p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList ;p_IncMode : RLC_IncMode) Group : BasicM_Security_Steps/ Objective : Configure ciphering for RLC layer Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Ciphering_Activate_REQ	ca_CRLC_UL_CipherActReq (tsc_CellDedicated , tcv_CellIndInfo. recentSecureDomain, p_RB_ActivationTimeInfoList ,p_IncMode)		configure ciphering for signaling radio bearers
2		CRLC ? CRLC_Ciphering_Activate_CNF	ca_CRLC_CipherActCnf(ts_c_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_UL_CipherCfg_RAB (p_CN_Domain : CN_DomainIdentity; p_RB_ActivationTimeInfoList : RB_ActivationTimeInfoList ;p_IncMode : RLC_IncMode) Group : BasicM_Security_Steps/ Objective : Configure ciphering for RLC layer Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Ciphering_Activate_REQ	ca_CRLC_UL_CipherActReq (tsc_CellDedicated , p_CN_Domain, p_RB_ActivationTimeInfoList ,p_IncMode)		configure ciphering for signaling radio bearers
2		CRLC ? CRLC_Ciphering_Activate_CNF	ca_CRLC_CipherActCnf(tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_UL_Integrity (p_IntegrityProtActivationInfo : IntegrityProtActivationInfo) Group : BasicM_Security_Steps/ Objective : To start the integrity protection Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Integrity_Activate_REQ	ca_CRLC_UL_IntegrityActivateReq (tsc_CellDedicated , tcv_CellIndInfo. recentSecureDomain, c_RestRB_IntegrityProtActivationInfoList(p_IntegrityProtActivationInfo.rrc_MessageSequenceNumberList.[0], p_IntegrityProtActivationInfo.rrc_MessageSequenceNumberList.[1], p_IntegrityProtActivationInfo.rrc_MessageSequenceNumberList.[3], p_IntegrityProtActivationInfo.rrc_MessageSequenceNumberList.[4]))		
2		CRLC ?CRLC_Integrity_Activate_CNF	ca_CRLC_IntegrityActivateCnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RRC_Security (<p> p_CellId : INTEGER; p_KC : KeyCiphering; p_IK : IntegrityKey; p_GSM_ck : GSM_CipheringKey; p_NewKey : BOOLEAN; p_CN_Domain : CN_DomainIdentity) </p> Group : BasicM_Security_Steps/ Objective : Configure and Activate (or deactivate) ciphering for all concerned RBs Default : RRC_Def1 Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			To Start Integrity on the UL RB2
2		+ It_RRC_InitVariables			
3		+ ts_SS_DownloadSecurityKey (p_CellId, p_KC, p_IK, p_GSM_ck, p_CN_Domain)			
4		+ It_ActivateSecurity_DL_SS			
5		+It_RB2_UL_IntegrityActivate			
6		+ It_StartSecurity_UE			If Start of Integrity Set DL RRC_M SN to 0, else skipt it.
7		[((tcv_CellIndInfo.ps_cipheringStarted = TRUE) AND (tcv_CellIndInfo.recentSecureDomain = ps_domain)) OR ((tcv_CellIndInfo.cs_cipheringStarted = TRUE) AND(tcv_CellIndInfo.recentSecureDomain = cs_domain))]			
8		+ ts_CRLC_ResumeSecurity (p_CellId)			
9		[TRUE]			
10		It_RB2_UL_IntegrityActivate [tcv_Int_ModifyFlag]			
11		+ ts_RB2_UL_IntegrityActivate(tcv_RRC_MSN_RB2_UL)			If Start of Integrity Set DL RRC_M SN to 0, else skipt it.
12		[NOT tcv_Int_ModifyFlag]			
13		+ ts_RB2_UL_IntegrityActivate(0)			
14		It_ActivateSecurity_DL_SS + It_InitialiseRRC_MSN			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		+ ts_CRRC_DL_Integrity (tcv_CellIndInfo.dL_Integrity)			
16		[((tcv_CellIndInfo.ps_cipheringStarted = TRUE) AND (tcv_CellIndInfo.recentSecureDomain =ps_domain)) OR ((tcv_CellIndInfo.cs_cipheringStarted = TRUE) AND(tcv_CellIndInfo.recentSecureDomain =cs_domain))]			
17		+ ts_CRRC_GetRLC_SeqNumSecurity (p_CellId)			
18		+ It_AssignRB_ActivationTimeInfoList			
19		+ ts_CRRC_SuspendSecurity (p_CellId)			Suspend SRBs 1, 3 and 4
20		+ ts_CRRC_DL_CipherCfgSRB (tcv_CellIndInfo.dL_CipherMode,notInc)			Configure ciphering for RLC (RBs 1, 2, 3 and 4)
21		+ ts_CRRC_DL_CipherCfgRB (tcv_CellIndInfo.dL_CipherMode, tcv_TmpCellInfo.cellConfig,notInc)			
22		+ ts_CMAR_CipherCfg (p_CellId ,TRUE , tcv_CellIndInfo.dL_CipherMode ,incPerCFN_Cycle)			
23		[TRUE]			If no New Ciphering config
24		It_InitialiseRRC_MSN [NOT tcv_Int_ModifyFlag]			If Start of Integrity Set DL RRC_MSN to 0, else skipt it.
25		+ ts_SetDL_RRC_MessageSN (p_CellId)			
26		[tcv_Int_ModifyFlag]			
27		It_StartSecurity_UE			
27		+ It_SendSecurityModeCommand			
28		[px_CipheringOnOff]			Ciphering and Integrity

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
29	TSP1	AM?RLC_AM_DATA_IND (tcv_CellIndInfo.uL_Integrity := RLC_AM_DATA_IND.aM_message.uL_DCCH_ Message.message.securityModeComplete.ul_Int egProtActivationInfo, tcv_CellIndInfo.uL_CipherMode := RLC_AM_DATA_IND.aM_message.uL_DCCH_ Message.message.securityModeComplete.rb_U L_CiphActivationTimeInfo)	car_RRC_SecModeCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_SecModeCmpl (tcv_RRC_Ti , ?))	(P)	UL cipherin g informati on is present
30		+ ts_CRLC_UL_CipherCfg (tcv_CellIndInfo.uL_CipherMode ,notInc)			
31		+ ts_CRLC_UL_Integrity (tcv_CellIndInfo.uL_Integrity)			
32	TSF1	AM?RLC_AM_DATA_IND	car_RRC_SecModeFail (tsc_CellDedicated, tsc_RB2, cr_108_SecModeFail (tcv_RRC_Ti , ?))	(F)	
33		[NOT (px_CipheringOnOff)]			Integrity only no cipherin g
34	TSP2	AM?RLC_AM_DATA_IND (tcv_CellIndInfo.uL_Integrity := RLC_AM_DATA_IND.aM_message.uL_DCCH_ Message.message.securityModeComplete.ul_Int egProtActivationInfo)	car_RRC_SecModeCmpl (tsc_CellDedicated, tsc_RB2, cbr_108_RRC_SecModeCmpl (tcv_RRC_Ti , OMIT))	(P)	No UL Cipherin g informati on
35		+ ts_CRLC_UL_Integrity (tcv_CellIndInfo.uL_Integrity)			
36	TSF2	AM?RLC_AM_DATA_IND	car_RRC_SecModeFail (tsc_CellDedicated, tsc_RB2, cr_108_SecModeFail (tcv_RRC_Ti , ?))	(F)	
37		lt_SendSecurityModeCommand [pc_UMTS_GSM]			Branchin g if GSM Support ed
38		[px_CipheringOnOff]			Cipherin g ON and integrity ON

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
39		[((tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR ((tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_PS_CS) AND (tcv_CellIndInfo. recentSecureDomain =cs_domain)) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_PS_CS) OR(tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_DSCH_CS_PS)) AND (tcv_CellIndInfo. recentSecureDomain =cs_domain)]			Cipherin g on TM RAB present, hence include Cipherin g activation time
40		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dL_Integrity CheckInfo, cs_RRC_SecModeCmdCiph Int (tcv_RRC_Ti, tcv_CellIndInfo.dL_CipherM ode, tcv_RB_ActivationTimeInfoL ist , tcv_CipherActTime, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlg orithmCapability, cs_UE_SysSpecCap (INT_TO_BIT (tcv_UE_SystemSpecificCa p,7)))))		Cipherin g for signallin g RBs 1 to 4
41		[TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
42		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dl_Integrity CheckInfo, cs_RRC_SecModeCmdCiphInt (tcv_RRC_Ti, tcv_CellIndInfo.dL_CipherMode, tcv_RB_ActivationTimeInfoList , OMIT, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlgorithmCapability,cs_UE_SysSpecCap (INT_TO_BIT (tcv_UE_SystemSpecificCap,7))))))		Ciphering for signalling RBs 1 to 4
43		[NOT (px_CipheringOnOff)]			Integrity ON and ciphering OFF
44		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dl_Integrity CheckInfo, cs_RRC_SecModeCmdInt (tcv_RRC_Ti, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlgorithmCapability, cs_UE_SysSpecCap (INT_TO_BIT (tcv_UE_SystemSpecificCap,7))))))		
45		[NOT pc_UMTS_GSM]			Branching if GSM Not Supported
46		[px_CipheringOnOff]			Ciphering ON and integrity ON

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
47		[((tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR ((tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_PS_CS) AND (tcv_CellIndInfo. recentSecureDomain =cs_domain)) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_PS_CS) OR(tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_DSCH_CS_PS)) AND (tcv_CellIndInfo. recentSecureDomain =cs_domain)]			Cipherin g on TM RAB present, hence include Cipherin g activation n time
48		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dL_Integrity CheckInfo, cs_RRC_SecModeCmdCiph Int (tcv_RRC_Ti, tcv_CellIndInfo.dL_CipherM ode, tcv_RB_ActivationTimeInfoL ist , tcv_CipherActTime, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlg orithmCapability,OMIT)))		Cipherin g for signallin g RBs 1 to 4
49		[TRUE]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
50		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dl_Integrity CheckInfo, cs_RRC_SecModeCmdCiph Int (tcv_RRC_Ti, tcv_CellIndInfo.dL_CipherM ode, tcv_RB_ActivationTimeInfoL ist , OMIT, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlg orithmCapability,OMIT)))		Cipherin g for signallin g RBs 1 to 4
51		[NOT (px_CipheringOnOff)]			Integrity ON and cipherin g OFF
52		AM!RLC_AM_DATA_REQ	cas_RRC_SecModeCmd (tsc_CellDedicated, tsc_RB2, cs_108_RRC_SecModeCmd (tcv_CellIndInfo.dl_Integrity CheckInfo, cs_RRC_SecModeCmdInt (tcv_RRC_Ti, p_CN_Domain, tcv_CellIndInfo.dL_Integrity , tcv_CellIndInfo.cipheringAlg orithmCapability,OMIT)))		
53		It_RRC_InitVariables			
54		+ It_InitCipherMode			
55		+ It_InitIntegrity			
56		+ ts_InitSystemSpecificCap			
57		+ ts_SaveCellInfo (p_CellId) [p_NewKey = TRUE]			Authenti cation was done, reset HFN
58		(tcv_HFN := '00000000000000000000'B)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
59		[p_CN_Domain = cs_domain]			
60		(tcv_CellIndInfo.start_CS := '00000000000000000000'B)			
61		[p_CN_Domain = ps_domain]			
62		(tcv_CellIndInfo.start_PS := '00000000000000000000'B)			
63		[p_NewKey = FALSE]			
64		[p_CN_Domain = cs_domain]			
65		(tcv_HFN := tcv_CellIndInfo.start_CS)			
66		[p_CN_Domain = ps_domain]			
67		(tcv_HFN := tcv_CellIndInfo.start_PS)			
		It_InitCipherMode			
68		[px_CipheringOnOff = TRUE]			
69		[pc_UEA1_Supp]			
70		(tcv_CellIndInfo.dL_CipherMode := cs_CipheringModeCmdOn (uea1), tcv_CellIndInfo.cipheringAlgorithmCapability := '000000000000001'B)			Switch On cipherin g
71		[p_CN_Domain = cs_domain]			
72		(tcv_CellIndInfo.cs_cipheringStarted := TRUE)			
73		[p_CN_Domain = ps_domain]			
74		(tcv_CellIndInfo.ps_cipheringStarted := TRUE)			
75		[NOT (pc_UEA1_Supp)]			
76		(tcv_CellIndInfo.dL_CipherMode := cs_CipheringModeCmdOn (uea0), tcv_CellIndInfo.cipheringAlgorithmCapability := '000000000000001'B)			Switch On cipherin g
77		[p_CN_Domain = cs_domain]			
78		(tcv_CellIndInfo.cs_cipheringStarted := TRUE)			
79		[p_CN_Domain = ps_domain]			
80		(tcv_CellIndInfo.ps_cipheringStarted := TRUE)			
81		[NOT (px_CipheringOnOff)]			
82		(tcv_CellIndInfo.cs_cipheringStarted := FALSE , tcv_CellIndInfo.ps_cipheringStarted := FALSE)			
83		[pc_UEA1_Supp]			
84		(tcv_CellIndInfo.cipheringAlgorithmCapability := '000000000000001'B)			
85		[NOT (pc_UEA1_Supp)]			
86		(tcv_CellIndInfo.cipheringAlgorithmCapability := '000000000000001'B)			
		It_InitIntegrity			
87		(tcv_CellIndInfo.recentSecureDomain:= p_CN_Domain)			
88		[tcv_CellIndInfo.integrityStarted]			
89		+ts_GetRRC_MessageSN (p_CellId)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
90		(tcv_CellIndInfo.dL_Integrity := cs_IntegrityProtectModify_P(tcv_RRC_MSN_RB0 , tcv_RRC_MSN_RB1 , tcv_RRC_MSN_RB2, tcv_RRC_MSN_RB3, tcv_RRC_MSN_RB4) , tcv_Int_ModifyFlag := TRUE)			Modify integrity
91		[NOT tcv_CellIndInfo.integrityStarted]			Start integrity
92		(tcv_CellIndInfo.dL_Integrity := cs_IntegrityProtectStart (px_FRESH), tcv_CellIndInfo.integrityStarted := TRUE, tcv_CellIndInfo.dL_IntegrityCheckInfo := cs_IntegrityCheckInfo0, tcv_Int_ModifyFlag := FALSE)			
93		It_AssignRB_ActivationTimeInfoList [((tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS)) AND (p_CN_Domain = ps_domain)]			
94		(tcv_RB_ActivationTimeInfoList := cs_RB_ActTimeInfoListSRBs_20 (tcv_RLC_SeqNumDL_RB1, tcv_RLC_SeqNumDL_RB2+2, tcv_RLC_SeqNumDL_RB3, tcv_RLC_SeqNumDL_RB4, tcv_RLC_SeqNumDL_RB20))			
95		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) AND (p_CN_Domain = ps_domain)]			
96		(tcv_RB_ActivationTimeInfoList := cs_RB_ActTimeInfoListSRBs_21 (tcv_RLC_SeqNumDL_RB1, tcv_RLC_SeqNumDL_RB2+2, tcv_RLC_SeqNumDL_RB3, tcv_RLC_SeqNumDL_RB4, tcv_RLC_SeqNumDL_RB21))			
97		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) AND (p_CN_Domain = ps_domain)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
98		(tcv_RB_ActivationTimeInfoList := cs_RB_ActTimeInfoListSRBs_20_21 (tcv_RLC_SeqNumDL_RB1, tcv_RLC_SeqNumDL_RB2+2, tcv_RLC_SeqNumDL_RB3, tcv_RLC_SeqNumDL_RB4, tcv_RLC_SeqNumDL_RB20, tcv_RLC_SeqNumDL_RB21))			
99		[((tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call)AND (p_CN_Domain = ps_domain)]			
100		(tcv_RB_ActivationTimeInfoList := cs_RB_ActTimeInfoListSRBs_20_22 (tcv_RLC_SeqNumDL_RB1, tcv_RLC_SeqNumDL_RB2+2, tcv_RLC_SeqNumDL_RB3, tcv_RLC_SeqNumDL_RB4, tcv_RLC_SeqNumDL_RB20, tcv_RLC_SeqNumDL_RB22))			
101		[TRUE]			
102		(tcv_RB_ActivationTimeInfoList := cs_RB_ActTimeInfoListSRBs (tcv_RLC_SeqNumDL_RB1 , tcv_RLC_SeqNumDL_RB2 +2, tcv_RLC_SeqNumDL_RB3 , tcv_RLC_SeqNumDL_RB4))			
Detailed Comments : 1. this step is necessary if (ciphering is tested and shall be switch on) OR (ciphering is tested and shall be switch off and was previously switched on) OR integrity is tested					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ResetSecurityKey Group : BasicM_Security_Steps/ Objective : To download all security keys to CMAC (for DCH cell configurations only) and CRLC. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_CellIndInfo:=c_CellIndInfoDef)			
2		[pc_UEA1_Supp]			
3		(tcv_CellIndInfo.cipheringAlgorithmCapability := '0000000000000011'B)			
4		[NOT (pc_UEA1_Supp)]			
5		(tcv_CellIndInfo.cipheringAlgorithmCapability := '0000000000000001'B)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_DownloadSecurityKey (p_CellId : INTEGER; p_KC : KeyCiphering; p_IK : IntegrityKey; p_GSM_ck : GSM_CipheringKey; p_CN_Domain : CN_DomainIdentity) Group : BasicM_Security_Steps/ Objective : To download all security keys to CMAC (for DCH cell configurations only) and CRLC. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_CipheringOnOff]			
3		[(tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_No Conn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a)O R			Cell FACH

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
4		(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)] + It_DownloadKeyCRLC (tcv_HFN, p_KC, p_IK)			
5		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS_Init) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS_Init)] + It_DownloadKeyCRLC (tcv_HFN, p_KC,p_IK)			Cell DCH no TM RAB
6					
7		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_PS_CS_Init) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_PS_CS_Init) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS)] + It_DownloadKeyCRLC (tcv_HFN,p_KC,p_IK)			cell DCH with TM RAB
8					
9		+ It_DownloadKeyCMAC (tcv_HFN, p_KC)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
10		[TRUE]			
11		[NOT px_CipheringOnOff]			
12		[(tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_No Conn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_No Conn)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a)O R (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)] + It_DownloadKeyCRLC (Cell FACH
13		tcv_HFN,OMIT,p_IK)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
14		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS_Init) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS_Init)]			Cell DCH no TM RAB
15		+ It_DownloadKeyCRLC (tcv_HFN ,OMIT, p_IK)			
16		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB)]			cell DCH with TM RAB
17		+ It_DownloadKeyCRLC (tcv_HFN,OMIT, p_IK)			
18		[TRUE] It_DownloadKeyCMAC (p_HFN : B20; p_KC_LT : KeyCiphering)			
19		CMAC ! CMAC_SecurityMode_Config_REQ	ca_CMAC_SecurityModeCfgReq (tsc_CellDedicated , p_CN_Domain, p_HFN , p_KC_LT, OMIT, OMIT)		Download security keys for MAC
20		CMAC ? CMAC_SecurityMode_Config_CNF It_DownloadKeyCRLC (p_HFN : B20; p_KC_LT : KeyCiphering; p_IK_LT : IntegrityKey)	ca_CMAC_SecurityModeCfgCnf (tsc_CellDedicated)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21		CRLC ! CRLC_SecurityMode_Config_REQ	ca_CRLC_SecurityModeCfg Req (tsc_CellDedicated , p_CN_Domain, p_HFN , p_KC_LT, p_IK_LT, OMIT)		Download security keys for RLC. CRLC is configur ed with cellId -1 (tsc_Cell Dedicate d)
22		CRLC ? CRLC_SecurityMode_Config_CNF	ca_CRLC_SecurityModeCfg Cnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_SecurityDownloadStart (p_domain : CN_DomainIdentity ; p_StartValue : B20) Group : BasicM_Security_Steps/ Objective : To download a new START value In the cell Independent Record Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[p_domain = cs_domain]			
2		(tcv_CellIndInfo.start_CS := p_StartValue)			
3		[p_domain = ps_domain]			
4		(tcv_CellIndInfo.start_PS := p_StartValue)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SetDL_RRC_MessageSN (p_ActCell : INTEGER) Group : BasicM_Security_Steps/ Objective : To Set the Message sequence number in DL to all 0 for RB0 to RB4 Default : SS_Def Comments : This step to be used when Integrity is to be started Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_SetRRC_MessageSN_REQ	ca_DL_CRLC_SetRRC_MS N_REQ (p_ActCell, tsc_RB0, 0)		
2		CRLC ?CRLC_SetRRC_MessageSN_CNF	ca_CRLC_SetRRC_MSN_C NF (p_ActCell, tsc_RB0)		
3		CRLC ! CRLC_SetRRC_MessageSN_REQ	ca_DL_CRLC_SetRRC_MS N_REQ(tsc_CellDedicated, tsc_RB1, 0)		
4		CRLC ?CRLC_SetRRC_MessageSN_CNF	ca_CRLC_SetRRC_MSN_C NF (tsc_CellDedicated, tsc_RB1)		
5		CRLC ! CRLC_SetRRC_MessageSN_REQ	ca_DL_CRLC_SetRRC_MS N_REQ(tsc_CellDedicated, tsc_RB2, 0)		
6		CRLC ?CRLC_SetRRC_MessageSN_CNF	ca_CRLC_SetRRC_MSN_C NF (tsc_CellDedicated, tsc_RB2)		
7		CRLC ! CRLC_SetRRC_MessageSN_REQ	ca_DL_CRLC_SetRRC_MS N_REQ(tsc_CellDedicated, tsc_RB3, 0)		
8		CRLC ?CRLC_SetRRC_MessageSN_CNF	ca_CRLC_SetRRC_MSN_C NF (tsc_CellDedicated, tsc_RB3)		
9		CRLC ! CRLC_SetRRC_MessageSN_REQ	ca_DL_CRLC_SetRRC_MS N_REQ(tsc_CellDedicated, tsc_RB4, 0)		
10		CRLC ?CRLC_SetRRC_MessageSN_CN F	ca_CRLC_SetRRC_MSN_C NF (tsc_CellDedicated, tsc_RB4)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_RB2_UL_IntegrityActivate(p_RRCN : RRC_MessageSequenceNumber) Group : BasicM_Security_Steps/ Objective : Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Integrity_Activate_REQ	ca_CRLC_UL_IntegrityActivateReq (tsc_CellDedicated , tcv_CellIndInfo.recentSecureDomain, c_RB2_IntegrityProtActivationInfoList(p_RRCN))		
2		CRLC ?CRLC_Integrity_Activate_CNF	ca_CRLC_IntegrityActivateCnf (tsc_CellDedicated)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_CipherCfg (p_CellId : INTEGER;p_CalcNewActTime : BOOLEAN; p_CipherMode: CipheringModeCommand; p_IncrDcr : Increment_Mode) Group : BasicM_Security_Steps/ Objective : To Configure the MAC Layer with the Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[((tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS) OR (tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig =cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig =cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS)) AND(tcv_CellInfo.recentSecureDomain =cs_domain)]			
3		+It_CalcActivationTime			
4		+ts_CMAC_DL_CipherCfg (p_CipherMode, tcv_CipherActTime, p_IncrDcr)			
5		+ts_CMAC_UL_CipherCfg (p_CipherMode, tcv_CipherActTime, p_IncrDcr)			
6		[TRUE]		(P)	No TM RAB configured
		It_CalcActivationTime			
7		[p_CalcNewActTime = TRUE]			
8		CPHY ! CPHY_Frame_Number_REQ	cas_GetFrameNum(p_CellId, tsc_DL_DPCH1)		
9		CPHY ? CPHY_Frame_Number_CNF (tcv_FrameNumber := CPHY_Frame_Number_CNF.frameNumber)	car_GetFrameNum(p_CellId, tsc_DL_DPCH1)		
10		(tcv_CipherActTime := (256 + tcv_FrameNumber- (tcv_FrameNumber MOD 8 +8)) MOD 256)			Calculate the Ciphering Activation Time
11		[p_CalcNewActTime = FALSE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_GetRRC_MessageSN(p_CellId : INTEGER) Group : BasicM_Security_Steps/ Objective : To Calculate the Message sequence numbers for RB0 to RB4 and store in TCV's. Default : Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC !CRLC_RRC_MessageSN_REQ	cas_CRLC_RRC_MessageS N_REQ (tsc_CellDedicated, tsc_RB0)		
2		CRLC ? CRLC_RRC_MessageSN_CNF (tcv_RRC_MSN_RB0 := CRLC_RRC_MessageSN_CNF.count_I_LSB_D L)	car_CRLC_RRC_MessageS N_CNF (tsc_CellDedicated, tsc_RB0)		
3		CRLC !CRLC_RRC_MessageSN_REQ	cas_CRLC_RRC_MessageS N_REQ (tsc_CellDedicated, tsc_RB1)		
4		CRLC ? CRLC_RRC_MessageSN_CNF (tcv_RRC_MSN_RB1 := CRLC_RRC_MessageSN_CNF.count_I_LSB _DL)	car_CRLC_RRC_MessageS N_CNF (tsc_CellDedicated, tsc_RB1)		
5		CRLC ! CRLC_RRC_MessageSN_REQ	cas_CRLC_RRC_MessageS N_REQ (tsc_CellDedicated, tsc_RB2)		
6		CRLC ? CRLC_RRC_MessageSN_CNF (tcv_RRC_MSN_RB2 := CRLC_RRC_MessageSN_CNF.count_I_L SB_DL, tcv_RRC_MSN_RB2_UL := CRLC_RRC_MessageSN_CNF.count_I_L SB_UL)	car_CRLC_RRC_MessageS N_CNF (tsc_CellDedicated, tsc_RB2)		
7		CRLC ! CRLC_RRC_MessageSN_REQ	cas_CRLC_RRC_MessageS N_REQ (tsc_CellDedicated, tsc_RB3)		
8		CRLC ? CRLC_RRC_MessageSN_CNF (tcv_RRC_MSN_RB3 := CRLC_RRC_MessageSN_CNF.count_ I_LSB_DL)	car_CRLC_RRC_MessageS N_CNF (tsc_CellDedicated, tsc_RB3)		
9		CRLC ! CRLC_RRC_MessageSN_REQ	cas_CRLC_RRC_MessageS N_REQ (tsc_CellDedicated, tsc_RB4)		
10		CRLC ? CRLC_RRC_MessageSN_CNF (tcv_RRC_MSN_RB4 := CRLC_RRC_MessageSN_CNF.cou nt_I_LSB_DL)	car_CRLC_RRC_MessageS N_CNF (tsc_CellDedicated, tsc_RB4)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_UL_DL_CipherCfg (p_CipherMode: CipheringModeCommand; p_ActTime: INTEGER; p_IncrDcr : Increment_Mode) Group : BasicM_Security_Steps/ Objective : Configure ciphering on the MAC layer for UL and DLRBs. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_CMAC_DL_CipherCfg (p_CipherMode, p_ActTime, p_IncrDcr)			
2		+ts_CMAC_UL_CipherCfg (p_CipherMode, p_ActTime, p_IncrDcr)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_ReconfRLC_Size (p_urnti:BOOLEAN) Group : BasicM_SS_Configuration_Steps/ Objective : To Reconfigure RLC size to allow U-RNTI in MAC header or C-RNTI in MAC header depending on parameter if it is TRUE allow U-RNTI in MAC header and if it is false allow C-RNTI in MAC header Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[p_urnti=TRUE]			
2		CRLC ! CRLC_Config_REQ	cad_RB_UM_ReconfInfo (tsc_CellDedicated , tsc_RB1, {uLogicalChannelIdentity tsc_UL_DCCH1, dLogicalChannelIdentity tsc_DL_DCCH1})		configure radio bearers : RB1 (UM + DCCH) and (UM + DCCH) Payload size=120
3		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB1)		
4		[p_urnti=FALSE]			
5		CRLC ! CRLC_Config_REQ	ca_RB_UM_ReconfInfo (tsc_CellDedicated , tsc_RB1, {uLogicalChannelIdentity tsc_UL_DCCH1, dLogicalChannelIdentity tsc_DL_DCCH1})		configure radio bearers : RB1 (UM + DCCH) and (UM + DCCH) Payload size=136 (default)
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB1)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_New_RNTI_Reconf (p_urnti:BOOLEAN; p_CellId : INTEGER; p_U_RNTI : U_RNTI; p_C_RNTI : BITSTRING) Group : BasicM_SS_Configuration_Steps/ Objective : Reconfigure MAC when a new U_RNTI or C_RNTI is assigned to UE. Default : SS_Def Comments : U-RNTI and C-RNTI are not required on DPCH. U-RNTI and C-RNTI is necessary when DCCH/DTCH mapped on S-CCPCH. C-RNTI is necessary when DCCH/DTCH mapped on PRACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_CRLC_ReconfRLC_Size (p_urnti)			
3		+ lt_CMAC_Reconf (p_urnti)			
4		lt_CMAC_Reconf (p_urnti: BOOLEAN)			
5		[p_urnti] CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH1, c_UE_Info(p_U_RNTI, OMIT), c_TrChInfoPCH_FACH_PS , c_TrLogMappingPCH_FACH_PS)		SS has valid U-RNTI, C-RNTI is not valid
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH1)		
7		[NOT p_urnti]			
8		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn)]			
9		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_PRACH1, c_UE_Info (OMIT, p_C_RNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DTCH)		SS has valid C-RNTI, U-RNTI is not valid Only C-RNTI is required on PRACH
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_PRACH1)		
11		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId , tsc_S_CCPCH1, c_UE_Info(OMIT, p_C_RNTI), c_TrChInfoPCH_FACH_PS , c_TrLogMappingPCH_FACH_PS)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
12		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , tsc_S_CCPCH1)		
13		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_Pag1_Cfg (p_CellId: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : Configure paging on the MAC layer. Default : SS_Def Comments : This step shall be used when UE in idle mode. The DRX cycle length to use is the shortest of the CN domain specific Drx cycle length. The test case variable tcv_dRX_CycleLengthPaging is assigned to: the smallest value of CN Drx cycle length for the CN domain the UE is attached to.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		+ It_CalculateDrxCycleLength			
4		CMAC ! CMAC_PAGING_Config_REQ	ca_CMAC_PagingCfgReq(p_CellId, tsc_S_CCPCH1, fdd, c_MAC_PagingCfg (o_GetPI(px_IMSI_Def, 18), tcv_dRX_CycleLengthPaging))		
5		CMAC ? CMAC_PAGING_Config_CNF	ca_CMAC_PagingCfgCnf(p_CellId, tsc_S_CCPCH1)		
6	ERR1	[px_RAT = tdd]		I	
7	ERR2	[TRUE]		I	
		It_CalculateDrxCycleLength			
8		[pc_CS AND pc_PS]			0.
9		[tcv_TmpCellInfo.dRX_CycleLength.cN_CS_DRX_CycleLength <= tcv_TmpCellInfo.dRX_CycleLength.cN_PS_DRX_CycleLength]			1.
10		(tcv_dRX_CycleLengthPaging := tcv_TmpCellInfo.dRX_CycleLength.cN_CS_DRX_CycleLength)			
11		[TRUE]			2.
12		(tcv_dRX_CycleLengthPaging := tcv_TmpCellInfo.dRX_CycleLength.cN_PS_DRX_CycleLength)			
13		[pc_CS AND NOT (pc_PS)]			3.
14		(tcv_dRX_CycleLengthPaging := tcv_TmpCellInfo.dRX_CycleLength.cN_CS_DRX_CycleLength)			
15		[pc_PS AND NOT (pc_CS)]			4.
16		(tcv_dRX_CycleLengthPaging := tcv_TmpCellInfo.dRX_CycleLength.cN_PS_DRX_CycleLength)			
Detailed Comments : 0. UE supports CS and PS 1. CN Drx Cycle length for CS is smaller than PS, then CN Drx Cycle length for CS is used 2. CN Drx Cycle length for CS is smaller than PS, then CN Drx Cycle length for PS is used 3. UE supports only CS, the CN Drx cycle length for CS is used 4. UE supports only PS, the CN Drx cycle length for PS is used					

Test Step Dynamic Behaviour					
Test Step Name : ts_CMAC_Rel (p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity) Group : BasicM_SS_Configuration_Steps/ Objective : To request to release the Radio Link Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CMAC ! CMAC_Config_REQ	cas_MAC_Rel (p_CellId , p_PhyCH)		
2		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId , p_PhyCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CPHY_ActTime (p_CellId: INTEGER; p_PhysicalChannelIdentity : PhysicalChannelIdentity; p_ttiValue : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To calculate the activation time based on CHY frame number Default : SS_Def Comments : p_ttiValue : is equal to tti/10 The following tcv are assigned: tcv_FrameNumber, tcv_ActTime Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CPHY ! CPHY_Frame_Number_REQ	cas_GetFrameNum(p_CellId, p_PhysicalChannelIdentity)		
2		CPHY ? CPHY_Frame_Number_CNF (tcv_FrameNumber := CPHY_Frame_Number_CNF.frameNumber)	car_GetFrameNum(p_CellId, p_PhysicalChannelIdentity)		
3		(tcv_ActTime := (256 + tcv_FrameNumber- (tcv_FrameNumber MOD 8 +8)) MOD 256, tcv_TGCFN := (tcv_FrameNumber+(256-p_ttiValue)) MOD 256)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CPHY_TrChRelDCH_NoSHO (p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity) Group : BasicM_SS_Configuration_Steps/ Objective : To request to release the Radio Link Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CPHY ! CPHY_TrCH_Release_REQ	ca_PHY_RelReqDCH_NoSHO (p_CellId , p_PhyCH)		
2		CPHY ? CPHY_TrCH_Release_CNF	ca_PHY_RelCnf (p_CellId , p_PhyCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CPHY_TrChRelNonDch (p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity) Group : BasicM_SS_Configuration_Steps/ Objective : To request to release the Radio Link Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CPHY ! CPHY_TrCH_Release_REQ	ca_PHY_RelReqNonDch (p_CellId , p_PhyCH)		
2		CPHY ? CPHY_TrCH_Release_CNF	ca_PHY_RelCnf (p_CellId , p_PhyCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_Rel (p_CellId : INTEGER; p_RB_Id : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To release RLC entity. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_CRLC_RB_RelReq (p_CellId, p_RB_Id)		release radio bearer
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (p_CellId, p_RB_Id)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_RelReconfSRB (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To release RLC counter sequence number for SRB 1 to 4, by first releasing them and then setting them up again. Default : SS_Def Comments : This step is used only in conjunction with the RRC Connexion Release step. The configuration for SRBs 1 to 4 is the same as the one used for cell_DCH and cell_FACH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			
2		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
3		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
4		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
5		+ ts_SS_RB1_ToRB4_Cfg			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_ResumeSecurity (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : resume radio bearers for the security procedure Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			Switch On ciphering
2		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB1)		
3		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB1)		
4		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB3)		
5		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB3)		
6		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB4)		
7		CRLC ? CRLC_Resume_CNF (tcv_RB_SigResumed := TRUE)	car_ResumeRB(tsc_CellDedicated , tsc_RB4)		
8		[((tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS))AND (tcv_CellIndInfo. recentSecureDomain = ps_domain)]			
9		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB20)		
10		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB20)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
11		[tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB]			
12		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB21)		
13		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB21)		
14		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB)]			
15		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB20)		
16		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB20)		
17		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB21)		
18		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB21)		
19		[(tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call)]			
20		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB20)		
21		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB20)		
22		CRLC ! CRLC_Resume_REQ	cas_ResumeRB(tsc_CellDedicated , tsc_RB22)		
23		CRLC ? CRLC_Resume_CNF	car_ResumeRB(tsc_CellDedicated , tsc_RB22)		
24		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_CRLC_SuspendSecurity (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : suspend radio bearers for the security procedure Default : SS_Def_Special Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			Switch On ciphering
2		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB1, tcv_RLC_SeqNumDL_RB1)		2
3		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB1)		
4		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB3, tcv_RLC_SeqNumDL_RB3)		2
5		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB3)		
6		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB4, tcv_RLC_SeqNumDL_RB4)		2
7		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB4)		
8		[(tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CT			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CH) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_DSCH_CS_PS)) AND (tcv_CellIndInfo. recentSecureDomain =ps_domain)] CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB20, tcv_RLC_SeqNumDL_RB20)		2
10		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB20)		
11		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) AND (tcv_CellIndInfo. recentSecureDomain =ps_domain)]			
12		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB21, tcv_RLC_SeqNumDL_RB21)		2
13		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB21)		
14		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB)AND (tcv_CellIndInfo. recentSecureDomain =ps_domain)]			
15		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB20, tcv_RLC_SeqNumDL_RB20)		2
16		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB20)		
17		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB21, tcv_RLC_SeqNumDL_RB21)		2
18		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB21)		
19		[((tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call)) AND (tcv_CellIndInfo. recentSecureDomain =ps_domain)]			
20		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB20, tcv_RLC_SeqNumDL_RB20)		2

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB20)		2
22		CRLC ! CRLC_Suspend_REQ	cas_RLC_SuspendRB (tsc_CellDedicated , tsc_RB22, tcv_RLC_SeqNumDL_RB22)		
23		CRLC ? CRLC_Suspend_CNF	car_SuspendRB (tsc_CellDedicated , tsc_RB22)		
24		[TRUE]			
Detailed Comments : 1. Get the RLC sequence number 2. suspend the SRB at the current RLC sequence number					

Test Step Dynamic Behaviour					
Test Step Name : ts_ReconfigFACH_ToNoDedicated (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To reconfig the cell from cell_FACH to cell_FACH_NoDedicated. Default : SS_Def Comments : In cell_FACH_NoDedicated, no DCCH/DTCH are configured: no C-RNTI nor U-RNTI are downloaded to CMAC. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		+ It_RelBCCH_FACH			
4		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH1, c_UE_Info(OMIT, OMIT), c_TrChInfoPCH_FACH, c_TrLogMappingPCH_FACH_CellDCH)		map PCCH to PCH, CCCH to FACH
5		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH1)		
6		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_PRACH1, c_UE_Info(OMIT, OMIT), cb_TrChInfoRACH1, cb_TrLogMappingRACH2)		mapping CCCH to RACH
7		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_PRACH1)		
8	ERR1	[px_RAT = tdd]		I	
9	ERR2	[TRUE]		I	
10		It_RelBCCH_FACH [(tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS)]			
11		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_1DCH_DCCH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : to configure physical channel DPCH1 and connect DCH5 to the physical channel, then map DCCH1-4 on to the DCH5 transport channel. Used for setting up stand-alone UL:13.6 DL:13.6 kbps SRBs Default : SS_Def Comments : The transport channel DCH5 carries only dedicated control channels. MAC-d is configured with cellId -1 (tsc_CellDedicated). Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		(tcv_TGCFN := 0)			
4		CPHY!CPHY_RL_Setup_REQ	ca_DL_DPCH_Info (p_CellId, tsc_DL_DPCH1, cb_DL_DPCH_SRB_StandAloneDPCH_Offset (tcv_TmpCellInfo.dl_DPCH_2ndScrCode))		1.
5		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_DL_DPCH1)		
6		CPHY!CPHY_TrCH_Config_REQ	ca_DCH_148_TTI_10_DL_InfoActNow (p_CellId, tsc_DL_DPCH1)		2.
7		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
8		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (tsc_CellDedicated, tsc_DL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoDL_13_6_StandAlone, c_TrLogMappingDL_4DCC H)		3. C-RNTI and U-RNTI are not needed on DPCH
9		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(tsc_CellDedicated, tsc_DL_DPCH1)		
10		CPHY!CPHY_RL_Setup_REQ	ca_UL_DPCH_Info(p_CellId, tsc_UL_DPCH1, c_UL_DPCH_13_6_StandAlone(tcv_TmpCellInfo.ul_ScramblingCode))		1.
11		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_UL_DPCH1)		
12		CPHY!CPHY_TrCH_Config_REQ	ca_DCH_148_TTI_10_UL_InfoActNow (p_CellId, tsc_UL_DPCH1)		2.
13		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_UL_DPCH1)		
14		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo (tsc_CellDedicated, tsc_UL_DPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoUL_13_6_StandAlone, c_TrLogMappingUL_4DCC H)		3. C-RNTI and U-RNTI are not needed on DPCH

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
15		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (tsc_CellDedicated, tsc_UL_DPCH1)		
16	ERR1	[px_RAT = tdd]		I	
17	ERR2	[TRUE]		I	
Detailed Comments : 1. configure physical channel DPCH1. 2. connect DCH5 to DPCH1. 3. map logical channels: DCCH1–4 to DCH5. MAC–d is to be configured with cellId –1.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_2DCH_Modify (p_CellId : INTEGER; p_UL_TrchConfig: CphyTrchConfigReq; p_DL_TrchConfig: CphyTrchConfigReq; p_UL_TrchInfo, p_DL_TrchInfo: TrchInfo; p_UL_TrLogMapping, p_DL_TrLogMapping: Trch_LogCHMappingList1; p_ActTime: ActivationTime; p_DL_DPCHInfo : DL_DPCHInfo; p_UL_DPCH_Info : UL_DPCH_Info)					
Group : BasicM_SS_Configuration_Steps/					
Objective : to modify physical channel DPCH1 and connect DCH1 and DCH5 to the physical channel, then map DCCH1–4 on to the DCH5 transport channel and map DTCH1 to the DCH1 transport channel. used for RLC testing.					
Default : SS_Def					
Comments : SS is in TM mode different from the mode of UE. Transport channel configuration is parameter (of type: CphyTrchConfigReq); DPCH is 64 kbps physical channel.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Modify_REQ	ca_DL_DPCH_ModifyInfo (p_CellId, tsc_DL_DPCH1, p_DL_DPCHInfo, p_ActTime)		1.
4		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_DL_DPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_TrChCfgInfo(p_CellId, tsc_DL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_DL_TrchConfig)		3.
6		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_DL_DPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAR_ReconfigInfo(tsc_CellDedicated , tsc_DL_DPCH1, c_UE_Info(OMIT, OMIT), p_DL_TrchInfo, p_DL_TrLogMapping, p_ActTime)		4. U–RNTI and C–RNTI are not needed on DPCH
8		CMAC ? CMAC_Config_CNF	ca_CMAR_CfgCnf(tsc_CellDedicated , tsc_DL_DPCH1)		
9		CPHY!CPHY_RL_Modify_REQ	ca_UL_DPCH_ModifyInfo(p_CellId, tsc_UL_DPCH1, p_UL_DPCH_Info, p_ActTime)		1.
10		CPHY?CPHY_RL_Modify_CNF	ca_RL_ModifyCnf(p_CellId, tsc_UL_DPCH1)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_TrChCfgInfo (p_CellId, tsc_UL_DPCH1, c_TrChConfigTypeDCH_NoSHO, p_UL_TrchConfig)		2.
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_UL_DPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfo(tsc_CellDedicated , tsc_UL_DPCH1, c_UE_Info(OMIT, OMIT), p_UL_TrCHInfo, p_UL_TrLogMapping,p_Act Time)		4. U-RNTI and C-RNTI are not needed on DPCH
14		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(tsc_CellDedicated , tsc_UL_DPCH1)		
15	ERR1	[px_RAT = tdd]		I	
16	ERR2	[TRUE]		I	
Detailed Comments : 1. configure DPCH1 supporting 64 kspb data rate. 2. connect uplink DCH1 and DCH5 to DPCH1. 3. connect downlink DCH1 and DCH5 to DPCH1. 4. map logical channels: DCCH1-4 to DCH5, DTCH1 to DCH1 for both uplink and downlink and send relevant transport channel configuration information to MAC.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_AddDPCH (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To reconfigure the cell p_CellId from cell_NoDPCH to cell_DCH_StandaloneSRB_NoConn. Default : SS_Def Comments : The following channels need to be created: physical channels: DPCH; transport channels: DCH logical channels: DCCH; and signalling radio bearer: signalling bearer RB1, RB2, RB3, RB4 on DCH.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SS_1DCH_DCCH_Cfg (p_CellId)			
2		+ts_SS_RB1_ToRB4_Cfg			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_BCH_SCH_CPICH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To configure P-CCPCH, P-SCH, S-SCH and P-CPICH physical channels. To map BCH to P-PCCPCH, then to map logical channel BCCH to transport channel BCH. Default : SS_Def Comments : To configure P-CCPCH, P-SCH, S-SCH and P-CPICH physical channels and map BCH to P-PCCPCH, then to map logical channel BCCH to transport channel BCH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_pCPICH_Info (p_CellId, (tcv_TmpCellInfo.powerpCPICH))		p-CPICH
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_P_CPICH)		
5		CPHY!CPHY_RL_Setup_REQ	ca_pSCH_Info (p_CellId, (tcv_TmpCellInfo.powerpSCH))		p-SCH
6		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_P_SCH)		
7		CPHY!CPHY_RL_Setup_REQ	ca_sSCH_Info (p_CellId, (tcv_TmpCellInfo.powersSCH))		s-SCH
8		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_S_SCH)		
9		CPHY!CPHY_RL_Setup_REQ	ca_pCCPCH_Info (p_CellId, (tcv_TmpCellInfo.powerpCCPCH))		p-CCPCH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_P_CCPCH)		
11		CPHY!CPHY_TrCH_Config_REQ	ca_BCH_InfoActNow (p_CellId)		BCH connected to p-CCPCH
12		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_P_CCPCH)		
13		CMAC!CMAC_Config_REQ	ca_CMAC_CfgInfo(p_CellId, tsc_P_CCPCH, c_UE_Info (OMIT, OMIT) , c_TrChInfoBCH1, c_TrLogMappingBCH1)		mapping BCCH to BCH. C-RNTI and U-RNTI are not needed on P-CCPCH
14		CMAC?CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId, tsc_P_CCPCH)		
15	ERR1	[px_RAT = tdd]		I	
16	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_CellCfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To setup the cell parameter in CPHY, if the cell 'p_CellId' is the first one to be created, then CPHY shall be initialised using CHY_INIT_Req ASP. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			Fetch table corresponding to the cell
2	ERR	[tcv_TmpCellInfo.cellConfig <> cell_NotConfigured]		I	Fatal error: cell already configured
3		[tcv_TmpCellInfo.cellConfig = cell_NotConfigured]			Cell not yet configured
4		+ It_CellsAlreadyStarted			
5		[px_RAT = fdd]			
6		CPHY!CPHY_Cell_Config_REQ	ca_CellCfgReq(p_CellId, tcv_TmpCellInfo.tCell, tcv_TmpCellInfo.frequencyInfo, tcv_TmpCellInfo.priScrmCode, tcv_TmpCellInfo.attenuationLevel, tcv_TmpCellInfo.sfnOffset, tcv_TmpCellInfo.cellTxPowerLevel)		
7		CPHY?CPHY_Cell_Config_CNF	ca_CellCfgCnf(p_CellId)		
8		+ ts_SaveCellInfo (p_CellId)			
9	ERR2	[px_RAT = tdd]		I	
10	ERR3	[TRUE]		I	
		It_CellsAlreadyStarted			
11		+ ts_CountConfiguredCell			
12		[tcv_NumCfgCell = 0]			1.
13		+ts_MM_PwrOrUSIM_Off(tsc_USIM_NeedRmv)			1. Deactivate the UE
14		[tcv_DefaultRadioCnf = TRUE]			3.
15		CPHY ! CPHY_Ini_REQ	cas_InitReqDef		
16		CPHY ? CPHY_Ini_CNF	car_IniCnf		
17		(tcv_TmpCellInfo.tCell := 0)			
18		[tcv_DefaultRadioCnf = FALSE]			4.
19		CPHY ! CPHY_Ini_REQ	cas_InitReqNonDef		
20		CPHY ? CPHY_Ini_CNF	car_IniCnf		
21		(tcv_TmpCellInfo.tCell := 0)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		[tcv_NumCfgCell <> 0]			2.
Detailed Comments : 1. The cell 'p_CellId' is the first one to be created 2. The cell 'p_CellId' is not the first one to be created 3. CPHY_Init_Request is sent with a defaultRadioEnvironment value 4. CPHY_Init_Request is sent with a nonDefaultMultiCell value					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_CreateCellDCH (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : This test Step sets up a cell with stand-alone UL:3.4 DL:3.4 kbps SRBs for DCCH which consists of the following: For a first cell: physical channels: p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH, PRACH and DPCH; transport channels: BCH, FACH, PCH, RACH, DCH; logical channels: BCCH, CCCH, PCCH, DCCH; and signalling radio bearer RB0 on FACH and RACH; signalling bearer RB1, RB2, RB3, RB4 on DCH. For cells other than the first cell physical channels: p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH, PRACH (no DPCH) transport channels: BCH, FACH, PCH, RACH (no DCH) logical channels: BCCH, CCCH, PCCH (no DCCH) signalling radio bearer RB0 on FACH and RACH (not RB1 RB2 RB3 RB4 on DCH) Default : InitOtherwiseFail Comments : Cell configuration supporting UL:3.4 DL:3.4 kbps stand-alone signalling RB. CRLC is configured with cellId -1 (tsc_CellDedicated) CMAC for DCCH (MAC-d) is configured with cellId -1 (tsc_CellDedicated). Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SS_CellCfg(p_CellId)			First cell to be created
2		+ts_SS_BCH_SCH_CPICH_Cfg(p_CellId)			
3		+ts_SS_PCH_FACH_CCCH_Cfg(p_CellId)			
4		+ts_SS_RACH_CCCH_Cfg(p_CellId)			
5		+ ts_CountConfiguredCell			
6		[tcv_NumCfgCell = 0]			
7		+ts_SS_1DCH_DCCH_Cfg(p_CellId)			1. Next cell to be created
8		+ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
9		+ts_SS_RB_PCCH_Cfg(p_CellId)			
10		+ts_SS_RB0_Cfg (p_CellId)			
11		+ts_SS_RB1_ToRB4_Cfg			
12		(tcv_TmpCellInfo.DL_DPCH_S HO := TRUE, tcv_TmpCellInfo.UL_DPCH_S HO := TRUE, tcv_TmpCellInfo.cellConfig := cell_DCH_StandAloneSRB_No Conn)			
13		+ ts_SaveCellInfo (p_CellId)			
14		[tcv_NumCfgCell <> 0]			
15		+ ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
16		+ ts_SS_RB_PCCH_Cfg(p_CellId)			
17		+ ts_SS_RB0_Cfg(p_CellId)			
18		+ ts_SetCellCfg (p_CellId, cell_NoDPCH)			
Detailed Comments : 1. Set the cell configuration in the CellInfoCfg record					

Test Step Dynamic Behaviour

Test Step Name : ts_SS_CreateCellFACH (p_CellId : INTEGER)

Group : BasicM_SS_Configuration_Steps/

Objective : To setup a baseline cell which consists of the following:
 For a first cell_FACH cell
 physical channels: p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH and PRACH;
 transport channels: BCH, FACH, PCH, RACH;
 logical channels: BCCH, CCCH, PCCH, DCCH; and
 signalling radio bearer RB0(CCCH), RB1(UM DCCH), RB2(AM DCCH), RB3(AM DCCH for NAS high priority), RB4(AM DCCH for NAS low priority) on FACH and RACH, RB-3(TM BCCH FACH) on FACH, RB20(AM DTCH) on FACH and RACH, RB-1 (TM, BCCH) on BCH, RB-2 (TM, PCCH, PCH)

 For cells other than the first cell
 Same physical channels as for the first cell (p-SCH, s-SCH, p-CPICH, p-CCPCH, s-CCPCH and PRACH);
 Same transport channels as for the first cell (BCH, FACH, PCH, RACH);
 Logical channels: BCCH, CCCH, PCCH, no DCCH
 Signalling radio bearer RB0(CCCH), RB-3(TM BCCH FACH) on FACH, RB-1 (TM, BCCH) on BCH, RB-2 (TM, PCCH, PCH)

Default : InitOtherwiseFail

Comments : CRLC is configured with cellId -1 (tsc_CellDedicated)

Description :

Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SS_CellCfg(p_CellId)			
2		+ts_SS_BCH_SCH_CPICH_Cfg(p_CellId)			
3		+ ts_CountConfiguredCell			
4		[tcv_NumCfgCell = 0]			First cell to be created
5		+ts_SS_PCH_2FACH_CCCH_DCCH_BCH_DTCH_Cfg (p_CellId)			
6		+ts_SS_RACH_CCCH_DCCH_DTCH_Cfg (p_CellId)			
7		+ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
8		+ts_SS_RB_PCCH_Cfg(p_CellId)			
9		+ts_SS_RB0_Cfg(p_CellId)			
10		+ts_SS_RB1_ToRB4_Cfg			
11		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			RB9 is on BCCH-FACH
12		+ts_SS_RB20_AM_PS_Cfg (320)			
13		+ts_SetCellCfg (p_CellId, cell_FACH_NoConn)			
14		[tcv_NumCfgCell <> 0]			Not first cell to be created
15		+ts_SS_PCH_FACH_CCCH_Cfg (p_CellId)			
16		+ ts_SS_RACH_CCCH_Cfg (p_CellId)			
17		+ts_SS_RB_BCCH_BCH_Cfg(p_CellId)			
18		+ts_SS_RB_PCCH_Cfg(p_CellId)			
19		+ts_SS_RB0_Cfg(p_CellId)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		+ ts_SetCellCfg (p_CellId, cell_FACH_NoDedicated)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_PCH_2FACH_CCCH_DCCH_BCCH_DTCH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH1), then connect PCH and 2 FACH's to the secondary CCPCH . finally to map PCCH to PCH and DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH1 and DTCH to FACH2. Default : SS_Def Comments : one secondary CCPCH(tsc_S_CCPCH1) for PCH and FACH. PCCH mapping to PCH and DCCH1, DCCH2, DCCH3, DCCH4, CCCH, BCCH(for BCCH_FACH) to FACH1, and DTCH to FACH2. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info(p_CellId, tsc_S_CCPCH1, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH1_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_PCH_2_FACH_InfoActNow (p_CellId, tsc_S_CCPCH1)		connect PCH and FACH to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_S_CCPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_S_CCPCH1, c_UE_Info(tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoPCH_FACH_PS , c_TrLogMappingPCH_FACH_PS)		map PCCH to PCH, and map CCCH, BCCH, DTCH and DCCH's to FACH. C-RNTI and U-RNTI are needed when DCCH/DTCH are mapped on S-CCPCH
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH1)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
9		CPHY!CPHY_RL_Setup_REQ	ca_PICH_Info (p_CellId, c_PichInfo, (tsc_TmpCellInfo.powerPIC H),tsc_S_CCPCH1)		PICH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_PICH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_PCH_FACH_CCCH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To configure a secondary CCPCH (tsc_S_CCPCH1), then connect PCH and FACH to the secondary CCPCH .(34.108 cl. 4.2.1), finally to map PCCH to PCH and CCCH to FACH. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_sCCPCH_Info(p_CellId, tsc_S_CCPCH1, tsc_S_CCPCH_2ndScrCode, tsc_S_CCPCH1_ChC, tcv_TmpCellInfo.slotFormat sCCPCH1, (tcv_TmpCellInfo.powersCCPCH1), tcv_TmpCellInfo.timingsCCPCH1)		s-CCPCH1
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_S_CCPCH1)		
5		CPHY!CPHY_TrCH_Config_REQ	ca_PCH_2_FACH_InfoAct Now (p_CellId, tsc_S_CCPCH1)		connect PCH and FACH to s-CCPCH1
6		CPHY ? CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_S_CCPCH1)		
7		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_S_CCPCH1, c_UE_Info (OMIT, OMIT), c_TrChInfoPCH_FACH, c_TrLogMappingPCH_FACH_CellDCH)		map PCCH to PCH. U-RNTI and C-RNTI are not needed (no DCCH/DTCH)
8		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId, tsc_S_CCPCH1)		
9		CPHY!CPHY_RL_Setup_REQ	ca_PICH_Info(p_CellId, c_PichInfo, (tcv_TmpCellInfo.powerPICH), tsc_S_CCPCH1)		PICH
10		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_PICH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_PrepareCellRRC_ConnEst (p_CellId: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To reconfigure the cell if it is not ready for an RRC connection establishment. 4 cases are handled: 1> the cell is already configure (cell_DCH_StandAloneSRB_NoConn OR cell_FACH_NoConn OR cell_FACH_BMC_NoConn OR cell_FACH_2PRACH_NoConn OR cell_FACH_2_SCCPCH_NoConn) 2> the cell is configured to cell_NoDPCH, then remove the DPCH from the 'old' cell and configure the DPCH in the cell p_CellId 3> the cell is configured to cell_FACH_NoDedicated , then reconfigure the old cell where the 4> the configuration of the cell to not allow an RRC connection establishment -> error case Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_NoConn)]			0

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3		[(tcv_TmpCellInfo.cellConfig = cell_NoDPCH) OR (tcv_CellInfoA.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoA.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoA.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoA.cellConfig = cell_DCH_64kPS_RAB_SRB)]			1
4		+ It_ReconfOldDPCH_Cell			
5		+ ts_SS_AddDPCH(p_CellId)			
6		(tcv_TmpCellInfo.cellConfig := cell_DCH_StandAloneSRB_NoConn, tcv_TmpCellInfo.DL_DPCH_SHO := TRUE, tcv_TmpCellInfo.UL_DPCH_SHO := TRUE)			
7		+ ts_SaveCellInfo (p_CellId)			
8		[tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated]			2
9		+ It_ReconfOldFACH_Cell			
10		+ ts_SS_ReconfNoDedicatedToCellFACH (p_CellId)			
11		+ ts_SetCellCfg (p_CellId, cell_FACH_NoConn)			
12	ERR	[TRUE]		I	Program ming error
		It_ReconfOldFACH_Cell			
13		[(tcv_CellInfoA.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoA.cellConfig = cell_FACH) OR (tcv_CellInfoA.cellConfig = cell_FACH_PS)]			
14		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellA)			
15		(tcv_CellInfoA.cellConfig := cell_FACH_NoDedicated)			
16		[(tcv_CellInfoB.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoB.cellConfig = cell_FACH) OR (tcv_CellInfoB.cellConfig = cell_FACH_PS)]			
17		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellB)			
18		(tcv_CellInfoB.cellConfig := cell_FACH_NoDedicated)			
19		[(tcv_CellInfoC.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoC.cellConfig = cell_FACH) OR (tcv_CellInfoC.cellConfig = cell_FACH_PS)]			
20		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellC)			
21		(tcv_CellInfoC.cellConfig := cell_FACH_NoDedicated)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		[(tcv_CellInfoD.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoD.cellConfig = cell_FACH) OR (tcv_CellInfoD.cellConfig = cell_FACH_PS)]			
23		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellD)			
24		(tcv_CellInfoD.cellConfig := cell_FACH_NoDedicated)			
25		[(tcv_CellInfoE.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoE.cellConfig = cell_FACH) OR (tcv_CellInfoE.cellConfig = cell_FACH_PS)]			
26		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellE)			
27		(tcv_CellInfoE.cellConfig := cell_FACH_NoDedicated)			
28		[(tcv_CellInfoF.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoF.cellConfig = cell_FACH) OR (tcv_CellInfoF.cellConfig = cell_FACH_PS)]			
29		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellF)			
30		(tcv_CellInfoF.cellConfig := cell_FACH_NoDedicated)			
31		[(tcv_CellInfoG.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoG.cellConfig = cell_FACH) OR (tcv_CellInfoG.cellConfig = cell_FACH_PS)]			
32		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellG)			
33		(tcv_CellInfoG.cellConfig := cell_FACH_NoDedicated)			
34		[(tcv_CellInfoH.cellConfig = cell_FACH_NoConn) OR (tcv_CellInfoH.cellConfig = cell_FACH) OR (tcv_CellInfoH.cellConfig = cell_FACH_PS)]			
35		+ ts_ReconfigFACH_ToNoDedicated (tsc_CellH)			
36		(tcv_CellInfoH.cellConfig := cell_FACH_NoDedicated)			
37	ERR1	[TRUE] It_ReconfOldDPCH_Cell		I	

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
38		[(tcv_CellInfoA.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoA.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoA.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoA.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoA.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoA.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoA.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoA.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoA.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoA.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoA.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoA.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoA.cellConfig = cell_PDCP_AM_UM_RAB)]			
39		+ ts_SS_RelDPCH (tsc_CellA)			
40		(tcv_CellInfoA.cellConfig := cell_NoDPCH, tcv_CellInfoA.DL_DPCH_SHO := FALSE, tcv_CellInfoA.UL_DPCH_SHO := FALSE)			
41		[(tcv_CellInfoB.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoB.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoB.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoB.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoB.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoB.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoB.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoB.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoB.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoB.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoB.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoB.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoB.cellConfig = cell_PDCP_AM_UM_RAB)]			
42		+ ts_SS_RelDPCH (tsc_CellB)			
43		(tcv_CellInfoB.cellConfig := cell_NoDPCH , tcv_CellInfoB.DL_DPCH_SHO := FALSE, tcv_CellInfoB.UL_DPCH_SHO := FALSE)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
44		[(tcv_CellInfoC.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoC.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoC.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoC.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoC.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoC.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoC.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoC.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoC.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoC.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoC.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoC.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoC.cellConfig = cell_PDCP_AM_UM_RAB)]			
45		+ ts_SS_RelDPCH (tsc_CellC)			
46		(tcv_CellInfoC.cellConfig := cell_NoDPCH, tcv_CellInfoC.DL_DPCH_SHO := FALSE, tcv_CellInfoC.UL_DPCH_SHO := FALSE)			
47		[(tcv_CellInfoD.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoD.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoD.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoD.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoD.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoD.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoD.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoD.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoD.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoD.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoD.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoD.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoD.cellConfig = cell_PDCP_AM_UM_RAB)]			
48		+ ts_SS_RelDPCH (tsc_CellD)			
49		(tcv_CellInfoD.cellConfig := cell_NoDPCH, tcv_CellInfoD.DL_DPCH_SHO := FALSE, tcv_CellInfoD.UL_DPCH_SHO := FALSE)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
50		[(tcv_CellInfoE.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoE.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoE.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoE.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoE.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoE.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoE.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoE.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoE.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoE.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoE.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoE.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoE.cellConfig = cell_PDCP_AM_UM_RAB)]			
51		+ ts_SS_RelDPCH (tsc_CellE)			
52		(tcv_CellInfoE.cellConfig := cell_NoDPCH, tcv_CellInfoE.DL_DPCH_SHO := FALSE, tcv_CellInfoE.UL_DPCH_SHO := FALSE)			
53		[(tcv_CellInfoF.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoF.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoF.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoF.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoF.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoF.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoF.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoF.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoF.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoF.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoF.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoF.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoF.cellConfig = cell_PDCP_AM_UM_RAB)]			
54		+ ts_SS_RelDPCH (tsc_CellF)			
55		(tcv_CellInfoF.cellConfig := cell_NoDPCH, tcv_CellInfoF.DL_DPCH_SHO := FALSE, tcv_CellInfoF.UL_DPCH_SHO := FALSE)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
56		[(tcv_CellInfoG.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoG.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoG.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoG.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoG.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoG.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoG.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoG.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoG.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoG.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoG.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoG.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoG.cellConfig = cell_PDCP_AM_UM_RAB)]			
57		+ ts_SS_RelDPCH (tsc_CellG)			
58		(tcv_CellInfoG.cellConfig := cell_NoDPCH, tcv_CellInfoG.DL_DPCH_SHO := FALSE, tcv_CellInfoG.UL_DPCH_SHO := FALSE)			
59		[(tcv_CellInfoH.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_CellInfoH.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_CellInfoH.cellConfig = cell_DCH_Speech) OR (tcv_CellInfoH.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_CellInfoH.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_CellInfoH.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_CellInfoH.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_CellInfoH.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_CellInfoH.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_CellInfoH.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_CellInfoH.cellConfig = cell_PDCP_AM_RAB) OR (tcv_CellInfoH.cellConfig = cell_PDCP_UM_RAB) OR (tcv_CellInfoH.cellConfig = cell_PDCP_AM_UM_RAB)]			
60		+ ts_SS_RelDPCH (tsc_CellH)			
61		(tcv_CellInfoH.cellConfig := cell_NoDPCH, tcv_CellInfoH.DL_DPCH_SHO := FALSE, tcv_CellInfoH.UL_DPCH_SHO := FALSE)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
62		[TRUE]			
Detailed Comments : 0. No reconfiguration of the cell is needed before RRC connection establishment 1. Reconfigure the cell p_CellId to cell_DCH_StandAloneSRB_NoConn and reconfigure the cell set to cell_DCH_StandAloneSRB_NoConn to cell_NoDPCH 2. Reconfigure the cell p_CellId to cell_FACH_NoConn and reconfigure the cell set to cell_FACH_NoConn to cell_FACH_NoDedicated					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RACH_CCCH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To configure AICH and PRACH physical channels and connect RACH onto PRACH, then map one logical channel (CCCH) to RACH Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_AichInfo (p_CellId, tsc_AICH1, c_AICH_Info, tcv_TmpCellInfo.powerAICH)		AICH
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_AICH1)		
5		CPHY!CPHY_RL_Setup_REQ	ca_PRACH_Info (p_CellId, tsc_PRACH1, tsc_PRACH1_Signatures, tsc_PRACH1_ScrC, tcv_TmpCellInfo.puncLimit, tcv_TmpCellInfo.sf_PRACH , tcv_SubChNum)		PRACH
6		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf (p_CellId, tsc_PRACH1)		
7		CPHY!CPHY_TrCH_Config_REQ	cab_RACH_InfoActNow (p_CellId, tsc_PRACH1)		connect RACH to PRACH
8		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf (p_CellId, tsc_PRACH1)		
9		CMAC ! CMAC_Config_REQ	ca_CMAC_CfgInfo (p_CellId, tsc_PRACH1, c_UE_Info (OMIT, OMIT), cb_TrChInfoRACH1, cb_TrLogMappingRACH2)		mapping CCCH to RACH. C-RNTI and U-RNTI are not needed on
10		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf (p_CellId, tsc_PRACH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RACH_CCCH_DCCH_DTCH_Cfg (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To configure AICH and PRACH physical channels and connect RACH onto PRACH, then map five logical channels (CCCH, DCCH1, DCCH2, DCCH3, DCCH4) to RACH Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CPHY!CPHY_RL_Setup_REQ	ca_AichInfo(p_CellId, tsc_AICH1, c_AICH_Info, (tcv_TmpCellInfo.powerAICH))		AICH
4		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_AICH1)		
5		CPHY!CPHY_RL_Setup_REQ	ca_PRACH_Info(p_CellId, tsc_PRACH1, tsc_PRACH1_Signatures, tsc_PRACH1_ScrC, tcv_TmpCellInfo.puncLimit, tcv_TmpCellInfo.sf_PRACH , tcv_SubChNum)		PRACH
6		CPHY?CPHY_RL_Setup_CNF	ca_RL_SetupCnf(p_CellId, tsc_PRACH1)		
7		CPHY!CPHY_TrCH_Config_REQ	cab_RACH_InfoActNow (p_CellId, tsc_PRACH1)		connect RACH to PRACH
8		CPHY?CPHY_TrCH_Config_CNF	ca_TrChCfgCnf(p_CellId, tsc_PRACH1)		
9		CMAC ! CMAC_Config_REQ	ca_CMACEfgInfo(p_CellId , tsc_PRACH1, c_UE_Info(OMIT, tcv_TmpCellInfo.cRNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DTCH)		CCCH, DCCH1, DCCH2, DCCH3, DCCH4 to RACH. Only C-RNTI is used for DCCH/ DTCH on PRACH
10		CMAC ? CMAC_Config_CNF	ca_CMACEfgCnf(p_CellId , tsc_PRACH1)		
11	ERR1	[px_RAT = tdd]		I	
12	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB0_Cfg(p_CellId : INTEGER)					
Group : BasicM_SS_Configuration_Steps/					
Objective : to setup radio bearers : RB0 (the downlink is UM + CCCH + FACH + sCCPCH1 and uplink is TM + CCCH + RACH + PRACH).The configuration is adapted from 34.108 cl. 6.10.2.4.3 and 6.10.2.4.4					
Default : SS_Def					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_UM_DL_Info(p_CellId, tsc_RB0, {dLogicalChannelIdentity tsc_DL_CCCH5})		configure radio bearers (downlink): RB0 (UM + CCCH + FACH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB0)		
3		CRLC ! CRLC_Config_REQ	ca_RB_TM_UL_Info(p_CellId, tsc_RB0, 166, {uLogicalChannelIdentity tsc_UL_CCCH5})		configure radio bearers (uplink): RB0 (TM + CCCH + RACH)
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB0)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB1_ToRB4_Cfg Group : BasicM_SS_Configuration_Steps/ Objective : To setup radio bearers : RB1, RB2, RB3, RB4. default values from 34.123-1 Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_UM_Info (tsc_CellDedicated , tsc_RB1, {uLogicalChannelIdentity tsc_UL_DCCH1, dLogicalChannelIdentity tsc_DL_DCCH1})		configure radio bearers : RB1 (UM + DCCH) and (UM + DCCH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB1)		
3		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_SRB (tsc_CellDedicated, tsc_RB2, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DCCH2, dLogicalChannelIdentity tsc_DL_DCCH2}, 128)		configure radio bearers : RB2 (AM + DCCH) and (AM + DCCH)
4		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB2)		
5		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_SRB (tsc_CellDedicated, tsc_RB3, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DCCH3, dLogicalChannelIdentity tsc_DL_DCCH3}, 128)		configure radio bearers : RB3 (AM + DCCH) and (AM + DCCH)
6		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB3)		
7		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_SRB (tsc_CellDedicated, tsc_RB4, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollSDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DCCH4, dLogicalChannelIdentity tsc_DL_DCCH4}, 128)		configure radio bearers : RB4(AM + DCCH) and (AM + DCCH)
8		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB4)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB20_AM_PS_Cfg (p_Payloadsize: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : setup radio bearers : RB20. default values from 34.108 cl. 6.10.2.4.4 and 6.10.2.4.3.3 Default : SS_Def Comments : CRLC is configured with cellId -1 (tsc_CellDedicated) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_AM_Info_RAB (tsc_CellDedicated, tsc_RB20, tcv_TimerPollProhibit, tcv_TimerPoll, tcv_PollISDU, tcv_PollWindow, {uLogicalChannelIdentity tsc_UL_DTCH1, dLogicalChannelIdentity tsc_DL_DTCH1},p_Payload size)		cofigure radio bearers :
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf (tsc_CellDedicated, tsc_RB20)		RB20 (AM + DTCH)
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB_BCCH_BCH_Cfg(p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : to setup the radio bearer (RB_BCCH) which is used for sending system information blocks. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC!CRLC_Config_REQ	ca_RB_BCCH_Info(p_CellId, tsc_RB_BCCH, {dLogicalChannelIdentity tsc_BCCH1})		configure radio bearer (RB_BCCH) for BCCH on TM + BCCH + BCH, used for sending system information blocks
2		CRLC?CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB_BCCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB_BCCH_FACH_Cfg (p_CellId: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : setup radio bearers : RB9 (downlink only) this bearer is for BCCH–FACH (TM + BCCH + FACH + SCCPCH1). The configuration is adapted from 34.108 cl. 6.10.2.4.3. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_TM_DL_InfoNoSeg (p_CellId, tsc_RB_BCCH_FACH, 166, {dLlogicalChannelIdentity tsc_BCCH6})		configure radio bearers (downlink): RB9 (TM + BCCH + FACH)
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB_BCCH_FACH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RB_PCCH_Cfg(p_CellId: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : to setup radio bearer (RB_PCCH) used for paging message sending Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CRLC ! CRLC_Config_REQ	ca_RB_PCCH_Info(p_CellId, tsc_RB_PCCH, {dLlogicalChannelIdentity tsc_PCCH1})		configure radio bearer(RB_PCCH) on TM + PCCH + PCH, used for sending paging message
2		CRLC ? CRLC_Config_CNF	ca_CRLC_CfgCnf(p_CellId, tsc_RB_PCCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ReconfigRAB_ToSRB (p_CellId: INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To reconfigure SS from a configuration including RABS to cell_DCH. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		+ ts_SS_ReIDPCH (p_CellId)			
3		+ ts_SS_1DCH_DCCH_Cfg (p_CellId)			
4		+ ts_SS_RB1_ToRB4_Cfg			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_ReconfNoDedicatedToCellFACH (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To reconfig the cell from cell_FACH_NoDedicated to cell_FACH. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_RAT = fdd]			
3		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_S_CCPCH1, c_UE_Info(tcv_TmpCellInfo.uRNTI, tcv_TmpCellInfo.cRNTI), c_TrChInfoPCH_FACH_PS , c_TrLogMappingPCH_FACH_PS)		map PCCH to PCH, and map CCCH, BCCH, DTCH and DCCH's to FACH
4		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_S_CCPCH1)		
5		CMAC ! CMAC_Config_REQ	ca_CMAC_ReconfigInfoAct Now (p_CellId, tsc_PRACH1, c_UE_Info(OMIT, tcv_TmpCellInfo.cRNTI), cb_TrChInfoRACH1, c_TrLogMappingRACH_DTCH)		CCCH, DCCH1, DCCH2, DCCH3, DCCH4 to RACH
6		CMAC ? CMAC_Config_CNF	ca_CMAC_CfgCnf(p_CellId , tsc_PRACH1)		
7		+ lt_RB_BCCH_FACH_Cfg			
8	ERR1	[px_RAT = tdd]		I	
9	ERR2	[TRUE]		I	
10		lt_RB_BCCH_FACH_Cfg [tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated]			
11		+ts_SS_RB_BCCH_FACH_Cfg(p_CellId)			Config BCCH on FACH
12		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_Rel (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To release all channels that are configured in the SS. Default : SS_Def Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_Speech) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_2_PS_Call) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn)]			
3		+ ts_SS_RelDPCH (p_CellId)			1.
4		+ lt_ReleaseCommonCh			
5		+ lt_Release_BCCH			
6		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
7		[(tcv_TmpCellInfo.cellConfig = cell_NoDPCH)]			
8		+ lt_ReleaseCommonCh			
9		+ lt_Release_BCCH			
10		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
11		[(tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn)]			
12		+ lt_RelSRB1_4			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
14		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
15		+ It_ReleaseCommonCh			
16		+ It_Release_BCCH			
17		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
18		[(tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB_NoConn)]			
19		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			1.
20		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
21		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_DCCH_FACH_MAC)			
22		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
23		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
24		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
25		+ It_ReleaseCommonCh			
26		+ It_Release_BCCH			
27		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
28		[(tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_MAC_SRB0_NoConn)]			
29		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			1.
30		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
31		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
32		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
33		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
34		+ ts_CRLC_Rel (p_CellId, tsc_RB_CCCH_FACH_MAC)			
35		+ It_ReleaseCommonCh			
36		+ It_Release_BCCH			
37		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
38		[(tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC_NoConn)]			
39		+ It_RelSRB1_4			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
40		+ ts_CRLC_Rel (p_CellId , tsc_RB30)			
41		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
42		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
43		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
44		+ It_ReleaseCommonCh			
45		+ It_Release_BCCH			
46		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
47		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH)]			
48		+ It_RelSRB1_4			
49		+ ts_CRLC_Rel (p_CellId , tsc_RB_2ndCCCH)			
50		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
51		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
52		+ It_ReleaseCommonCh			
53		+ ts_CMAC_Rel (p_CellId, tsc_PRACH2)			
54		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH2)			
55		+ ts_SS_StopRL (p_CellId , tsc_PRACH2)			
56		+ ts_SS_StopRL (p_CellId , tsc_AICH2)			
57		+ It_Release_BCCH			
58		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
59		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_SCCPCH)]			
60		+ It_RelSRB1_4			
61		+ ts_CRLC_Rel (p_CellId , tsc_RB30)			
62		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
63		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
64		+ ts_CRLC_Rel (p_CellId , tsc_RB31)			
65		+ ts_CRLC_Rel (p_CellId , tsc_RB_2ndPCCH)			
66		+ It_ReleaseCommonCh			
67		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
68		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
69		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
70		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
71		+ It_Release_BCCH			
72		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
73		[(tcv_TmpCellInfo.cellConfig = cell_FACH_NoDedicated)]			
74		+ It_ReleaseCommonCh			
75		+ It_Release_BCCH			
76		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
77		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS)]			
78		+ It_RelSRB1_4			
79		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
80		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
81		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
82		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
83		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
84		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
85		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
86		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH2)			
87		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
88		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
89		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
90		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
91		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
92		+ ts_CPHY_TrChRelNon Dch (p_CellId , tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
93		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
94		+ lt_Release_BCCH			
95		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
96		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_2a) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_PS_2a)]			
97		+ lt_RelSRB1_4			
98		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
99		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
100		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
101		+ ts_CRLC_Rel (p_CellId , tsc_RB0)			2.
102		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
103		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
104		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
105		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
106		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH2)			
107		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
108		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
109		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
110		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
111		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
112		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
113		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
114		+ It_Release_BCCH			
115		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
116		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2)]			
117		+ It_RelSRB1_4			
118		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
119		+ ts_CRLC_Rel (p_CellId, tsc_RB29)			
120		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
121		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			
122		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
123		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
124		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
125		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
126		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
127		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
128		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
129		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
130		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
131		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
132		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
133		+ ts_CPHY_TrChRel NonDch (p_CellId , tsc_S_CCPCH2)			
134		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
135		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH 3)			
136		+ ts_CPHY_TrC hRelNonDch (p_CellId , tsc_S_CCPC H3)			
137		+ ts_SS_Stop RL (p_CellId , tsc_S_CCP CH3)			
138		+ It_Release_ BCCH			
139		+ ts_SetCe llCfg (p_CellId, cell_Not Configur ed)			
140		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH)]			
141		+ It_RelSRB1_4			
142		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
143		+ ts_CRLC_Rel (p_CellId, tsc_RB30)			
144		+ ts_CRLC_Rel (p_CellId, tsc_RB29)			
145		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
146		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			
147		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
148		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
149		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
150		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
151		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
152		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
153		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
154		+ ts_CPHY_TrChRelNonD ch (p_CellId , tsc_S_CCPCH1)			
155		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
156		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
157		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
158		+ts_CPHY_TrChRelNonDch(p_CellId , tsc_S_CCPCH2)			
159		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
160		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPC H3)			
161		+ ts_CPHY_TrChRelNonDch(p_CellId , tsc_S_CCPCH3)			
162		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH3)			
163		+ It_Release_BCCH			
164		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
165		[tcv_TmpCellInfo.cellConfig = cell_Two_DTCH]			
166		+ It_RelSRB1_4			
167		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
168		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
169		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
170		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
171		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
172		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
173		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
174		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
175		+ It_ReleaseCommonCh			
176		+ It_Release_BCCH			
177		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
178		[tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS]			
179		+ It_RelSRB1_4			
180		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
181		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
182		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
183		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB13)			
184		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
185		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
186		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
187		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
188		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
189		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
190		+ It_ReleaseCommonCh			
191		+ It_Release_BCCH			
192		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
193		[(tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_PS_CS)]			
194		+ It_RelSRB1_4			
195		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
196		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
197		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
198		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
199		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
200		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
201		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
202		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
203		+ It_ReleaseCommonCh			
204		+ It_Release_BCCH			
205		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
206		[(tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS) OR (tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_PS_CS)]			
207		+ It_RelSRB1_4			
208		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
209		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
210		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
211		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
212		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
213		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
214		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
215		+ts_CPHY_TrChRelDCH_NoSH O (p_CellId , tsc_UL_DPCH1)			
216		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
217		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
218		+ It_ReleaseCommonCh			2.
219		+ It_Release_BCCH			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
220		+ ts_SetCellCfg (
		p_CellId,			
		cell_NotConfigured)			
221		[(tcv_TmpCellInfo.cellConfig =			
		cell_DCH_DSCH_PS)]			
222		+ It_RelSRB1_4			
223		+ ts_CRLC_Rel (tsc_CellDedicated,			
		tsc_RB20)			
224		+ ts_CMAC_Rel (tsc_CellDedicated,			
		tsc_DL_DPCH1)			
225		+ ts_CMAC_Rel (tsc_CellDedicated,			
		tsc_UL_DPCH1)			
226		+ ts_CPHY_TrChRelDCH_NoSHO (
		p_CellId , tsc_DL_DPCH1)			
227		+ ts_CPHY_TrChRelDCH_NoSHO (
		p_CellId , tsc_UL_DPCH1)			
228		+ ts_SS_StopRL (p_CellId ,			
		tsc_DL_DPCH1)			
229		+ ts_SS_StopRL (p_CellId ,			
		tsc_UL_DPCH1)			
230		+ ts_CMAC_Rel			
		(tsc_CellDedicated,			
		tsc_DL_PDSCH1)			
231		+ ts_CPHY_TrChRelNonDch (
		p_CellId , tsc_DL_PDSCH1)			
232		+ ts_SS_StopRL (p_CellId ,			
		tsc_DL_PDSCH1)			
233		+ It_ReleaseCommonCh			
234		+ It_Release_BCCH			
235		+ ts_SetCellCfg (
		p_CellId,			
		cell_NotConfigured)			
236		[(tcv_TmpCellInfo.cellConfig =			
		cell_DCH_DSCH_CS_PS)]			
237		+ It_RelSRB1_4			
238		+ ts_CRLC_Rel (tsc_CellDedicated,			
		tsc_RB10)			
239		+ ts_CRLC_Rel (tsc_CellDedicated,			
		tsc_RB11)			
240		+ ts_CRLC_Rel (tsc_CellDedicated,			
		tsc_RB12)			
241		+ ts_CRLC_Rel (tsc_CellDedicated,			
		tsc_RB20)			
242		+ ts_CMAC_Rel (tsc_CellDedicated,			
		tsc_DL_DPCH1)			
243		+ ts_CMAC_Rel (tsc_CellDedicated,			
		tsc_UL_DPCH1)			
244		+ ts_CPHY_TrChRelDCH_NoSHO			
		(p_CellId , tsc_DL_DPCH1)			
245		+ ts_CPHY_TrChRelDCH_NoSHO			
		(p_CellId , tsc_UL_DPCH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
246		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
247		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
248		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_PDSCH1)			
249		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_DL_PDSCH1)			
250		+ ts_SS_StopRL (p_CellId , tsc_DL_PDSCH1)			
251		+ lt_ReleaseCommonCh			
252		+ lt_Release_BCCH			
253		+ ts_SetCellCfg (p_CellId, cell_NotConfigured)			
254		[(tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH)]			
255		+ lt_RelSRB1_4			
256		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
257		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
258		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
259		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH2)			
260		+ ts_CRLC_Rel (p_CellId , tsc_RB0)			2.
261		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
262		+ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
263		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
264		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
265		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
266		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
267		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
268		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
269		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
270		+ ts_CPHY_TrChRelN onDch (p_CellId , tsc_S_CCPCH2)			
271		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH2)			
272		+ ts_SS_StopRL (p_CellId , tsc_PICH2)			
273		+ It_ReleaseComm onCh			
274		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPC H2)			
275		+ ts_CPHY_Tr ChRelNonDc h (p_CellId , tsc_S_CCP CH2)			
276		+ ts_SS_Sto pRL (p_CellId , tsc_S_CC PCH2)			
277		+ ts_SS_S topRL (p_CellId , tsc_PIC H2)			
278		+ It_Relea se_BC CH			
279		+ ts_Se tCellC fg (p_Cell Id, cell_N otCo nfigur ed)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
280		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1_N oConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg1) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2_ NoConn) OR (tcv_TmpCellInfo.cellConfig =cell_FACH_3_SCCPCH_4_FACH_2a_Cnfg2)]			
281		+ It_RelSRB1_4			
282		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
283		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
284		+ ts_CRLC_Rel (p_CellId, tsc_RB29)			
285		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
286		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			
287		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
288		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
289		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
290		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
291		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
292		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
293		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH1)			
294		+ ts_CPHY_TrChRelNonD ch (p_CellId , tsc_S_CCPCH1)			
295		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
296		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
297		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPCH2)			
298		+ ts_CPHY_TrChR elNonDch (p_CellId , tsc_S_CCPCH2)			
299		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH 2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
300		+ ts_CMAC_Rel (p_CellId, tsc_S_CCPC H3)			
301		+ ts_CPHY_Tr ChRelNonDc h (p_CellId ,			
302		tsc_S_CCP CH3)			
303		+ ts_SS_Sto pRL (
304		p_CellId , tsc_S_CC PCH3)			
305		+ It_Releas e_BCCH			
306		+ ts_Set CellCfg (p_CellId, cell_No tConfig ured)			
307		[(tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH_N oConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_2a_CTCH)]			
308		+ It_RelSRB1_4			
309		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
310		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB24)			
311		+ ts_CRLC_Rel (p_CellId, tsc_RB30)			
312		+ ts_CRLC_Rel (p_CellId, tsc_RB29)			
313		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH)			
314		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH_FACH_RAB)			
315		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			
316		+ ts_CRLC_Rel (p_CellId, tsc_RB0)			2.
317		+ ts_CMAC_Rel (p_CellId, tsc_PRACH1)			
318		+ts_CPHY_TrChRelNonDch (
319		p_CellId , tsc_PRACH1)			
320		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
318		+ ts_SS_StopRL (
319		p_CellId , tsc_AICH1)			
320		+ ts_CMAC_Rel			
		(p_CellId,			
		tsc_S_CCPCH1)			
		+			
		ts_CPHY_TrChRelNon			
		Dch (p_CellId ,			
		tsc_S_CCPCH1)			
321		+ ts_SS_StopRL (
		p_CellId ,			
		tsc_S_CCPCH1)			
322		+ ts_SS_StopRL (
		p_CellId ,			
		tsc_PICH1)			
323		+ ts_CMAC_Rel			
		(p_CellId,			
		tsc_S_CCPCH2)			
324		+ts_CPHY_TrC			
		hRelNonDch(
		p_CellId ,			
		tsc_S_CCPCH			
		2)			
325		+			
		ts_SS_StopR			
		L (p_CellId ,			
		tsc_S_CCPC			
		H2)			
326		+			
		ts_CMAC_R			
		el (p_CellId,			
		tsc_S_CCP			
		CH3)			
327		+			
		ts_CPHY_			
		TrChRelNo			
		nDch(
		p_CellId ,			
		tsc_S_CC			
		PCH3)			
328		+			
		ts_SS_S			
		topRL (
		p_CellId ,			
		tsc_S_C			
		CPCH3)			
329		+			
		It_Relea			
		se_BC			
		CH			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
330		+ ts_Se tCellC fg (p_Cell Id, cell_N otCo nfigur ed)			
331		[TRUE]			
		It_Release_BCCH			
332		+ ts_CRLC_Rel (p_CellId , tsc_RB_BCCH)			
333		+ ts_CMAC_Rel (p_CellId , tsc_P_CCPCH)			
334		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_P_CCPCH)			
335		+ ts_SS_StopRL (p_CellId , tsc_S_SCH)			
336		+ ts_SS_StopRL (p_CellId , tsc_P_SCH)			
337		+ ts_SS_StopRL (p_CellId , tsc_P_CCPCH)			
338		+ ts_SS_StopRL (p_CellId , tsc_P_CPICH)			
339		CPHY ! CPHY_Cell_Release_REQ	ca_CPHY_Cell_Release_RE Q(p_CellId)		
340		CPHY ? CPHY_Cell_Release_CNF	ca_CPHY_Cell_Release_CN F(p_CellId)		
		It_ReleaseCommonCh			
341		+ ts_CRLC_Rel (p_CellId , tsc_RB0)			2.
342		+ ts_CMAC_Rel (p_CellId , tsc_PRACH1)			
343		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_PRACH1)			
344		+ ts_SS_StopRL (p_CellId , tsc_AICH1)			
345		+ ts_SS_StopRL (p_CellId , tsc_PRACH1)			
346		+ ts_CRLC_Rel (p_CellId , tsc_RB_PCCH)			3.
347		+ ts_CMAC_Rel (p_CellId , tsc_S_CCPCH1)			
348		+ ts_CPHY_TrChRelNonDch (p_CellId , tsc_S_CCPCH1)			
349		+ ts_SS_StopRL (p_CellId , tsc_PICH1)			
350		+ ts_SS_StopRL (p_CellId , tsc_S_CCPCH1)			
		It_RelSRB1_4			
351		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB1)			1.
352		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB2)			
353		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB3)			
354		+ ts_CRLC_Rel (tsc_CellDedicated , tsc_RB4)			

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Test Step Dynamic Behaviour	
Detailed Comments	: 1. Release DPCH 2. Release PRACH 3. Release S-CCPCH

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_RelDPCH (p_CellId : INTEGER) Group : BasicM_SS_Configuration_Steps/ Objective : To release the DPCH channel. Default : SS_Def Comments : The following channels need to be removed: physical channels: DPCH; transport channels: DCH logical channels: DCCH; and signalling radio bearer: signalling bearers on DCH radio access bearer on DCH. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_StandAloneSRB)]			
3		+ It_RelSRB1_4			
4		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
5		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
6		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
7		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
8		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
9		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
10		[(tcv_TmpCellInfo.cellConfig = cell_DCH_Speech)]			
11		+ It_RelSRB1_4			
12		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
13		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
14		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
15		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
16		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
17		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
18		+ ts_CPHY_TrChRelDCH_NoSHO(p_CellId , tsc_UL_DPCH1)			
19		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
20		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
21		[(tcv_TmpCellInfo.cellConfig = cell_DCH_64kCS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_57_6kCS_RAB_SRB)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22		+ lt_RelSRB1_4			
23		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
24		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
25		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
26		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
27		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
28		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
29		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
30		[(tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_RAB)]			
31		+ lt_RelSRB1_4			
32		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
33		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
34		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
35		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
36		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
37		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
38		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
39		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_UM_RAB)]			
40		+ lt_RelSRB1_4			
41		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB21)			
42		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
43		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
44		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
45		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
46		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
47		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
48		[(tcv_TmpCellInfo.cellConfig = cell_PDCP_AM_UM_RAB)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
49		+ lt_RelSRB1_4			
50		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
51		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB21)			
52		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
53		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
54		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
55		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
56		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
57		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
58		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis) OR (tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis)]			
59		+ lt_RelSRB1_4			
60		+ lt_ReleaseRLC_RB			
61		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
62		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
63		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
64		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
65		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
66		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
67		[(tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB) OR (tcv_TmpCellInfo.cellConfig = cell_DCH_MAC_SRB_NoConn)]			
68		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			
69		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
70		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_DCCH_DCH_MAC)			
71		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
72		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
73		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
74		+ ts_CPHY_TrChRelDCH_NoSHO(p_CellId , tsc_DL_DPCH1)			
75		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
76		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
77		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
78		[(tcv_TmpCellInfo.cellConfig = cell_DCH_2AM_PS) OR (tcv_TmpCellInfo.cellConfig =cell_DCH_2_PS_Call)]			
79		+ It_RelSRB1_4			
80		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
81		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB22)			
82		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
83		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
84		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
85		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
86		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
87		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
88		[(tcv_TmpCellInfo.cellConfig = cell_Two_DTCH_CS_PS)]			
89		+ It_RelSRB1_4			
90		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
91		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
92		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
93		+ ts_CMAC_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
94		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
95		+ ts_CPHY_TrChRelDCH_NoSHO(p_CellId , tsc_UL_DPCH1)			
96		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
97		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
98		[(tcv_TmpCellInfo.cellConfig = cell_Four_DTCH_CS_PS)]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
99		+ lt_RelSRB1_4			
100		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB10)			
101		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB11)			
102		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB12)			
103		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB20)			
104		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
105		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
106		+ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
107		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
108		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
109		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
110		[(tcv_TmpCellInfo.cellConfig = cell_DCH_64kPS_RAB_SRB_HS)]			
111		+ lt_RelSRB1_4			
112		+ts_CRLC_Rel (tsc_CellDedicated, tsc_RB25)			
113		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_DL_DPCH1)			
114		+ ts_CMAL_Rel (tsc_CellDedicated, tsc_UL_DPCH1)			
115		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_DL_DPCH1)			
116		+ ts_CPHY_TrChRelDCH_NoSHO (p_CellId , tsc_UL_DPCH1)			
117		+ ts_SS_StopRL (p_CellId , tsc_DL_DPCH1)			
118		+ ts_SS_StopRL (p_CellId , tsc_UL_DPCH1)			
119	ERR	[TRUE] lt_ReleaseRLC_RB		I	
120		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_15Lis)]			
121		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_AM_15_RLC)			
122		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_AM_RAB_7Lis)]			
123		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_AM_7_RLC)			
124		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_15Lis)]			
125		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_UM_15_RLC)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
126		[(tcv_TmpCellInfo.cellConfig = cell_RLC_DCH_UM_RAB_7Lis)]			1.
127		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB_UM_7_RLC)			
		It_RelSRB1_4			
128		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB1)			
129		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB2)			
130		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB3)			
131		+ ts_CRLC_Rel (tsc_CellDedicated, tsc_RB4)			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SS_StopRL(p_CellId : INTEGER; p_PhyCH : PhysicalChannelIdentity) Group : BasicM_SS_Configuration_Steps/ Objective : To stop transmission and receiving on the specified physical channel Default : SS_Def Comments : To release (stop transmission and receiving) the specified physical channel Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		CPHY!CPHY_RL_Release_REQ	ca_RL_RelReq(p_CellId, p_PhyCH)		
2		CPHY?CPHY_RL_Release_CNF	ca_RL_RelCnf(p_CellId, p_PhyCH)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_InitializeSIB11_SIB12 (p_CellID : INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To assign tcv_SIB11 and tcv_SIB12 Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellID)			
2		[tcv_NumOfPLMN = 1]			Default I
3		+It_1Or2PLMN			PLMN test case
4		[tcv_NumOfPLMN = 2]			2 PLMN test case
5		+It_1Or2PLMN			3 PLMN test case
6		[tcv_NumOfPLMN = 3]			
7		+It_3PLMN			
8		[TRUE]		(I)	Test step not designed for this
9		It_1Or2PLMN			
10		[p_CellID = tsc_CellA] (tcv_SIB11 := cb_SIB11_Def (tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
11		[p_CellID = tsc_CellB]			
12		(tcv_SIB11 := cb_SIB11_Def (tcv_CellInfoB, tcv_CellInfoA, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
13		[p_CellID = tsc_CellC]			
14		(tcv_SIB11 := cb_SIB11_Def (tcv_CellInfoC, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoG, tcv_CellInfoH, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
15		[p_CellID = tsc_CellD]			
16		(tcv_SIB11 := cb_SIB11_Freq2 (tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			
17		[p_CellID = tsc_CellE]			
18		(tcv_SIB11 := cb_SIB11_Freq2 (tcv_CellInfoE, tcv_CellInfoD, tcv_CellInfoF, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			
19		[p_CellID = tsc_CellF]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
20		(tcv_SIB11 := cb_SIB11_Freq2 (tcv_CellInfoF, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			
21		[p_CellID = tsc_CellG]			
22		(tcv_SIB11 := cb_SIB11_Def (tcv_CellInfoG, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoH, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
23		[p_CellID = tsc_CellH]			
24		(tcv_SIB11 := cb_SIB11_Def (tcv_CellInfoH, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
25		[TRUE]		I	no such cell
		It_3PLMN			
26		[p_CellID = tsc_CellA]			
27		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2 (tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF , tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Def)			
28		[p_CellID = tsc_CellB]			
29		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2 (tcv_CellInfoB, tcv_CellInfoA, tcv_CellInfoC, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF , tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Def)			
30		[p_CellID = tsc_CellC]			
31		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2 (tcv_CellInfoC, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF , tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Def)			
32		[p_CellID = tsc_CellD]			
33		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2(tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			
34		[p_CellID = tsc_CellE]			
35		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2 (tcv_CellInfoE, tcv_CellInfoD, tcv_CellInfoF, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			
36		[p_CellID = tsc_CellF]			
37		(tcv_SIB11 := cb_SIB11_Freq3_PLMN1Or2 (tcv_CellInfoF, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoG, tcv_CellInfoH), tcv_SIB12 := cb_SIB12_Freq2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
38		[p_CellID = tsc_CellG]			
39		(tcv_SIB11 := cb_SIB11_Freq3_PLMN3 (tcv_CellInfoG, tcv_CellInfoH, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
40		[p_CellID = tsc_CellH]			
41		(tcv_SIB11 := cb_SIB11_Freq3_PLMN3 (tcv_CellInfoH, tcv_CellInfoG, tcv_CellInfoA, tcv_CellInfoB, tcv_CellInfoC, tcv_CellInfoD, tcv_CellInfoE, tcv_CellInfoF), tcv_SIB12 := cb_SIB12_Def)			
42		[TRUE]		I	no such cell
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_InitializeSIB2AndSIB18(p_CellInfo: CellInfoCfg) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To initialize tcv_SIB2 and tcv_SIB18 Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB2 := c_SIB2_Def (p_CellInfo))			
2		[tcv_NumOfPLMN = 1]			Default I
3		(tcv_SIB18 := c_SIB18_Def(p_CellInfo))			PLMN test case
4		[tcv_NumOfPLMN = 2]			2 PLMN Test Case
5		+ It_Init2PLMN			
6		[tcv_NumOfPLMN = 3]			3 PLMN Test case
7		+ It_Init3PLMN			
8		[TRUE]		(I)	The test step not designed for this
9		It_Init2PLMN [(p_CellInfo.cellId = tsc_CellA) OR (p_CellInfo.cellId = tsc_CellB) OR (p_CellInfo.cellId = tsc_CellC) OR (p_CellInfo.cellId = tsc_CellG) OR (p_CellInfo.cellId = tsc_CellH)]			PLMN Group 1 cells, Hence MCC and MNC of Cell D used
10		(tcv_SIB18 := c_SIB18_2PLMN(tcv_CellInfoD))			
11		[TRUE]			PLMN Group 2 cells, Hence MCC and MNC of Cell A used
12		(tcv_SIB18 := c_SIB18_2PLMN(tcv_CellInfoA)) It_Init3PLMN			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
13		[(p_CellInfo.cellId = tsc_CellA) OR (p_CellInfo.cellId = tsc_CellB) OR (p_CellInfo.cellId = tsc_CellC)]			PLMN Group 1 cells, Hence MCC and MNC of Cell D and G used
14		(tcv_SIB18 := c_SIB18_3PLMN(tcv_CellInfoD, tcv_CellInfoG))			
15		[(p_CellInfo.cellId = tsc_CellD) OR (p_CellInfo.cellId = tsc_CellE) OR (p_CellInfo.cellId = tsc_CellF)]			PLMN Group 2 cells, Hence MCC and MNC of Cell A and G used
16		(tcv_SIB18 := c_SIB18_3PLMN(tcv_CellInfoA, tcv_CellInfoG))			
17		[TRUE]			PLMN Group 3 cells, Hence MCC and MNC of Cell A and D used
18		(tcv_SIB18 := c_SIB18_3PLMN(tcv_CellInfoA, tcv_CellInfoD))			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendDefSysInfo (p_CellId: INTEGER)					
Group : BasicM_SysInfoHandling_Steps/Default/					
Objective : To broadcast default system information.					
Default : InitOtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_UTRAN_GERAN_Paralnit(p_CellId)			ts_SetT mpCellIn fo done already in ts_UTR AN_GE RAN_Pa ralnit
2		+ts_CellDependentPara(p_CellId)			
3		+ts_InitializeSIB2AndSIB18(tcv_TmpCellInfo)			
4		+ ts_InitializeSIB11_SIB12 (p_CellId)			
5		[px_RAT = fdd]			
6		+ts_SendNoSegDefSchedul(p_CellId)			
7		+ts_SendSIB1 (cb_SIB1_Def(tcv_TmpCellInfo), p_CellId, tsc_Now)			
8		+ts_SendSIB2 (tcv_SIB2 , p_CellId, tsc_Now)			
9		+ts_SendSIB3(tcv_SIB3, p_CellId, tsc_Now)			
10		+ts_SendSIB4(tcv_SIB4, p_CellId, tsc_Now)			
11		+ts_SendSIB5(cb_SIB5_Def(tcv _TmpCellInfo), p_CellId, tsc_Now)			
12		+ts_SendSIB6(cb_SIB6_Def(tc v_TmpCellInfo), p_CellId, tsc_Now)			
13		+ts_SendSIB7(c_SIB7_Def, p_CellId, tsc_Now)			
14		+ts_SendSIB11(tcv_SIB11, p_CellId, tsc_Now)			
15		+ts_SendSIB12(tcv_SIB12, p_CellId, tsc_Now)			
16		+ts_SendSIB18(tcv_SIB18, p_CellId, tsc_Now)			
17		+ts_SendSB1_DefSch edul(tcv_SB1, p_CellId, tsc_Now)			
18		+ts_SendMIB(tcv_MI B, p_CellId, tsc_Now)			
19	ERR1	[px_RAT = tdd]		I	
20	ERR2	[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendNoSegDefSchedul(p_CellId : INTEGER)					
Group : BasicM_SysInfoHandling_Steps/Default/					
Objective : To deliver the system information message with NoSegment to SS on the frames on which there is no any SIB/MIB/SB scheduled					
Default : InitOtherwiseFail					
Comments : Current scheduling assumption: Repetition period: 64 frames; Not scheduled positions: frame 54 (SIB_POS 27). Above unscheduled positions can be used for other SIBs later, if so this test Step shall be modified accordingly.					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_Scheduling(p_CellId, 6, 27, tsc_Now)			pos = 27
2		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
3		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSB1_DefSchedul (p_SB: SysInfoTypeSB1; p_CellId : INTEGER; p_Timing : INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SysInfoTypeSB1 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation (1.
2		o_SIB_PER_Encoding (sB1 : p_SB)))			
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 4, 1, p_Timing)			
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId,		4.
6		+It_CompleteSIB(schedulingBlock1)	tsc_RB_BCCH)		
7		+ts_ChangeSB1_ValueTag			
8		(tcv_MIB.sibSb_ReferenceList.[0].sched			
9		uling := c_SB1_Schedul1,			
10		tcv_SB1_ValueTagChanged := FALSE)			
11		[tcv_Segs.segCount <>1]		I	2.
12		It_CompleteSIB(p_SIBType : SIB_Type)			
		[LENGTH_OF(tcv_Segs.seg1) = 226]			
		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId,		4.
			tsc_RB_BCCH,		
			cs_SIB_MsgCmpl(
			p_SIBType, tcv_Segs.seg1))		
		[LENGTH_OF(tcv_Segs.seg1) <> 226]			
		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId,		4.
			tsc_RB_BCCH,		
			cs_SIB_MsgCmplList1(
			p_SIBType, tcv_Segs.seg1))		
Detailed Comments : 1. Unaligned PER encoding of the SB1 then segmentation. 2. The result of segmentation shall be one segment for the SB1 (current assumption). 3. Send the scheduling info to SS.(one segment; REP=16; POS=1). 4. Construct the system information message containing the first segment of SB1 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB1 (p_SIB: SysInfoType1; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIBType1 to SS Default : InitOtherwiseFail Comments : SIB1 is concatenated with SIB2. system information on air changes imediatly if p_Timing => 512. change of system information on air starts at the frame number = p_Timing. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB1 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB1: p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB2 : tcv_SIB2))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 11, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 1, tcv_Segs.seg1, systemInformationBlockType 2, tcv_Segs.seg2))		4.
7		(tcv_MIB.sibSb_ReferenceList.[1].sibSb_T ype.sysInfoType1 := (tcv_MIB.sibSb_ReferenceList.[1].sibSb_T ype.sysInfoType1) MOD 256 + 1)			
8		+ts_ChangeMIB_ValueTag			
Detailed Comments : 1. Save the new SIB1 value to tcv_SIB1, unaligned PER encoding of the SIB1 and SIB2. 2. The concatenated SIB1 and SIB2 is too long . 3. Send the scheduling info to SS. one segment: REP=64, POS=11. 4. Construct the system information message containing completeList of SIB1+SIB2 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB11(p_SIB: SysInfoType11; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIBType11 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB11 : p_SIB)))			1.
2		[tcv_Segs.segCount >3]		I	2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType11 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB11_Schedul1)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+It_CompleteSIB(systemInformationBl ockType11)			4.
10		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config _CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_C onfig_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType11 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysl nfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB11_Schedul2)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
22		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 2, tcv_Segs.seg1))		4.
25		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType11 := (tcv_SB1.sib_ReferenceList.[2].sib_Type.sysInfoType11) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[2].scheduling := c_SIB11_Schedul3)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 6, 30, p_Timing)			5.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType11, 1, tcv_Segs.seg2))		7.
37		+ts_Scheduling(p_CellId, 6, 29, p_Timing)			3.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType11, 3, tcv_Segs.seg1))		4.
40		+ts_Scheduling(p_CellId, 6, 31, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg3) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType11, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType11, 2, tcv_Segs.seg3))		9.
46		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
47		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
48		[TRUE]			
49		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB11 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB11 (current assumption). 3. Send the scheduling info to SS. one segment: REP=64, POS=29. 4. Construct the system information message containing first segment of SIB11 and send it to SS. 5. Send the scheduling info to SS. one segment: REP=64, POS=30. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB11 and send it to SS. 8. Send the scheduling info segment to SS. one segment: REP=64, POS=31. 9. Construct the system information message containing the third segment of SIB11 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB12(p_SIB: SysInfoType12; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIBType12 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB12 : p_SIB)))			1.
2		[tcv_Segs.segCount >3]		I	2.
3		[tcv_Segs.segCount <=3]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[3].sib_Type.sysl nfoType12 := (tcv_SB1.sib_ReferenceList.[3].sib_Type.sysl nfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[3].scheduling := c_SIB12_Schedul1)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+It_CompleteSIB(systemInformationBl ockType12)			4.
10		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config _CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_C onfig_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		[tcv_Segs.segCount = 2]			
17		(tcv_SB1.sib_ReferenceList.[3].sib_Type.sysl nfoType12 := (tcv_SB1.sib_ReferenceList.[3].sib_Type.sysl nfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[3].scheduling := c_SIB12_Schedul2)			
18		+ts_ChangeSB1_ValueTag			
19		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
20		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
21		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
22		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 2, tcv_Segs.seg1))		4.
25		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
28		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
29		[TRUE]			
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
31		[tcv_Segs.segCount = 3]			
32		(tcv_SB1.sib_ReferenceList.[3].sib_Type.sysInfoType12 := (tcv_SB1.sib_ReferenceList.[3].sib_Type.sysInfoType12) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[3].scheduling := c_SIB12_Schedul3)			
33		+ts_ChangeSB1_ValueTag			
34		+ts_Scheduling(p_CellId, 6, 14, p_Timing)			5.
35		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType12, 1, tcv_Segs.seg2))		7.
37		+ts_Scheduling(p_CellId, 6, 13, p_Timing)			3.
38		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
39		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType12, 3, tcv_Segs.seg1))		4.
40		+ts_Scheduling(p_CellId, 6, 15, p_Timing)			8.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		[LENGTH_OF(tcv_Segs.seg3) <= 214]			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(syst emInformationBlockType12, 2, tcv_Segs.seg3))		9.
44		[TRUE]			
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInf ormationBlockType12, 2, tcv_Segs.seg3))		9.
46		[TRUE]			
47		It_CompleteSIB(p_SIBType : SIB_Type)			
48		[LENGTH_OF(tcv_Segs.seg1) = 226]			
48		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
49		[TRUE]			
50		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB12 then segmentation. 2. The result of segmentation shall be one or two or three segments for the SIB12 (current assumption). 3. Send the scheduling info segment to SS. one segment: REP=64, POS=13 . 4. Construct the system information message containing the first segment of SIB12 and send it to SS. 5. Send the scheduling info segment to SS. one segment: REP=64, POS=14. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB12 and send it to SS. 8. Send the scheduling info to SS. one segment: REP=64, POS=15 . 9. Construct the system information message containing the third segment of SIB12 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB18 (p_SIB: SysInfoType18; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIB18 to SS Default : InitOtherwiseFail Comments : SIB18 is concatenated with SIB7, default scheduling described in 3GPP TS 34.123-3 clause 8.4.3 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB18 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB18 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB7 : tcv_SIB7))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) > 201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 18, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg2, systemInformationBlockType 18, tcv_Segs.seg1))		4.
7		(tcv_SB1.sib_ReferenceList.[4].sib_Type.sysInfoType18 := (tcv_SB1.sib_ReferenceList.[4].sib_Type.sysInfoType18) MOD 4 + 1)			
8		+ts_ChangeSB1_ValueTag			
Detailed Comments : 1. save the new value of SIB18, unaligned PER encoding of the SIB7 and SIB18. 2. The concatenated SIB7 and SIB18 is too long. 3. Send the scheduling info to SS. one segment: REP=64, POS=18. 4. Construct the system information message containing SIB18 + SIB7 and sent to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB2 (p_SIB: SysInfoType2; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIB2 to SS Default : InitOtherwiseFail Comments : SIB2 is concatenated with SIB1 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB2 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB2 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB1 : tcv_SIB1))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 11, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq (p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2 (systemInformationBlockType 1, tcv_Segs.seg2, systemInformationBlockType 2, tcv_Segs.seg1))		4.
7		(tcv_MIB.sibSb_ReferenceList.[2].sibSb_T ype.sysInfoType2 := (tcv_MIB.sibSb_ReferenceList.[2].sibSb_T ype.sysInfoType2) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
Detailed Comments : 1. Save the new SIB2 value to tcv_SIB2, unaligned PER encoding of the SIB1 and SIB2. 2. The concatenated SIB1 and SIB2 is too long. 3. Send the scheduling info to SS. one segment, REP=64, POS=11. 4. Construct the system information message containing SIB2 + SIB1 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB3(p_SIB: SysInfoType3; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIB3 to SS Default : InitOtherwiseFail Comments : SIB3 is concatenated with SIB7. Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB3 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB3 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB7 : tcv_SIB7))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 10, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg2, systemInformationBlockType 3, tcv_Segs.seg1))		4.
7		(tcv_MIB.sibSb_ReferenceList.[3].sibSb_Type.sysInfoType3 := (tcv_MIB.sibSb_ReferenceList.[3].sibSb_Type.sysInfoType3) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
Detailed Comments : 1. Save the new value of SIB3 to tcv_SIB3, unaligned PER encoding of the SIB3 and SIB7. 2. The concatenated SIB3 and SIB7 is too long. 3. Send the scheduling info to SS. one segment; REP=64, POS=10. 4. Construct the system information message containing SIB3 + SIB7 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB4(p_SIB: SysInfoType4; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIB4 to SS Default : InitOtherwiseFail Comments : SIB4 is concatenated with SIB7 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_SIB4 := p_SIB, tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB4 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB7 : tcv_SIB7))			1.
2		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) > 201]		I	2.
3		[TRUE]			
4		+ts_Scheduling(p_CellId, 6, 26, p_Timing)			3.
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg2, systemInformationBlockType 4, tcv_Segs.seg1))		4.
7		(tcv_MIB.sibSb_ReferenceList.[4].sibSb_Type.sysInfoType4 := (tcv_MIB.sibSb_ReferenceList.[4].sibSb_Type.sysInfoType4) MOD 4 + 1)			
8		+ts_ChangeMIB_ValueTag			
Detailed Comments : 1. Save the new value of SIB3 to tcv_SIB3, unaligned PER encoding of the SIB3 and SIB7. 2. The concatenated SIB4 and SIB7 is too long. 3. Send the scheduling info to SS. one segment; REP=64, POS=10. 4. Construct the system information message containing SIB4 + SIB7 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB5 (p_SIB: SysInfoType5; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIBType5 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation (o_SIB_PER_Encoding (sIB5 : p_SIB)))			1.
2		[tcv_Segs.segCount >4]		I	2.
3		[tcv_Segs.segCount <=4]			
4		[tcv_Segs.segCount = 1]			
5		+ts_Scheduling(p_CellId, 6, 19, p_Timing)			3.
6		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
7		+lt_CompleteSIB(systemInformationBlock Type5)			4.
8		+ts_Scheduling(p_CellId, 6, 21, p_Timing)			5.
9		CMAC?CMAC_SYSINFO_Config_C NF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
10		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
11		+ts_Scheduling(p_CellId, 6, 22, p_Timing)			8.
12		CMAC?CMAC_SYSINFO_Confi g_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
13		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
14		+ts_Scheduling(p_CellId, 6, 23, p_Timing)			10.
15		CMAC?CMAC_SYSINFO_ Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
16		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
17		(tcv_MIB.sibSb_Referen ceList.[5].sibSb_Type.sys InfoType5 := (tcv_MIB.sibSb_Referen ceList.[5].sibSb_Type.sys InfoType5) MOD 4 + 1, tcv_MIB.sibSb_Referenc eList.[5].scheduling := c_SIB5_Schedul1)			
18		+ts_ChangeMIB_Value Tag			
19		[tcv_Segs.segCount = 2]			
20		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type .sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type .sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul2)			

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21		+ts_ChangeMIB_ValueTag			
22		+ts_Scheduling(p_CellId, 6, 22, p_Timing)			8.
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
25		+ts_Scheduling(p_CellId, 6, 23, p_Timing)			10.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
28		+ts_Scheduling(p_CellId, 6, 19, p_Timing)			3.
29		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 2, tcv_Segs.seg1))		4.
31		+ts_Scheduling(p_CellId, 6, 21, p_Timing)			5.
32		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
33		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
34		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 1, tcv_Segs.seg2))		7.
35		[TRUE]			
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 1, tcv_Segs.seg2))		7.
37		[tcv_Segs.segCount = 3]			
38		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul3)			
39		+ts_ChangeMIB_ValueTag			
40		+ts_Scheduling(p_CellId, 6, 23, p_Timing)			10.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
43		+ts_Scheduling(p_CellId, 6, 19, p_Timing)			3.
44		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 3, tcv_Segs.seg1))		4.
46		+ts_Scheduling(p_CellId, 6, 21, p_Timing)			5.
47		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
48		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		7.
49		+ts_Scheduling(p_CellId, 6, 22, p_Timing)			8.
50		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
51		[LENGTH_OF(tcv_Segs.seg3) <= 214]			
52		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 2, tcv_Segs.seg3))		9.
53		[TRUE]			
54		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 2, tcv_Segs.seg3))		9.
55		[tcv_Segs.segCount = 4]			
56		(tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5 := (tcv_MIB.sibSb_ReferenceList.[5].sibSb_Type.sysInfoType5) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[5].scheduling := c_SIB5_Schedul4)			
57		+ts_ChangeMIB_ValueTag			
58		+ts_Scheduling(p_CellId, 6, 19, p_Timing)			3.
59		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
60		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType5, 4, tcv_Segs.seg1))		4.
61		+ts_Scheduling(p_CellId, 6, 21, p_Timing)			5.
62		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
63		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 1, tcv_Segs.seg2))		7.
64		+ts_Scheduling(p_CellId, 6, 22, p_Timing)			8.
65		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
66		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType5, 2, tcv_Segs.seg3))		9.
67		+ts_Scheduling(p_CellId, 6, 23, p_Timing)			10.
68		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
69		[LENGTH_OF(tcv_Segs.seg4) <= 214]			
70		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType5, 3, tcv_Segs.seg4))		11.
71		[TRUE]			
72		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType5, 3, tcv_Segs.seg4))		11.
73		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
74		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
75		[TRUE]			
76		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB5 then segmentation. 2. The result of segmentation shall be less than 5 segments for the SIB5 (current assumption). 3. Send the scheduling info segment to SS. one segment: REP=64, POS=19. 4. Construct the system information message containing the first segment of SIB5 and send it to SS. 5. Send the scheduling info segment to SS. one segment: REP=64, POS=21. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB5 and send it to SS. 8. Send the scheduling info to SS. one segment: REP=64, POS=22. 9. Construct the system information message containing the third segment of SIB5 and send it to SS. 10. Send the scheduling info to SS. one segment: REP=64, POS=23. 11. Construct the system information message containing the fourth segment of SIB5 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB6(p_SIB: SysInfoType6; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIBType6 to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB6 : p_SIB)))			1.
2		[tcv_Segs.segCount >4]		I	2.
3		[tcv_Segs.segCount <=4]			
4		[tcv_Segs.segCount = 1]			
5		(tcv_SB1.sib_ReferenceList.[0].sib_Type.sysI nfoType6 := (tcv_SB1.sib_ReferenceList.[0].sib_Type.sysI nfoType6) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[0].scheduling := c_SIB6_Schedul1)			
6		+ts_ChangeSB1_ValueTag			
7		+ts_Scheduling(p_CellId, 6, 3, p_Timing)			3.
8		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
9		+It_CompleteSIB(systemInformationBl ockType6)			4.
10		+ts_Scheduling(p_CellId, 6, 5, p_Timing)			5.
11		CMAC?CMAC_SYSINFO_Config _CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
12		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
13		+ts_Scheduling(p_CellId, 6, 6, p_Timing)			8.
14		CMAC?CMAC_SYSINFO_C onfig_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
15		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
16		+ts_Scheduling(p_CellId, 6, 7, p_Timing)			10.
17		CMAC?CMAC_SYSINF O_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
18		TM!RLC_TR_DATA_RE Q	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
19		[tcv_Segs.segCount = 2]			
20		(tcv_SB1.sib_ReferenceList.[0].sib_Type.sysI nfoType6 := (tcv_SB1.sib_ReferenceList.[0].sib_Type.sysI nfoType6) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[0].scheduling := c_SIB6_Schedul2)			
21		+ts_ChangeSB1_ValueTag			
22		+ts_Scheduling(p_CellId, 6, 6, p_Timing)			8.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
24		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
25		+ts_Scheduling(p_CellId, 6, 7, p_Timing)			10.
26		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
27		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
28		+ts_Scheduling(p_CellId, 6, 3, p_Timing)			3.
29		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
30		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType6, 2, tcv_Segs.seg1))		4.
31		+ts_Scheduling(p_CellId, 6, 5, p_Timing)			5.
32		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
33		[LENGTH_OF(tcv_Segs.seg2) <= 214]			
34		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType6, 1, tcv_Segs.seg2))		7.
35		[TRUE]			
36		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType6, 1, tcv_Segs.seg2))		7.
37		[tcv_Segs.segCount = 3]			
38		(tcv_SB1.sib_ReferenceList.[0].sib_Type.systemInformationBlockType6 := (tcv_SB1.sib_ReferenceList.[0].sib_Type.systemInformationBlockType6) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[0].scheduling := c_SIB6_Schedul3)			
39		+ts_ChangeSB1_ValueTag			
40		+ts_Scheduling(p_CellId, 6, 7, p_Timing)			10.
41		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
42		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgNoSegment)		6.
43		+ts_Scheduling(p_CellId, 6, 3, p_Timing)			3.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
44		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
45		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType6, 3, tcv_Segs.seg1))		4.
46		+ts_Scheduling(p_CellId, 6, 5, p_Timing)			5.
47		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
48		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType6, 1, tcv_Segs.seg2))		7.
49		+ts_Scheduling(p_CellId, 6, 6, p_Timing)			8.
50		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
51		[LENGTH_OF(tcv_Segs.seg3) <= 214]			
52		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType6, 2, tcv_Segs.seg3))		9.
53		[TRUE]			
54		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType6, 2, tcv_Segs.seg3))		9.
55		[tcv_Segs.segCount = 4]			
56		(tcv_SB1.sib_ReferenceList.[0].sib_Type.sysInfoType6 := (tcv_SB1.sib_ReferenceList.[0].sib_Type.sysInfoType6) MOD 4 + 1, tcv_SB1.sib_ReferenceList.[0].scheduling := c_SIB6_Schedul4)			
57		+ts_ChangeSB1_ValueTag			
58		+ts_Scheduling(p_CellId, 6, 3, p_Timing)			3.
59		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
60		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgFirst(systemInformationBlockType6, 4, tcv_Segs.seg1))		4.
61		+ts_Scheduling(p_CellId, 6, 5, p_Timing)			5.
62		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
63		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType6, 1, tcv_Segs.seg2))		7.
64		+ts_Scheduling(p_CellId, 6, 6, p_Timing)			5.
65		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
66		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgSubsequent(systemInformationBlockType6, 2, tcv_Segs.seg3))		7.
67		+ts_Scheduling(p_CellId, 6, 7, p_Timing)			10.
68		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
69		[LENGTH_OF(tcv_Segs.seg4) <= 214]			
70		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLastShort(systemInformationBlockType6, 3, tcv_Segs.seg4))		11.
71		[TRUE]			
72		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgLast(systemInformationBlockType6, 3, tcv_Segs.seg4))		11.
73		It_CompleteSIB(p_SIBType : SIB_Type) [LENGTH_OF(tcv_Segs.seg1) = 226]			
74		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
75		[TRUE]			
76		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the SIB6 then segmentation. 2. The result of segmentation shall be one or two or three or four segments for the SIB6 (current assumption). 3. Send the scheduling info to SS. one segment: REP=64, POS=3. 4. Construct the system information message containing the first segment of SIB6 and send it to SS. 5. Send the scheduling info to SS. one segment: REP=64, POS=5. 6. Send no segment system information message to SS. 7. Construct the system information message containing the second segment of SIB6 and send it to SS. 8. Send the scheduling info to SS. one segment: REP=64, POS=6. 9. Construct the system information message containing the third segment of SIB6 and send it to SS. 10. Send the scheduling info to SS. one segment: REP=64, POS=7. 11. Construct the system information message containing the fourth segment of SIB6 and send it to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSIB7(p_SIB: SysInfoType7; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/Default/ Objective : To deliver the SIB7 to SS Default : InitOtherwiseFail Comments : single SIB7 or concatenated with SIB3 or SIB4 or SIB18, default scheduling described in 3GPP TS 34.123-3 clause 8.4.3 Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (sIB7 : p_SIB)), tcv_SIB7 := p_SIB)			1.
2		[tcv_Segs.segCount <>1]		I	2.
3		[tcv_Segs.segCount =1]			3.
4		+ts_Scheduling(p_CellId, 6, 2, p_Timing)			
5		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
6		+It_CompleteSIB(systemInformationBlockType7)			4.
7		+It_ConcatWithSIB3			
8		+It_ConcatWithSIB18			
9		+It_ConcatWithSIB4			
		It_CompleteSIB(p_SIBType : SIB_Type)			
10		[LENGTH_OF(tcv_Segs.seg1) = 226]			
11		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl(p_SIBType, tcv_Segs.seg1))		4.
12		[TRUE]			
13		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
		It_ConcatWithSIB3			
14		(tcv_Segs.seg1 := o_SIB_PER_Encoding (sIB7 : p_SIB), tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB3 : tcv_SIB3))			5.
15		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	6.
16		[TRUE]			
17		+ts_Scheduling(p_CellId, 6, 10, p_Timing)			7.
18		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
19		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType7, tcv_Segs.seg1, systemInformationBlockType3, tcv_Segs.seg2))		8.
		It_ConcatWithSIB18			
20		(tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB18 : tcv_SIB18))			9.

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
21		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	10.
22		[TRUE]			
23		+ts_Scheduling(p_CellId, 6, 18, p_Timing)			11.
24		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
25		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg1, systemInformationBlockType 18, tcv_Segs.seg2))		12.
26		It_ConcatWithSIB4 (tcv_Segs.seg2 := o_SIB_PER_Encoding (sIB4 : tcv_SIB4))			13.
27		[(LENGTH_OF(tcv_Segs.seg1) + LENGTH_OF(tcv_Segs.seg2)) >201]		I	14.
28		[TRUE]			
29		+ts_Scheduling(p_CellId, 6, 26, p_Timing)			15.
30		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf(p_CellId, tsc_RB_BCCH)		
31		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList2(systemInformationBlockType 7, tcv_Segs.seg1, systemInformationBlockType 4, tcv_Segs.seg2))		16.
<p>Detailed Comments : 1. save the new value of SIB7, unaligned PER encoding of the SIB7 then segmentation. 2. The result of segmentation shall be one segment for the SIB7 (current assumption). 3. Send the scheduling info to SS. one segment: REP=64, POS=2. 4. Construct the system information message containing SIB7 and send it to SS. 5. unaligned PER encoding of the SIB7 and SIB3. 6. concatenated SIB7 and SIB3 is too long. 7. Send the scheduling info to SS. one segment: REP=64, POS=10. 8. Construct the system information message containing SIB7 + SIB3 and send it to SS. 9. unaligned PER encoding of the SIB18. 10. concatenated SIB7 and SIB18 is too long. 11. Send the scheduling info to SS. one segment: REP=64, POS=18. 12. Construct the system information message containing SIB7 + SIB18 and send it to SS. 13. unaligned PER encoding of the SIB4. 14. concatenated SIB7 and SIB4 is too long. 15. Send the scheduling info to SS. one segment: REP=64, POS=26. 16. Construct the system information message containing SIB7 + SIB4 and send it to SS.</p> <p>In system information broadcasting, SIB7 is concatenated with SIB3, SIB4, and SIB18, the assignment tcv_SIB7 := p_SIB in line 1 is to provide a communication mechanism between ts_SendSIB7 and ts_SendSIB4, ts_SendSIB3, ts_SendSIB18. Normally the ts_SendSIB7 is called in the preamble part of the test case to establish the default system information broadcasting. If in the test body the contents of SIB7 need be changed and afterwards SIB3 or SIB4 or SIB18 needs also be changed, the ts_SendSIB7 will be called again with a new SIB7 value and ts_SendSIB3 or ts_SendSIB4 or ts_SendSIB18 will also be called again with new SIB value, in the new call of ts_SendSIB3 or ts_SendSIB4 or ts_SendSIB18 new SIB7 value, which was used in the new call of ts_SendSIB7, shall be used. the assignment tcv_SIB7 := p_SIB in line 1 is to provide this new value for ts_SendSIB3 or ts_SendSIB4 or ts_SendSIB18.</p>					

Test Step Dynamic Behaviour					
Test Step Name : ts_CellDependentPara (p_CellID : INTEGER) Group : BasicM_SysInfoHandling_Steps/ Objective : To set cell dependent parameters Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellID)			
2		[p_CellID = tsc_CellA]			
3		(tcv_SIB3.cellIdentity := tsc_CellIdCellA, tcv_SIB4.cellIdentity := tsc_CellIdCellA)			
4		[p_CellID = tsc_CellB]			
5		(tcv_SIB3.cellIdentity := tsc_CellIdCellB, tcv_SIB4.cellIdentity := tsc_CellIdCellB)			
6		[p_CellID = tsc_CellC]			
7		(tcv_SIB3.cellIdentity := tsc_CellIdCellC, tcv_SIB4.cellIdentity := tsc_CellIdCellC)			
8		[p_CellID = tsc_CellD]			
9		(tcv_SIB3.cellIdentity := tsc_CellIdCellD, tcv_SIB4.cellIdentity := tsc_CellIdCellD)			
10		[p_CellID = tsc_CellE]			
11		(tcv_SIB3.cellIdentity := tsc_CellIdCellE, tcv_SIB4.cellIdentity := tsc_CellIdCellE)			
12		[p_CellID = tsc_CellF]			
13		(tcv_SIB3.cellIdentity := tsc_CellIdCellF, tcv_SIB4.cellIdentity := tsc_CellIdCellF)			
14		[p_CellID = tsc_CellG]			
15		(tcv_SIB3.cellIdentity := tsc_CellIdCellG, tcv_SIB4.cellIdentity := tsc_CellIdCellG)			
16		[p_CellID = tsc_CellH]			
17		(tcv_SIB3.cellIdentity := tsc_CellIdCellH, tcv_SIB4.cellIdentity := tsc_CellIdCellH)			
18		[TRUE]		I	no such cell
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ChangeMIB_ValueTag Group : BasicM_SysInfoHandling_Steps/ Objective : To increment MIBValueTag if tcv_MIB_ValueTagChanged = FALSE. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_MIB_ValueTagChanged = FALSE]			
2		(tcv_MIB.mib_ValueTag := (tcv_MIB.mib_ValueTag) MOD 8 + 1, tcv_MIB_ValueTagChanged := TRUE)			
3		[tcv_MIB_ValueTagChanged = TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_ChangeSB1_ValueTag Group : BasicM_SysInfoHandling_Steps/ Objective : To increment SB1ValueTag if tcv_SB1_ValueTagChanged = FALSE. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_SB1_ValueTagChanged = FALSE]			
2		(tcv_SB1_ValueTag := (tcv_MIB.sibSb_ReferenceList.[0].sibSb_Type.sysInfoTypeSB1) MOD 4 + 1, tcv_MIB.sibSb_ReferenceList.[0].sibSb_Type.sysInfoTypeSB1 := tcv_SB1_ValueTag, tcv_SB1_ValueTagChanged := TRUE)			
3		+ts_ChangeMIB_ValueTag			
4		[TRUE]			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_Scheduling(p_CellId: INTEGER; p_REP : INTEGER; p_POS : INTEGER; p_Timing : INTEGER) Group : BasicM_SysInfoHandling_Steps/ Objective : Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		(tcv_BCCH_ModifyTime := p_Timing)			
2		[p_Timing >= 512]			
3		CMAC!CMAC_SYSINFO_Config_REQ	ca_SchedulNow (p_CellId, p_REP, p_POS)		
4		[p_Timing <= 511]			
5		CMAC!CMAC_SYSINFO_Config_REQ	ca_SchedulLater(p_CellId, p_REP, p_POS, p_Timing)		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendMIB(p_MIB: MasterInformationBlock; p_CellId : INTEGER; p_Timing: INTEGER) Group : BasicM_SysInfoHandling_Steps/ Objective : To deliver the MIB to SS Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		(tcv_MIB.plmn_Type.gsm_MAP.plmn_Identity.mcc := o_HexToDigitsMCC (tcv_TmpCellInfo.mcc), tcv_MIB.plmn_Type.gsm_MAP.plmn_Identity.mnc := o_HexToDigitsMNC (tcv_TmpCellInfo.mnc))			
3		(tcv_Segs := o_SIB_Segmentation(o_SIB_PER_Encoding (mIB : tcv_MIB)))			1.
4		[tcv_Segs.segCount <>1]		I	2.
5		[tcv_Segs.segCount =1]			
6		+ts_Scheduling(p_CellId, 3, 0, p_Timing)			3.
7		CMAC?CMAC_SYSINFO_Config_CNF	ca_SysInfoCfgCnf (p_CellId, tsc_RB_BCCH)		
8		+It_CompleteSIB(masterInformationBlock)			4.
9		(tcv_MIB_ValueTagChanged := FALSE)			
		It_CompleteSIB(p_SIBType : SIB_Type)			
10		[LENGTH_OF(tcv_Segs.seg1) = 226]			
11		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmpl (p_SIBType, tcv_Segs.seg1))		4.
12		[TRUE]			
13		TM!RLC_TR_DATA_REQ	ca_TR_DataReq(p_CellId, tsc_RB_BCCH, cs_SIB_MsgCmplList1(p_SIBType, tcv_Segs.seg1))		4.
Detailed Comments : 1. Unaligned PER encoding of the MIB then segmentation. 2. The result of segmentation shall be one segment for the MIB (current assumption). 3. Send the scheduling info to SS.(one segment; REP=8; POS=0) 4. Construct the system information message containing MIB and sent to SS.					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendSysInfoChangeInd_InFACHConfig(p_CellId:INTEGER; p_mib_valuetag: MIB_ValueTag) Group : BasicM_SysInfoHandling_Steps/ Objective : Transmit System Information change indication message with IE "BCCH modification info" on the BCCH, to inform UE the change of System Information. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[(tcv_TmpCellInfo.cellConfig = cell_FACH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_PS) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH_NoConn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2_PRACH)OR (tcv_TmpCellInfo.cellConfig = cell_FACH_BMC) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_2SCCPCH_StandAlonePCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg1) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2_NoC onn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_4_FACH_Cnfg2) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCo nn) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH) OR (tcv_TmpCellInfo.cellConfig = cell_FACH_3_SCCPCH_3_FACH_CTCH_NoCo nn)]			If BCCH on FACH is configured then send System info change indication

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Test Step Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
3		TM ! RLC_TR_DATA_REQ	cas_SysInfoChangeInd (p_CellId, tsc_RB_BCCH_FACH, cs_SysInfoChangeInd (p_mib_valuetag))		SS sends SYSTE M INFORM ATION CHANG E INDICA TION message containi ng IE "BCCH modifica tion info"on the BCCH to inform UE the change of system informati on. do nothing
4		[TRUE]			do nothing
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_SendPage1_ModifySI (<p> p_CellId:INTEGER; p_mib_valuetag: MIB_ValueTag) </p> Group : BasicM_SysInfoHandling_Steps/ Objective : Transmit Paging Type 1 with IE "BCCH modification info" on the PCCH, to informed UE the change of System Information. and Transmit System Information change indication message with IE "BCCH modification info" on the BCCH, to inform UE the change of System Information. Default : InitOtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ts_RRC_Delay(tsc_WaitBeforePaging)			Give delay before paging type1
2		+ts_CMAC_Pag1_Cfg(p_CellId)			
3		TM ! RLC_TR_DATA_REQ	cas_PagingType1 (<p> p_CellId, tsc_RB_PCCH, cs_RRC_PagingType1_ModifySI (<p> p_mib_valuetag) </p>) </p>		SS sends PAGING TYPE1 message containing IE "BCCH modification info"on the PCCH to inform UE the change of system information.
4		+ts_SendSysInfoChangeInd_InFACHConfig(<p> p_CellId, p_mib_valuetag) </p>			
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_UTRAN_GERAN_ParaInit (p_CellId: INTEGER) Group : BasicM_SysInfoHandling_Steps/ Objective : Initialize default parameters for different region Default : InitOtherwiseFail Comments : currently only UTRAN and UTRAN/GERAN are defined Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		+ ts_SetTmpCellInfo (p_CellId)			
2		[px_UTRAN_GERAN = "UTRAN and GERAN"]			
3		(tcv_SIB3 := cb_SIB3_DefUTRAN_GERAN (tcv_TmpCellInfo), tcv_SIB4 := cb_SIB4_DefUTRAN_GERAN (tcv_TmpCellInfo))			
4		[px_UTRAN_GERAN = "UTRAN only"]			
5		(tcv_SIB3 := cb_SIB3_DefUTRAN (tcv_TmpCellInfo), tcv_SIB4 := cb_SIB4_DefUTRAN (tcv_TmpCellInfo))			
6		[TRUE]		I	
Detailed Comments :					

Test Step Dynamic Behaviour						
Test Step Name : ts_TC_ActivateRB_TestMode (p_CellId: INTEGER)						
Group : BasicM_TC_Steps/						
Objective : Activate UE radio bearer test mode.						
Default : NAS_OtherwiseFailActRB_TM						
Comments : tcv_TestModeActivated is assigned to TRUE to keep track that RB test mode has been activated during the test case; to be used in the postamble.						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1	TSP1	[tcv_CN_Domain = cs_domain]	ca_DataReq (tsc_CellDedicated, tsc_RB3, c_ActivateRB_TestMode)	(P)	1. no answer received	
2		Dc!RRC_DataReq START t_Dly (tsc_TT01)				
3		Dc?RRC_DataInd CANCEL t_Dly				
4	TSF1	(tcv_RB_TestModeActivated := TRUE)		(F)		
5		?TIMEOUT t_Dly				
6	TSP2	[tcv_CN_Domain = ps_domain]	ca_PS_DataReq (tsc_CellDedicated, tsc_RB3, c_ActivateRB_TestMode)	(P)	1. no answer received	
7		Dc!RRC_DataReq START t_Dly (tsc_TT01)				
8		Dc?RRC_DataInd CANCEL t_Dly				
9	TSF2	(tcv_RB_TestModeActivated := TRUE)		(F)		
10		?TIMEOUT t_Dly				
Detailed Comments :						

Test Step Dynamic Behaviour					
Test Step Name : ts_TC_CloseUE_TestLoop(p_CellId: INTEGER; p_UE_TestLoopMode: UE_TestLoopMode; p_UE_TestLoopMode1LB_Setup: UE_TestLoopMode1LB_Setup)					
Group : BasicM_TC_Steps/					
Objective : Close UE test loop					
Default : NAS_OtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_CN_Domain = cs_domain]			
2		Dc!RRC_DataReq START t_Dly (tsc_TT01)	ca_DataReq(tsc_CellDedicated, tsc_RB3,c_CloseUE_TestLo op(p_UE_TestLoopMode, p_UE_TestLoopMode1LB_ Setup))		
3	TSP1	Dc?RRC_DataInd CANCEL t_Dly	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_CloseUE_TestLoopCmpl)	(P)	
4	TSF1	?TIMEOUT t_Dly		(F)	no answer received
5		[tcv_CN_Domain = ps_domain]			
6		Dc!RRC_DataReq START t_Dly (tsc_TT01)	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3,c_CloseUE_TestLo op(p_UE_TestLoopMode, p_UE_TestLoopMode1LB_ Setup))		
7	TSP2	Dc?RRC_DataInd CANCEL t_Dly	car_PS_UplinkDirectTransfe r (tsc_CellDedicated, tsc_RB3, c_CloseUE_TestLoopCmpl)	(P)	
8	TSF2	?TIMEOUT t_Dly		(F)	no answer received
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_TC_DeactivateRB_TestMode(p_CellId: INTEGER)					
Group : BasicM_TC_Steps/					
Objective : Deactivate UE radio bearer test mode					
Default : NAS_OtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	TSP1	[tcv_CN_Domain = cs_domain]	ca_DataReq(tsc_CellDedicated, tsc_RB3,c_DeactivateRB_TestMode)	(P)	no answer received
2		Dc!RRC_DataReq START t_Dly (tsc_TT01)			
3		Dc?RRC_DataInd CANCEL t_Dly			
4	TSF1	?TIMEOUT t_Dly	(F)		
5	TSP2	[tcv_CN_Domain = ps_domain]	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3,c_DeactivateRB_TestMode)	(P)	
6		Dc!RRC_DataReq START t_Dly (tsc_TT01)			
7		Dc?RRC_DataInd CANCEL t_Dly			
8	TSF2	?TIMEOUT t_Dly	(F)	no answer received	
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_TC_OpenUE_TestLoop(p_CellId: INTEGER)					
Group : BasicM_TC_Steps/					
Objective : Open UE test loop					
Default : NAS_OtherwiseFail					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_CN_Domain = cs_domain]			
2		Dc!RRC_DataReq START t_Dly (tsc_TT01)	ca_DataReq(tsc_CellDedicated, tsc_RB3,c_OpenUE_TestLo op)		
3	TSP1	Dc?RRC_DataInd CANCEL t_Dly	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, c_OpenUE_TestLoopCmpl)	(P)	
4	TSF1	?TIMEOUT t_Dly		(F)	no answer received
5		[tcv_CN_Domain = ps_domain]			
6		Dc!RRC_DataReq START t_Dly (tsc_TT01)	ca_PS_DataReq(tsc_CellDedicated, tsc_RB3,c_OpenUE_TestLo op)		
7	TSP2	Dc?RRC_DataInd CANCEL t_Dly	car_PS_UplinkDirectTransfe r (tsc_CellDedicated, tsc_RB3, c_OpenUE_TestLoopCmpl)	(P)	
8	TSF2	?TIMEOUT t_Dly		(F)	no answer received
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_AT_TriggerGMM_Attach Group : BasicM_UT_Steps/ Objective : Trigger UE to start GMM Attach procedure via AT command +CGATT Default : UT_OtherwiseFail Comments : (see 3GPP 27.007 / 10.1.9) Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		Ut ! AT_CmdReq	ca_AT_CmdReq ("AT+CGATT=1<CR>")		
2		Ut ? AT_CmdCnf	ca_AT_CmdCnf		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_UE_PwrOff Group : BasicM_UT_Steps/ Objective : To make the operator power off the UE Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_UE_SwitchedOn = TRUE]			UE is ON
2		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please power off the UE")		
3		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
4		(tcv_UE_SwitchedOn := FALSE)			UE is now OFF
5		[TRUE]			UE is Already OFF
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_UE_PwrOn Group : BasicM_UT_Steps/ Objective : To make the operator power on the UE Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_UE_SwitchedOn = FALSE]			UE is OFF
2		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please power on the UE")		
3		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
4		(tcv_UE_SwitchedOn := TRUE)			UE is now ON
5		[TRUE]			UE is Already ON
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_UE_SwitchOff Group : BasicM_UT_Steps/ Objective : To make the operator switch off the UE Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_UE_SwitchedOn = TRUE]			UE is ON
2		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please switch off the UE")		
3		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
4		(tcv_UE_SwitchedOn := FALSE)			UE is now OFF
5		[TRUE]			UE is Already OFF
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_UE_SwitchOn Group : BasicM_UT_Steps/ Objective : To make the operator switch on the UE Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		[tcv_UE_SwitchedOn = FALSE]			UE is OFF
2		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please switch on the UE")		
3		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
4		(tcv_UE_SwitchedOn := TRUE)			UE is now ON
5		[TRUE]			UE is Already ON
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_USIM_Insert Group : BasicM_UT_Steps/ Objective : To make the operator insert the USIM card Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please insert the USIM card into the UE")		
2		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
Detailed Comments :					

Test Step Dynamic Behaviour					
Test Step Name : ts_MMI_USIM_Remove Group : BasicM_UT_Steps/ Objective : To make the operator remove the USIM card Default : UT_OtherwiseFail Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("Please remove the USIM card from the UE")		
2		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
Detailed Comments :					

Default Dynamic Behaviour					
Default Name : InitOtherwiseFail					
Group : Init_Defaults/					
Objective :					
Comments : Handle any irrelevant sync/outsync indications during cell setup					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	DFF1	CPHY?CPHY_Sync_IND	ca_SyncInd (?)	(F)	
2		RETURN			
3		CPHY?CPHY_Out_of_Sync_IND	ca_OutOfSyncInd (?)		
4		RETURN			
5		CRCLC?CRCLC_Integrity_Failure_IND	car_CRCLC_IntegrityFail		
6	DFF1	[tcv_CellIndInfo.integrityStarted]		(F)	
7		RETURN			
8		[NOT tcv_CellIndInfo.integrityStarted]			
9		RETURN			
10		?TIMEOUT			
11	DFI8	[tcv_TestBody = FALSE]		(I)	
12		CANCEL			
13	DFF8	[tcv_TestBody = TRUE]		(F)	
14		CANCEL			
Detailed Comments :					

Default Dynamic Behaviour					
Default Name : NAS_OtherwiseFail Group : NAS_Defaults/ Objective : To match unexpected events and fail the test case. Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	DFI1	?TIMEOUT t_Guard	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")	(I)	1.
2		Ut ! MMI_CmdReq			4.
3		Ut ? MMI_CmdCnf			
4		[tcv_TestBody = FALSE]			
5	DFF1	CANCEL	ca_MMI_CmdCnf	(F)	
6		[tcv_TestBody = TRUE]			
7		CANCEL			
8		Dc ? RRC_DataInd[tcv_MM_TestExecution]			6.
9	DFI8	Dc ! RRC_DataReq	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v,?)) ca_PS_DataReq (tsc_CellDedicated, tsc_RB3, cs_AttachRej('07'O))	(I)	7.
10		RETURN			
11		?TIMEOUT t_TimeoutInDefault			
12		(tcv_TimeoutInDefault := TRUE)			
13	DFF8	RETURN	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v,?))	(F)	
14		?TIMEOUT			
15		[tcv_TestBody = FALSE]			
16		CANCEL			
17		[tcv_TestBody = TRUE]	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_lv, ?))		
18		CANCEL			
19		Dc ? RRC_DataInd [tcv_GMM_AttachExpect = TRUE] (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_AttachRec := TRUE)			ATTACH REQUE ST – Extract Attach type requeste d
20		RETURN			
21		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]			SERVIC E REQUE ST
22		RETURN			

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
23		Dc ? RRC_DataInd [tcv_GMM_RAU_Expect = TRUE](tcv_TmpRAU_ReqPDU := RRC_DataInd.msg, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_RAU_Rec := TRUE)	car_PS_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cbr_RA_UpdReqAny (c_GMM_UpdateType_v(?,?) , c_RAI_Any_v, ?))		ROUTIN G AREA UPDAT E REQUE ST
24		RETURN			
25		Dc ? RRC_DataInd [tcv_GMM_DetachExpect = TRUE](tcv_GMM_DetachExpect := FALSE)	car_PS_UplinkDirectTransfe r (tsc_CellDedicated , tsc_RB3, cr_DetachRequest_MO)		DETAC H REQUE ST
26		RETURN			
27	DFI2	Dc?OTHERWISE [tcv_TestBody = FALSE]		(I)	2.
28		CANCEL			3.
29	DFF2	Dc?OTHERWISE [tcv_TestBody = TRUE]		(F)	5.
30		CANCEL			3.
31		CRLC?CRLC_Integrity_Failure_IND	car_CRLC_IntegrityFail		
32	DFF3	[tcv_CellIndInfo.integrityStarted]		(F)	
33		RETURN			
34		[NOT tcv_CellIndInfo.integrityStarted]			
35		RETURN			
Detailed Comments : 1. The guard timer times out, inconclusive. 2. If unexpected data is received in the preambles or postambles, a preliminary inconclusive verdict is assigned, and the test case is terminated. 3. Cancel of all running timers. 4. Depending upon the context appropriate measures may differ: the guard timer duration may be increased, operator action during a test may be speeded up, ... 5. If unexpected data is received in the test body, a preliminary failure verdict is assigned, and the test case is terminated. 6. ATTACH REQUEST with any contents received during MM_TestExecution 7. ATTACH REJECT with cause 'GPRS services not allowed' 8. DETACH REQUEST received during MM_TestExecution 9. DETACH ACCEPT					

Default Dynamic Behaviour					
Default Name : NAS_OtherwiseFailActRB_TM Group : NAS_Defaults/ Objective : To match unexpected events and fail the test case. Any Setup or Activate PDP context request message can be received and ignored. Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		Dc ? RRC_DataInd [tcv_CN_Domain = cs_domain]	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_SetupMO_Any)		0.
2		RETURN			
3		Dc ? RRC_DataInd [tcv_CN_Domain = ps_domain]	car_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_ActPDP_ContextReqMO_Any)		0.
4		RETURN			
5		?TIMEOUT t_Guard			1.
6		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")		4.
7		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
8		[tcv_TestBody = FALSE]			
9	DFI1	CANCEL		(I)	
10		[tcv_TestBody = TRUE]			
11	DFF1	CANCEL		(F)	
12		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_lv, ?))		SERVICE REQUEST
13		RETURN			
14	DFI2	Dc?OTHERWISE [tcv_TestBody = FALSE]		(I)	2.
15		CANCEL			3.
16	DFF2	Dc?OTHERWISE [tcv_TestBody = TRUE]		(F)	5.
17		CANCEL			3.
18		CRLC?CRLC_Integrity_Failure_IND	car_CRLC_IntegrityFail		
19	DFF3	[tcv_CellIndInfo.integrityStarted]		(F)	
20		RETURN			
21		[NOT tcv_CellIndInfo.integrityStarted]			
22		RETURN			
Detailed Comments : 0. UE may send the SETUP or the Activate PDP context request message, then it shall be ignored 1. The guard timer times out, inconclusive. 2. If unexpected data is received in the preambles or postambles, a preliminary inconclusive verdict is assigned, and the test case is terminated. 3. Cancel of all running timers. 4. Depending upon the context appropriate measures may differ: the guard timer duration may be increased, operator action during a test may be speeded up, ... 5. If unexpected data is received in the test body, a preliminary failure verdict is assigned, and the test case is terminated.					

Default Dynamic Behaviour					
Default Name : RRC_Def1 Group : RRC_Defaults/ Objective : To match unexpected events and fail the test case. Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		TM?RxStatus [tcv_RLC_IgnoreStatus = TRUE]	car_StatusInd(tsc_RB_AM_7_RLC)		
2		RETURN			
3		Dc ? RRC_DataInd [tcv_GMM_AttachExpect = TRUE] (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_AttachRec := TRUE)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUEST – Extract Attach type requested
4		RETURN			
5		Dc ? RRC_DataInd [tcv_GMM_RAU_Expect = TRUE](tcv_TmpRAU_ReqPDU := RRC_DataInd.msg, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_RAU_Rec := TRUE)	car_PS_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cbr_RA_UpdReqAny (c_GMM_UpdateType_v(?,?) , c_RAI_Any_v, ?))		ROUTING AREA UPDATE REQUEST
6		RETURN			
7		Dc ? RRC_DataInd [tcv_GMM_DetachExpect = TRUE](tcv_GMM_DetachExpect := FALSE)	car_PS_UplinkDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_DetachRequest_MO)		DETACH REQUEST
8		RETURN			
9		AM?RLC_AM_DATA_IND	car_RRC_Status(?, tsc_RB2, cr_RRC_RrcStatus)		
10		RETURN			
11		AM?RLC_AM_DATA_IND	car_MeasRepAM(?, tsc_RB2, cr_RRC_MeasRep)		
12		RETURN			
13		UM?RLC_UM_DATA_IND	car_MeasRepUM(?, tsc_RB1, cr_RRC_MeasRep)		
14		RETURN			
15		AM?RLC_AM_DATA_CNF	car_AM_DataCnf(?, tsc_RB2)		
16		RETURN			
17		CPHY?CPHY_Sync_IND CANCEL t_T312	ca_SyncInd (tsc_UL_DPCH1)		
18		RETURN			
19		CPHY?CPHY_Out_of_Sync_IND CANCEL t_T312	ca_OutOfSyncInd (tsc_UL_DPCH1)		
20		RETURN			
21		CRLC?CRLC_Integrity_Failure_IND	car_CRLC_IntegrityFail		

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
22	DFF0	[tcv_CellIndInfo.integrityStarted]		(F)	
23		RETURN			
24		[NOT tcv_CellIndInfo.integrityStarted]			
25		RETURN			
26		Dc ? RRC_DataInd[tcv_MM_TestExecution]	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v,?))		
27		Dc ! RRC_DataReq	ca_PS_DataReq (tsc_CellDedicated, tsc_RB3, cs_AttachRej('07'O))		
28		RETURN			
29		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_lv, ?))		SERVICE REQUEST
30		RETURN			
31		?TIMEOUT t_Guard			
32		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")		
33		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
34		[tcv_TestBody = FALSE]			
35	DFI1	CANCEL		(I)	
36		[tcv_TestBody = TRUE]			
37	DFF1	CANCEL		(F)	
38		?TIMEOUT t_TimeoutInDefault			
39		(tcv_TimeoutInDefault := TRUE)			
40		RETURN			
41		?TIMEOUT			
42		[tcv_TestBody = FALSE]			
43	DFI8	CANCEL		(I)	
44		[tcv_TestBody = TRUE]			
45	DFF8	CANCEL		(F)	
46		AM?OTHERWISE [tcv_TestBody = FALSE]			
47	DFI2	CANCEL		(I)	
48		UM?OTHERWISE [tcv_TestBody = FALSE]			
49	DFI3	CANCEL		(I)	
50		TM?OTHERWISE [tcv_TestBody = FALSE]			
51	DFI4	CANCEL		(I)	

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
52		Dc?RRC_DataInd [tcv_TestBody = FALSE]	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cbr_Deact_PDP_ContextReq_MO(?))	(I)	2.
53	DFI5	CANCEL			3.
54		AM?OTHERWISE [tcv_TestBody = TRUE]			
55	DFF2	CANCEL		(F)	
56		UM?OTHERWISE [tcv_TestBody = TRUE]			
57	DFF3	CANCEL		(F)	
58		TM?OTHERWISE [tcv_TestBody = TRUE]			
59	DFF4	CANCEL		(F)	
60		Dc?RRC_DataInd [tcv_TestBody = TRUE]	car_PS_UplinkDirectTransfer (tsc_CellDedicated, tsc_RB3, cbr_Deact_PDP_ContextReq_MO(?))	(F)	5.
61	DFF5	CANCEL			3.
62		CRLC?OTHERWISE			
63	DFI6	CANCEL		(I)	
64		CMAC?OTHERWISE			
65	DFI7	CANCEL		(I)	
66		CPHY?OTHERWISE			
67	DFI9	CANCEL		(I)	
Detailed Comments :					

Default Dynamic Behaviour					
Default Name : RRC_DefConnEst Group : RRC_Defaults/ Objective : To match unexpected events during an RRC connection establishment i.e to match the repetition of RRC CONNECTION REQUEST. Comments : Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	DFF0	TM?RLC_TR_DATA_IND	car_RRC_ConnReq(?, tsc_RB0, cr_RRC_RrcConnReqAny)	(F)	
2		RETURN			
3		AM?RLC_AM_DATA_IND	car_RRC_Status(?, tsc_RB2, cr_RRC_RrcStatus)		
4		RETURN			
5		CPHY?CPHY_Sync_IND CANCEL t_T312	ca_SyncInd (tsc_UL_DPCH1)		
6		RETURN			
7		CPHY?CPHY_Out_of_Sync_IND CANCEL t_T312	ca_OutOfSyncInd (tsc_UL_DPCH1)		
8		RETURN			
9		AM?RLC_AM_DATA_IND	car_MeasRepAM(?, tsc_RB2, cr_RRC_MeasRep)		
10		RETURN			
11		UM?RLC_UM_DATA_IND	car_MeasRepUM(?, tsc_RB1, cr_RRC_MeasRep)		
12		RETURN			
13		AM?RLC_AM_DATA_CNF	car_AM_DataCnf(?, tsc_RB2)		
14		RETURN			
15		CRRLC?CRRLC_Integrity_Failure_IND	car_CRRLC_IntegrityFail		
16		[tcv_CellIndInfo.integrityStarted]			
17		RETURN			
18		[NOT tcv_CellIndInfo.integrityStarted]			
19		RETURN			
20		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_lv, ?))		SERVICE REQUEST
21		RETURN			
22	DFI1	?TIMEOUT t_Guard		(I)	4.
23		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")		
24		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
25		[tcv_TestBody = FALSE]			
26		CANCEL			
27		[tcv_TestBody = TRUE]			

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
28	DFF1	CANCEL		(F)	
29		?TIMEOUT t_TimeoutInDefault			
30		(tcv_TimeoutInDefault := TRUE)			
31		RETURN			
32		?TIMEOUT			
33		[tcv_TestBody = FALSE]			
34	DFI8	CANCEL		(I)	
35		[tcv_TestBody = TRUE]			
36	DFF8	CANCEL		(F)	
37		AM?OTHERWISE [tcv_TestBody = FALSE]			
38	DFI2	CANCEL		(I)	
39		UM?OTHERWISE [tcv_TestBody = FALSE]			
40	DFI3	CANCEL		(I)	
41		TM?OTHERWISE [tcv_TestBody = FALSE]			
42	DFI4	CANCEL		(I)	
43		AM?OTHERWISE [tcv_TestBody = TRUE]			
44	DFF2	CANCEL		(F)	
45		UM?OTHERWISE [tcv_TestBody = TRUE]			
46	DFF3	CANCEL		(F)	
47		TM?OTHERWISE [tcv_TestBody = TRUE]			
48	DFF4	CANCEL		(F)	
49		CRLC?OTHERWISE			
50	DFI5	CANCEL		(I)	
51		CMAC?OTHERWISE			
52	DFI6	CANCEL		(I)	
53		CPHY?OTHERWISE			
54	DFI7	CANCEL		(I)	
Detailed Comments :					

Default Dynamic Behaviour					
Default Name : UT_OtherwiseFail					
Group : UT_Defaults/					
Objective : To match unexpected events and fail the test case at the UtT PCO.					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1	DFI1	Ut?OTHERWISE	car_CRLC_IntegrityFail	I	1.
2		CANCEL			
3	DFF2	CRLC?CRLC_Integrity_Failure_IND		(F)	
4		[tcv_CellIndInfo.integrityStarted]			
5		RETURN			
6		[NOT tcv_CellIndInfo.integrityStarted]			
7		RETURN			
8		?TIMEOUT			
9	DFI8	[tcv_TestBody = FALSE]		(I)	
10		CANCEL			
11	DFF8	[tcv_TestBody = TRUE]		(F)	
12		CANCEL			
Detailed Comments : 1. Unexpected UT MMI events, fail. 2. Cancel of all running timers.					

Default Dynamic Behaviour						
Default Name : SS_Def_Special						
Group : SS_Defaults/						
Objective : To match unexpected events during SS configuration/reconfiguration steps. This default does not send any message.						
Comments :						
Description :						
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments	
1		?TIMEOUT t_Guard	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")		4.	
2		Ut ! MMI_CmdReq				
3		Ut ? MMI_CmdCnf				
4		[tcv_TestBody = FALSE]				
5	DFI5	CANCEL	ca_MMI_CmdCnf	(I)		
6	DFF7	[tcv_TestBody = TRUE]				
7		CANCEL				
8		?TIMEOUT t_TimeoutInDefault				
9		(tcv_TimeoutInDefault := TRUE)				
10		RETURN				
11		?TIMEOUT				
12		[tcv_TestBody = FALSE]				
13	DFI8	CANCEL	ca_SyncInd (?)	(I)		
14	DFF8	[tcv_TestBody = TRUE]				
15		CANCEL				
16		CPHY?CPHY_Sync_IND				
17		RETURN	ca_OutOfSyncInd (?)			
18		CPHY?CPHY_Out_of_Sync_IND				
19		RETURN				
20		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]				
21		RETURN	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_lv, ?))		SERVICE REQUEST	
22		Dc ? RRC_DataInd [tcv_GMM_AttachExpect = TRUE] (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_AttachRec := TRUE)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))			
23		RETURN	car_CRLC_IntegrityFail	(F)		
24		CRLC?CRLC_Integrity_Failure_IND				
25	DFF5	[tcv_CellIndInfo.integrityStarted]		ATTACH REQUEST – Extract Attach type requested		
26		RETURN				
27		[NOT tcv_CellIndInfo.integrityStarted]				
28		RETURN				
29		CPHY?OTHERWISE				
30		DFI2				CANCEL

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
31		CMAC?OTHERWISE			
32	DFI3	CANCEL		(I)	
33		CRLC?OTHERWISE			
34	DFI4	CANCEL		(I)	
Detailed Comments :					

Default Dynamic Behaviour					
Default Name : SS_Def					
Group : SS_Defaults/					
Objective : To match unexpected events during SS configuration/reconfiguration steps.					
Comments :					
Description :					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
1		?TIMEOUT t_Guard			
2		Ut ! MMI_CmdReq	ca_MMI_CmdReq ("The guard timer has run out. Please take appropriate measures")		4.
3		Ut ? MMI_CmdCnf	ca_MMI_CmdCnf		
4		[tcv_TestBody = FALSE]			
5	DFI5	CANCEL		(I)	
6		[tcv_TestBody = TRUE]			
7	DFF7	CANCEL		(F)	
8		?TIMEOUT t_TimeoutInDefault			
9		(tcv_TimeoutInDefault := TRUE)			
10		RETURN			
11		?TIMEOUT			
12		[tcv_TestBody = FALSE]			
13	DFI8	CANCEL		(I)	
14		[tcv_TestBody = TRUE]			
15	DFF8	CANCEL		(F)	
16		CPHY?CPHY_Sync_IND	ca_SyncInd (?)		
17		RETURN			
18		CPHY?CPHY_Out_of_Sync_IND	ca_OutOfSyncInd (?)		
19		RETURN			
20		Dc ? RRC_DataInd[tcv_MM_TestExecution]	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_Iv, c_RAI_Any_v,?))		
21		Dc ! RRC_DataReq	ca_PS_DataReq (tsc_CellDedicated, tsc_RB3, cs_AttachRej('07'O))		
22		RETURN			
23		Dc ? RRC_DataInd [tcv_ReceivePS_ServiceReq = TRUE]	car_InitDirectTransfer (tsc_CellDedicated , tsc_RB3, cr_ServiceRequest (c_ServiceType_v(?), c_MobileIdAny_Iv, ?))		SERVIC E REQUE ST
24		RETURN			

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Default Dynamic Behaviour					
Nr	Label	Behaviour Description	Constraints Ref	Verdict	Comments
25		Dc ? RRC_DataInd [tcv_GMM_AttachExpect = TRUE] (tcv_TmpAttachReqPDU := RRC_DataInd.msg, tcv_TmpB3:= tcv_TmpAttachReqPDU.attachType.type, tcv_CellIndInfo.start_PS := RRC_DataInd.start , tcv_GMM_AttachRec := TRUE)	car_PS_InitDirectTransfer (tsc_CellDedicated, tsc_RB3, cr_AttachReq (c_AttachTypeAny, c_MobileIdAny_lv, c_RAI_Any_v, ?))		ATTACH REQUEST – Extract Attach type requested
26		RETURN			
27		CRLC?CRLC_Integrity_Failure_IND	car_CRLC_IntegrityFail		
28	DFF5	[tcv_CellIndInfo.integrityStarted]		(F)	
29		RETURN			
30		[NOT tcv_CellIndInfo.integrityStarted]			
31		RETURN			
32		CPHY?OTHERWISE			
33	DFI2	CANCEL		(I)	
34		CMAC?OTHERWISE			
35	DFI3	CANCEL		(I)	
36		CRLC?OTHERWISE			
37	DFI4	CANCEL		(I)	
Detailed Comments :					