PITS System GPIO Allocation

		WiringPi	Name	Physical		Name	WiringPi	
		ı	3.3v	1	2	5v		
	SDA	8	SDA.1	3	4	5V		
	SCL	9	SCL.1	5	6	0v		
	302	7	GPIO. 7	7	8	TxD	15	UBLOX RXD/MTX2B TXD
		, ´	0v	9	10	RxD	16	UBLOX TXD
	MTX2 ENABLE	0	GPIO. 0	11	12	GPIO. 1	1	HX1 TXD
	UBLOX SDA	2	GPIO. 2	13	14	0v	-	
	UBLOX SCL	3	GPIO. 3	15	16	GPIO. 4	4	HX1 ENABLE
		ŭ	3.3v	17	18	GPIO. 5	5	CEO/DIO5
	MOSI	12	MOSI	19	20	0v	J	220, 2103
	MISO	13	MISO	21	22	GPIO. 6	6	CEO/DIO0
	SCLK	14	SCLK	23	24	CEO	10	CEO/NSS
	SCER	17	0v	25	26	CE1	11	CE1/NSS
		30	SDA.0	27	28	SCL.0	31	CL1/1433
	CEO/DATA LED	21	GPIO.21	29	30	0v	31	
	LAN LED	22	GPIO.22	31	32	GPIO.26	26	CE1/DIO5
	INTERNET LED	23	GPIO.23	33	34	0v	20	CL1/DIO3
CEO/DATA LED	WARN LED	23	GPIO.24	35	36	GPIO.27	27	CE1/DIO0
CE1/DATA LED	OK LED	24 25	GPIO.25	33 37	38	GPIO.27	28	
CEI/DATA LED	OKTED	25						GPS PPS
		1	0v	39	40	GPIO.29	29	CE1/DATA LED

