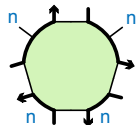
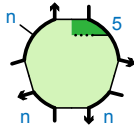


## Annotated multiloop stacking states

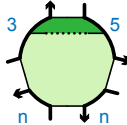
0 coaxial stacks  
0 dangle stacks



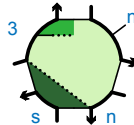
0 coaxial stacks  
1 dangle stack



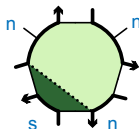
0 coaxial stacks  
2 dangle stacks



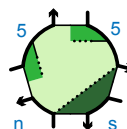
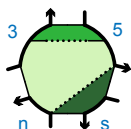
1 coaxial stack  
1 dangle stack



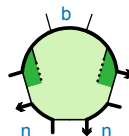
1 coaxial stack  
0 dangle stacks




1 coaxial stack, 2 dangle stacks




0 coaxial stacks  
2 dangle stacks



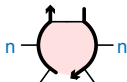
 multiloop

 multiloop dangle stack

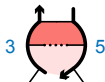
 multiloop coaxial stack

## Annotated exterior loop stacking states

0 coaxial stacks  
0 dangle stacks



0 coaxial stacks  
2 dangle stacks



0 coaxial stacks  
0 dangle stacks



1 coaxial stack  
0 dangle stacks



 exterior loop

 exterior loop dangle stack

 exterior loop coaxial stack