

Second SPICE Exercise

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Chapter 1

Analytic solution

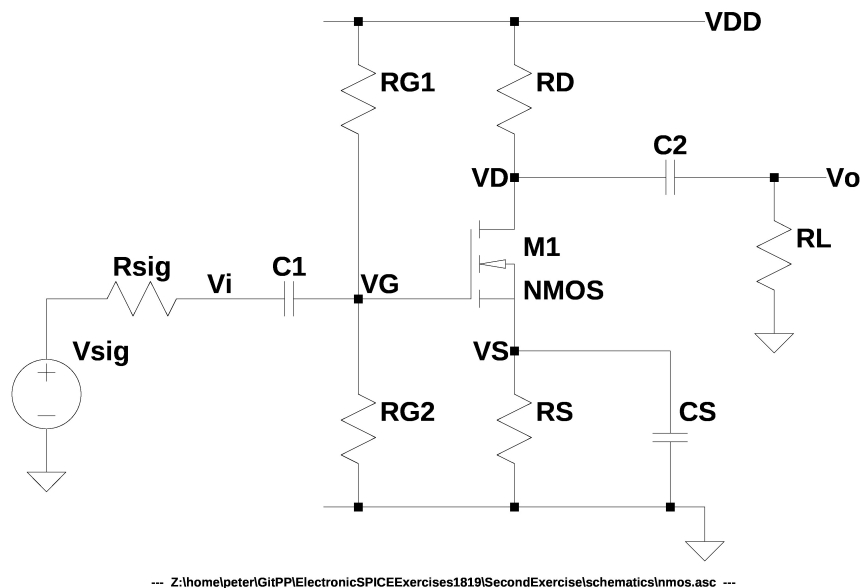


Figure 1.1: NMOS circuit

Chapter 2

SPICE simulations