## The SLAM Toolkit

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## 1 Introduction

The SLAM toolkit checks safety properties of software without the need for user-supplied annotations or abstractions. Given a safety property to check on a C program P, the SLAM process [4] iteratively refines a boolean program abstraction of P using three tools:

- C2BP, a predicate abstraction tool that abstracts P into a boolean program  $\mathcal{BP}(P,E)$  with respect to a set of predicates E over P [1,2];
- Bebop, a tool for model checking boolean programs [3], and
- Newton, a tool that discovers additional predicates to refine the boolean program, by analyzing the feasibility of paths in the C program.

Property violations are reported by the SLAM toolkit as paths over the program P. Since property checking is undecidable, the SLAM refinement algorithm may not converge. We have applied the SLAM toolkit to automatically check properties of device drivers taken from the Microsoft Driver Development Kit. While checking for various properties, we found that the SLAM process converges to a boolean program that is sufficiently precise to validate/invalidate the property [4].

Several ideas behind the SLAM tools are novel. C2BP is the first automatic predicate abstraction tool to handle a full-scale programming language with procedure calls and pointers, and perform a sound and precise abstraction. Bebop is the first model checker to handle procedure calls using an interprocedural dataflow analysis algorithm, augmented with representation tricks from the symbolic model checking community. Newton uses a path simulation algorithm in a novel way, to generate predicates for refinement.

## 2 Overview and Example

We introduce the SLAM refinement algorithm and apply it to a small code example. We have created a low-level specification language called SLIC (Specification Language for Interface Checking) for stating safety properties. Figure 1(a) shows a SLIC specification that states that it is an error to acquire (or release) a spin lock twice in a row. There are two events on which state transitions happen — returns of calls to the functions KeAcquireSpinLock and KeReleaseSpinLock.

We wish to check if a temporal safety property  $\varphi$  specified using SLIC is satisfied by a program P. We have built a tool that automatically instruments the program P with property  $\varphi$  to result in a program P' such that P satisfies

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```
state {
                                        enum { Unlocked=0, Locked=1 }
   enum { Unlocked=0, Locked=1 }
                                          state = Unlocked;
     state = Unlocked;
                                        void slic_abort() {
                                          SLIC_ERROR: ;
KeAcquireSpinLock.return {
  if (state == Locked)
                                       void KeAcquireSpinLock_return() {
    abort:
                                          if (state == Locked)
  else
    state = Locked;
                                            slic_abort();
                                          else
                                            state = Locked;
                                       }
KeReleaseSpinLock.return {
  if (state == Unlocked)
                                       void KeReleaseSpinLock_return {
    abort;
                                          if (state == Unlocked)
  else
    state = Unlocked;
                                            slic_abort();
                                          else
                                            state = Unlocked;
                                                       (b)
                 (a)
```

**Fig. 1.** (a) A SLIC Specification for Proper Usage of Spin Locks, and (b) Its Compilation into C Code.

 $\varphi$  iff the label SLIC\_ERROR is not reachable in P'. In particular, the tool first creates C code from the SLIC specification, as shown in Figure 1(b). The tool then inserts calls to the appropriate SLIC C functions in the program P to result in the instrumented program P'.

Now, we wish to check if the label SLIC\_ERROR is reachable in the instrumented program P'. Let i be a metavariable that records the SLAM iteration count. The first iteration (i = 0) starts with the set of predicates  $E_0$  that are present in the conditionals of the SLIC specification. Let  $E_i$  be some set of predicates over the state of P'. Then iteration i of SLAM is carried out using the following steps:

- 1. Apply C2BP to construct the boolean program  $\mathcal{BP}(P', E_i)$ . Program  $\mathcal{BP}(P', E_i)$  is guaranteed to abstract the program P', as every feasible execution path p of the program P' also is a feasible execution path of  $\mathcal{BP}(P', E_i)$ .
- 2. Apply Bebop to check if there is a path  $p_i$  in  $\mathcal{BP}(P', E_i)$  that reaches the SLIC\_ERROR label. If Bebop determines that SLIC\_ERROR is not reachable, then the property  $\varphi$  is valid in P, and the algorithm terminates.
- 3. If there is such a path  $p_i$ , then we use NEWTON to check if  $p_i$  is feasible in P'. There are two outcomes: "yes", the property  $\varphi$  is violated by P and the algorithm terminates with an error path  $p_i$ ; "no", NEWTON finds a set of predicates  $F_i$  that explain the infeasibility of path  $p_i$  in P'.
- 4. Let  $E_{i+1} := E_i \cup F_i$ , and i := i+1, and proceed to the next iteration.

Figure 2(a) presents a snippet of (simplified) C code from a PCI device driver. Figure 2(b) shows the instrumented program (with respect to the SLIC

```
void example() {
void example() {
do {
                                         do {
 KeAcquireSpinLock();
                                          KeAcquireSpinLock();
                                      A: KeAcquireSpinLock_return();
 nPacketsOld = nPackets;
                                          nPacketsOld = nPackets;
 req = devExt->WLHV;
                                          req = devExt->WLHV;
 if(req && req->status){
                                          if(req && req->status){
   devExt->WLHV = req->Next;
                                            devExt->WLHV = req->Next;
   KeReleaseSpinLock();
                                            KeReleaseSpinLock();
                                      B:
                                            KeReleaseSpinLock_return();
   irp = req->irp;
                                            irp = req->irp;
   if(req->status > 0){
                                            if(req->status > 0){
                                              irp->IoS.Status = SUCCESS;
      irp->IoS.Status = SUCCESS;
      irp->IoS.Info = req->Status;
                                              irp->IoS.Info = req->Status
   } else {
                                            } else {
      irp->IoS.Status = FAIL;
                                              irp->IoS.Status = FAIL;
      irp->IoS.Info = req->Status;
                                              irp->IoS.Info = req->Status
   SmartDevFreeBlock(req);
                                            SmartDevFreeBlock(req);
   IoCompleteRequest(irp);
                                            IoCompleteRequest(irp);
                                            nPackets++;
   nPackets++;
                                          }
} while(nPackets!=nPacketsOld);
                                         } while(nPackets!=nPacketsOld);
KeReleaseSpinLock();
                                         KeReleaseSpinLock();
                                      C: KeReleaseSpinLock_return();
           (a) Program P
                                                 (b) Program P'
```

**Fig. 2.** (a) A snippet of device driver code P, and (b) program P' resulting from instrumentation of program P due to SLIC specification in Figure 1(a).

specification in Figure 1(a)). Calls to the appropriate SLIC C functions (see Figure 1(b)) have been introduced (at labels A, B, and C).

The question we wish to answer is: is the label SLIC\_ERROR reachable in the program P' comprised of the code from Figure 1(b) and Figure 2(b)? The first step of the algorithm is to generate the initial boolean program. A boolean program [3] is a C program in which the only type is boolean.

For our example, the inital set of predicates  $E_0$  consists of two global predicates (state = Locked) and (state = Unlocked) that appear in the conditionals of the SLIC specification. These two predicates and the program P' are input to the C2BP (C to Boolean Program) tool. The translation of the SLIC C code from Figure 1(b) to the boolean program is shown in Figure 3. The translation of the example procedure is shown in Figure 4(a). Together, these two pieces of code comprise the boolean program  $\mathcal{BP}(P', E_0)$  output by C2BP.

```
decl {state==Locked},
   {state==Unlocked} := F,T;
void slic_abort() begin
 SLIC_ERROR: skip;
                                     void KeReleaseSpinLock_return()
                                     begin
                                       if ({state == Unlocked})
void KeAcquireSpinLock_return()
                                         slic_abort();
begin
                                       else
  if ({state==Locked})
                                         {state==Locked},
    slic_abort();
                                            {state==Unlocked} := F,T;
                                     end
    {state==Locked},
     {state==Unlocked} := T,F;
end
```

**Fig. 3.** The C code of the SLIC specification from Figure 1(b) compiled by C2BP into a boolean program.

As shown in Figure 3, the translation of the SLIC C code results in the global boolean variables, {state==Locked} and {state==Unlocked}. For every statement s in the C program and predicate  $e \in E_0$ , the C2BP tool determines the effect of statement s on predicate e and codes that effect in the boolean program. Non-determinism is used to conservatively model the conditions in the C program that cannot be abstracted precisely using the predicates in  $E_0$ , as shown in Figure 4(a). Many of the assignment statements in the example procedure are abstracted to the **skip** statement (no-op) in the boolean program. The C2BP tool uses an alias analysis to determine whether or not an assignment statement through a pointer dereference can affect a predicate e.

The second step of our process is to determine whether or not the label SLIC\_ERROR is reachable in the boolean program  $\mathcal{BP}(P', E_0)$ . We use the BEBOP model checker to determine the answer to this query. In this case, the answer is "yes" and BEBOP produces a (shortest) path  $p_0$  leading to SLIC\_ERROR (specified by the sequence of labels [A, A, SLIC\_ERROR]).

Does  $p_0$  represent a feasible execution path of P'? The Newton tool takes a C program and a (potential) error path as an input. It uses verification condition generation to determine if the path is feasible. If the path is feasible, we have found a real error in P'. If the answer is "no" then Newton uses a new algorithm to identify a small set of predicates that "explain" why the path is infeasible. In the running example, Newton detects that the path  $p_0$  is infeasible, and returns a single predicate (nPackets = npacketsOld) as the explanation for the infeasibility.

Figure 4(b) shows the boolean program  $\mathcal{BP}(P', E_1)$  that C2BP produces on the second iteration of the process. This program has one additional boolean

<sup>&</sup>lt;sup>1</sup> Boolean programs permit a variable identifier to be an arbitrary string enclosed between "{" and "}".

```
void example()
                                              void example()
    begin
                                              begin
     do
                                               do
       skip;
                                                skip;
A:
      KeAcquireSpinLock_return();
                                          A:
                                                KeAcquireSpinLock_return();
                                                b := T;
      skip;
       if (*) then
                                                if (*) then
         skip;
                                                   skip;
В:
        KeReleaseSpinLock_return();
                                          B:
                                                   KeReleaseSpinLock_return();
         if (*) then
                                                   if (*) then
           skip;
                                                     skip;
         else
                                                   else
           skip;
                                                     skip;
         fi
                                                   fi
         skip;
                                                   b := b ? F : *;
      fi
                                                fi
     while (*);
                                               while (!b);
     skip;
                                               skip;
C:
     KeReleaseSpinLock_return();
                                          C:
                                               KeReleaseSpinLock_return();
   (a) Boolean program \mathcal{BP}(P', E_0)
                                             (b) Boolean program \mathcal{BP}(P',E_1)
```

**Fig. 4.** The two boolean programs created while checking the code from Figure 2(b).

variable (b) that represents the predicate (nPackets = nPacketsOld). The assignment statement nPackets = nPacketsOld; makes this condition true, so in the boolean program the assignment b := T; represents this assignment. Using a theorem prover, C2BP determines that if the predicate is true before the statement nPackets++, then it is false afterwards. This is captured by the assignment statement in the boolean program "b := b ? F : \*". Applying BEBOP to the new boolean program shows that the label SLIC\_ERROR is not reachable.

## References

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