



# HARDWARE- BESCHREIBUNGSSPRACHEN

Hardwareentwurf mit VHDL

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Theoretische Informatik  
Studienbereich Angewandte Informatik  
Hochschule **RheinMain**



Notizen

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DATENBUSSE FÜR  
„SYSTEM-ON-A-CHIP“-SYSTEME

# TEST

## Notizen

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## Notizen

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