PHYS 301 - Digital Electronics Lecture / Laboratory

Demonstration Program C			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
0	NOP		-
1	CLR		0
2	LDA	0x22	0
3	STA	0x00	0
4	NOP		0
5	LDA	0x10	0
6	STA	0x01	0
7	LDA	0x3F	0
8	STA	0x03	0
9	ADI	0x07	0
10	LCA		70
11	NOP		70
12	SUBU	0x01	70
13	LCA		54
14	SUBU	0x00	54
15	LCA		20
16	ADDU	0x03	20
17	LCA		83
18	STA	0x02	83
19	LDA	0x45	83
20	SUB	0x02	83
21	LCA		14 *
22	NOP		14 *
23	ADI	0x7C	14 *

Demonstration Program C			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
24	LCA		110
25	ADI	0x9C	110
26	NOP		110
27	LCA		82
28	RST		82
29	NOP		
30	NOP		
31	HLT		

 * indicates a negative result

** indicates an overflow condition