PHYS 301 - Digital Electronics Lecture / Laboratory

Demonstration Program A			
P Step	OPCODE	DATA	BCD OUTPUT
0	NOP		-
1	CLR		0
2	LDA	0x6A	0
3	SUBU	0x07	0
4	LCA		99
5	STA	0x01	99
6	LDA	0x05	99
7	ADDU	0x01	99
8	LCA		104
9	MVI	0x02 0x07	104
10	NOP		104
11	ADDU	0x02	104
12	LCA		111
13	SBI	0x6B	111
14	LCA		4
15	ADI	0x2D	4
16	LCA		49
17	MVI	0x03 0x77	49
18	NOP		49
19	ADD	0x03	49
20	LCA		127 ** (168)
21	LDA	0x3D	127 ** (168)
22	SBI	0x1F	127 ** (168)
23	LCA		30

Demonstration Program A			
P Step	PGM MEM	DATA MEM	BCD OUTPUT
24	NOP		30
25	NOP		30
26	ADDU	0x02	30
27	LCA		37
28	HLT		37
29	ADI	0x11	37
30	LCA		37
31	HLT		37

 * indicates negative result

** indicates an overflow condition