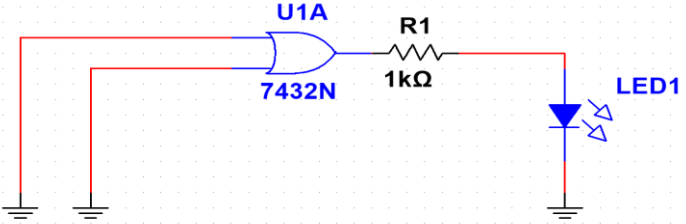
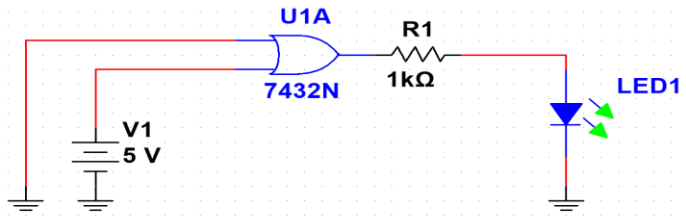
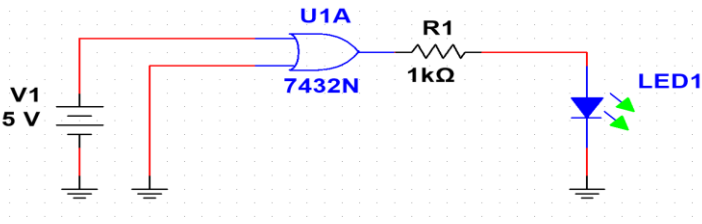
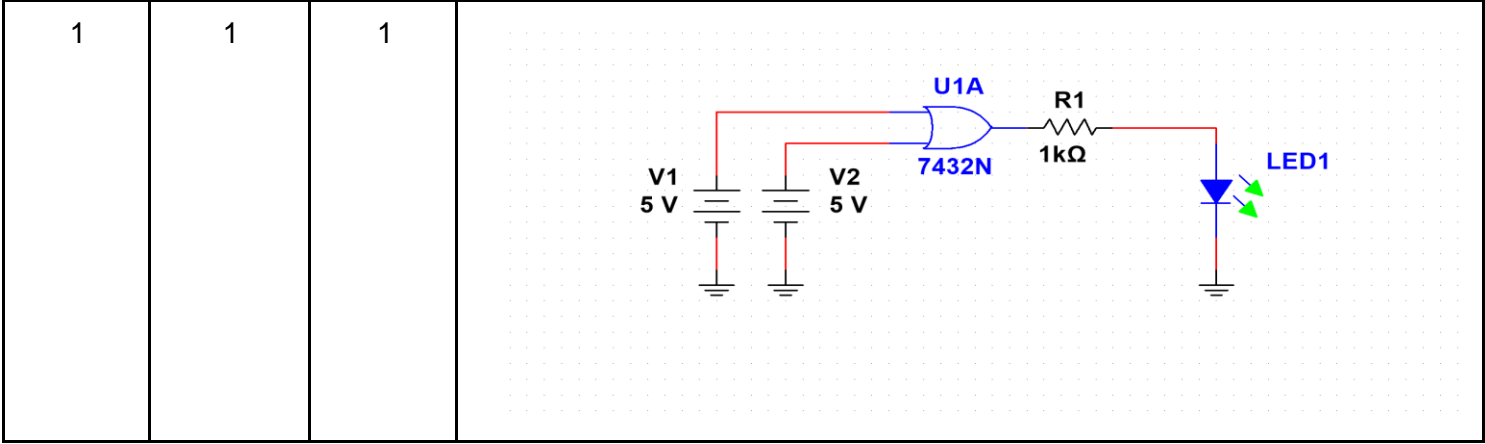


AIM - Studying Basic and Universal Gates on Multisim Software

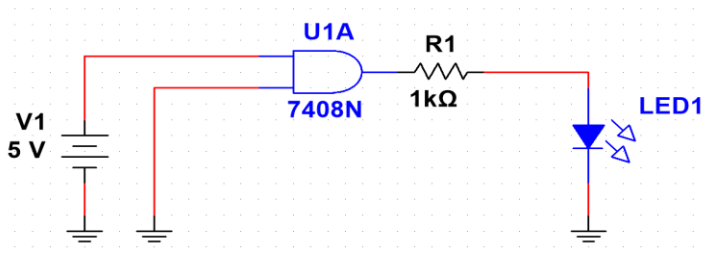
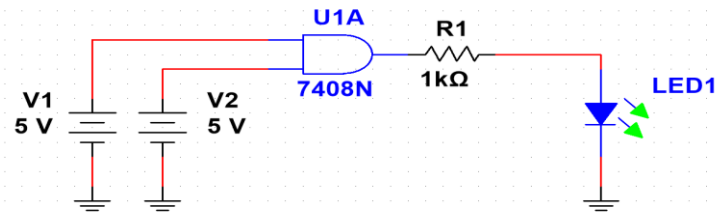
OR Gate

Input		Output	
A	B	Y	
0	0	0	
0	1	1	
1	0	1	

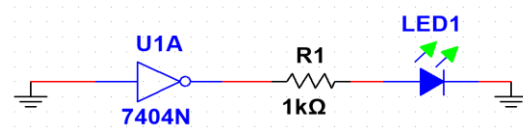


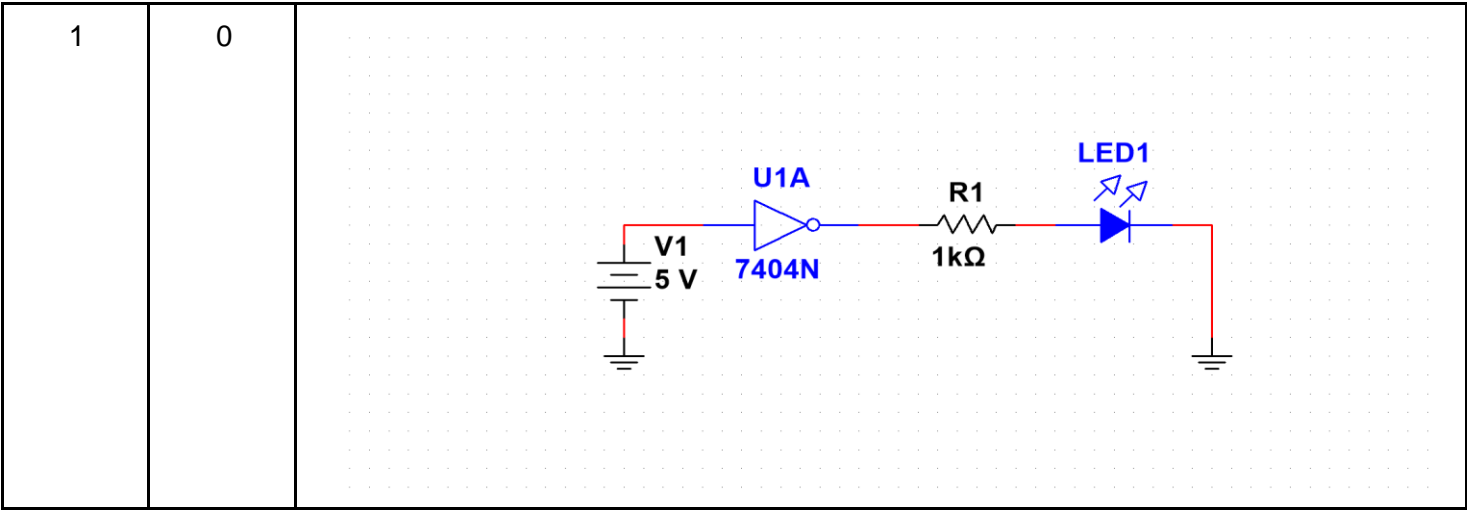
AND Gate

Input		Output	
A	B	Y	
0	0	0	
0	1	0	

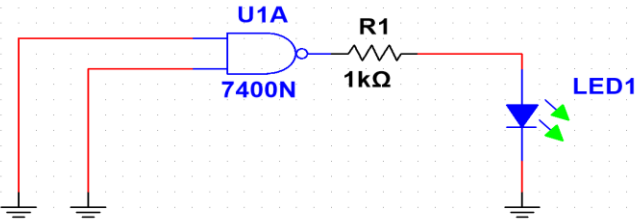
1	0	0	
1	1	1	

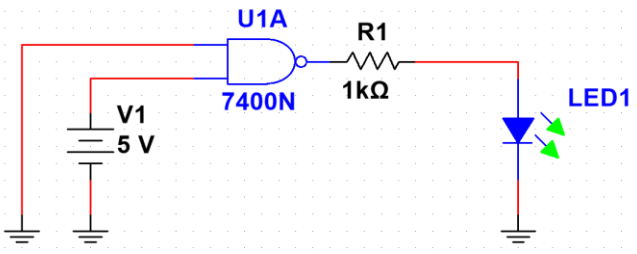
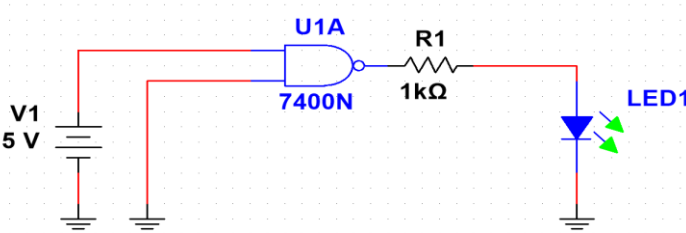
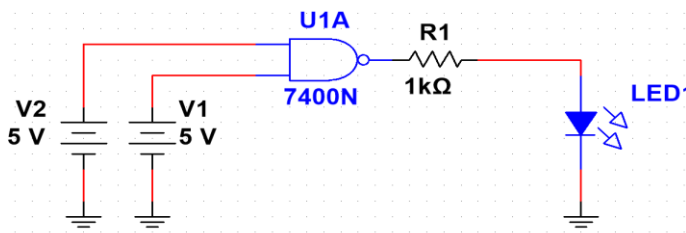
NOT Gate

Input	Output	
A	Y	
0	1	

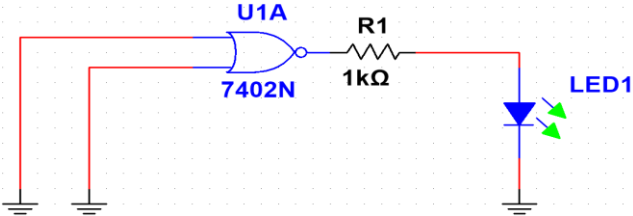
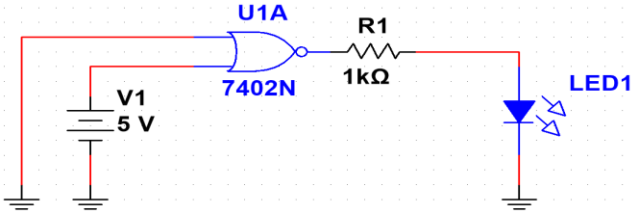
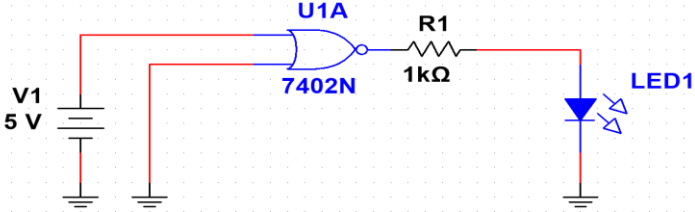


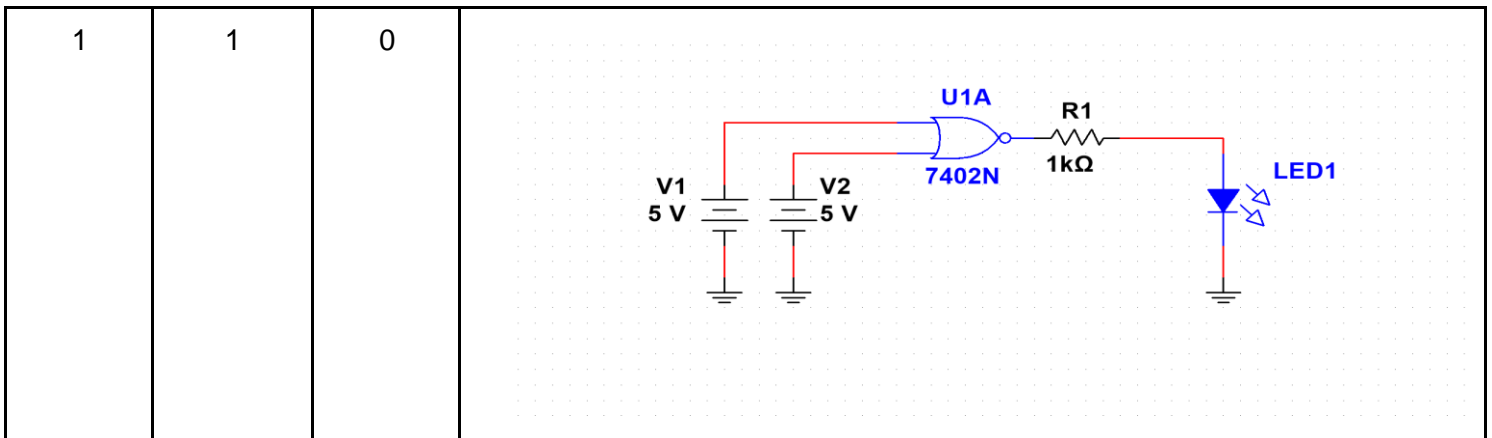
NAND Gate

Input		Output	
A	B	Y	
0	0	1	

0	1	1	
1	0	1	
1	1	0	

NOR Gate

Input		Output	
A	B	Y	
0	0	1	
0	1	0	
1	0	0	



CONCLUSION- Working of gates was observed and we successfully verified their truth tables