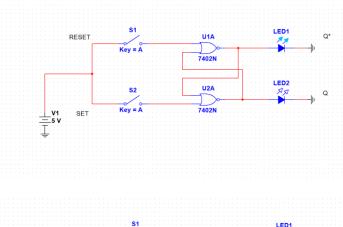
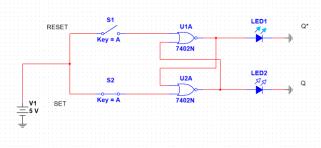
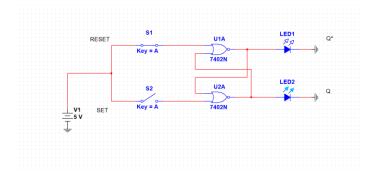
MULTISIM

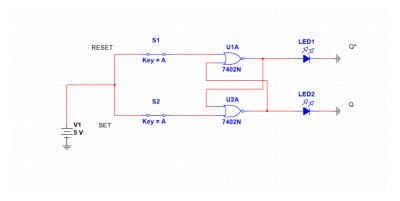
AIM- To visualize SR latch using NOR gates and verifying the truth.

OUTPUT-









Following is the truth table for the SR latch using NOR gates:-

S	R	Q	\overline{Q}
0	0	latch	latch
0	1	0	1
1	0	1	0
1	1	0	0

CONCLUSION- Thus, we have verified the truth table and observed the output.