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## **Constant-Current Driver Using the Analog Signal Conditioning (OPAMP) Peripheral**

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### **Introduction**

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It is sometimes preferable to drive an electronic component, for example, a light-emitting diode (LED), with constant current and not constant voltage. A microcontroller (MCU) containing the Analog Signal Conditioning (OPAMP) peripheral can be used to implement a constant-current driver using just one external resistor. The OPAMP peripheral also provides the ability to adjust the current setting under firmware control.

### **Features**

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- Constant-Current Drive
- Adjustable Current Under Firmware Control
- Low Bill of Materials – Only One External Resistor Required

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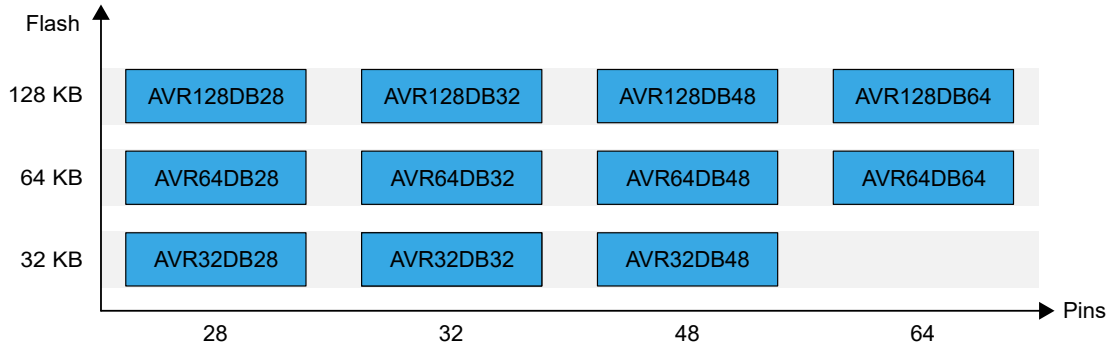
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### 1. Relevant Devices

This section lists the relevant devices for this document. The following figures show the different family devices, laying out pin count variants and memory sizes:

- Vertical migration upwards is possible without code modification, as these devices are pin-compatible and provide the same or more features
- Horizontal migration to the left reduces the pin count and, therefore, the available features
- Devices with different Flash memory sizes typically also have different SRAM and EEPROM

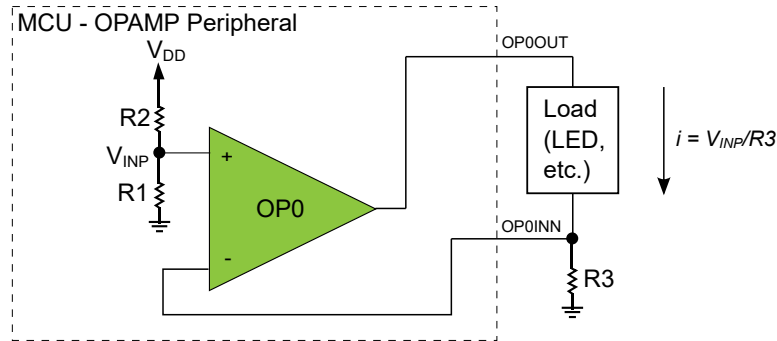
**Figure 1-1. AVR® DB Family Overview**



## 2. Constant-Current Driver Using OPAMP Peripheral

The microcontroller's (MCU's) OPAMP peripheral can be used to provide a constant-current drive to a load, for example, a light-emitting diode (LED). The schematic diagram of the constant-current driver is shown in the figure below. Only one component is required in addition to the load and MCU: Resistor R3 for converting current into voltage. The remainder of the constant-current driver is provided by the OPAMP peripheral inside the MCU.

**Figure 2-1. Schematic Diagram of a Constant-Current Driver Using OPAMP Peripheral**



As shown in the schematic diagram, op amp OP0 inside the MCU's OPAMP peripheral is configured with the non-inverting input (+) connected to the wiper of the internal resistor ladder. The top of the resistor ladder is internally connected to  $V_{DD}$ , and the bottom is internally connected to ground. Therefore, the resistor ladder acts as a voltage divider that generates a reference voltage,  $V_{INP}$ , on the non-inverting input of the op amp. The value of the reference voltage can be determined using the resistor divider formula:  $V_{INP} = (R1/(R2+R1))V_{DD}$ .

The output of the op amp, OP0OUT, is connected to one side of the load, and the other side of the load is connected to both R3 and the inverting input (-) OP0INN. Since the op amp input has a high impedance, the current flowing through the load will also flow through R3 to ground. The voltage generated across R3 will be proportional to the current through R3. The feedback loop created will cause the op amp to adjust its output voltage such that the voltage on its inverting input is the same as that on its non-inverting input,  $V_{INP}$ . The current flowing through R3 and the load will then be  $i = V_{INP}/R3$ .

As an example with realistic component values, consider the scenario where  $V_{DD}$  is 3.3V, and R3 is 205Ω. If the resistor ladder wiper setting is selected as WIP7 so that R2 is 15R and R1 is 1R, the ratio  $R1/(R2+R1)$  is (1/16), and  $V_{INP}$  will be  $(1/16)V_{DD}$  or 0.21V. The constant current through the load will then be  $V_{INP}/R3$  or 1.0 mA.

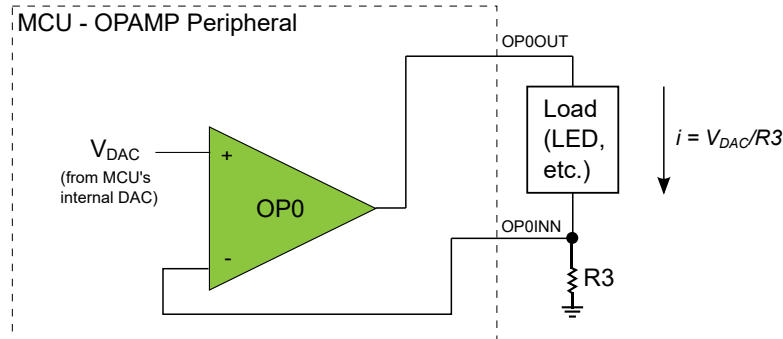
Because the OPAMP peripheral has eight different selections for the wiper position, determined by the value written to the MUXWIP bit field in the OPAMP.OPnRESMUX register, it is possible to select eight different current values under firmware control. The following table displays the current value provided by each MUXWIP bit field value.

**Table 2-1. MUXWIP Selection of Current Value**

MUXWIP Selection	Description	Voltage Divider Ratio	Current Value
WIP0	R1 = 15R, R2 = 1R	15/16	$0.9375 V_{DD}/R3$
WIP1	R1 = 14R, R2 = 2R	14/16	$0.875 V_{DD}/R3$
WIP2	R1 = 12R, R2 = 4R	12/16	$0.75 V_{DD}/R3$
WIP3	R1 = 8R, R2 = 8R	8/16	$0.5 V_{DD}/R3$
WIP4	R1 = 6R, R2 = 10R	6/16	$0.375 V_{DD}/R3$
WIP5	R1 = 4R, R2 = 12R	4/16	$0.25 V_{DD}/R3$
WIP6	R1 = 2R, R2 = 14R	2/16	$0.125 V_{DD}/R3$
WIP7	R1 = 1R, R2 = 15R	1/16	$0.0625 V_{DD}/R3$

If more precise control of the current value is required, the MCU's digital-to-analog converter (DAC) can be internally connected to the non-inverting (+) input of the op amp as shown in the figure below. This is achieved by writing the DAC setting to the MUXPOS bit field of the OPAMP.OPnINMUX register. In this case, the current value is given by the formula  $i = V_{DAC}/R3$ , where  $V_{DAC}$  is the output voltage of the DAC.

**Figure 2-2. Schematic Diagram of a Constant-Current Driver using OPAMP and DAC**



### 3. OPAMP Configuration Code

Just a few lines of code are required to configure the OPAMP peripheral to implement the constant-current driver:

```
// Set up the timebase of the OPAMP peripheral
OPAMP.TIMEBASE = 3; // Number of CLK_PER cycles that equal one us, minus one (4-1=3)

//Connect OP0 to wiper and pin connected to sense resistor
OPAMP.OP0INMUX = OPAMP_OP0INMUX_MUXPOS_WIP_gc | OPAMP_OP0INMUX_MUXNEG_INN_gc;
//Connect resistor ladder to VDD and ground so the wiper functions as a voltage divider
OPAMP.OP0RESMUX = OPAMP_OP0RESMUX_MUXBOT_GND_gc | OPAMP_OP0RESMUX_MUXWIP_WIP7_gc |
OPAMP_OP0RESMUX_MUXTOP_VDD_gc;
// Configure OP0 Control A
OPAMP.OP0CTRLA = OPAMP_OP0CTRLA_OUTMODE_NORMAL_gc | OPAMP_ALWAYS_ON_bm;

// Enable the OPAMP peripheral
OPAMP.CTRLA = OPAMP_ENABLE_bm;
```

After this code has been executed, the op amp stays active, and no further CPU intervention is required.



**View Code Example on GitHub**  
Click to browse repository

#### **4. Revision History**

Doc. Rev.	Date	Comments
A	09/2020	Initial document release

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ISBN: 978-1-5224-6647-5

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