

Lab 11, _div, testing print

By ADMIN | Published: APRIL 13, 2013

Interesting point we missed in lab10. Those are stubs that we have in main():
_div, _divu, _mod, _modu

Amazingly, I do not know what it is 😊,

Plan9/Inferno people, can you give a help regarding purpose of these references and way how they affect?
(I will update the lab as I get more info about this)

It is clear that without them fixed you can not have *print()* call working and other stuff in the kernel.
By checking/compiling/running/checking, we reveal that in other arm ports it is fixed in next way in **l.s(load.s)**:

```
1      ...
2      BL      ,main(SB)
3  dead:
4      B      dead
5      B      ,0(PC)
6      BL      _div(SB)      /* hack to load _div, etc. */
```

As we have such code in **load.s**, then *print()* call can be used after we initialize *serwrite* variable. Also let's add little more tracing to main() call to see more information:

```
01  pl011_puts("Entered main() at ");
02  pl011_addr(&main, 0);
03  pl011_puts(" with SP=");
04  pl011_addr((void *)getsp(), 1.);
05
06  pl011_puts("Clearing Mach: ");
07  memset(m, 0, sizeof(Mach));
08  pl011_addr((char *)m, 0); pl011_puts("-");
09  pl011_addr((char *)(m+1), 1);
10
11  pl011_puts("Clearing edata: ");
12  memset(edata, 0, end-edata);
13  pl011_addr((char *)&edata, 0); pl011_puts("-");
14  pl011_addr((char *)&end, 1);
15
16  conf.nmach = 1;
17  serwrite = &pl011_serputs;
18
19  confinit();
20  xinit();
21  poolinit();
22  poolsizeinit();
23
24  pl011_puts("to infinite loop\n\n");
25  for (;;);
```

Function *pl011_addr()* was created in the way that it can print address even when you have data segment broken (would be useful in debugging/tracing):

```
01  void
02  pl011_addr(void *a, int nl)
```

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```

03 {
04     int i;
05     unsigned char *ca = (unsigned char *)&a;
06     unsigned char h,l;
07
08     for (i=3;i>=0;--i) {
09         h = ca[i]/16;
10         l = ca[i]%16;
11         pl011_putc(h<10 ? h+0x30 : h-10+0x41);
12         pl011_putc(l<10 ? l+0x30 : l-10+0x41);
13     }
14     if (nl) {
15         pl011_putc(13);
16         pl011_putc(10);
17     }
18 }

```

So, that was a small lab to test everything that coded before with some small polishing.

Executing:

```

01 ...
02 TFTP from server 10.0.55.112; our IP address is 10.0.55.105
03 Filename 'irpi'.
04 Load address: 0x7fe0
05 Loading: T #T #####
06 done
07 Bytes transferred = 543966 (84cde hex)
08 ## Starting application at 0x00008000 ...
09 Entered main() at 00008404 with SP=00002FEC
10 Clearing Mach: 00002000-00002018
11 Clearing edata: 00064638-0006B760
12 Conf: top=134217728, npage0=32660, ialloc=26755072, nproc=735
13 to infinite loop

```

FILES:

- [rpi](#)
- [mkfile](#)
- [fns.h](#)
- [mem.h](#)
- [load.s](#)
- [armv6.s](#)
- [main.c](#)
- [plo11.c](#)

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