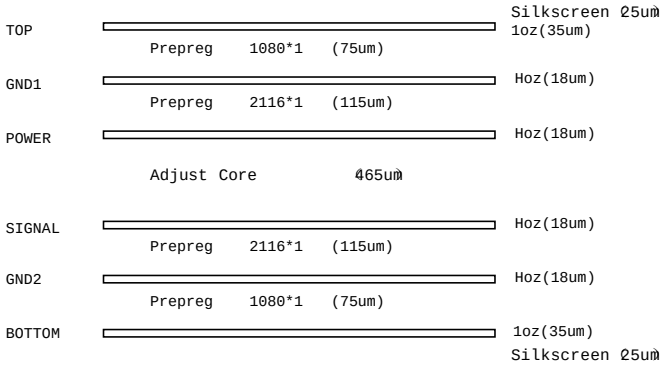
# RK3288 Tablet REF V1.7

## CONTENT INDEXING



## 6 LAYERS PCB STACK (e.g. PCB=1.0mm)

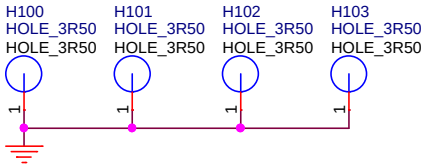



**Note:**

1: If the Value or option of the component properties is DNP indicating do not paste.

2: ES8316/ES8323/ALC4531 is single channel I2S audio codec, does not support BT voice and 4G voice;

3: ALC5672 supports BT voice with DSP, ALC5640 supports BT voice and 4G voice;

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# Change List

| Version | Date | Author | Change Note | Approved |
|---|---|---|---|---|
| V1.0 | 2014.04.29 | Linus.Lin | First edition | |
| V1.1 | 2014.06.25 | Linus.Lin | Please refer to document of 《RK3288 硬件 设计 图修 改点 V110402》 | |
| V1.2 | 2014.08.06 | Linus.Lin | Please refer to document of 《RK3288 硬件 设计 图修 改点 V120400》 | |
| V1.3 | 2014.12.10 | Linus.Lin | Please refer to document of 《RK3288 硬件 设计 图修 改点 V130412》 | |
| V1.4 | 2016.02.18 | Linus.Lin | Please refer to document of 《RK3288_Tablet_REF_V14_20160218 Modify Note》 | |
| V1.5 | 2016.08.10 | Linus.Lin | Modify the error of Power-RK808 design VCC_SYS does not have output;<br>Please refer to document of 《RK3288_Tablet_REF_V15_20160810 Modify Note》 | |
| V1.6 | 2016.09.07 | Linus.Lin | Add ALC5672 circuit for option;<br>Please refer to document of 《RK3288_Tablet_REF_V16_20160907 Modify Note》 | |
| V1.7 | 2017.10.20 | Linus.Lin | Please refer to document of 《RK3288(TABLET) 硬件 设计 布 说明 文件 列 表_V17_20171020》 | |

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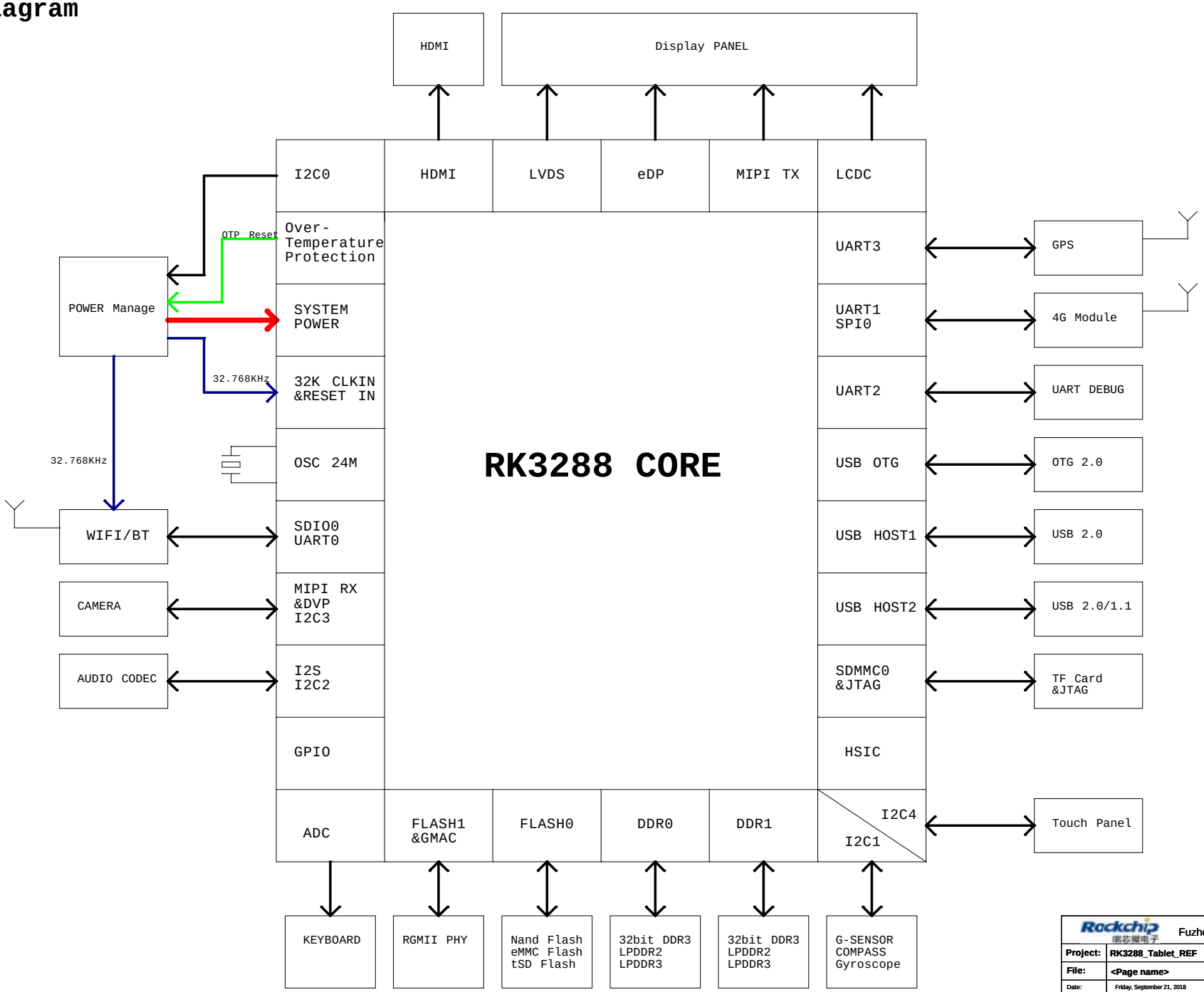
# Block Diagram

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# I2C MAP

| Port | Pin name | Domain | Bus name | Pull-up voltage | Slave Device | Slave Addr (MS 7Bits) | Note | Slave Bus Capability |
|---|---|---|---|---|---|---|---|---|
| I2C0 | I2C0_SDA/PMUGPIO0_B7<br>I2C0_SCL/PMUGPIO0_C0 | PMUIO | I2C0_SDA_PMIC<br>I2C0_SCL_PMIC | VCCIO_PMU | Rockchip RK808 | 0x1b | PMIC | 100kHz,400KHz |
| | | | | | Rockchip RK818 | 0x1c | PMIC | 100kHz,400KHz |
| | | | | | SYR837PKC | 0x40 | DC-DC BUCK | 100kHz,400KHz,3.4MHz |
| | | | | | SYR838PKC | 0x41 | DC-DC BUCK | 100kHz,400KHz,3.4MHz |
| | | | | | CW2013 | 0x62 | Fuel Gauge | 100kHz,400KHz,1MHz |
| I2C1 | I2C1_SDA/SC_RST/GPIO8_A4<br>I2C1_SCL/SC_CLK/GPIO8_A5 | APIO2 | I2C1_SDA_sensor<br>I2C1_SCL_sensor | VCC_IO | CM3218 | 0x10 | LIGHT-Sensor | 100kHz,400KHz |
| | | | | | AK8963C | 0x0d | Compass | 100kHz,400KHz |
| | | | | | MMA8452Q | 0x1d | Accelerometer | 100kHz,400KHz |
| | | | | | LIS3DH | 0x19 | Accelerometer | 100kHz,400KHz |
| | | | | | LSM303D | 0x1d | Accelerometer | 100kHz,400KHz |
| | | | | | LSM330TR | G:0x6a A:0x1e | Gyroscope+Accelerometer | 100kHz,400KHz |
| | | | | | MPU6500 | 0x68 | Gyroscope+Accelerometer | 100kHz,400KHz |
| I2C2 | I2C2_SDA/GPIO6_B1<br>I2C2_SCL/GPIO6_B2 | APIO4 | I2C2_SDA_AUDIO<br>I2C2_SCL_AUDIO | VCCIO_CODEC | ALC5640/ALC5631 | 0x1c | Audio Codec | 100kHz,400KHz |
| | | | | | ALC5672 | 0x1c | Audio Codec | 100kHz,400KHz |
| | | | | | ES8316/ES8323 | 0x10 | Audio Codec | 100kHz |
| I2C3 | I2C3_SCL/GPIO2_C0<br>I2C3_SDA/GPIO2_C1 | DVPIO | I2C3_SDA_CAM<br>I2C3_SCL_CAM | VCC18_DVP | OV2659 | 0x30 | Image Sensor | 100kHz,400KHz |
| | | | | | OV8825 | 0x36 | Image Sensor | 100kHz,400KHz |
| I2C4 | I2C4_SDA/GPIO7_C1<br>I2C4_SCL/GPIO7_C2 | APIO1 | I2C4_SDA_TP<br>I2C4_SCL_TP | VCCIO_PMU | CT363 | 0x1b | Touch IC | 100kHz,400KHz |
| | | | | | FT5506 | | Touch IC | 100kHz,400KHz |
| | | | | | GSL3680 | 0x40 | Touch IC | 100kHz,400KHz |
| I2C5 | I2C5_SDA/EDPHDMI_I2C_SDA/GPIO7_C3<br>I2C5_SCL/EDPHDMI_I2C_SCL/GPIO7_C4 | APIO1 | I2C5_SDA_HDMI<br>I2C5_SCL_HDMI | VCCIO_PMU | | | | |

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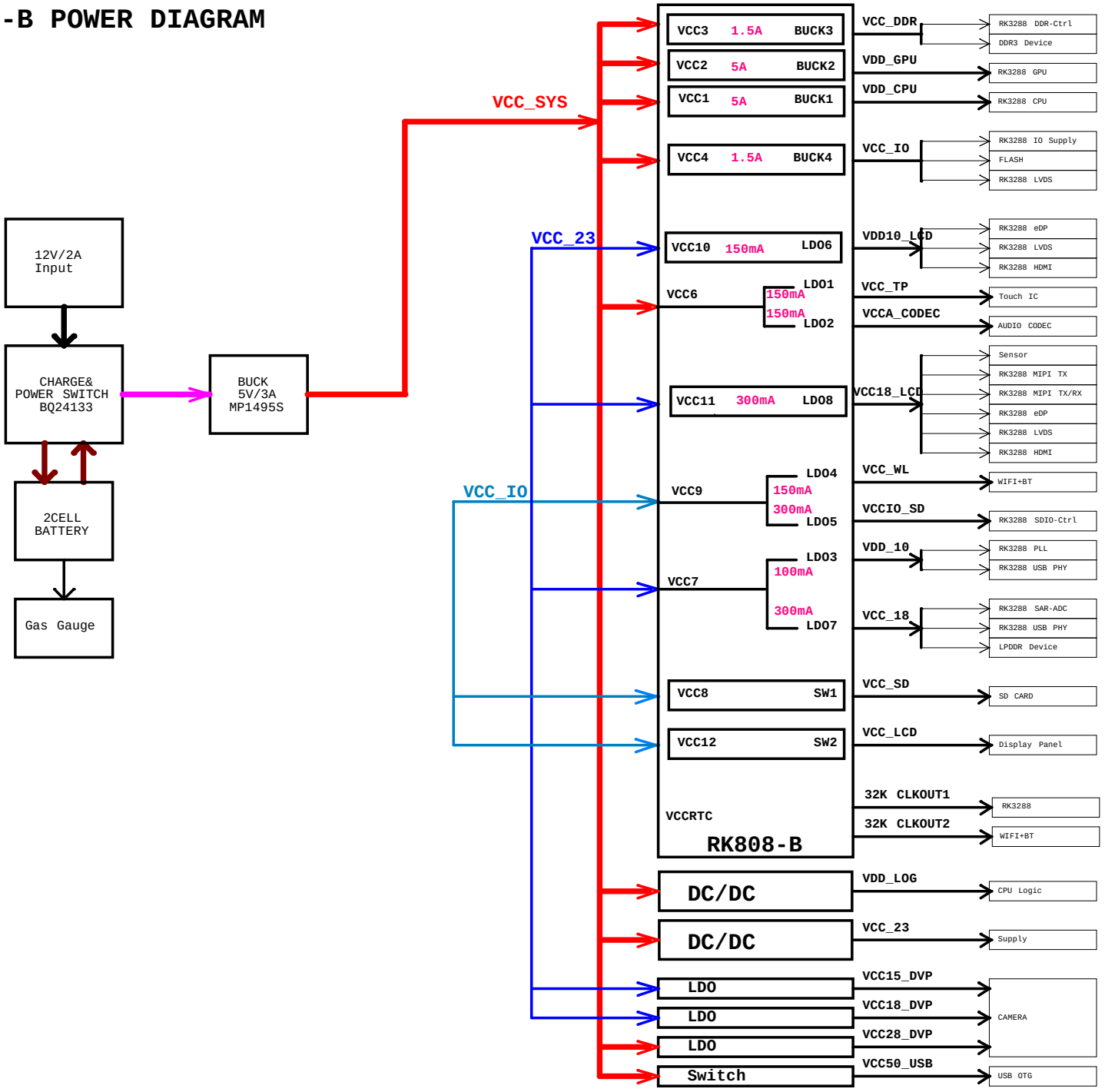
# RK808-B POWER DIAGRAM




| PowerName | PMIC Channel | TIMER (2mS) | default ON/OFF | default voltage | sleep ON/OFF |
|---|---|---|---|---|---|
| VDD_10 | VLDO3 | solt:1 | ON | 1.0V | ON |
| VDD_CPU | DCDC1 | solt:2 | ON | 1.1V | OFF |
| VDD_GPU | DCDC2 | solt:3 | ON | 1.1V | OFF |
| VCC_DDR | DCDC3 | solt:3 | ON | DDR3 1.5V<br>DDR3L 1.35V<br>LPDDR 1.25V | ON |
| VCC_LOG | EX_DCDC | solt:3A | ON | 1.1V | ON |
| VCC_18 | VLDO7 | solt:3 | ON | 1.8V | ON |
| VCC_TP | VLDO1 | solt:4 | ON | 1.8V | ON |
| VCC_IO | DCDC4 | solt:4 | ON | 3.3V | ON |
| VCCIO_SD | VLDO5 | solt:5 | ON | 3.3V | OFF |
| VCC_SD | VSWOUT1 | solt:5 | ON | 3.3V | OFF |
| VDD10_LCD | VLDO6 | | OFF | 1.0V | OFF |
| VCC18_LCD | VLDO8 | | OFF | 1.8V | OFF |
| VCCA_CODEC | VLDO2 | | OFF | 3.3V | OFF |
| VCC_WL | VLDO4 | | OFF | 1.8V | OFF |
| VCC_LCD | VSWOUT2 | | OFF | 3.3V | OFF |

| | | | |
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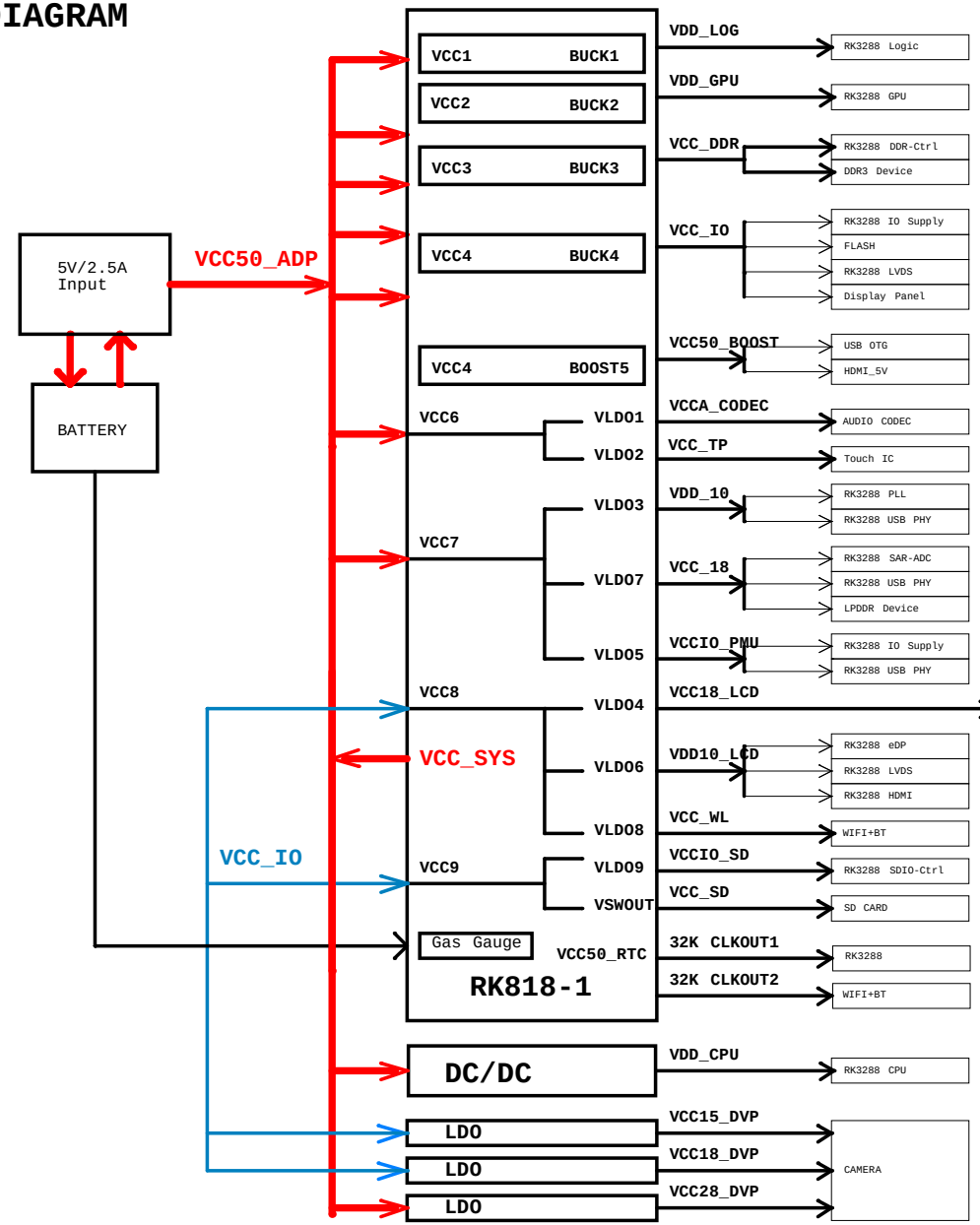
# RK818-1 POWER DIAGRAM




| PowerName | PMU Channel | TIMER (2mS) | OutPut voltage |
|---|---|---|---|
| VCC_LOG | DCDC1 | solt:3 | 1.1V |
| VDD_GPU | DCDC2 | solt:2 | 1.1V |
| VCC_DDR | DCDC3 | solt:4 | 1.0V |
| VCC_IO | DCDC4 | solt:7 | 3.3V |
| VDD_ARM | ExDCDC1 | solt:4 | 1.1V |
| VCCA_CODEC | VLDO1 | OFF | 3.3V |
| VCCA_TP | VLDO2 | OFF | 3.3V |
| VDD_10 | VLDO3 | solt:1 | 1.0V |
| VCC18_LCD | VLDO4 | OFF | 1.8V |
| VCCIO_PMU | VLDO5 | solt:6 | 1.8V |
| VDD10_LCD | VLDO6 | OFF | 1.0V |
| VCC_18 | VLDO7 | solt:5 | 1.8V |
| VCC_WL | VLDO8 | OFF | 1.8V |
| VCCIO_SD | VLDO9 | solt:8 | 3.3V |
| VCC_SD | VSW1 | solt:8 | 3.3V |
| RESET | 16*2mS+50mS | | |

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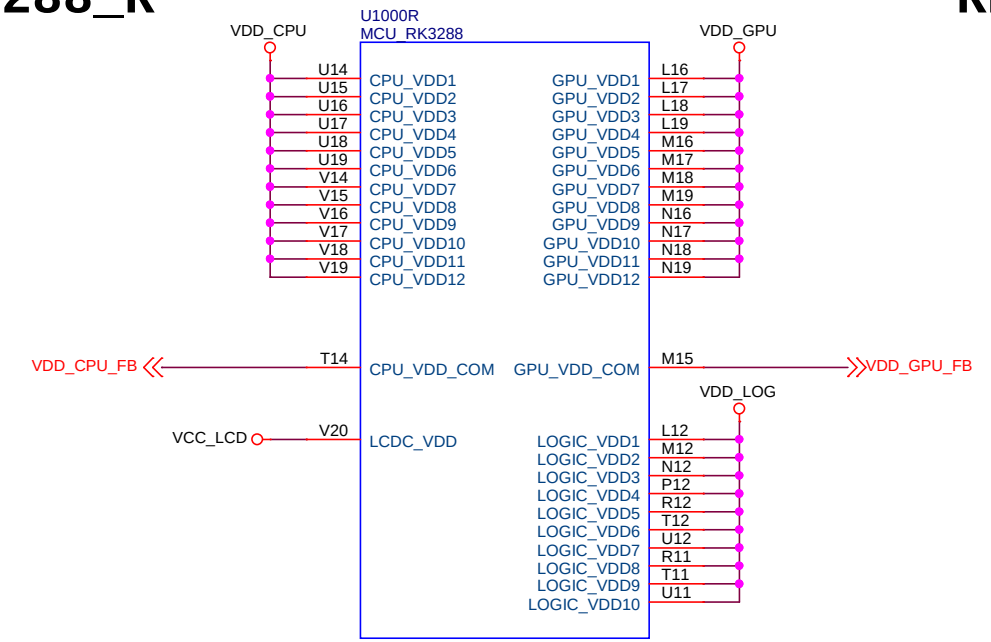
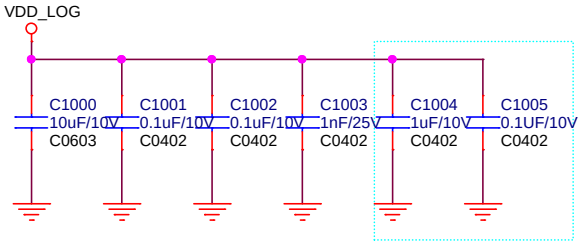
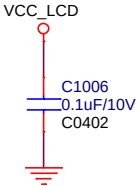
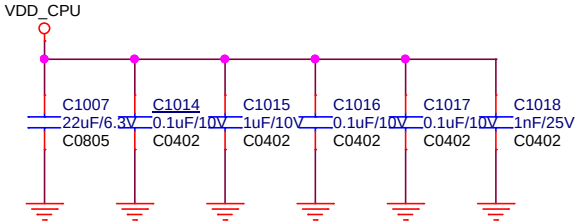
# RK3288_R

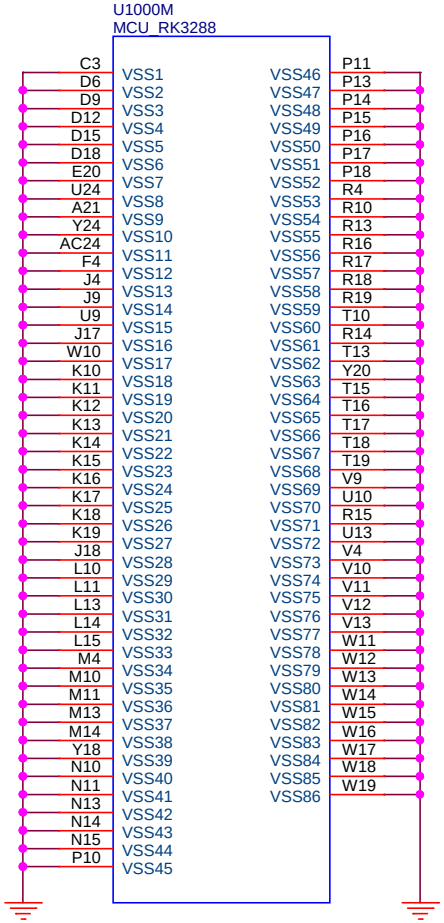


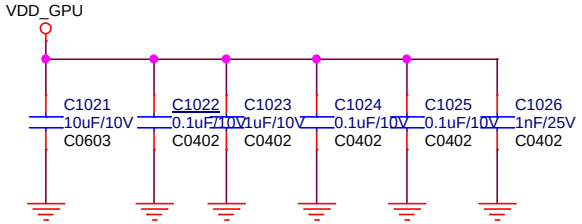




# RK3288_M




## RK3288 Power Filter

Note:All the capacitor should be place close to the power pin of RK3288。

| | | | |
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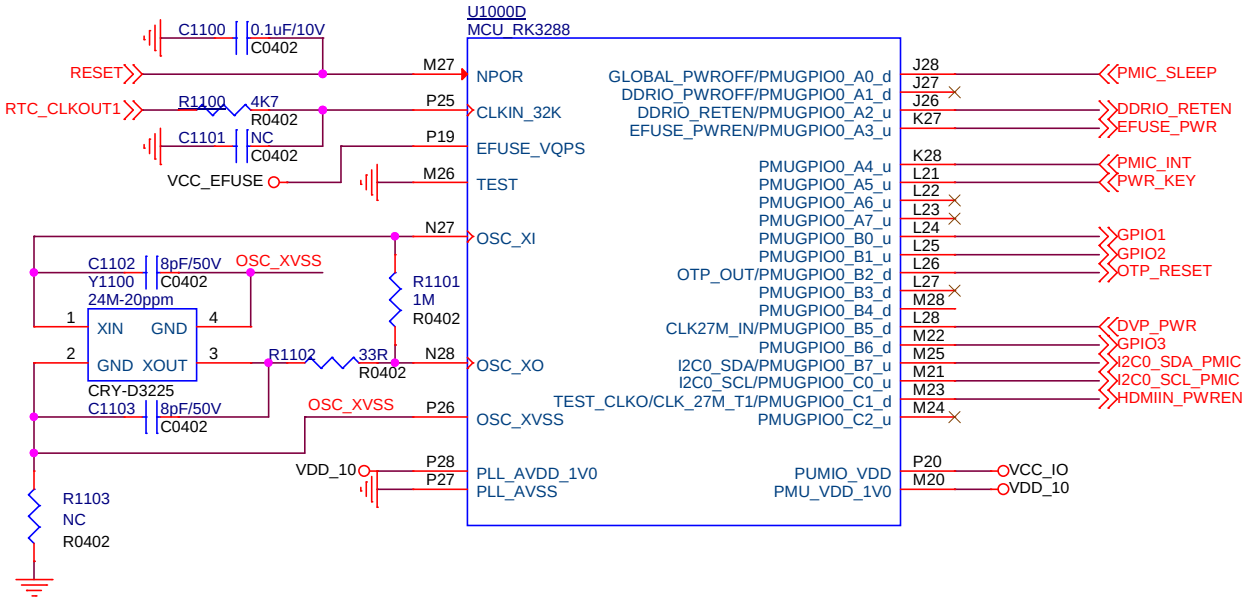
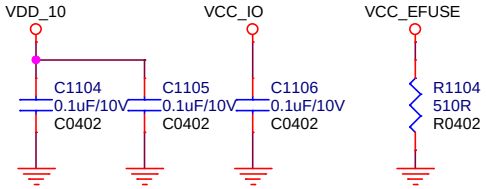
# RK3288_D




Note:All the capacitor should be place close to the power pin of RK3288。

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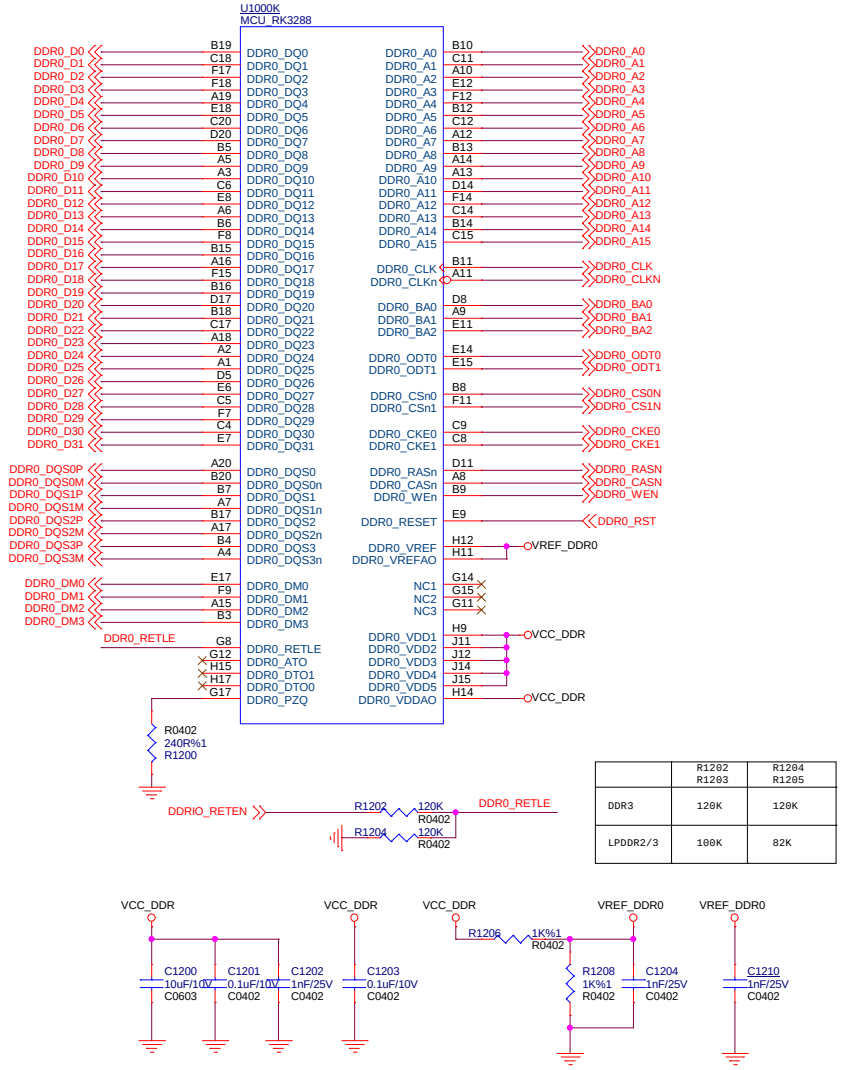
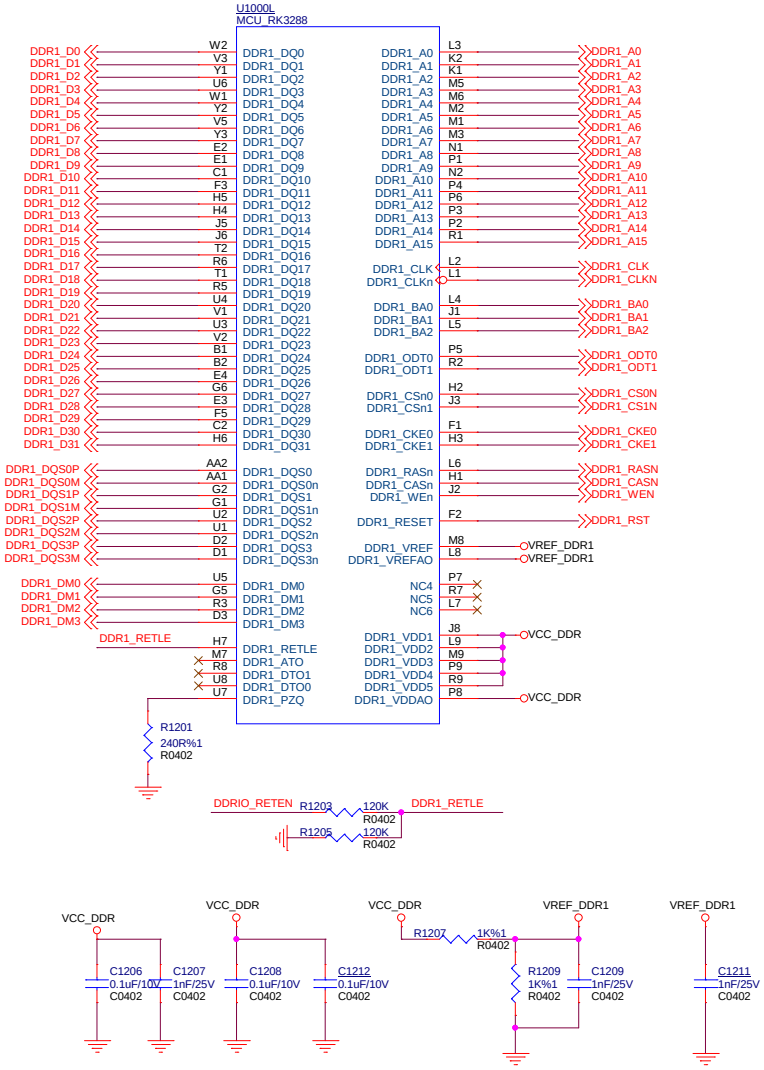
# RK3288_K DDR Channel-0




| | R1202 R1203 | R1204 R1205 |
|---|---|---|
| DDR3 | 120K | 120K |
| LPDDR2/3 | 100K | 82K |

Note:All the capacitor should be place close to the power pin of RK3288。

# RK3288_L DDR Channel-1




Note:All the capacitor should be place close to the power pin of RK3288。

| | | | |
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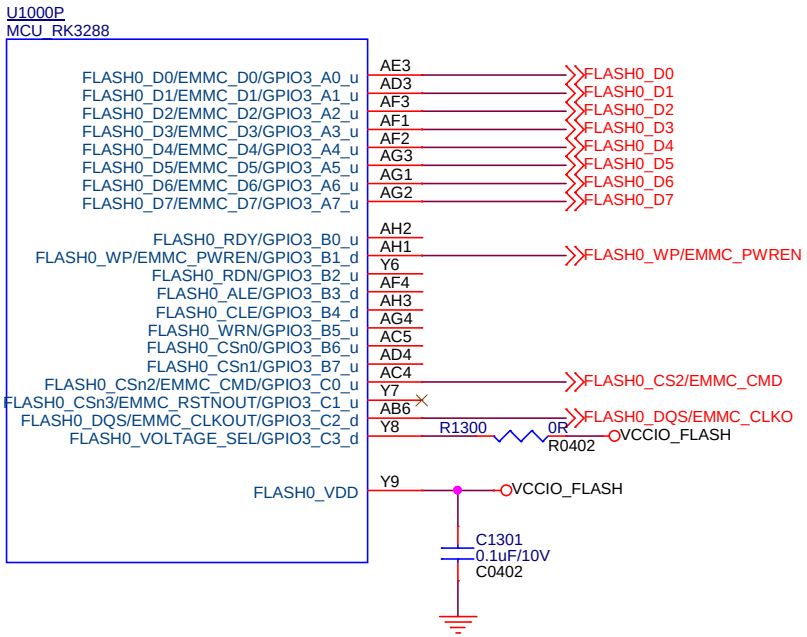
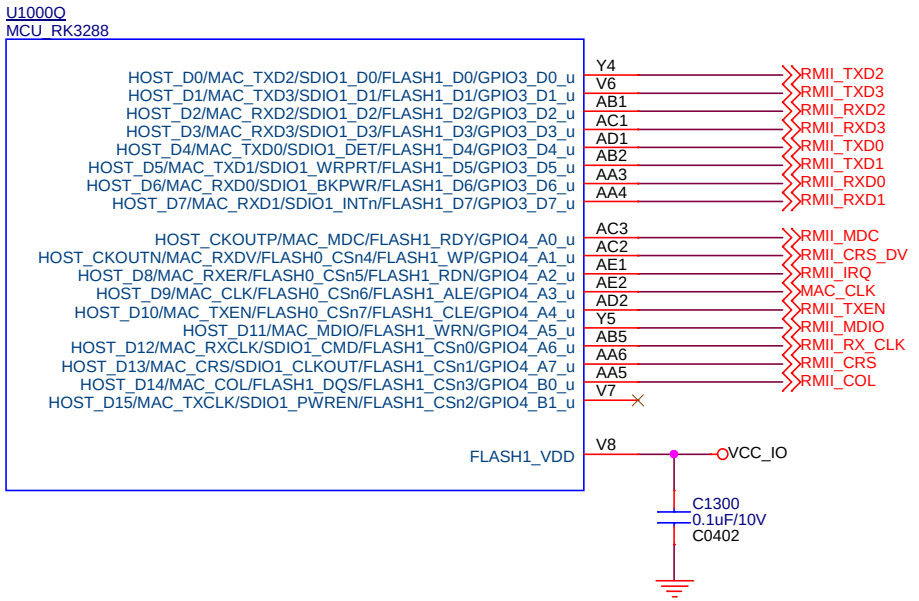
# RK3288_P




Note:All the capacitor should be place close to the power pin of RK3288。

| | VCCIO_FLASH=1.8V | VCCIO_FLASH=3.3V |
|---|---|---|
| FLASH0_VOLTAGE_SEL pin connect to | VCCIO_FLASH | Floating(Default) |
| R1300 | 0R | DNP |

# RK3288_Q




Note:All the capacitor should be place close to the power pin of RK3288。

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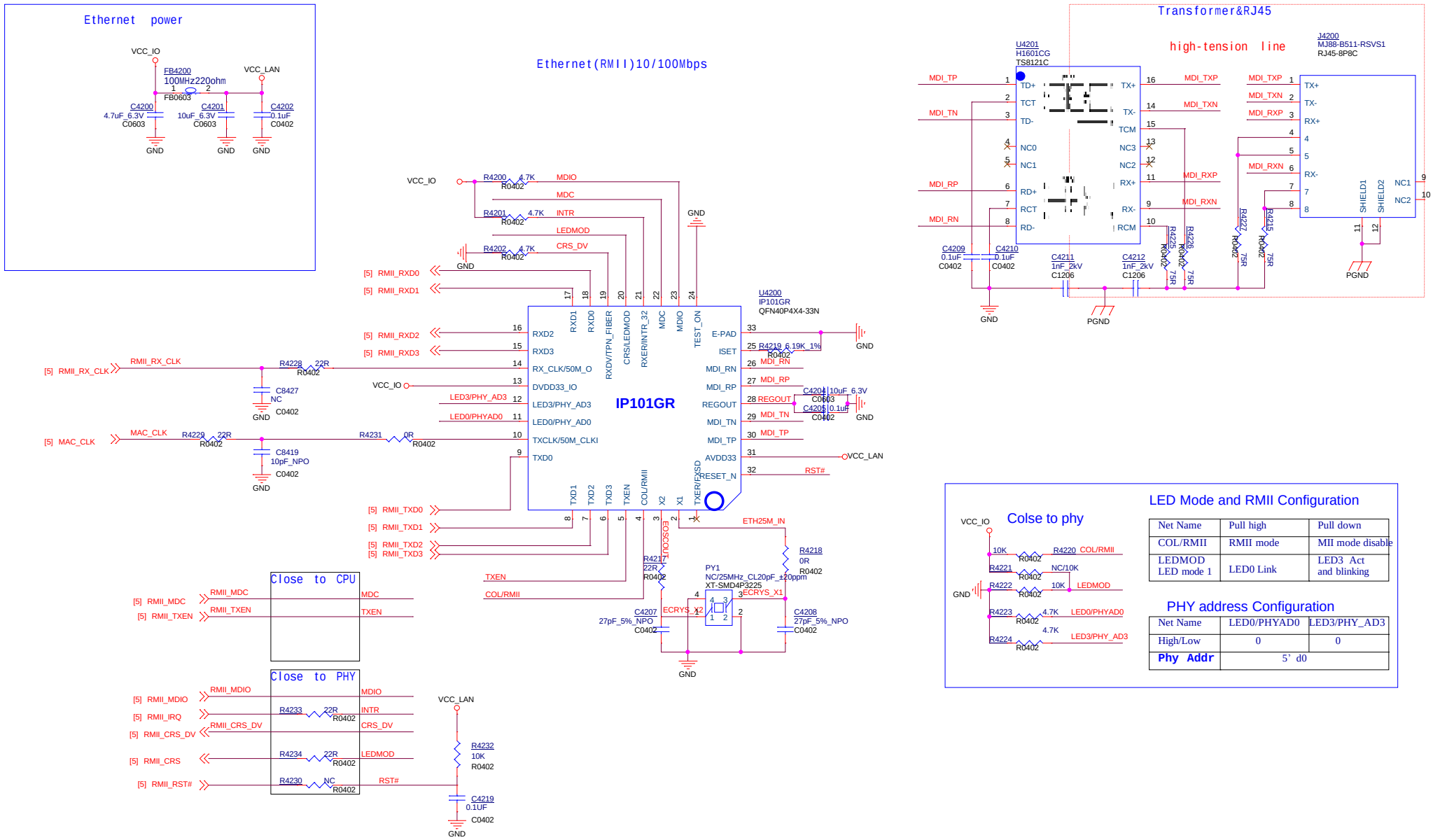
[5] RMII_RXD2

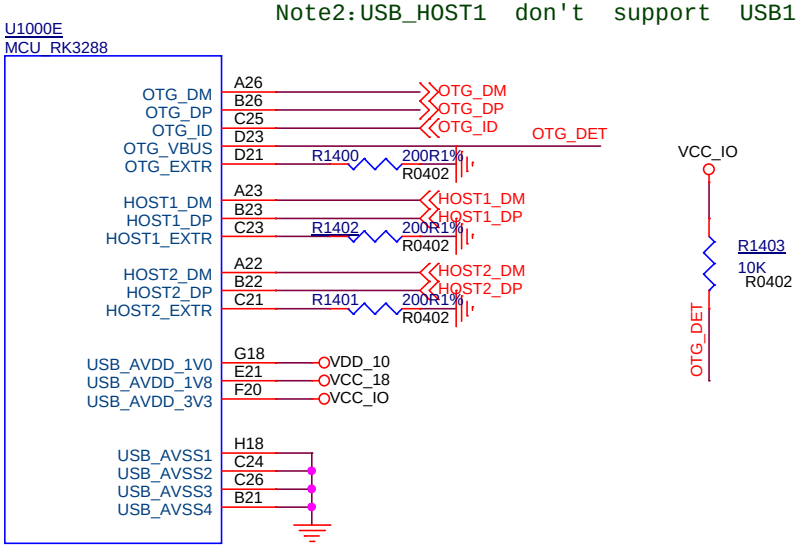



| Net Name | Pull high | Pull down |
|---|---|---|
| COL/RMII | RMII mode | MII mode disable |
| LEDMOD<br>LED mode 1 | LED0 Link | LED3 Act<br>and blinking |

| Net Name | LED0/PHYAD0 | LED3/PHY_AD3 |
|---|---|---|
| High/Low | 0 | 0 |
| **Phy Addr** | 5' d0 | |

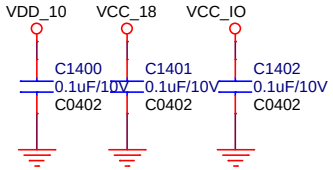
# RK3288_E

Note2:USB_HOST1 don't support USB1.1;



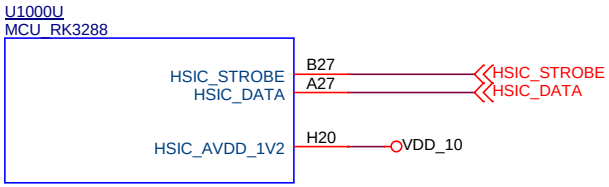







Note:All the capacitor should be place close to the power pin of RK3288。

# RK3288_U

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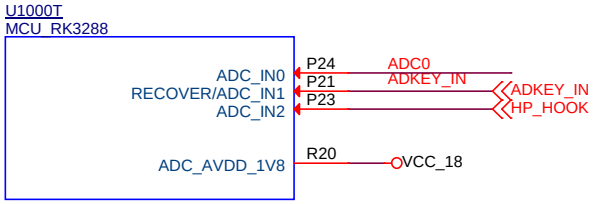
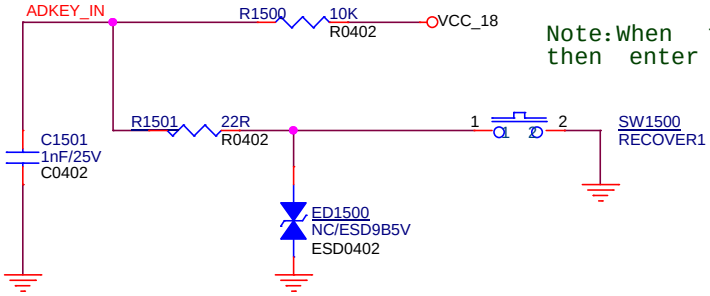
# RK3288_T

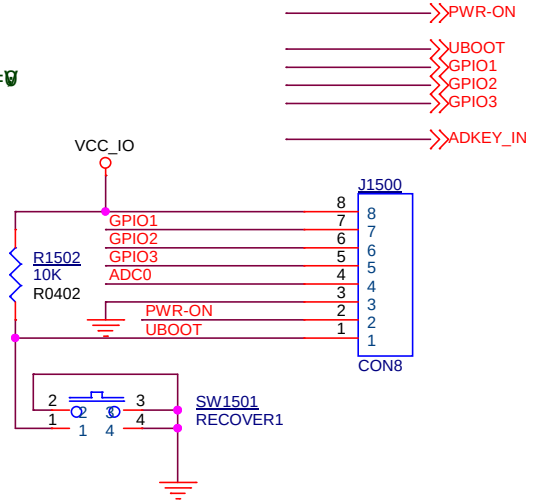



Note:All the capacitor should be place close to the power pin of RK3288。

# KEY




Note:When the system power on if ADKEY_IN=0 then enter Recovery mode;

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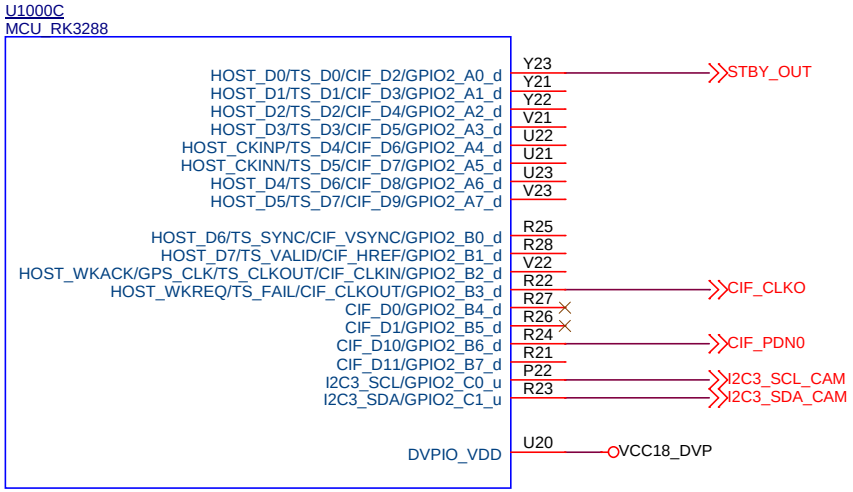
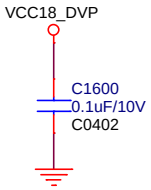
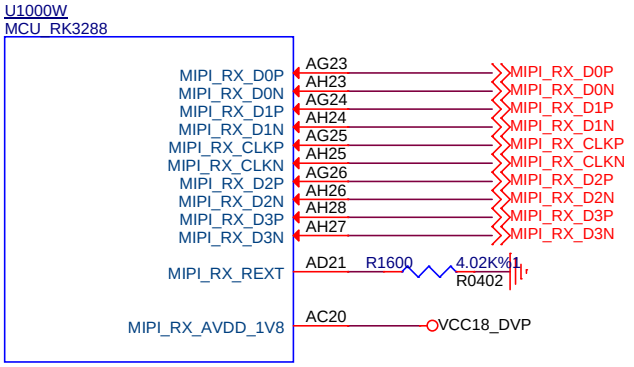
# RK3288_C

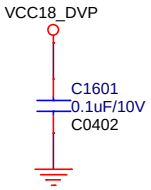



Note:All the capacitor should be place close to the power pin of RK3288。

# RK3288_W




| | | | |
|---|---|---|---|
| Rockchip 瑞芯微电子 | Fuzhou Rockchip Electronics | | |
| Project: | RK3288_Tablet_REF | | |
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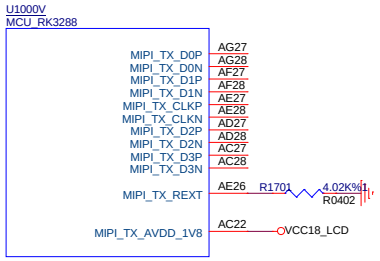
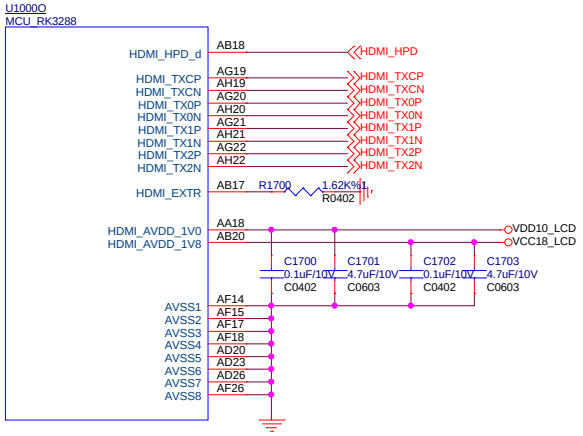
## RK3288_V

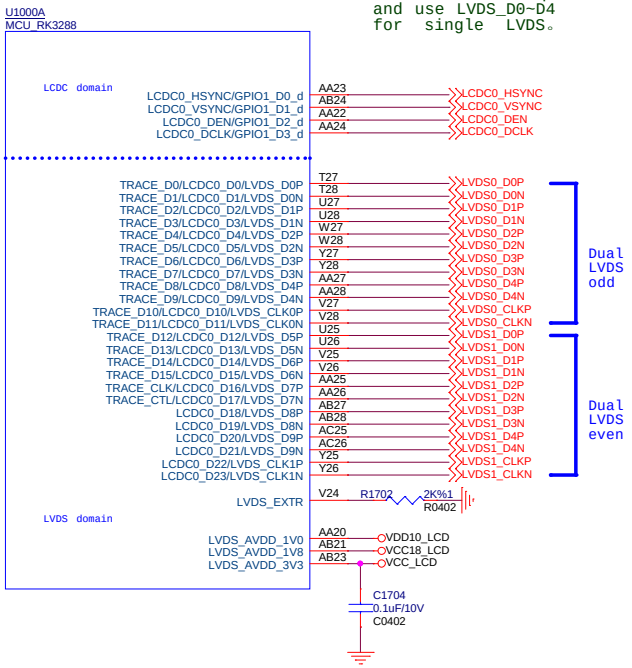



## RK3288_O

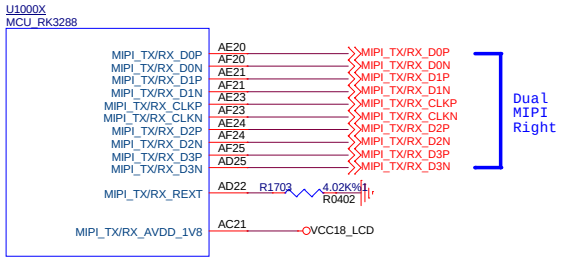



## RK3288_A

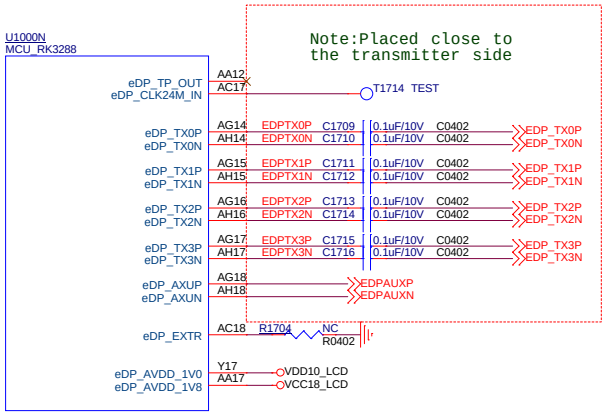
Note:Please hold the connection in sequence and use LVDS_D0~D4 for single LVDS。




## RK3288_X




## RK3288_N




Note:All the capacitor should be place close to the power pin of RK3288。

Correspondence between LCDC DATA and RGB

| | | | | | |
|---|---|---|---|---|---|
| LCDC_D0 | B0 | LCDC_D8 | G0 | LCDC_D16 | R0 |
| LCDC_D1 | B1 | LCDC_D9 | G1 | LCDC_D17 | R1 |
| LCDC_D2 | B2 | LCDC_D10 | G2 | LCDC_D18 | R2 |
| LCDC_D3 | B3 | LCDC_D11 | G3 | LCDC_D19 | R3 |
| LCDC_D4 | B4 | LCDC_D12 | G4 | LCDC_D20 | R4 |
| LCDC_D5 | B5 | LCDC_D13 | G5 | LCDC_D21 | R5 |
| LCDC_D6 | B6 | LCDC_D14 | G6 | LCDC_D22 | R6 |
| LCDC_D7 | B7 | LCDC_D15 | G7 | LCDC_D23 | R7 |

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| | | | |
|---|---|---|---|
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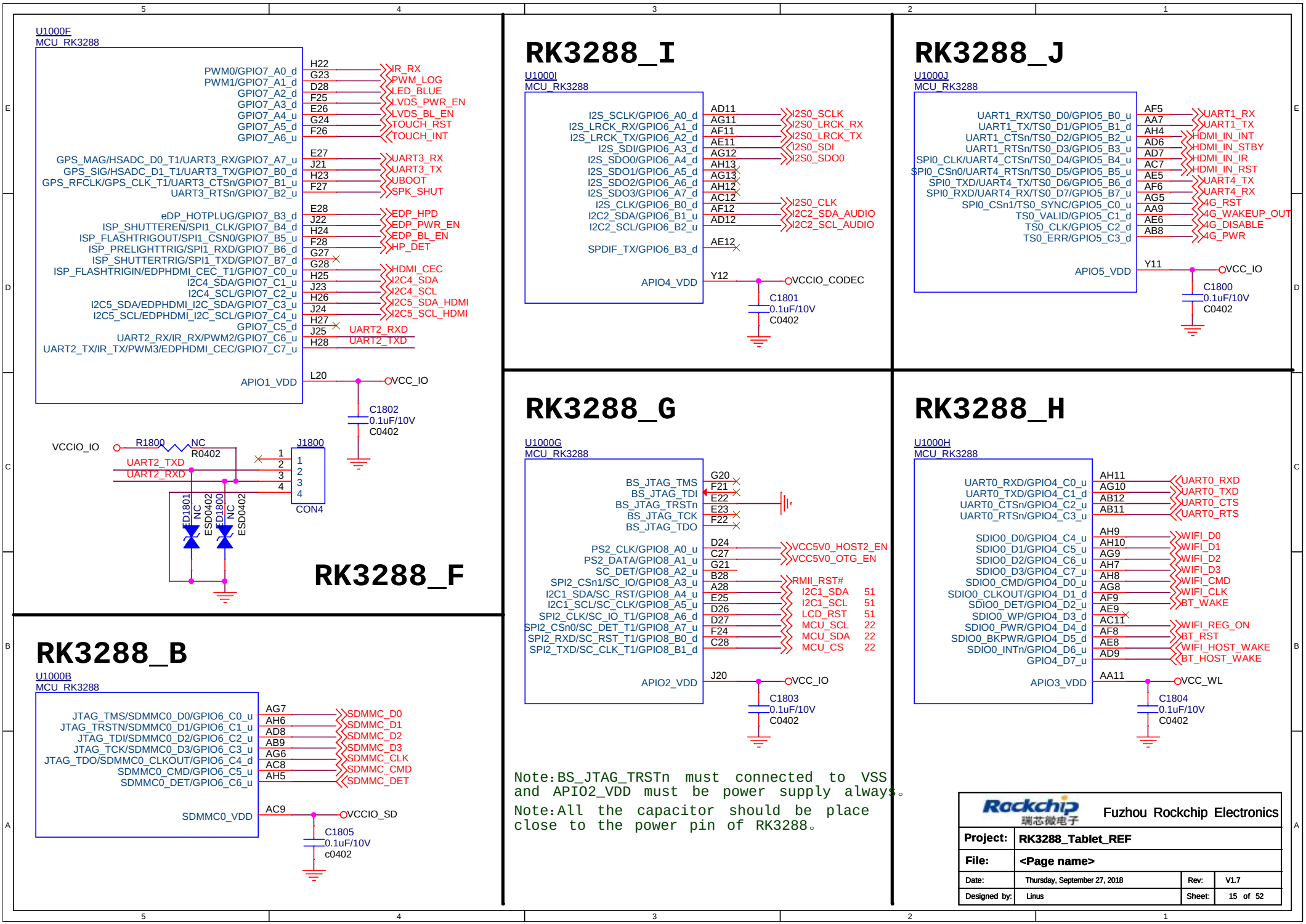
U1000F
MCU_RK3288
PWM0/GPIO7_A0_d
PWM1/GPIO7_A1_d
GPIO7_A2_d
GPIO7_A3_d
GPIO7_A4_u
GPIO7_A5_d
GPIO7_A6_u
H22
G23
D28
F25
E26
G24
F26
IR_RX
PWM_LOG
LED_BLUE
LVDS_PWR_EN
LVDS_BL_EN
TOUCH_RST
TOUCH_INT
GPS_MAG/HSADC_D0_T1/UART3_RX/GPIO7_A7_u
GPS_SIG/HSADC_D1_T1/UART3_TX/GPIO7_B0_d
GPS_RFCLK/GPS_CLK_T1/UART3_CTSn/GPIO7_B1_u
UART3_RTSn/GPIO7_B2_u
E27
J21
H23
F27
UART3_RX
UART3_TX
UBOOT
SPK_SHUT
eDP_HOTPLUG/GPIO7_B3_d
ISP_SHUTTEREN/SPI1_CLK/GPIO7_B4_d
ISP_FLASHTRIGOUT/SPI1_CSN0/GPIO7_B5_u
ISP_PRELIGHTTRIG/SPI1_RXD/GPIO7_B6_d
ISP_SHUTTERTRIG/SPI1_TXD/GPIO7_B7_d
ISP_FLASHTRIGIN/EDPHDMI_CEC_T1/GPIO7_C0_u
I2C4_SDA/GPIO7_C1_u
I2C4_SCL/GPIO7_C2_u
I2C5_SDA/EDPHDMI_I2C_SDA/GPIO7_C3_u
I2C5_SCL/EDPHDMI_I2C_SCL/GPIO7_C4_u
GPIO7_C5_d
UART2_RX/IR_RX/PWM2/GPIO7_C6_u
UART2_TX/IR_TX/PWM3/EDPHDMI_CEC/GPIO7_C7_u
E28
J22
H24
F28
G27
G28
H25
J23
H26
J24
H27
J25
H28
EDP_HPD
EDP_PWR_EN
EDP_BL_EN
HP_DET
HDMI_CEC
I2C4_SDA
I2C4_SCL
I2C5_SDA_HDMI
I2C5_SCL_HDMI
UART2_RXD
UART2_TXD
APIO1_VDD
L20
VCC_IO
C1802
0.1uF/10V
C0402
VCCIO_IO
R1800
NC
R0402
UART2_TXD
UART2_RXD
J1800
CON4
ESD1801
NC
ESD0402
ESD1800
NC
ESD0402
RK3288_F
RK3288_I
U1000I
MCU_RK3288
I2S_SCLK/GPIO6_A0_d
I2S_LRCK_RX/GPIO6_A1_d
I2S_LRCK_TX/GPIO6_A2_d
I2S_SDI/GPIO6_A3_d
I2S_SDO0/GPIO6_A4_d
I2S_SDO1/GPIO6_A5_d
I2S_SDO2/GPIO6_A6_d
I2S_SDO3/GPIO6_A7_d
I2S_CLK/GPIO6_B0_d
I2C2_SDA/GPIO6_B1_u
I2C2_SCL/GPIO6_B2_u
SPDIF_TX/GPIO6_B3_d
AD11
AG11
AF11
AE11
AG12
AH13
AG13
AH12
AC12
AF12
AD12
AE12
I2S0_SCLK
I2S0_LRCK_RX
I2S0_LRCK_TX
I2S0_SDI
I2S0_SDO0
I2S0_CLK
I2C2_SDA_AUDIO
I2C2_SCL_AUDIO
APIO4_VDD
Y12
VCCIO_CODEC
C1801
0.1uF/10V
C0402
RK3288_J
U1000J
MCU_RK3288
UART1_RX/TS0_D0/GPIO5_B0_u
UART1_TX/TS0_D1/GPIO5_B1_d
UART1_CTSn/TS0_D2/GPIO5_B2_u
UART1_RTSn/TS0_D3/GPIO5_B3_u
SPI0_CLK/UART4_CTSn/TS0_D4/GPIO5_B4_u
SPI0_CSn0/UART4_RTSn/TS0_D5/GPIO5_B5_u
SPI0_TXD/UART4_TX/TS0_D6/GPIO5_B6_d
SPI0_RXD/UART4_RX/TS0_D7/GPIO5_B7_u
SPI0_CSn1/TS0_SYNC/GPIO5_C0_u
TS0_VALID/GPIO5_C1_d
TS0_CLK/GPIO5_C2_d
TS0_ERR/GPIO5_C3_d
AF5
AA7
AH4
AD6
AD7
AC7
AE5
AF6
AG5
AA9
AE6
AB8
UART1_RX
UART1_TX
HDMI_IN_INT
HDMI_IN_STBY
HDMI_IN_IR
HDMI_IN_RST
UART4_TX
UART4_RX
4G_RST
4G_WAKEUP_OUT
4G_DISABLE
4G_PWR
APIO5_VDD
Y11
VCC_IO
C1800
0.1uF/10V
C0402
RK3288_G
U1000G
MCU_RK3288
BS_JTAG_TMS
BS_JTAG_TDI
BS_JTAG_TRSTn
BS_JTAG_TCK
BS_JTAG_TDO
G20
F21
E22
E23
F22
PS2_CLK/GPIO8_A0_u
PS2_DATA/GPIO8_A1_u
SC_DET/GPIO8_A2_u
SPI2_CSn1/SC_IO/GPIO8_A3_u
I2C1_SDA/SC_RST/GPIO8_A4_u
I2C1_SCL/SC_CLK/GPIO8_A5_u
SPI2_CLK/SC_IO_T1/GPIO8_A6_d
SPI2_CSn0/SC_DET_T1/GPIO8_A7_u
SPI2_RXD/SC_RST_T1/GPIO8_B0_d
SPI2_TXD/SC_CLK_T1/GPIO8_B1_d
D24
C27
G21
B28
A28
E25
D26
D27
F24
C28
VCC5V0_HOST2_EN
VCC5V0_OTG_EN
RMII_RST#
I2C1_SDA 51
I2C1_SCL 51
LCD_RST 51
MCU_SCL 22
MCU_SDA 22
MCU_CS 22
APIO2_VDD
J20
VCC_IO
C1803
0.1uF/10V
C0402
RK3288_H
U1000H
MCU_RK3288
UART0_RXD/GPIO4_C0_u
UART0_TXD/GPIO4_C1_d
UART0_CTSn/GPIO4_C2_u
UART0_RTSn/GPIO4_C3_u
AH11
AG10
AB12
AB11
UART0_RXD
UART0_TXD
UART0_CTS
UART0_RTS
SDIO0_D0/GPIO4_C4_u
SDIO0_D1/GPIO4_C5_u
SDIO0_D2/GPIO4_C6_u
SDIO0_D3/GPIO4_C7_u
SDIO0_CMD/GPIO4_D0_u
SDIO0_CLKOUT/GPIO4_D1_d
SDIO0_DET/GPIO4_D2_u
SDIO0_WP/GPIO4_D3_d
SDIO0_PWR/GPIO4_D4_d
SDIO0_BKPWR/GPIO4_D5_d
SDIO0_INTn/GPIO4_D6_u
GPIO4_D7_u
AH9
AH10
AG9
AH7
AH8
AG8
AF9
AE9
AC11
AF8
AE8
AD9
WIFI_D0
WIFI_D1
WIFI_D2
WIFI_D3
WIFI_CMD
WIFI_CLK
BT_WAKE
WIFI_REG_ON
BT_RST
WIFI_HOST_WAKE
BT_HOST_WAKE
APIO3_VDD
AA11
VCC_WL
C1804
0.1uF/10V
C0402
RK3288_B
U1000B
MCU_RK3288
JTAG_TMS/SDMMC0_D0/GPIO6_C0_u
JTAG_TRSTN/SDMMC0_D1/GPIO6_C1_u
JTAG_TDI/SDMMC0_D2/GPIO6_C2_u
JTAG_TCK/SDMMC0_D3/GPIO6_C3_u
JTAG_TDO/SDMMC0_CLKOUT/GPIO6_C4_d
SDMMC0_CMD/GPIO6_C5_u
SDMMC0_DET/GPIO6_C6_u
AG7
AH6
AD8
AB9
AG6
AC8
AH5
SDMMC_D0
SDMMC_D1
SDMMC_D2
SDMMC_D3
SDMMC_CLK
SDMMC_CMD
SDMMC_DET
SDMMC0_VDD
AC9
VCCIO_SD
C1805
0.1uF/10V
c0402
Note:BS_JTAG_TRSTn must connected to VSS and APIO2_VDD must be power supply always。
Note:All the capacitor should be place close to the power pin of RK3288。
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Fuzhou Rockchip Electronics
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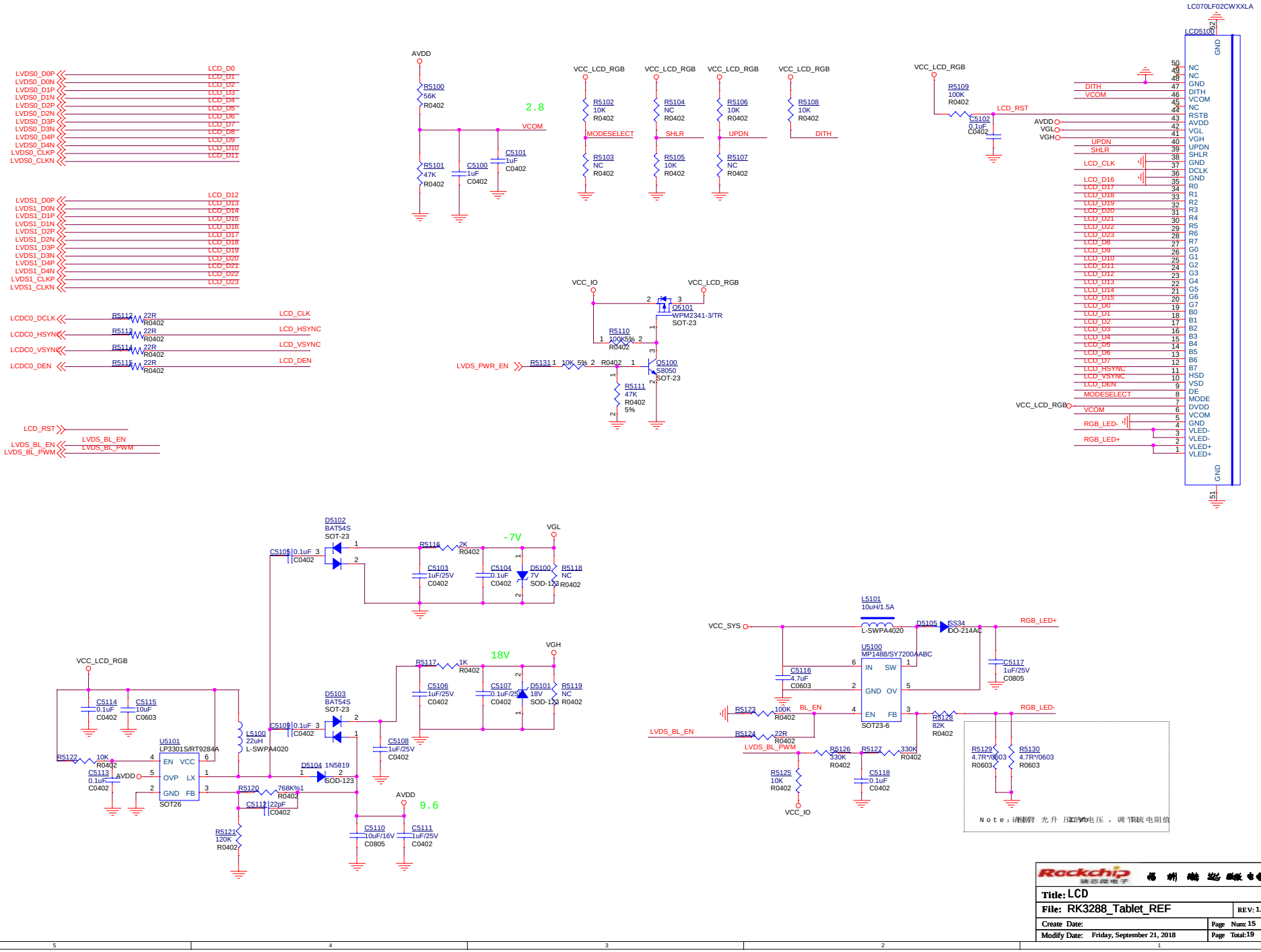
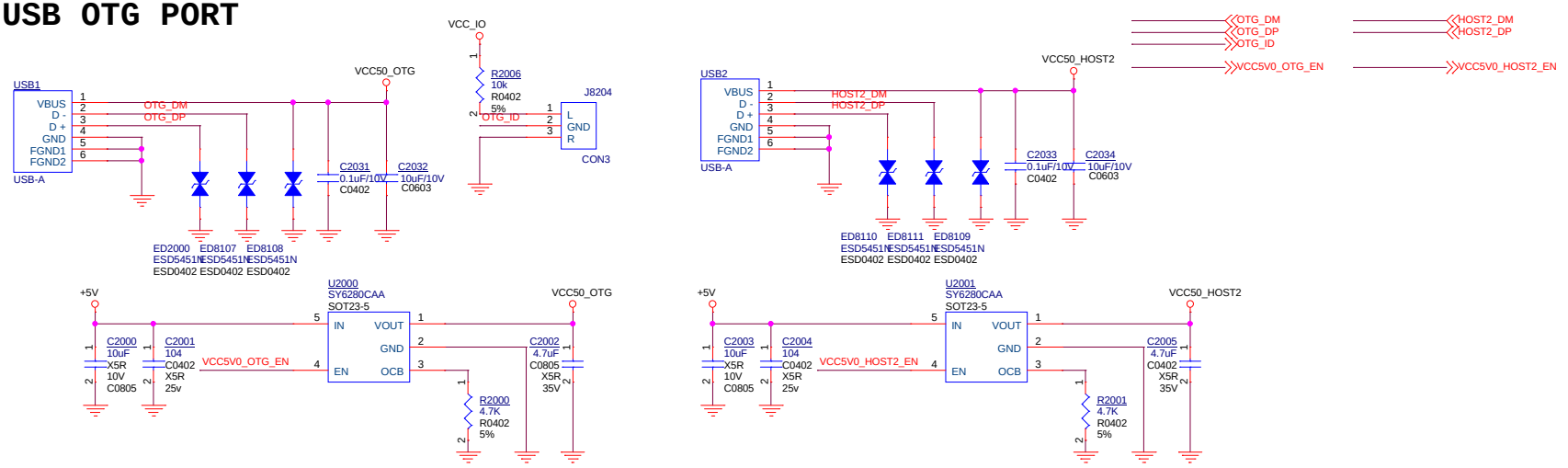
# LCD PANEL CONNECTOR

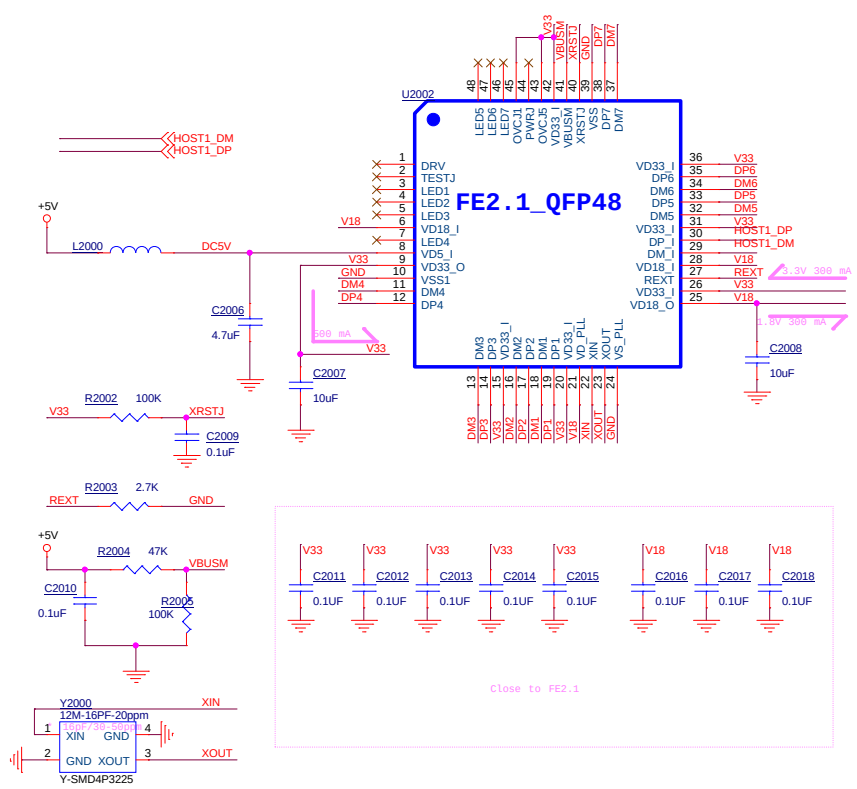


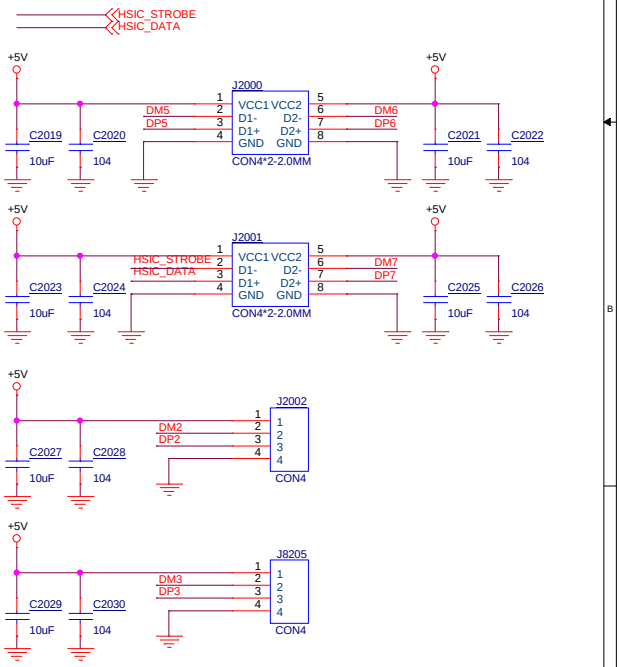

| | |
|---|---|
| Rockchip 瑞芯微电子 | 机 制 电 路 图 专 用 |
| Title: LCD | |
| File: RK3288_Tablet_REF | REV: 1.0 |
| Create Date: | Page Num: 15 |
| Modify Date: Friday, September 21, 2018 | Page Total: 19 |

# USB OTG PORT




# USB HUB

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| Fuzhou Rockchip Electronics | | | |
| Project: | RK3288_Tablet_REF | | |
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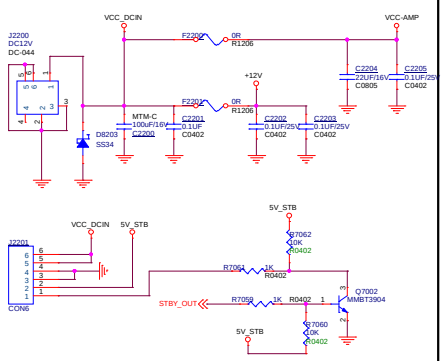
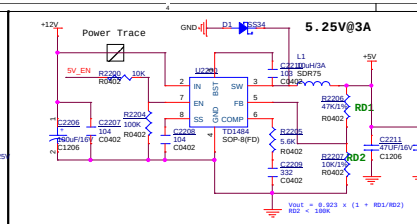
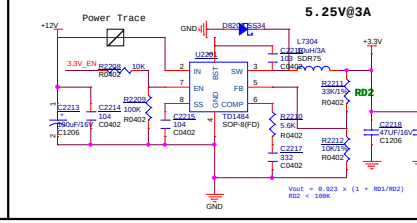
| Designed by: | Linus | Sheet: | 16 of 52 |

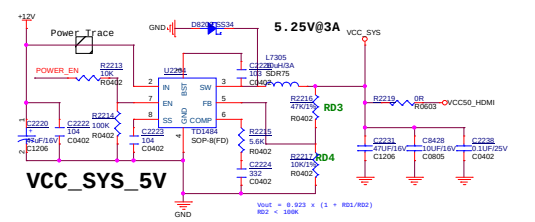
## DC IN

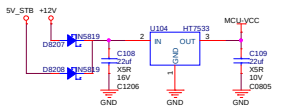
Note:Please use a 12V DC.

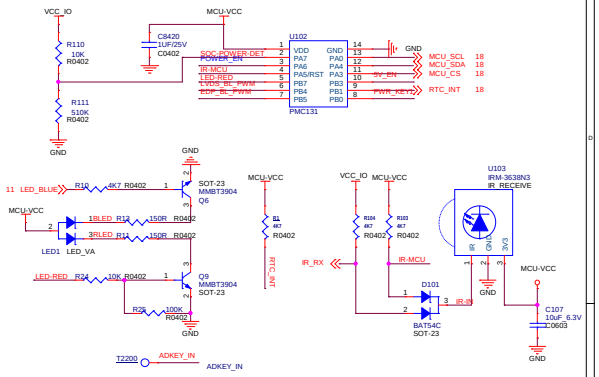


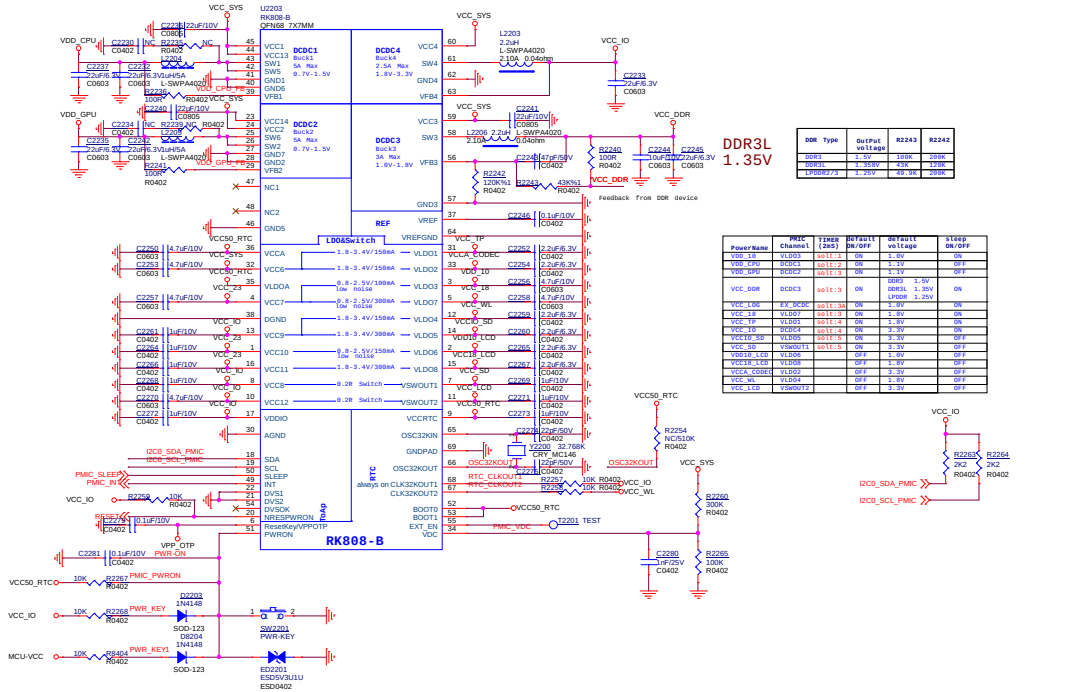



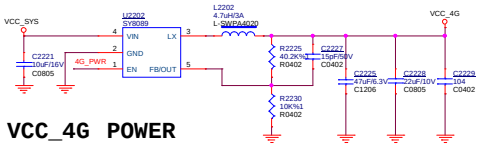

## PMIC RK808-B

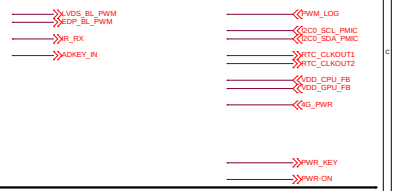



| DDR Type | OutPut voltage | R2243 | R2242 |
|---|---|---|---|
| DDR3 | 1.5V | 100K | 200K |
| DDR3L | 1.35V | 47K | 120K |
| LPDDR2/3 | 1.25V | 49.9K | 200K |

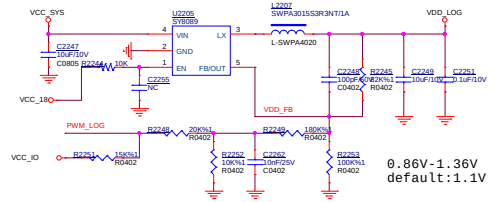
| PowerName | PMIC Channel | TIMER (2mS) | Default ON/OFF | default voltage | sleep ON/OFF |
|---|---|---|---|---|---|
| VDD_10 | VLDO3 | sel:1 | ON | 1.0V | ON |
| VDD_CPU | DCDC1 | sel:2 | ON | 1.1V | OFF |
| VDD_GPU | DCDC2 | sel:3 | ON | 1.1V | OFF |
| VCC_DDR | DCDC3 | sel:3 | ON | DDR3 1.5V<br>DDR3L 1.35V<br>LPDDR 1.25V | ON |
| VCC_LOG | EX_DCDC | sel:3A | ON | 1.0V | ON |
| VCC_18 | VLDO7 | sel:3 | ON | 1.8V | ON |
| VCC_TP | VLDO1 | sel:4 | ON | 1.8V | ON |
| VCC_IO | DCDC4 | sel:4 | ON | 3.3V | ON |
| VCC10_SD | VLDO5 | sel:5 | ON | 3.3V | OFF |
| VCC_SD | VSWOUT1 | sel:5 | ON | 3.3V | OFF |
| VDD18_LCD | VLDO6 | | OFF | 1.8V | OFF |
| VCC18_LCD | VLDO8 | | OFF | 1.8V | OFF |
| VCCA_CODEC | VLDO2 | | OFF | 3.3V | OFF |
| VCC_WL | VLDO4 | | OFF | 1.8V | OFF |
| VCC_LCD | VSWOUT2 | | OFF | 3.3V | OFF |

## VCC_4G POWER

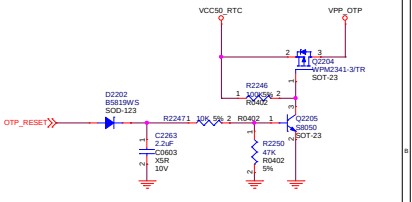


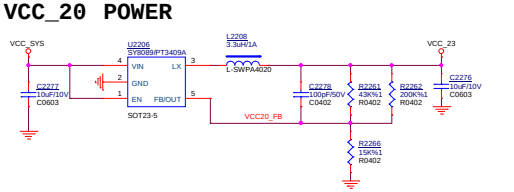

## VDD_LOG POWER




## OTP & RESET



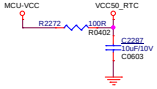

## VCC_20 POWER



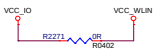

## OTG POWER

## RTC POWER

Note:If VCC50_RTC over-voltage of 6V,please use R2273=100ohm and C2288=1uF.




## Power for WIFI+BT

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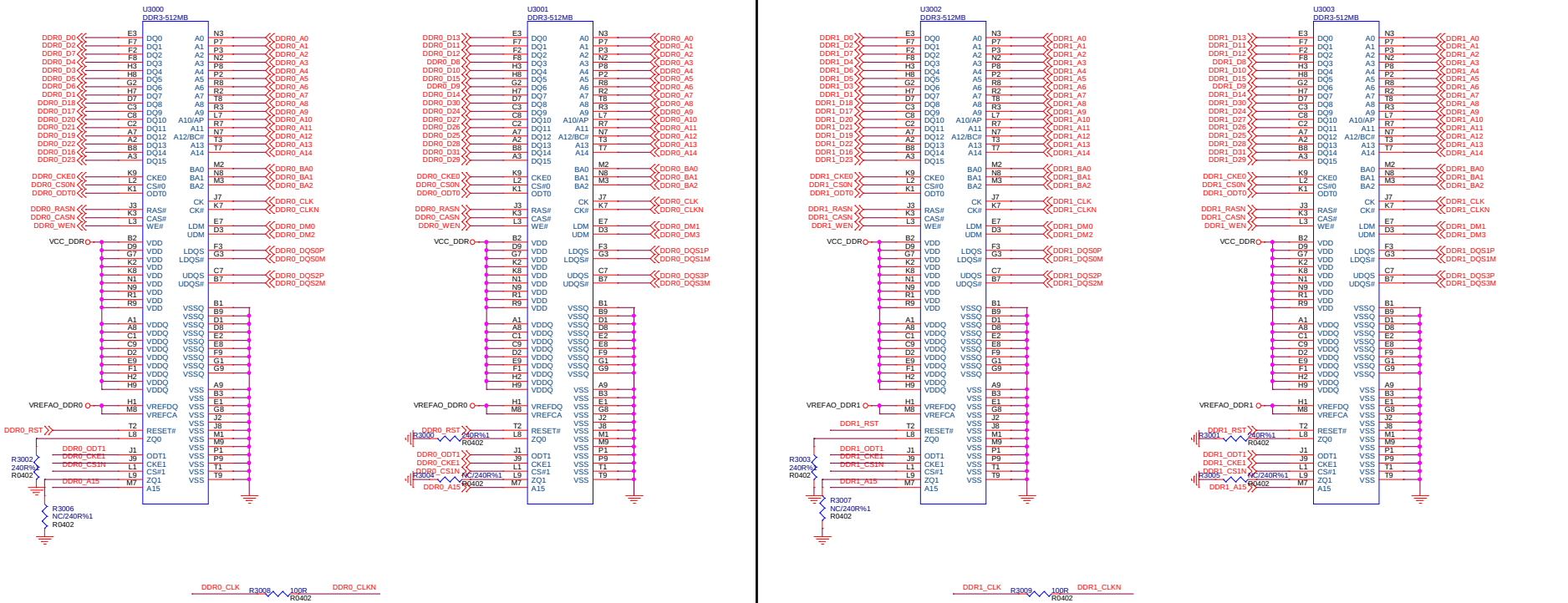
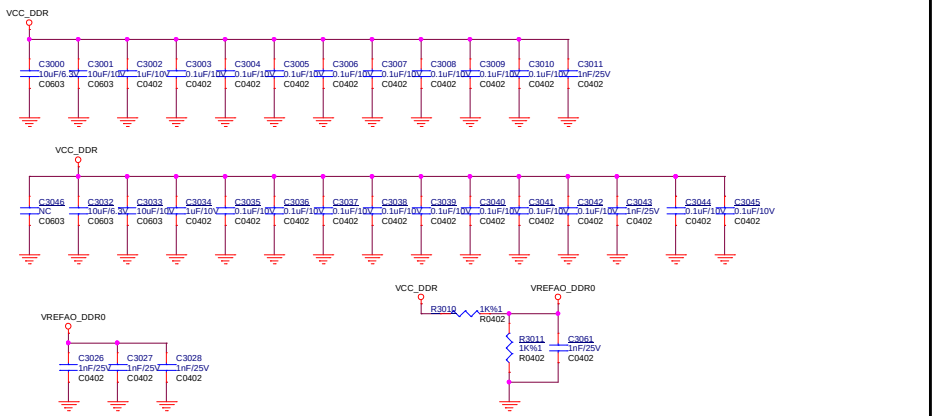
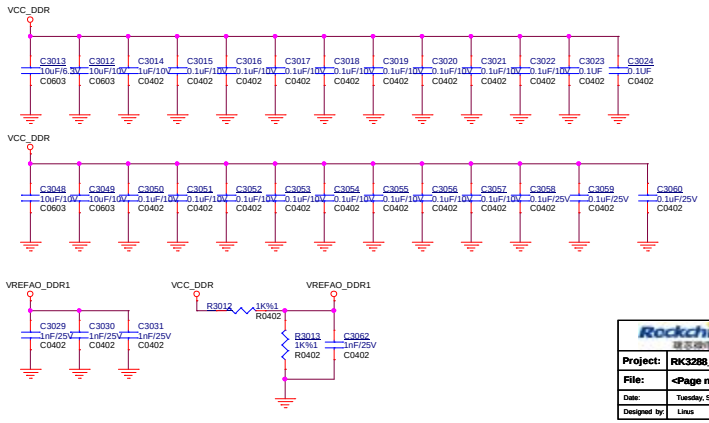
# DDR3 4x16bit




# DDR0 FILTER




# DDR1 FILTER

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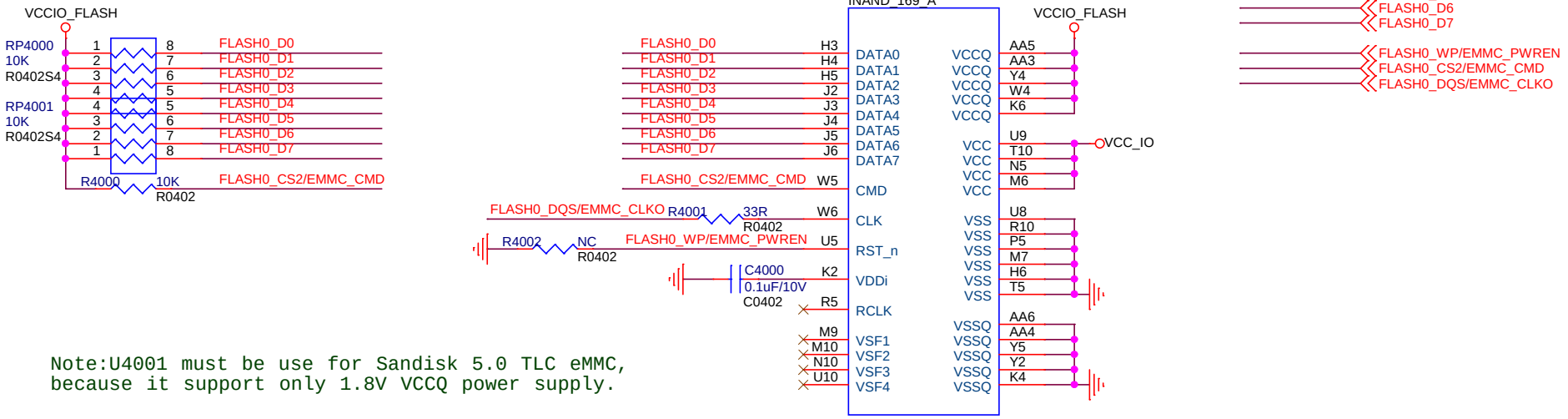
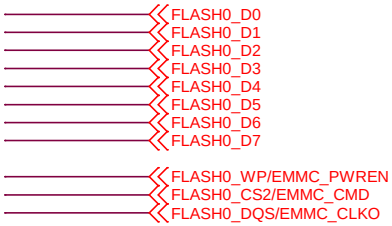
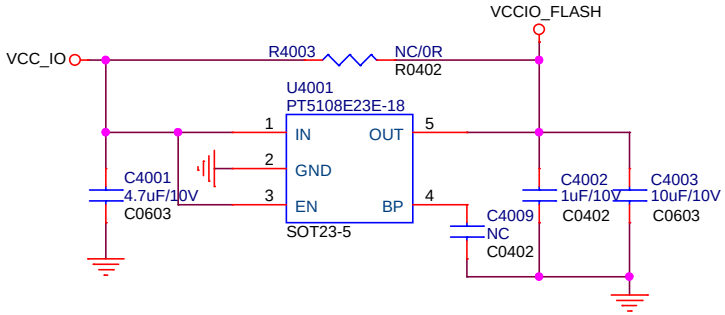
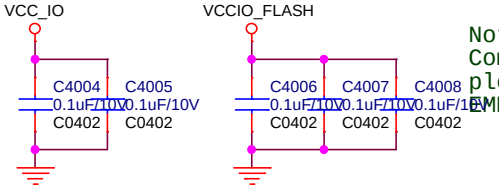
# eMMC FLASH




Note:U4001 must be use for Sandisk 5.0 TLC eMMC, because it support only 1.8V VCCQ power supply.



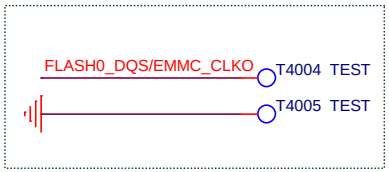




Note:For eMMC&Nand Flash Compatible layout, please make sure the EMMC_CLKO stub as short as possible.

Note:For eMMC&Nand Flash Compatible layout , please make the trace go through Nand Flash and then go through eMMC Flash.

Note:Reserve PAD for Update.




| | | | |
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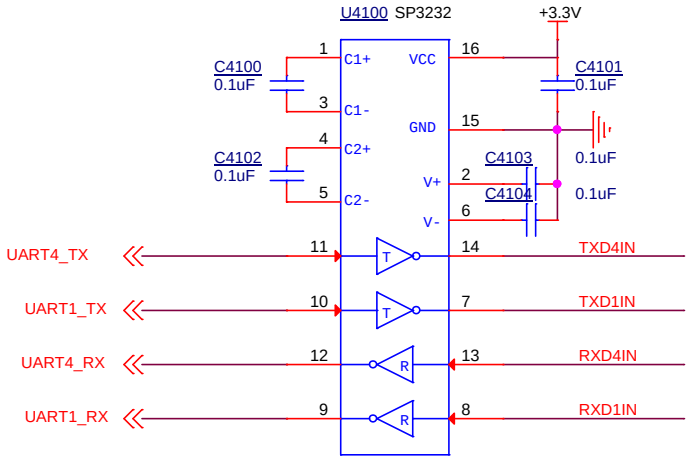


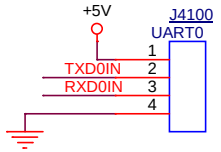


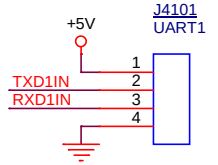


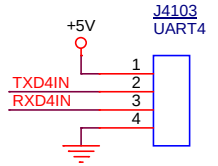


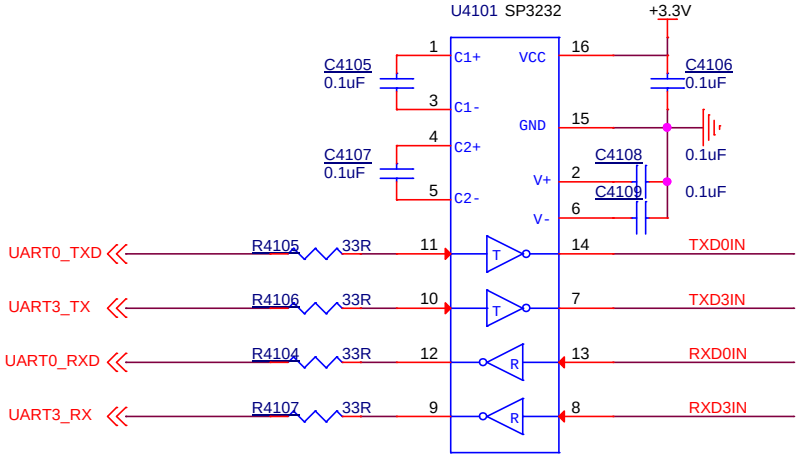


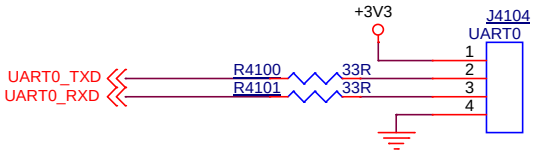


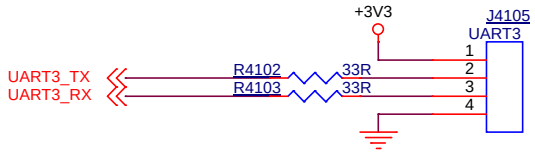


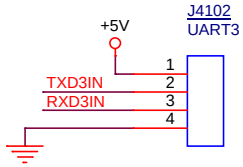


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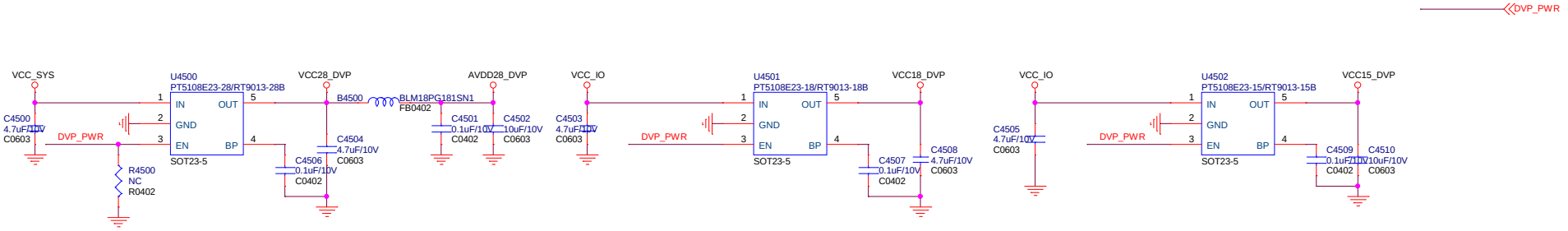
# DVP Power




| | | | |
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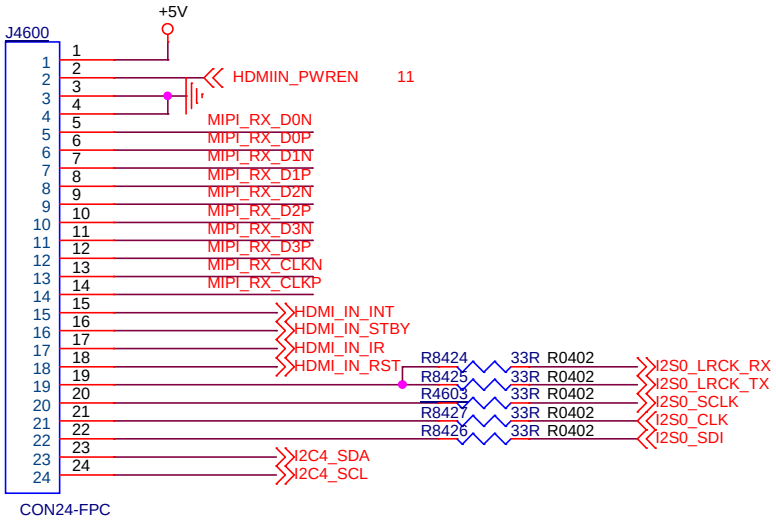
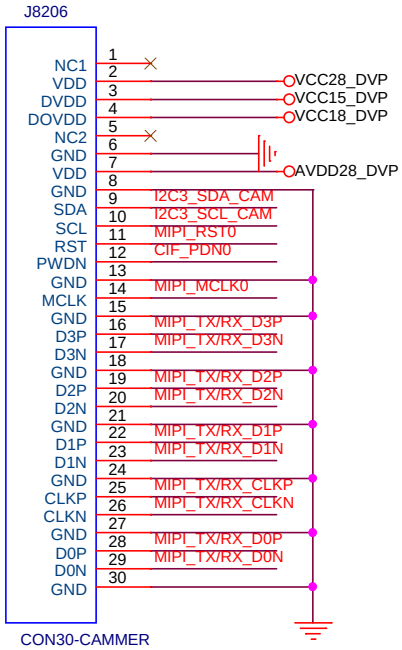
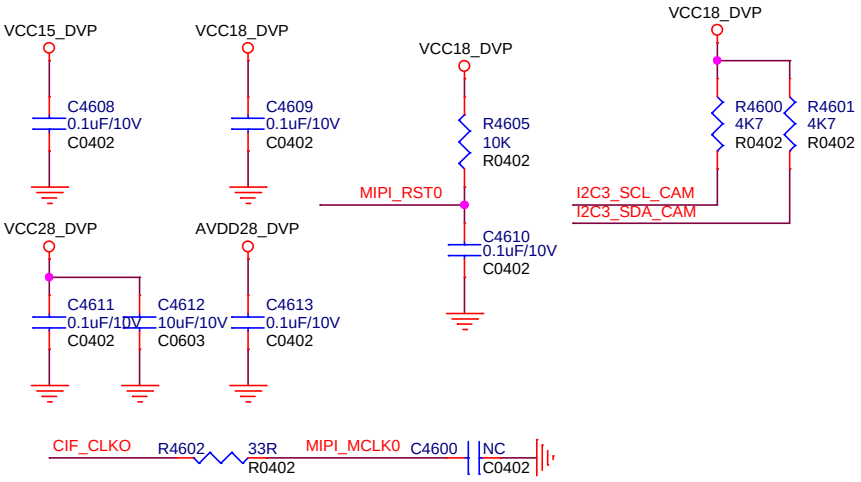
# MIPI Camera

Note:Do not use two camera with the same I2C address





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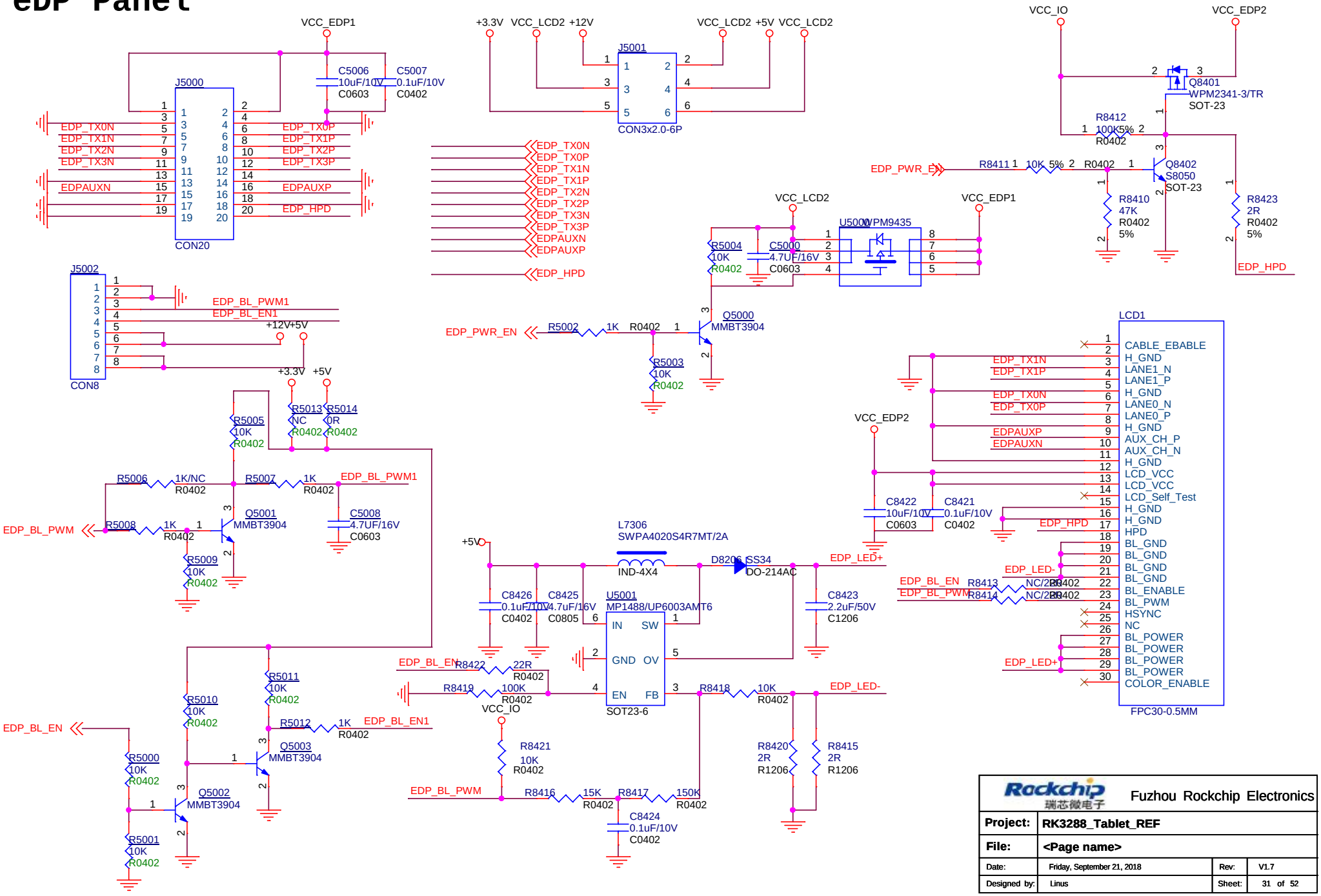
# eDP Panel

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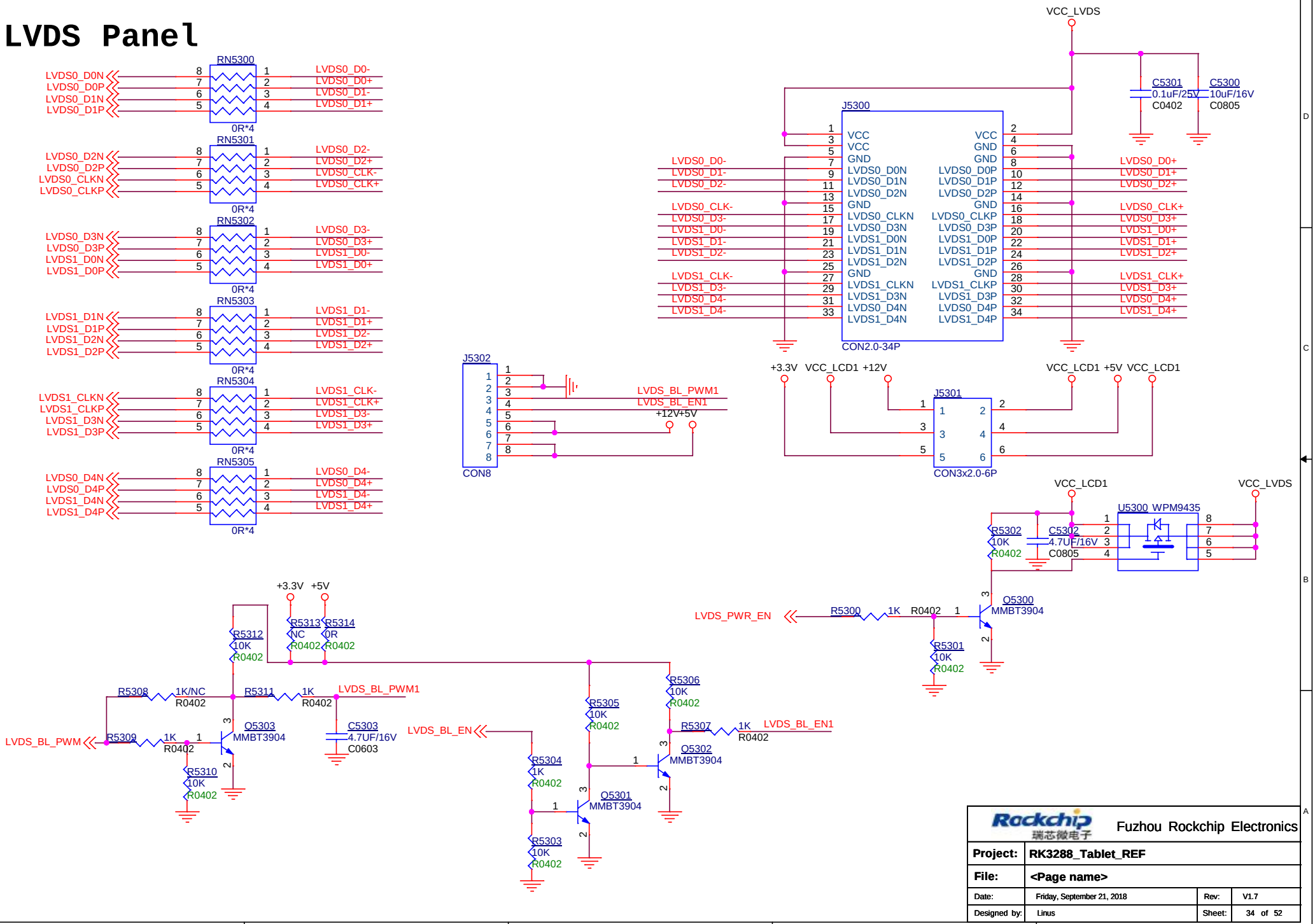
# LVDS Panel

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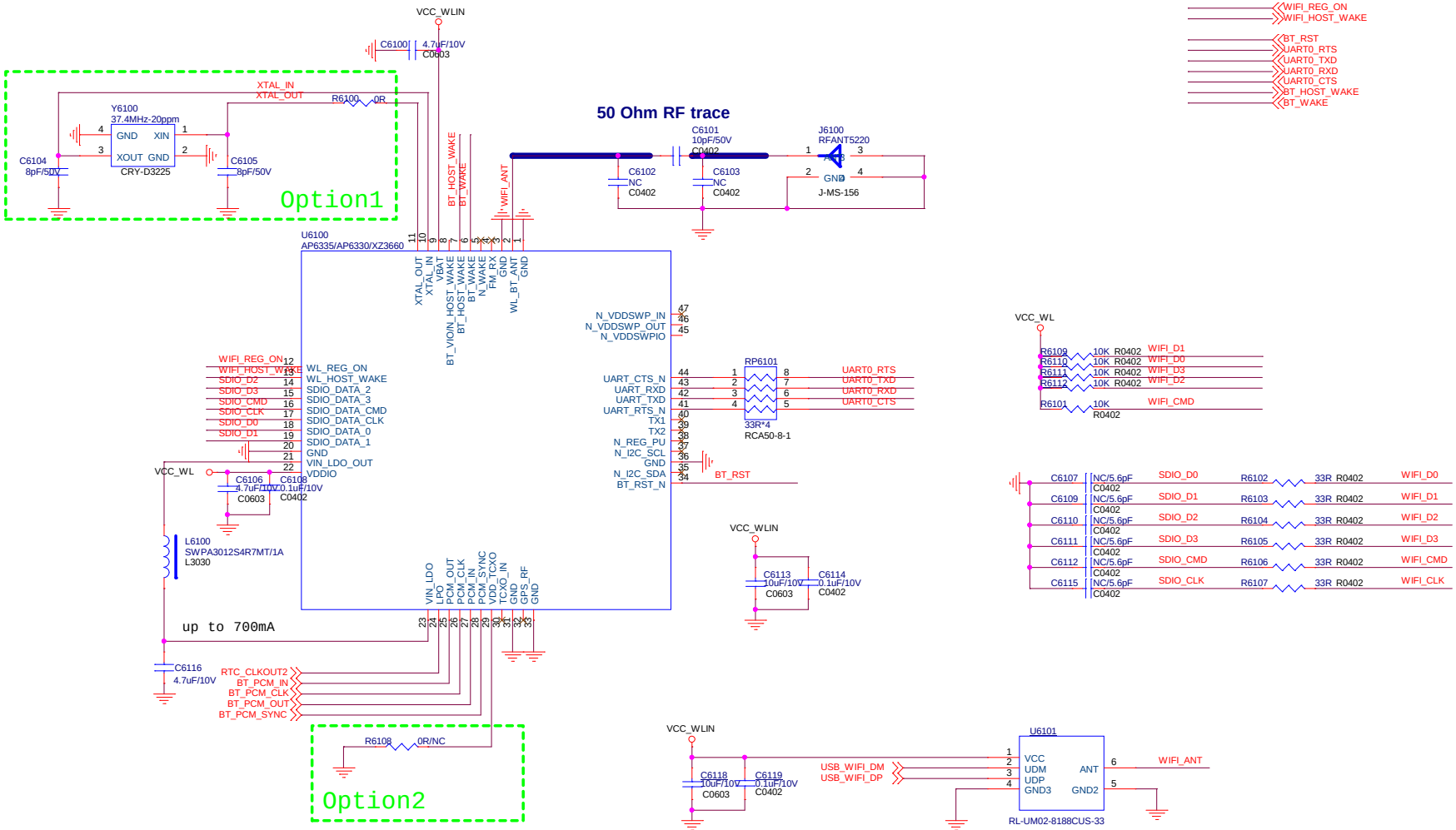
# WIFI/WIFI ac/BT MODULE

Note:VBAT supply from 3.0V~4.8V,
400mA of current。

| | Option1 | Option2 | Y6100 | Default:AP6335<br>Note:<br>YES:Paste<br>NO:Do not paste |
|---|---|---|---|---|
| AP6335 | YES | YES | 37.4MHz | |
| AP6330 | YES | NO | 26MHz | |
| XZ3660 | YES | NO | 26MHz | |

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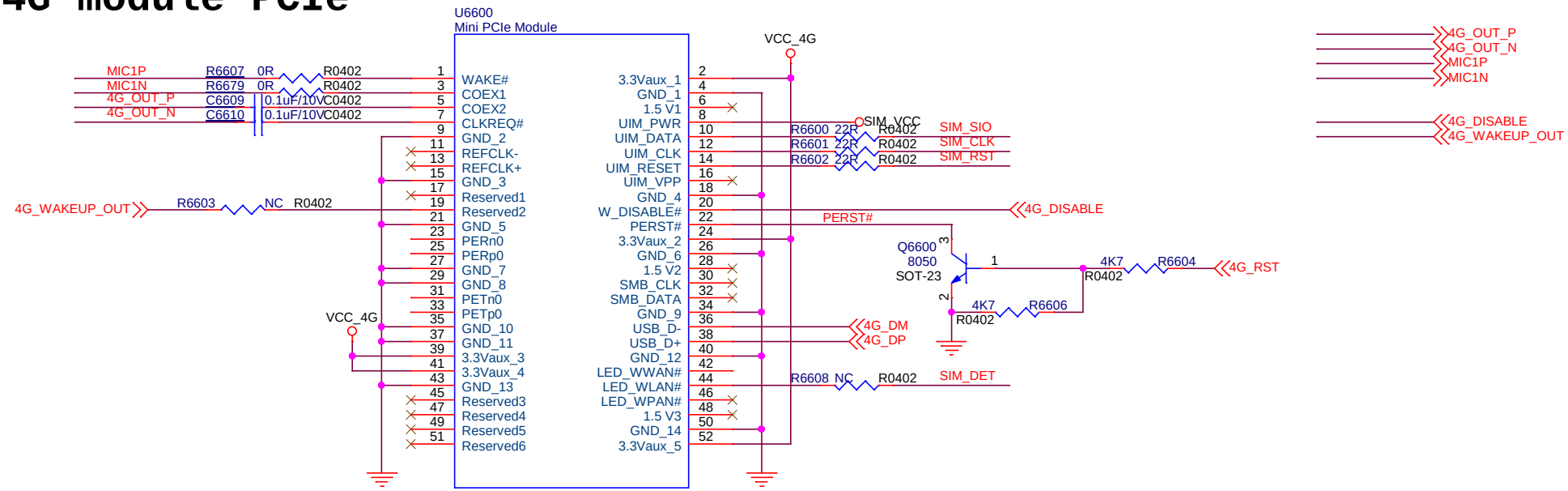
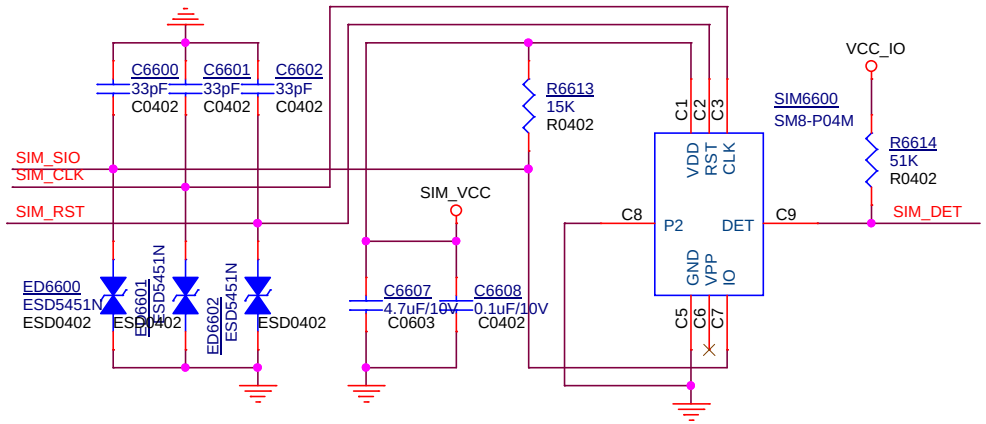
# 4G module PCIe

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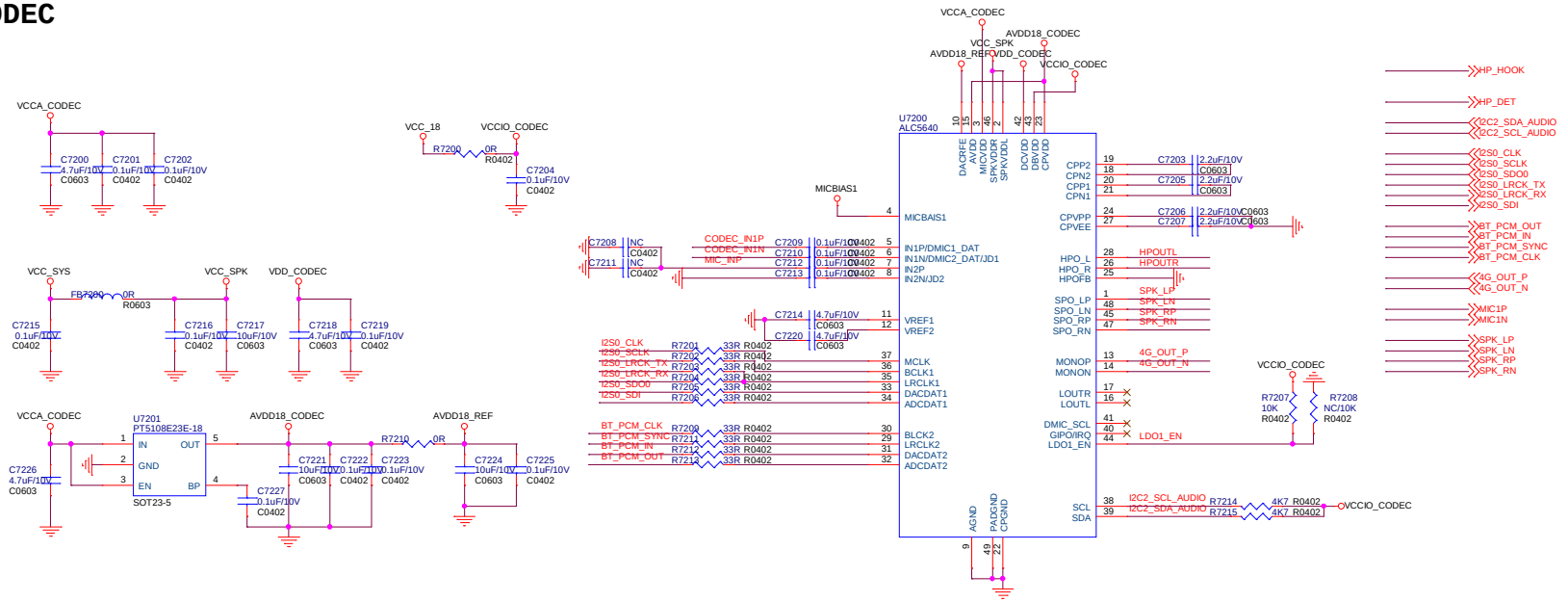
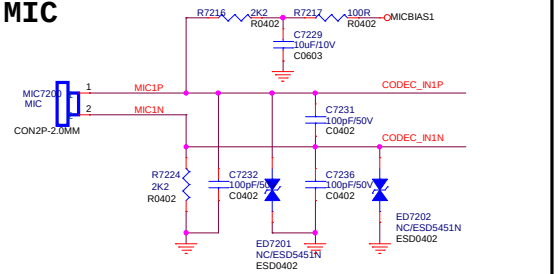
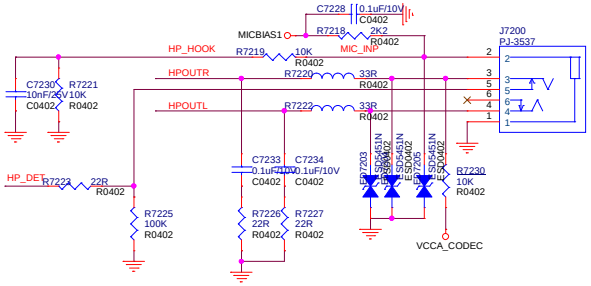
# CODEC




# MIC




# EARPHONE




# SPEAKER

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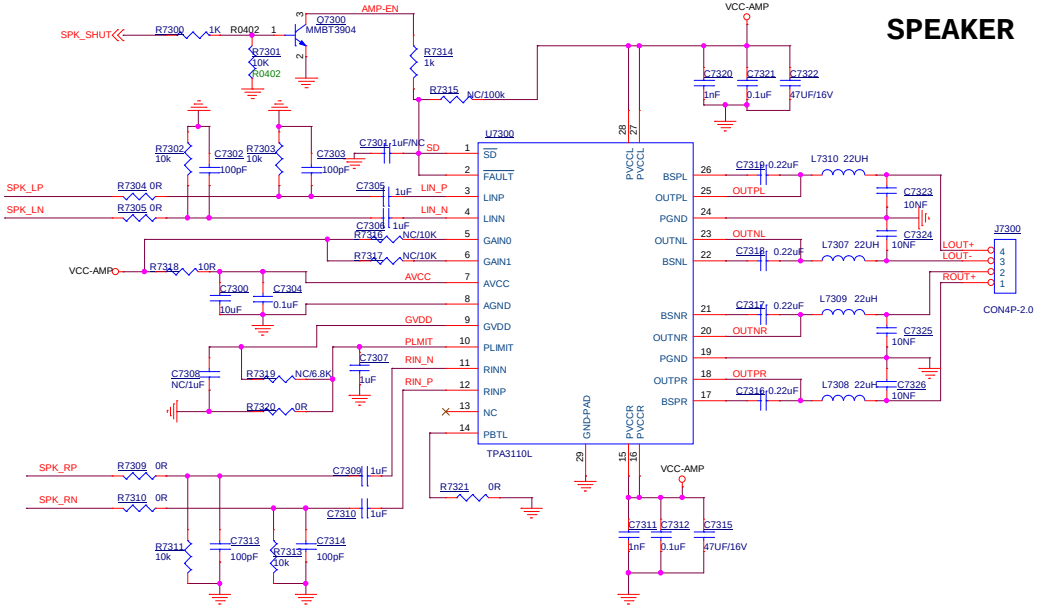
# SPEAKER

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|---|---|
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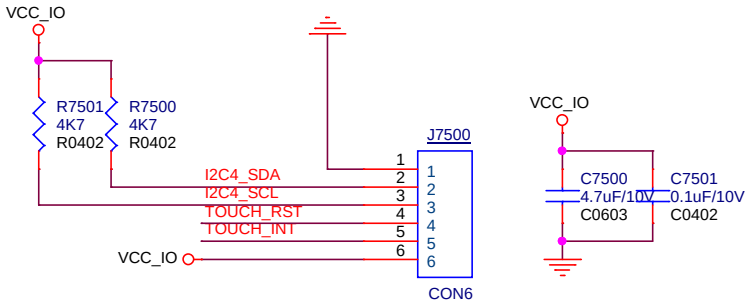
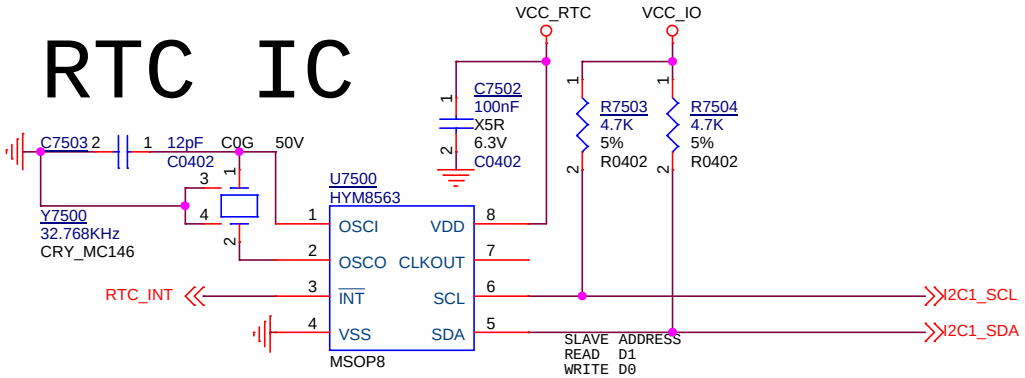
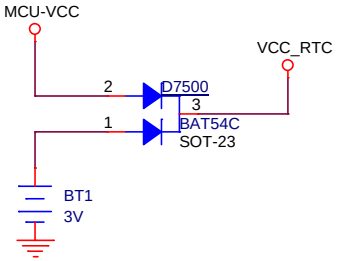
# Touch Panel connector





# RTC IC

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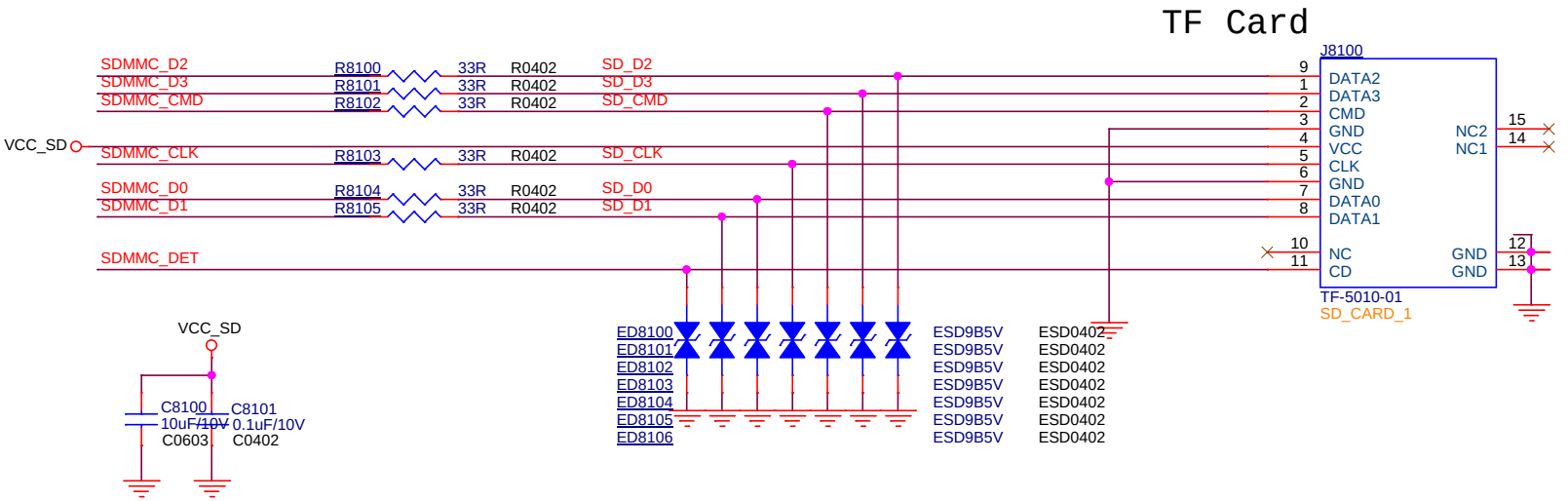
# TF CARD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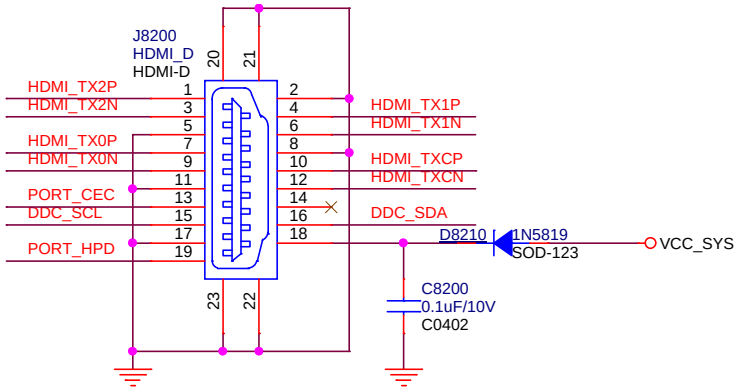
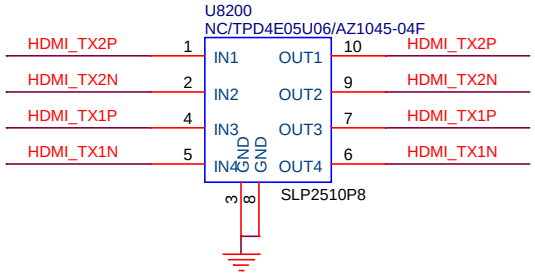
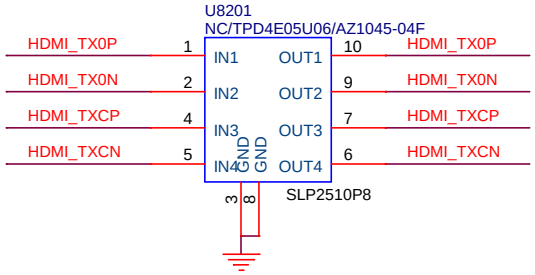
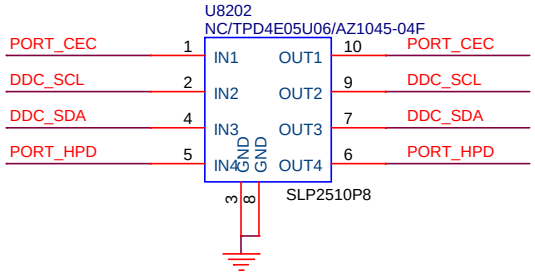
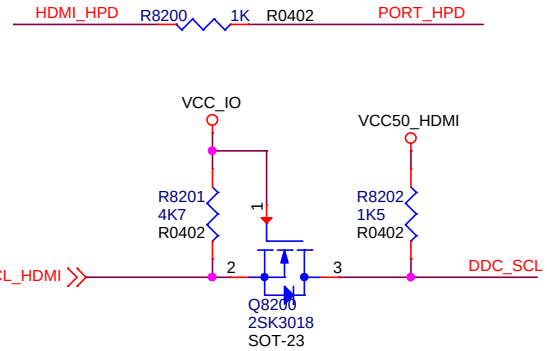
# HDMI C Type Port

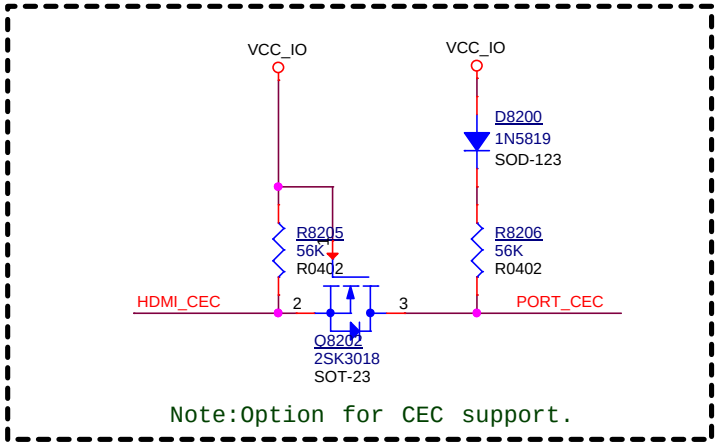


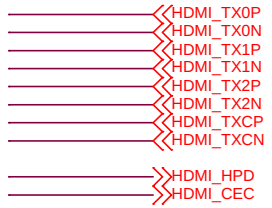

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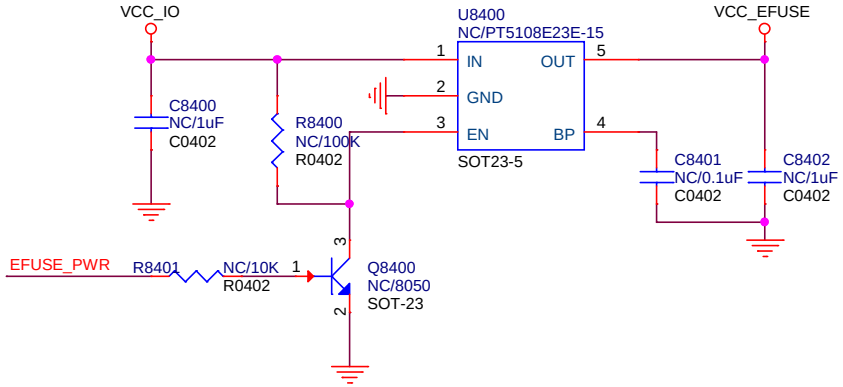
# EFUSE Power

Note:Place the Component if need to write eFUSE

EFUSE_PWR

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