

BLOCK DIAGRAM

POWER & BIAS

USBC

VBUS (from USB)

Digital & Analog 3.3V

MICRO

ADC

DAC

USB_D+
USB_D-

SPI1_NSSD
SPI1_SCKC
SPI1_MISOC

SPI2_NSSD
SPI2_SCKC
SPI2_MOSID

DAC_NCLRD
DAC_NLDACD

SPI1_NSS
SPI1_SCK
SPI1_MISO

SPI2_NSS
SPI2_SCK
SPI2_MOSI

DAC_NCLR
DAC_NLDAC

NOTES

Revision

Esto es una
nota para
PCB DESING

Esto es una
nota para
TESTER

Esto es una
nota para
SCH DESING

Mixed Signal v1.0

Content

Sheet

Block Diagram

1

Power Supply

2

Micro

3

DAC

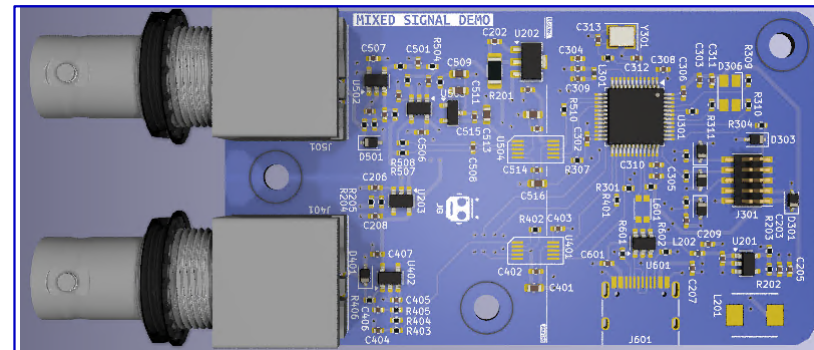
4

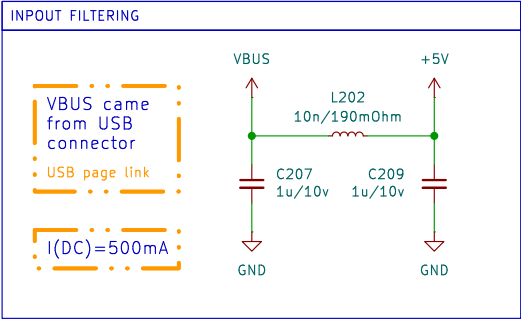
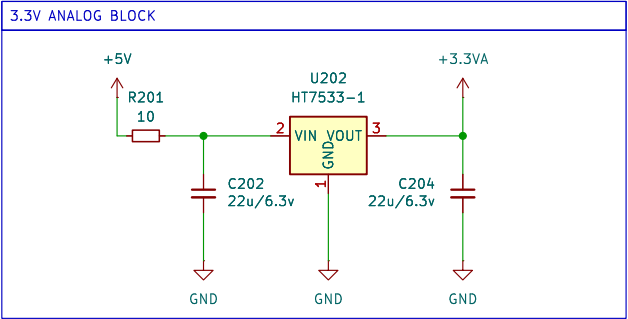
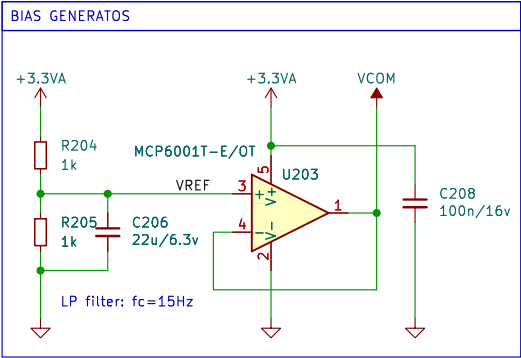
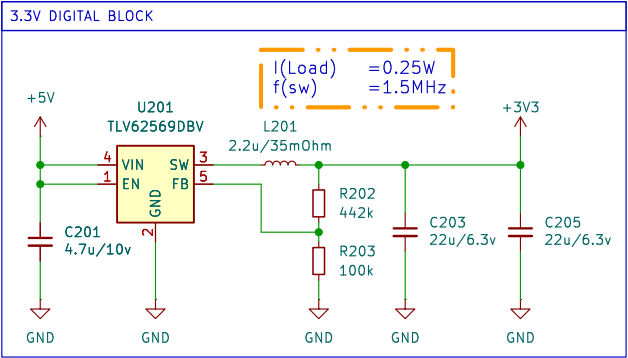
ADC

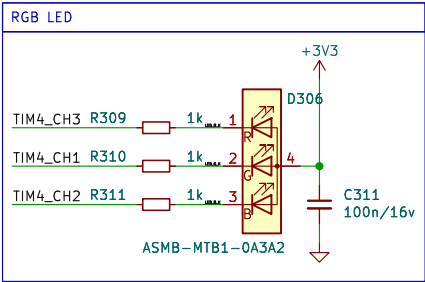
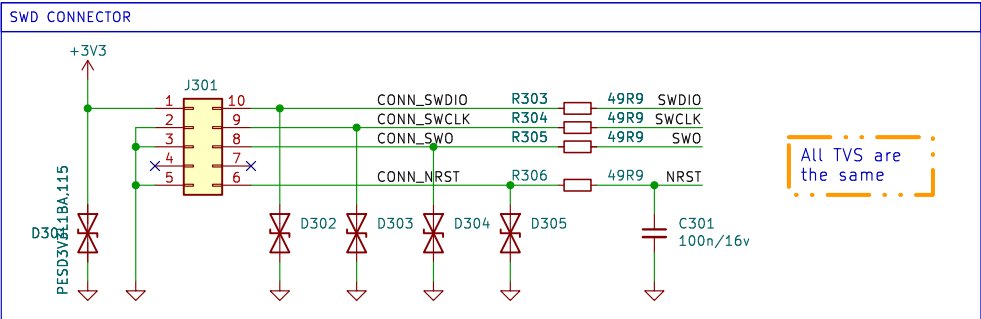
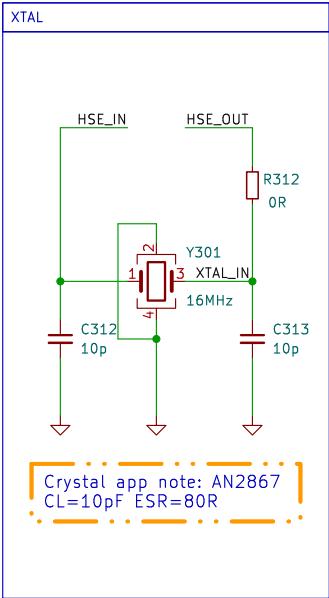
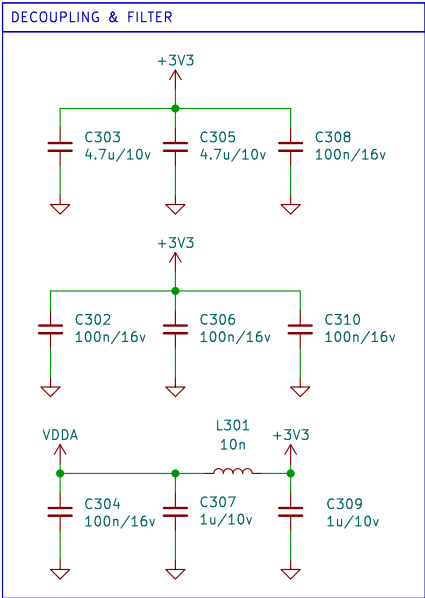
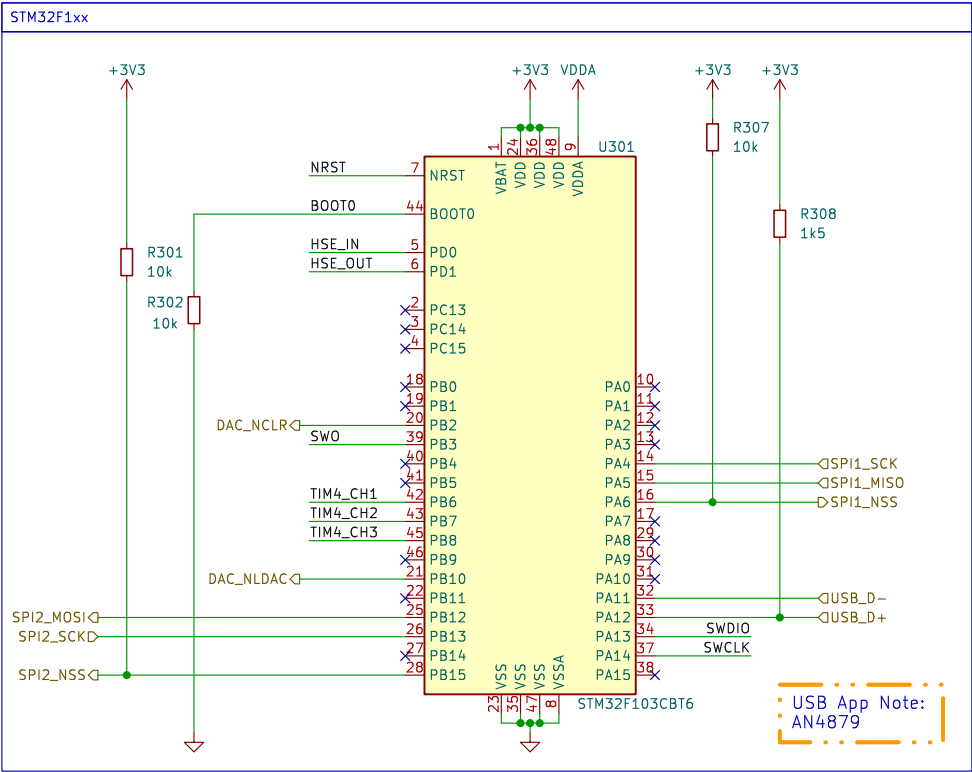
5

USB C

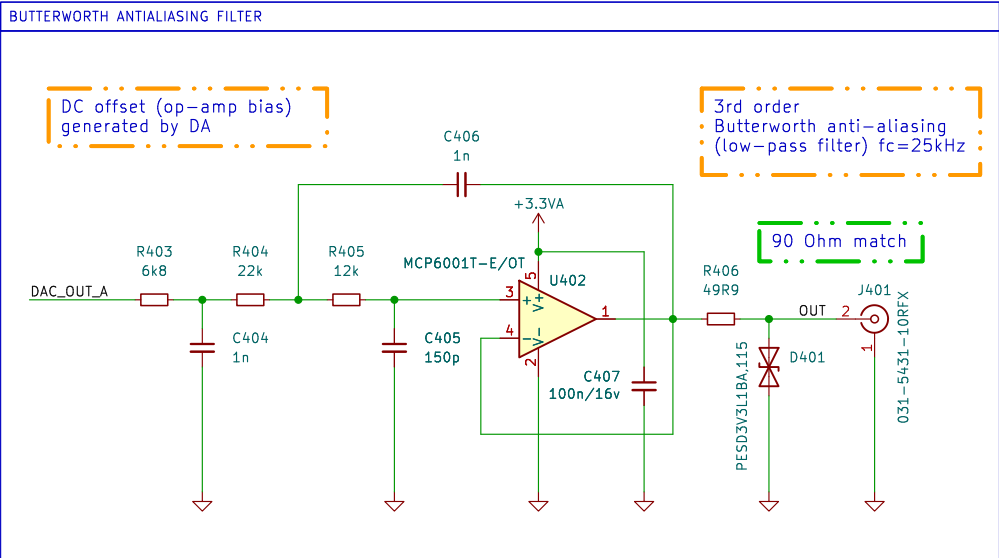
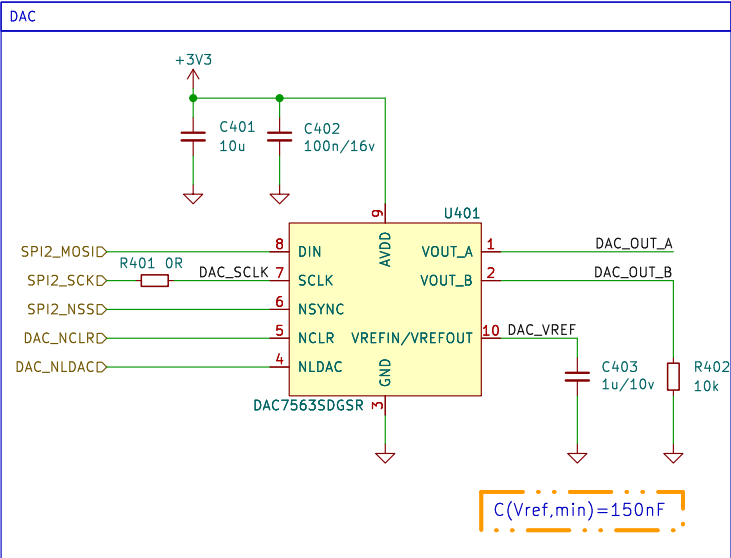
6







- H301 MountingHole
- H302 MountingHole
- H303 MountingHole



In butterworth, for a low noise desing we prefer:

- Increase capacitance
- Decrease resistance

Sheet: /DAC/
File: dac.kicad_sch

Title: Miced Signal demo board

Size: A4

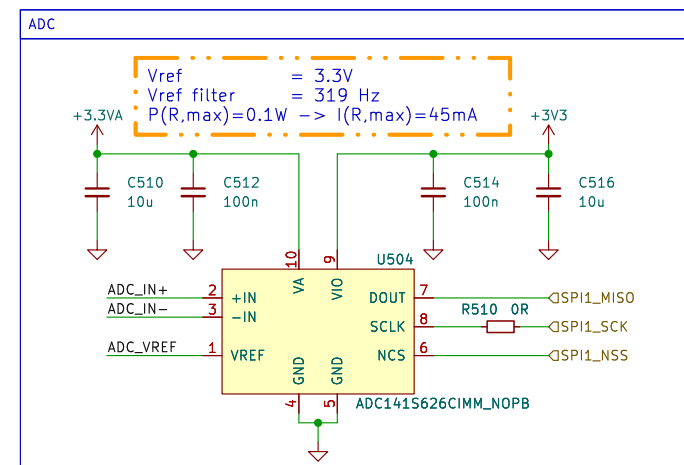
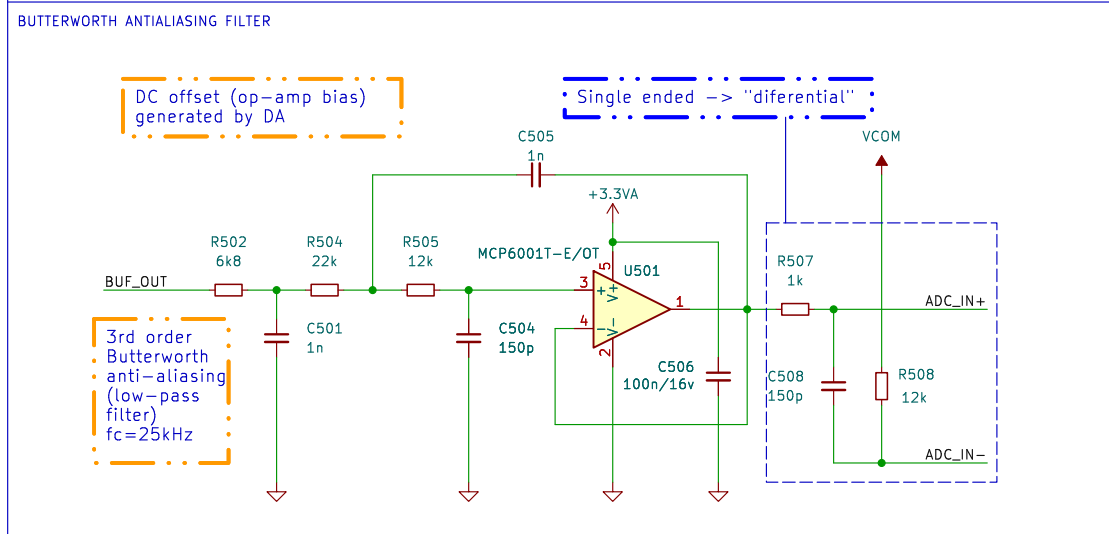
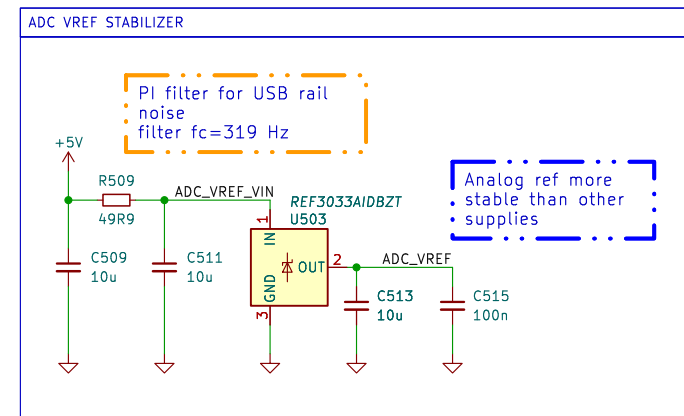
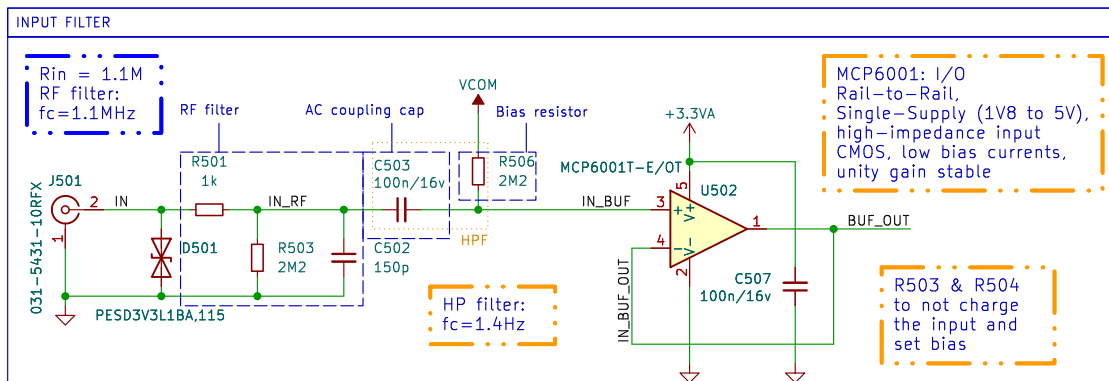
Date:

KiCad E.D.A. 9.0.0

Rev:

Id: 4/6

< Página principal



Sheet: /ADC/
File: adc.kicad_sch

Title: Miced Signal demo board

Size: A4
KiCad E.D.A. 9.0.0

Date:

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Id: 5/6

