

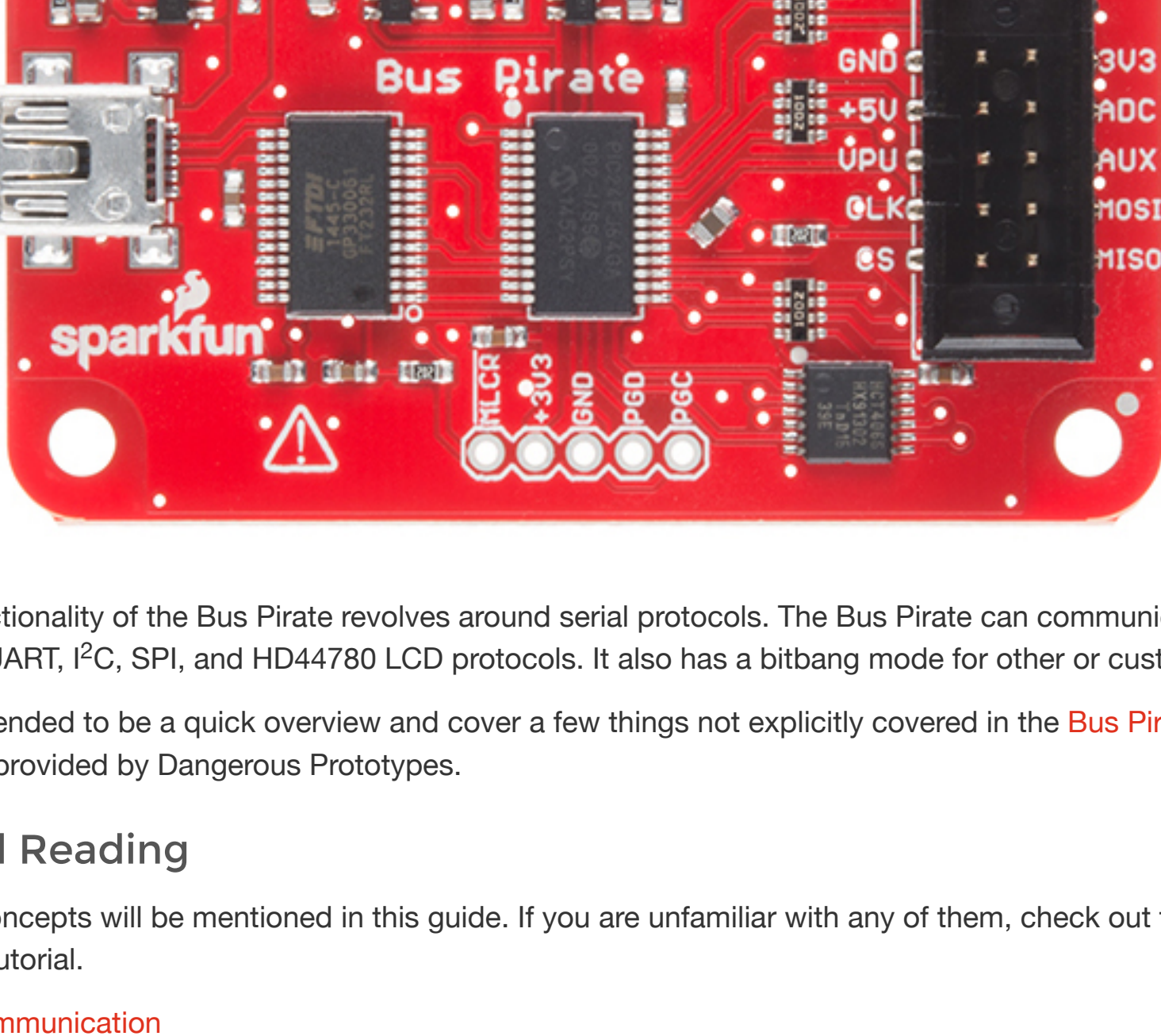
# Bus Pirate v3.6a Hookup Guide

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## Introduction

The **Bus Pirate** is an electronics prototyping dream. It's packed with many of the tools needed when getting a project up and running. The Bus Pirate can perform a variety of test equipment functions such as measuring voltage and frequency as well as generating PWM signals.



Most of the functionality of the Bus Pirate revolves around serial protocols. The Bus Pirate can communicate on 1-wire, 2-wire, 3-wire, UART, I<sup>2</sup>C, SPI, and HD44780 LCD protocols. It also has a bitbang mode for other or custom options.

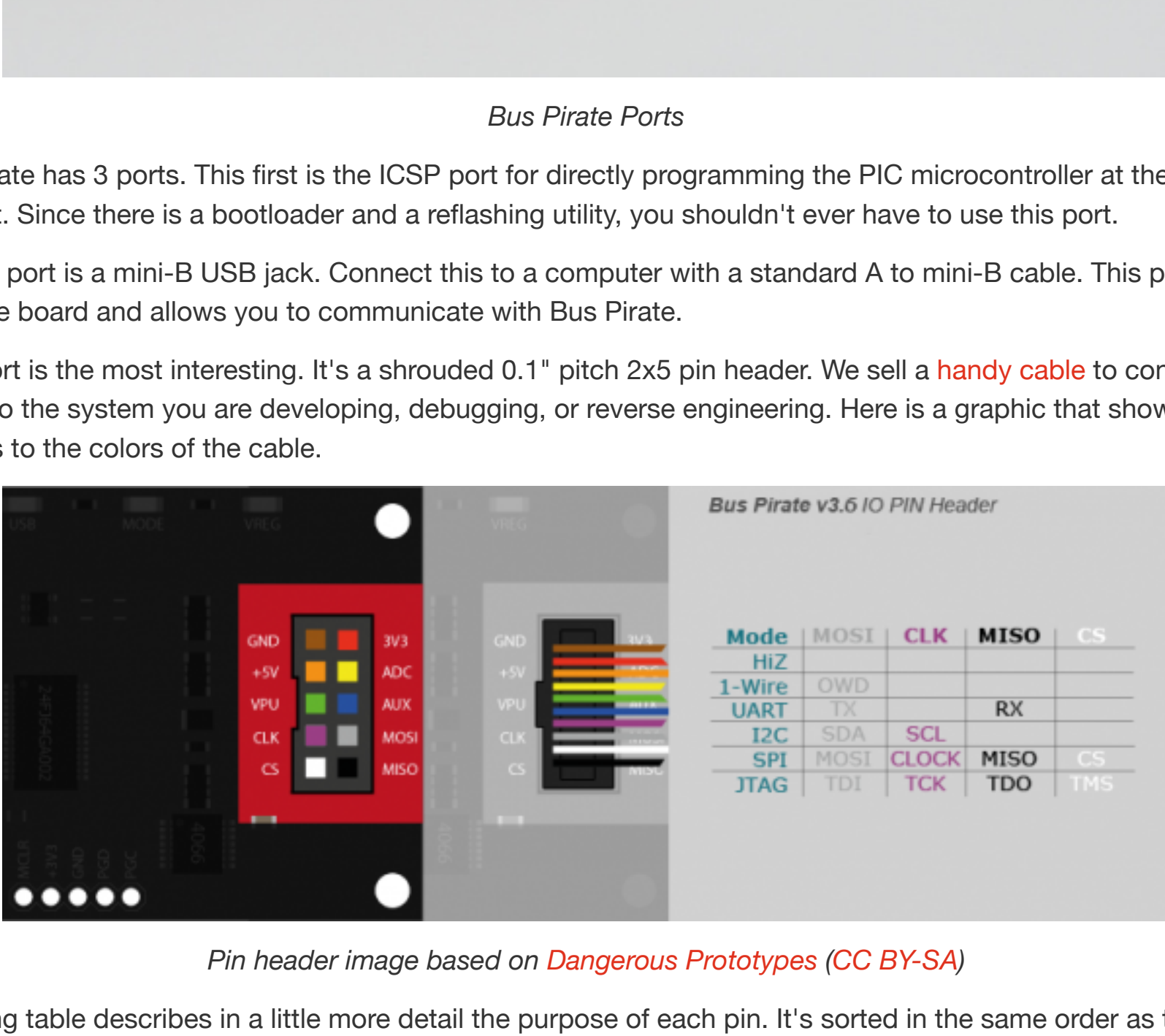
This guide is intended to be a quick overview and cover a few things not explicitly covered in the [Bus Pirate documentation](#) provided by Dangerous Prototypes.

## Suggested Reading

The following concepts will be mentioned in this guide. If you are unfamiliar with any of them, check out the corresponding tutorial.

- Serial Communication
- I<sup>2</sup>C Communication
- Serial Peripheral Interface (SPI)
- Pulse-width Modulation (PWM)
- Serial Terminal Basics

## Board Overview

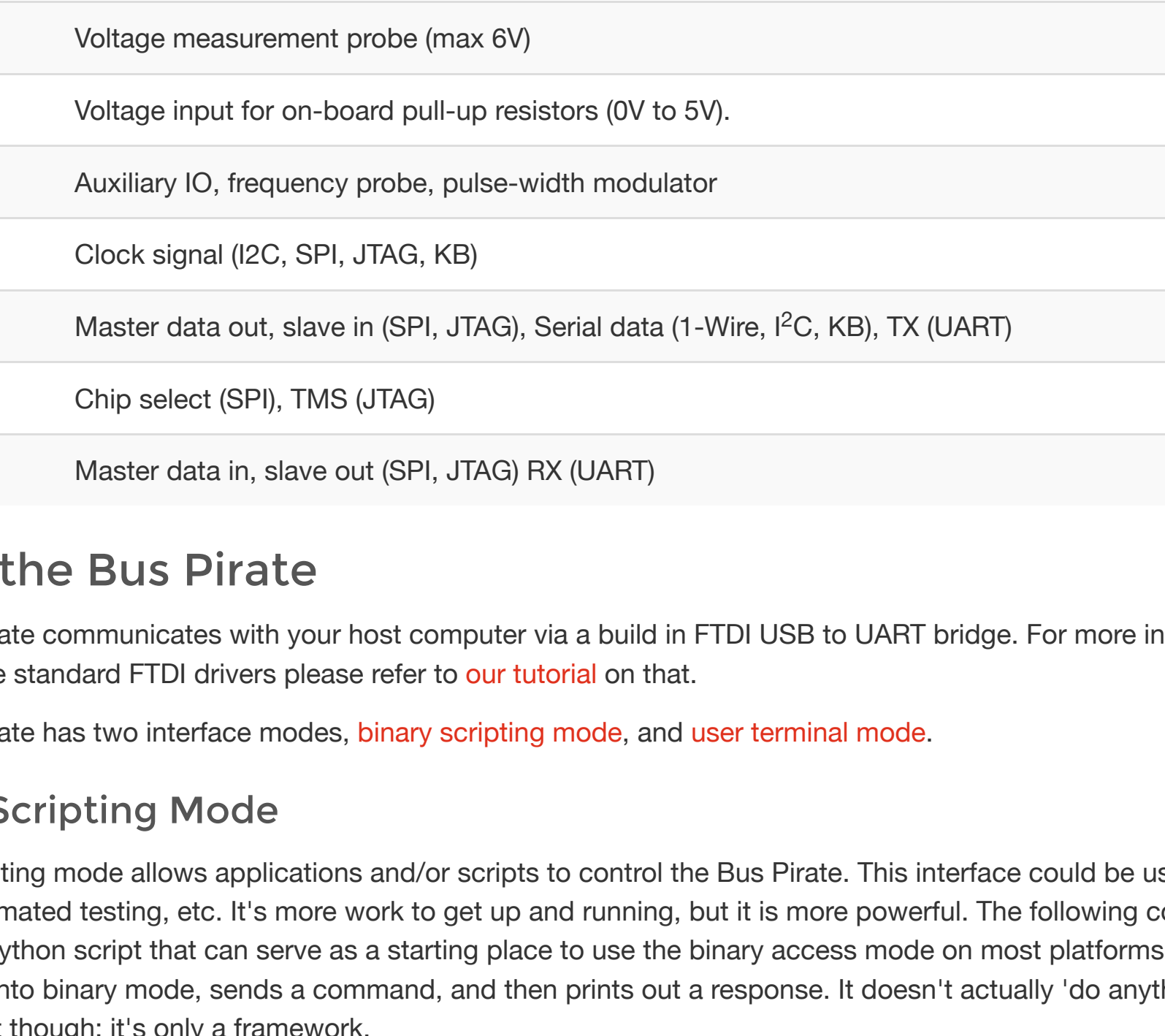


Bus Pirate Ports

The Bus Pirate has 3 ports. This first is the ICSP port for directly programming the PIC microcontroller at the heart of this product. Since there is a bootloader and a refashing utility, you shouldn't ever have to use this port.

The second port is a mini-B USB jack. Connect this to a computer with a standard A to mini-B cable. This provides the power to the board and allows you to communicate with Bus Pirate.

The third port is the most interesting. It's a shrouded 0.1" pitch 2x5 pin header. We sell a [handy cable](#) to connect the Bus Pirate to the system you are developing, debugging, or reverse engineering. Here is a graphic that shows maps the IO functions to the colors of the cable.



Pin header image based on [Dangerous Prototypes \(CC BY-SA\)](#)

The following table describes in a little more detail the purpose of each pin. It's sorted in the same order as the conductors on the cable. The power supplies can be switched on or off in software, and each can supply up to 150mA to power your project.

Bus Pirate - IO Pins

Pin Name	Description (Bus Pirate is the master)
GND	Ground, connect to ground of test circuit
+3.3V	+3.3 volt switchable power supply
+5.0V	+5 volt switchable power supply
ADC	Voltage measurement probe (max 6V)
VPU	Voltage input for on-board pull-up resistors (0V to 5V).
AUX	Auxiliary IO, frequency probe, pulse-width modulator
CLK	Clock signal (I <sup>2</sup> C, SPI, JTAG, KB)
MOSI	Master data out, slave in (SPI, JTAG), Serial data (1-Wire, I <sup>2</sup> C, KB), TX (UART)
CS	Chip select (SPI), TMS (JTAG)
MISO	Master data in, slave out (SPI, JTAG) RX (UART)

## Using the Bus Pirate

The Bus Pirate communicates with your host computer via a build in FTDI USB to UART bridge. For more information on using the standard FTDI drivers please refer to [our tutorial](#) on that.

The Bus Pirate has two interface modes, [binary scripting mode](#), and [user terminal mode](#).

### Binary Scripting Mode

Binary scripting mode allows applications and/or scripts to control the Bus Pirate. This interface could be used to write a GUI, automated testing, etc. It's more work to get up and running, but it's more powerful. The following code block is a minimal Python script that can serve as a starting place to use the binary access mode on most platforms. It puts the Bus Pirate into binary mode, sends a command, and then prints out a response. It doesn't actually 'do anything' beyond that though; it's only a framework.

On my Mac I run a command like: `./binarydemon.py -p /dev/tty.usbserial-AL0088EJ`. The **default baud rate for the Bus Pirate is 115200**. For help, run `python binarydemon.py --help`. It should run on any platform running a Python interpreter. Some tweaks might be necessary if running a non-2.7 version of Python.

```
#!/usr/bin/env python
# encoding: utf-8
...

Example code to interface the Bus Pirate in binary mode
Brent Wilkins 2015

This code requires pyserial:
$ sudo pip install pyserial
or:
$ sudo easy_install -U pyserial
...

import sys
import serial
import argparse

commands = {
    'B101': '\x00', # Enter reset binary mode
    'B111': '\x01', # Enter binary SPI mode
}
```

### User Terminal Mode

Terminal mode is likely the interface of choice for most users, at least to start. It provides an interactive menu system for configuring the Bus Pirate. All one needs to use it is a [terminal emulator](#).

The first thing to note is how to get help or list all of the commands. At the prompt enter `?`.

Help:

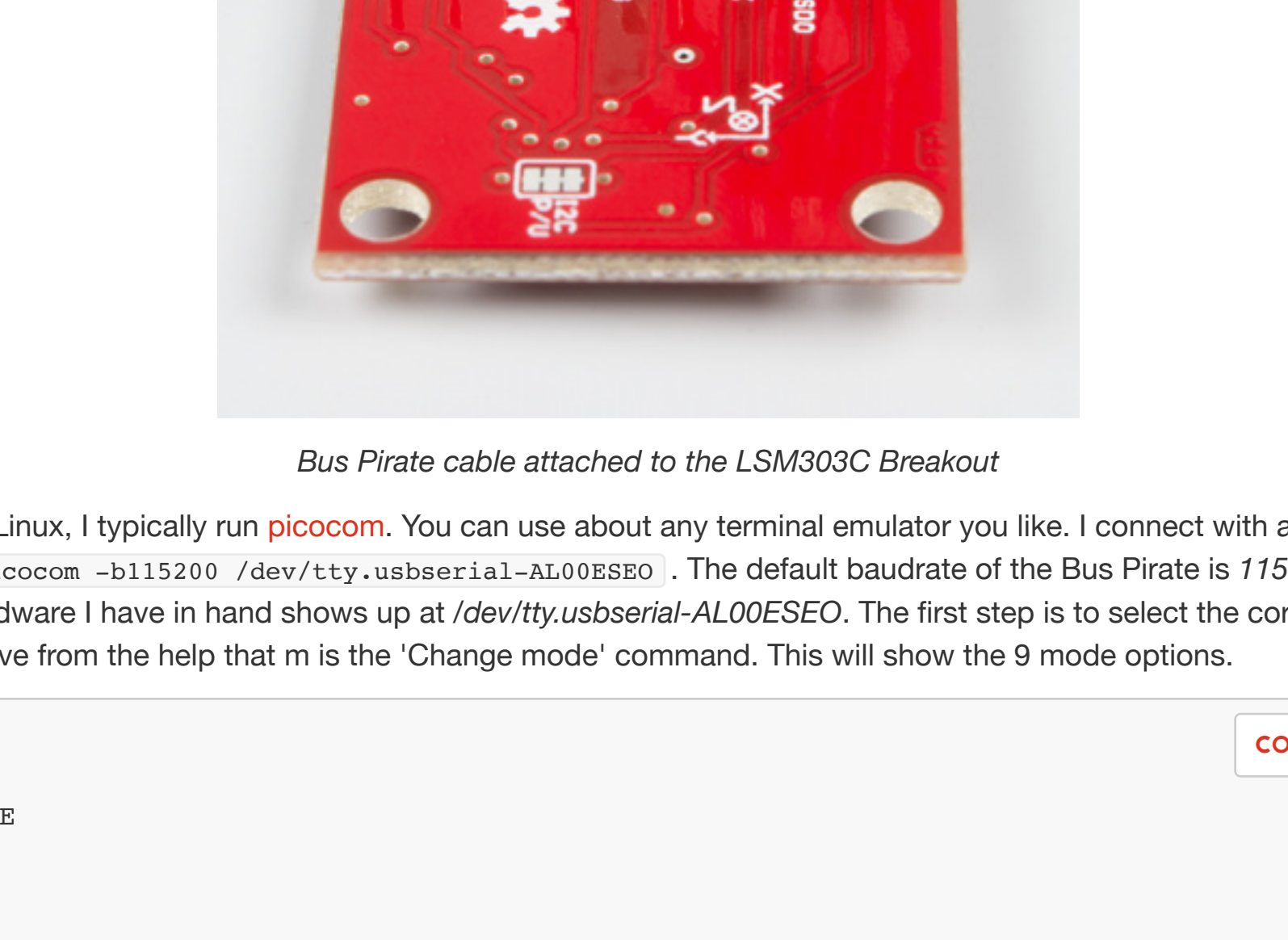
```
HEL>?
General
-----
? This help
~X/X Converts X/reverse X
- deltest
# Reset
$ Jump to bootloader
a/A Delay 1 us/ms
a/A/8 AUXIN (low/hi/READ)
b Set baudrate
c/C AUX assignment (aux/CS)
d/D Measure ADC (once/CONT.)
f Measure frequency
g/S Generate PWM/Servo
h Command history
i Version info/status info
l/L Bitorder (msb/lsb)

Protocol interaction
-----
(?) List current macros
(x) Macro x
[ Start
] Stop
( Start with read
) Stop
"abc" Send string
123
0x123
0b110 Send value
r Read
/ CLK hi
\ CLK lo
- CLK tick
_ DAT hi
```

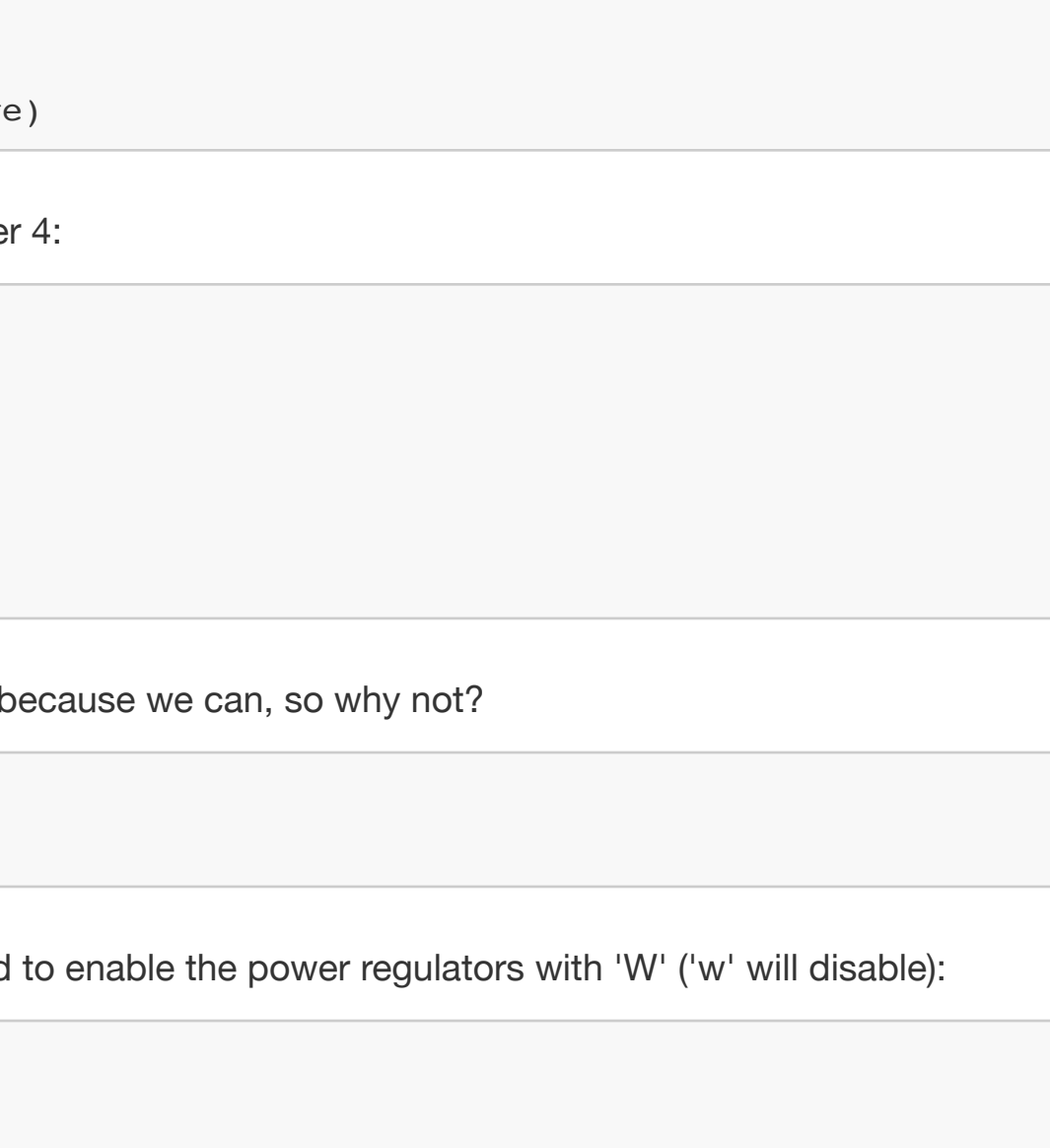
### Example

Here is a quick example of reading a register from a device on the I<sup>2</sup>C bus. I had an [LSM303C Breakout](#) laying on my desk, so I'll use it. Any I<sup>2</sup>C device will work a similar way. Here is a picture of all of the connections required to get the breakout board powered and communicating. This board runs on 3.3V, so we will power it with the red wire.

**Note:** By default VDD & VDD\_IO on the LSM303C breakout are connected and either could have been used to power the board.



Fritzing diagram of Bus Pirate connected to LSM303C



Bus Pirate cable attached to the LSM303C Breakout

On Mac or Linux, I typically run [picocom](#). You can use about any terminal emulator you like. I connect with a command such as `picocom -b115200 /dev/tty.usbserial-AL0088EJ`. The default baudrate of the Bus Pirate is 115200 baud, and the hardware I have in hand shows up at `/dev/tty.usbserial-AL0088EJ`. The first step is to select the correct mode. We say above from the help that m is the 'Change mode' command. This will show the 0 mode options.

```
HEL>m
1. HI-Z
2. 1-WIRE
3. UART
4. I2C
5. SPI
6. 2-WIRE
7. 3-WIRE
8. LCD
9. DIO
x. exit(without change)
```

We want I<sup>2</sup>C, so we'll enter 4:

```
(1)>4
Set speed:
1. ~5KHz
2. ~50KHz
3. ~100KHz
4. ~400KHz
```

Let's select 400KHz mode, because we can, so why not?

```
(1)>4
Ready

To power the board we need to enable the power regulators with 'W' (w' will disable):

I2C>W
Power supplies ON
```

At this point you might also want to enable pull-up resistors. To do so you need to connect the VPU pin to the correct voltage supply. Then 'P' will connect the resistors. The LSM303C Breakout already has pull-up resistors, so we can skip this step. We are ready to start communicating with the IC.

Let's take a look at the LSM303C datasheet to see what it expects to see on the I<sup>2</sup>C bus:

Table 14. Transfer when master is receiving (reading) one byte of data from slave									
Master	ST	SAD + W	SUB	SR	SAD + R	SAK	DATA	NMAK	SP
Slave			SAK	SAK		SAK	DATA		SP

It looks like as the master we need to send a start condition then a slave address with the read/write bit clear for a write. The LSM303C should then acknowledge receipt (ACK). Next we need to send the address of the register or interest. The slave LSM303C should ACK. Next we need to send a repeated start condition (same thing as a normal start condition), followed by the slave address with the r/w bit set to do a read. The LSM303C should respond, and we as the master don't acknowledge. The sequence ends with a stop condition.

Looks like we need more details such as the addresses. The LSM303C datasheet gives the 7-bit slave address to be 0011101b (0x1D) for the accelerometer and 0011110b (0x1E) for the magnetometer.

**Default Address:**  
The **accelerometer sensor slave address is 0011101b while magnetic sensor slave address is 0011110b.**

I<sup>2</sup>C Slave Addresses

To use these addresses we need to append the read/write bit to the end. In our case if we want to read from the accelerometer we need to shift 0b011101 left once and or that with 0x01 to indicate a read. The shift yields 0b0111010 and OR-ing that result with 1 results in 0b0111011 (0x3B). If we had OR-ed with 0x00 for a write we would have got 0b0111010 (0x3A) for the write address. Let's use a Bus Pirate macro to see if those addresses exist on the bus:

```
I2C>(1)
Searching I2C address space. Found devices at:
0x3A(0x1D W) 0x3B(0x1D R) 0x3C(0x1E W) 0x3D(0x1E R)
```

Sure enough, we can see a write address at 0x3A, and a read address at 0x3B! Now we need to know the address of a register to read from. The WHO\_AM\_I\_A (0x0F) register always contains 0x41, so that makes an easy read to verify.

**8.1 WHO\_AM\_I\_A (0Fh)**  
Accelerometer Who\_AM\_I register (r). This register is a read-only register. Its default value is 41h.

Who Am I Register Details

Let's put all of those addresses into Bus Pirate syntax and in the format that the LSM303C will respond to. Start condition `[ , ]`, write address `0x3A`, register address `0x0F`, repeated start condition `[ , ]`, read address with read command `0x3B r`, and end it all with a stop condition `[ ]`. Here is how the Bus Pirate responds to that input:

```
I2C>[0x3A 0x0F] [0x3B r]
I2C START BIT
WRITE: 0x3A ACK
I2C START BIT
WRITE: 0x3B ACK
READ: 0x41
NACK
I2C STOP BIT
I2C>
```

The Bus Pirate read `0x41`, which is what that register is supposed to contain!

### Other Commands

There are a lot of other commands available via the user terminal mode. Here is a quick reference to them and how they respond.

Convert base of one byte (x)

```
DIO>0x7E
0x7E = 126 = 0b01111110

Reverse one byte (X)
```

```
DIO>0b10011011
0x64 = 100 = 0b01100100

Self-test (-) - Runs in HI-Z mode only
```

```
Bus high
MOSI OK
CLK OK
MISO OK
CS OK
Bus HI-Z 0
MOSI OK
MISO OK
CS OK
Bus HI-Z 1
MOSI OK
CLK OK
MISO OK
CS OK
MODE and VREG LEDs should be on!
Any key to exit
Found 0 errors.
```

Reset (#)

```
HEL>#
RESET

Bus Pirate v3a
Firmware v5.10 (r559) Bootloader v4.4
DEVID:0x047 REVID:0x0045 (242640A002 B8)
http://dangerousprototypes.com
```

Jump to bootloader (\$)

```
HEL>$
Are you sure? y
BOOTLOADER

Delay (&%)
```

```
HEL>%
DELAY 1us
HEL>%
DELAY 1ms

Auxiliary Pin (a/A/B)
```

```
DIO>a
AUX LOW
DIO>a
AUX HIGH
DIO>b
DIO INPUT/BI-2, READ: 1

Set baudrate (b)
```

```
HEL>b
Set serial port speed: (bps)
1. 300
2. 1200
3. 2400
4. 4800
5. 9600
6. 19200
7. 38400
8. 57600
9. 115200
10. BUS raw value
(9)>>
Adjust your terminal
Space to continue

Auxiliary assignment (c/C)
```

```
HEL>c
a/A/8 controls AUX pin
HEL>c
a/A/8 controls CS pin

Measure ADC (d/D)
```

```
once:
1-WIRE-d
VOLTAGE PROBE: 3.33V
continuous:
DIO>d
VOLTMETER MODE
Any key to exit
VOLTAGE PROBE: 0.00V

g - 3.3V PWM on auxpin (blue wire)
```

```
DIO>g
PWM disabled
DIO>g
I2C=4,000KHz PWM
Frequency in KHz
(50)>
Duty cycle in %
(50)>
PWM active

HI-Z=0x1010
0x10 = 16 = 0b00010000

HEL>o
1. WEX
2. DEC
3. BIN
4. RAW

Modes:
```

```
HEL>m
1. HI-Z
2. 1-WIRE
3. UART
4. I2C
5. SPI
6. 2-WIRE
7. 3-WIRE
8. LCD
9. DIO
x. exit(without change)
(1)>
```

## Resources and Going Further

Now that you have had a brief overview of the Bus Pirate, take a look at the [official documentation](#) and the [Dangerous Prototypes SVN repository](#).



**Serial Communication**  
Asynchronous serial communication concepts: packets, signal levels, baud rates, UARTs and more!



**Serial Peripheral Interface (SPI)**  
SPI is commonly used to connect microcontrollers to peripherals such as sensors, shift registers, and SD cards.



**I2C**  
An introduction to I2C, one of the main embedded communications protocols in use today.



**Serial Terminal Basics**  
This tutorial will show you how to communicate with your serial devices using a variety of terminal emulator applications.

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