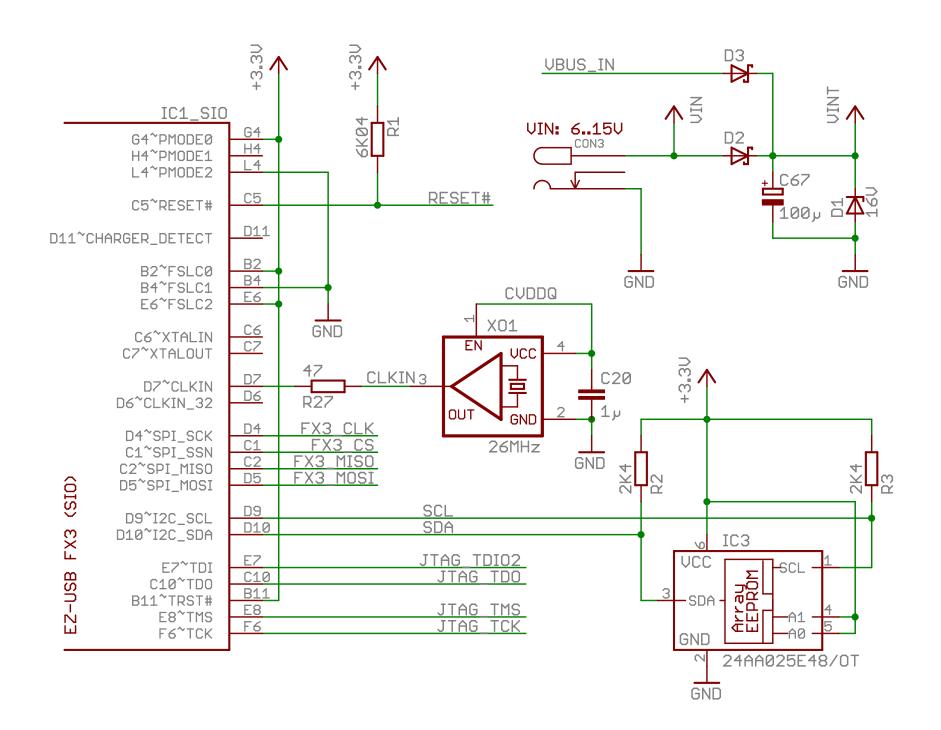
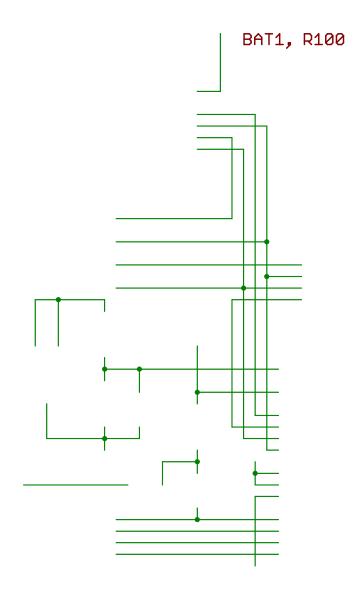
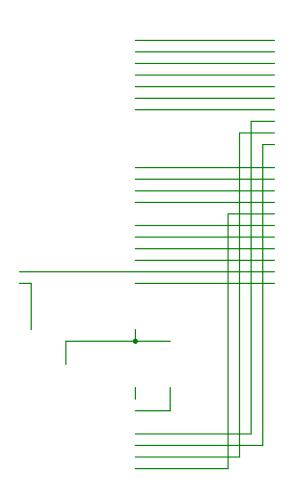
## Circuit diagram of ZTEX USB-FPGA Module 2.14

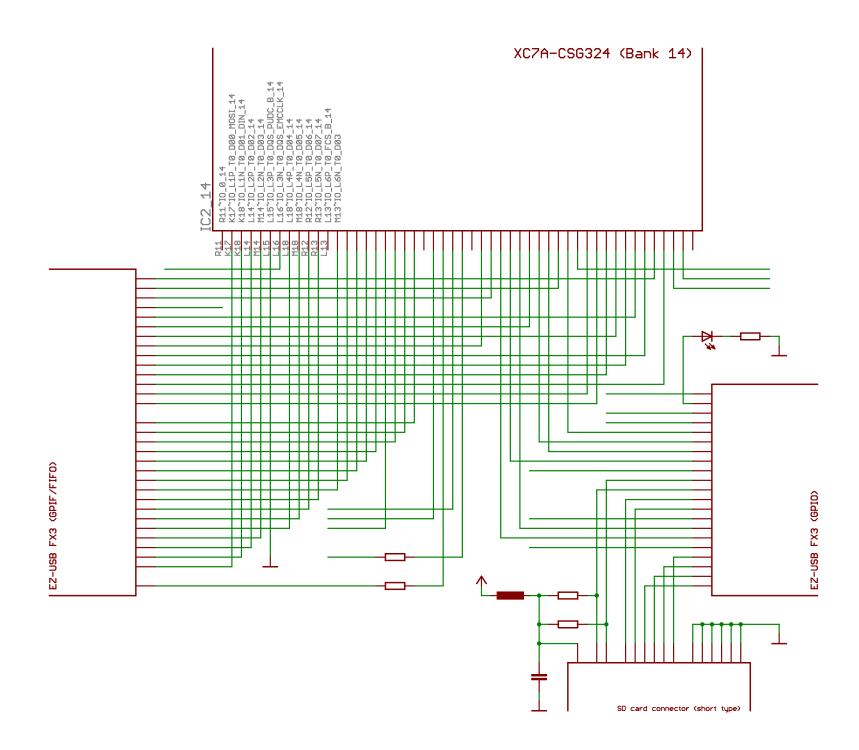
## Contents:

Sheet 1	EZ-USB FX3, MAC-EEPROM
Sheet 2	JTAG, SPI Flash, FPGA con guration, CPLD, Battery
Sheet 3	XC7A Bank 14 (FX3 - FPGA interface, SD card)
Sheet 4	XC7A Bank 15 (external I/O rows A and B)
Sheet 5	XC7A Bank 34 (external I/O rows C and D)
Sheet 6	DDR3 SDRAM (Bank 35)

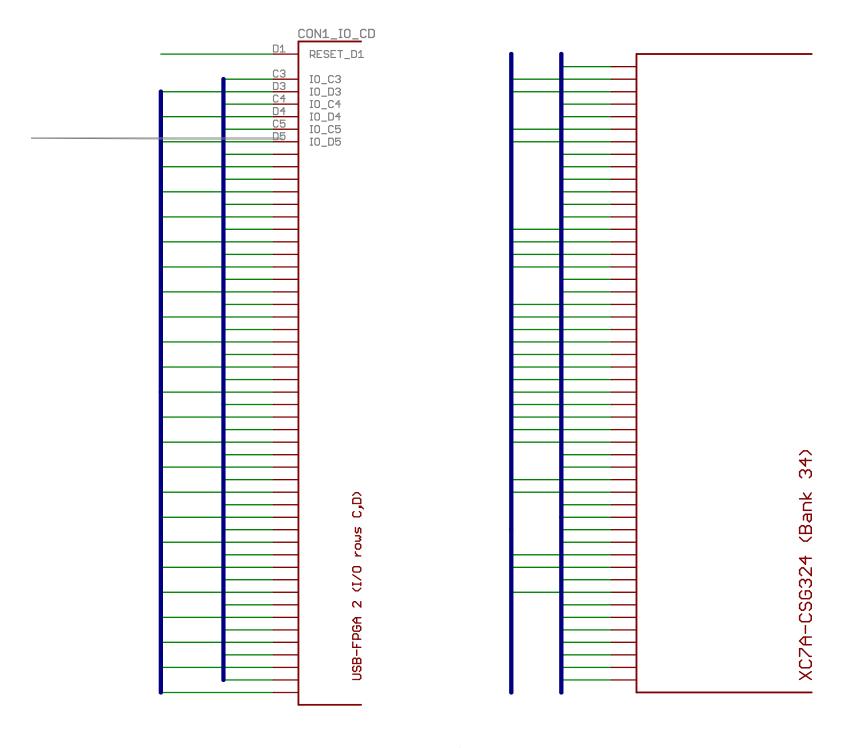








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Sheet 5 of 6

