

Logic Synthesis and Verification HW1

Programming ABC

Add file

- src/testC/module.make
- src/testC/testcmd.c
- src/testC/testC.c
- src/testC/testC.h

Change Code

- Makefile
- src/base/main/mainInit.c

Environment

- Ubuntu
- gcc (Ubuntu 7.5.0-3ubuntu1~18.04) 7.5.0
- g++ (Ubuntu 7.5.0-3ubuntu1~18.04) 7.5.0

Compile project

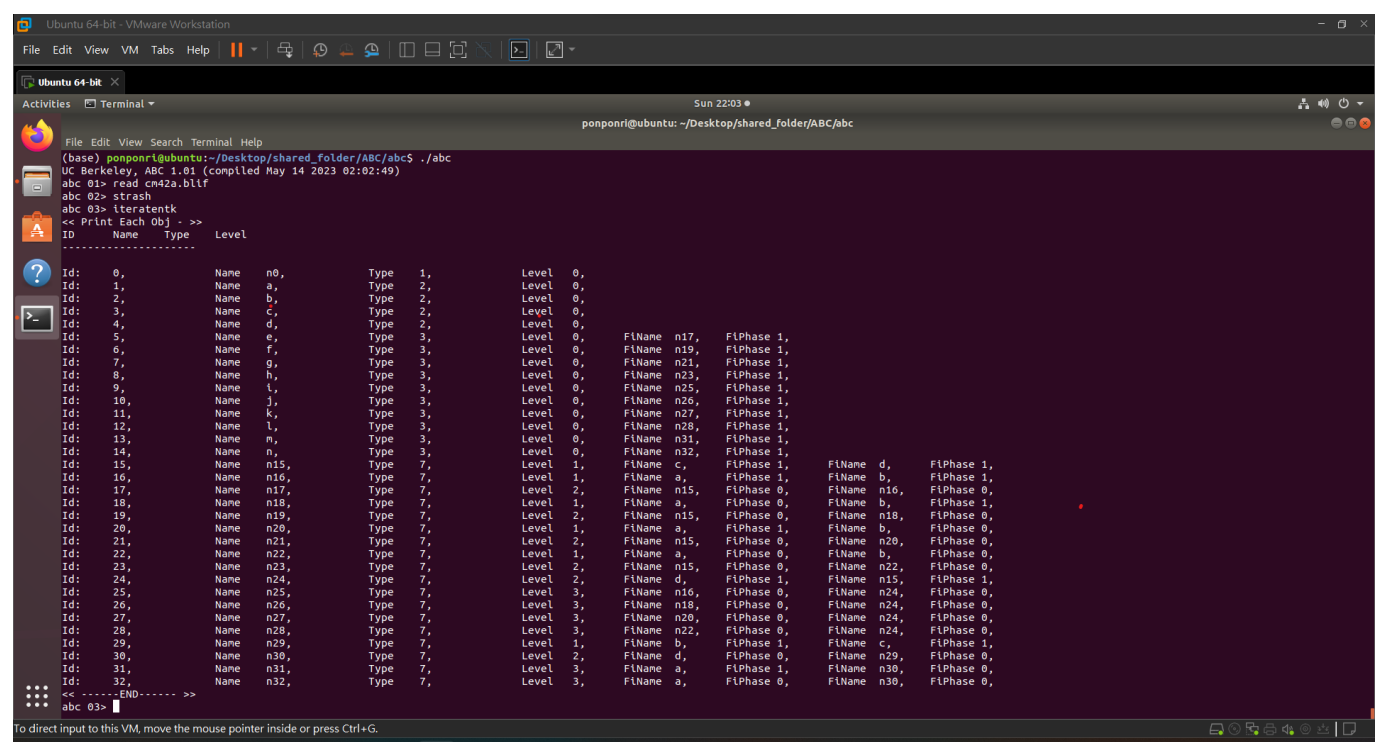
```
cd abc  
make
```

Results

Part 1

```
cd abc  
./abc  
read filename.blif  
strash  
iteratentk
```

example:



Part 2

```
cd abc
./abc
read filename.blif
strash
andtree [-S num] # Print leaveNum that num>=5 as default
```

example1:

```
(base) ponponri@ubuntu:~/Desktop/shared_folder/ABC/abc$ ./abc
UC Berkeley, ABC 1.01 (compiled May 14 2023 02:02:49)
abc 01> read cm42a.blif
abc 02> strash
abc 03> andtree

<< -----ANDTREE----- >>

<< -----END----- >>
abc 03> andtree -S 2

<< -----ANDTREE----- >>

2      n17
2      n19
2      n21
2      n22
2      n23
2      n25
2      n26
2      n27
2      n28
2      n30
2      n32
<< -----END----- >>
abc 03> █
```

example2:

```

(base) ponponri@ubuntu:~/Desktop/shared_folder/ABC/abc$ ./abc
UC Berkeley, ABC 1.01 (compiled May 14 2023 02:02:49)
abc 01> read C880.blif
abc 02> strash
abc 03> andtree

<< -----ANDTREE----- >>

6          n208      n209      n210      n211      n212
<< -----END----- >>
abc 03> andtree -S 4

<< -----ANDTREE----- >>

4          n112      n113      n114
4          n183      n184      n185
6          n208      n209      n210      n211      n212
<< -----END----- >>
abc 03> andtree -S 3

<< -----ANDTREE----- >>

3          n97       n98
3          n110      n111
4          n112      n113      n114
3          n172      n173
4          n183      n184      n185
3          n186      n187
6          n208      n209      n210      n211      n212
3          n214      n215
3          n256      n257
3          n277      n278
3          n296      n297
3          n304      n305
3          n360      n361
3          n375      n376
3          n395      n396
3          n412      n413
<< -----END----- >>
abc 03> █

```