

| QDFP64 pin | Name | v4.2.2 | v4.2.7 |
|------------|-----------------|----------------------------------|--|
| 1 | VBAT | | |
| 2 | PC13-TAMPER-RTC | | GPIO |
| 3 | PC14-OSC32_IN | | GPIO |
| 4 | PC15-OSC32_OUT | | GPIO |
| 5 | P05-OSC_IN | | |
| 6 | P01-OSC_OUT | | |
| 7 | HSET | | |
| 8 | PC0 | | GPIO |
| 9 | PC1 | | GPIO |
| 10 | PC2 | X_STEP_PIN | X_DIR_PIN From firmware disassembly |
| 11 | PC3 | (X Y Z)E0_ENABLE_PIN | (X Y Z)E0_ENABLE_PIN From firmware disassembly |
| 12 | VSSA | | |
| 13 | VDDA | | |
| 14 | PA0-WKUP | FAN_PIN | |
| 15 | PA1 | HEATER_0_PIN | |
| 16 | PA2 | HEATER_BED_PIN | |
| 17 | PA3 | | |
| 18 | VSS_4 | | |
| 19 | VDD_4 | | |
| 20 | PA4 | FIL_RUNOUT_PIN.ONBOARD_SD_CS_PIN | |
| 21 | PA5 | X_STOP_PIN | |
| 22 | PA6 | Y_STOP_PIN | |
| 23 | PA7 | Z_STOP_PIN | |
| 24 | PC4 | TEMP_BED_PIN | |
| 25 | PC5 | TEMP_0_PIN | |
| 26 | PB0 | SERVO0_PIN | |
| 27 | PB1 | Z_PROBE_PIN | |
| 28 | PB2 | BTN_ENC | From users compiling marlin |
| 29 | PB10 | BTN_EN1 | From users compiling marlin |
| 30 | PB11 | | |
| 31 | VSS_1 | | |
| 32 | VDD_1 | | |
| 33 | PB12 | LCD_PINS_RS | LCD_PINS_RS From users compiling marlin |
| 34 | PB13 | LCD_PINS_D4 | LCD_PINS_D4 From users compiling marlin |
| 35 | PB14 | BTN_EN2 | BTN_EN2 From users compiling marlin |
| 36 | PB15 | LCD_PINS_ENABLE | LCD_PINS_ENABLE From users compiling marlin |
| 37 | PC6 | BEEPER_PIN | |
| 38 | PC7 | SD_DETECT_PIN | |
| 39 | PC8 | | SDCard D0 |
| 40 | PC9 | | SDCard D1 |
| 41 | PA8 | | |
| 42 | PA9 | USART1_TX | USART1_TX From users compiling marlin |
| 43 | PA10 | USART1_RX | USART1_RX From users compiling marlin |
| 44 | PA11 | IIC_EEPROM_SDA | |
| 45 | PA12 | IIC_EEPROM_SCL | |
| 46 | PA13 | | SWDIO |
| 47 | VSS_3 | | |
| 48 | VDD_2 | | |
| 49 | PA14 | | SWCLK |
| 50 | PA15 | | |
| 51 | PC10 | | SDCard D2 |
| 52 | PC11 | | SDCard D3 |
| 53 | PC12 | | SDCard CE |
| 54 | PD2 | | SDCard CMD |
| 55 | PB3 | E0_DIR_PIN | E0_STEP_PIN From firmware disassembly |
| 56 | PB4-NTRST | E0_STEP_PIN | E0_DIR_PIN From firmware disassembly |
| 57 | PB5 | Z_DIR_PIN | Z_STEP_PIN From firmware disassembly |
| 58 | PB6 | Z_STEP_PIN | Z_DIR_PIN From firmware disassembly |
| 59 | PB7 | Y_DIR_PIN | Y_STEP_PIN From firmware disassembly |
| 60 | NODT0 | | |
| 61 | PB8 | Y_STEP_PIN | Y_DIR_PIN From firmware disassembly |
| 62 | PB9 | X_DIR_PIN | X_STEP_PIN From firmware disassembly |
| 63 | VSS_3 | | |
| 64 | VDD_3 | | |

Key

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