1. In the circuit shown, *A* and *B* are the inputs and *F* is the output. What is the functionality of the circuit? (GATE-EC2019,15)
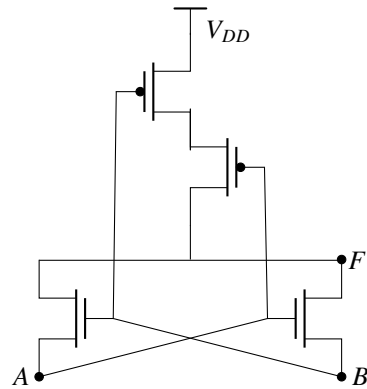

Figure 1: Circuit Diagram

1. Latch

2. XNOR

3. SRAM Cell

4. XOR