

WIEN BRIDGE OSCILLATOR

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Abstract—This project is about the study of Wien bridge oscillator. The main objective of this project is to analyze the Wien bridge oscillator using OpAmp.

I. INTRODUCTION

An oscillator is a circuit that produces periodic electrical signals such as sine wave or square wave without any input. Oscillators basically convert unidirectional current flow from a DC source into an alternating waveform which is of the desired frequency, as decided by its circuit components.

An oscillator consists of an amplifier (active device i.e., Opamp) and a feedback network (passive components such as RC or LC combinations). To start the oscillation with constant amplitude, the oscillator must satisfy

Barkhausen conditions:

- Magnitude of the loop gain ($A_v\beta$) = 1
- Phase shift around the loop must be 360 or 0 degrees.

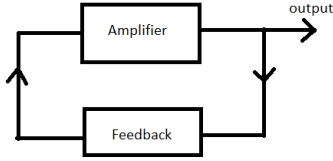


Fig. 1. Basic oscillator block diagram

Wien bridge oscillator: One of the simplest sine wave oscillators which uses an RC network is the Wien bridge oscillator. This is a two-stage RC coupled amplifier circuit that has good stability at its resonant frequency. The feedback signal in this circuit is connected to the non-inverting input terminal so that the op-amp is working as a non-inverting amplifier. The Wien bridge oscillator uses a feedback circuit consisting of a series RC circuit connected with a parallel RC circuit of the same component values, producing a phase delay or phase advance circuit depending on the frequency. The Wien Bridge Oscillator is so called because the circuit is based on a frequency-selective form of the Wheatstone bridge circuit.

II. MATHEMATICAL ANALYSIS

Considering the feedback shown in Fig. 2, on applying the voltage divider rule

$$V_f(s) = \frac{V_o(s) * Z_p(s)}{Z_p(s) + Z_s(s)} \quad (1)$$

$$\text{where } Z_s(s) = R_1 + \frac{1}{sC_1} \quad Z_p(s) = R_2 \parallel \frac{1}{sC_2} \quad (2)$$

$$\text{Let } R_1 = R_2 = R \quad C_1 = C_2 = C \quad (3)$$

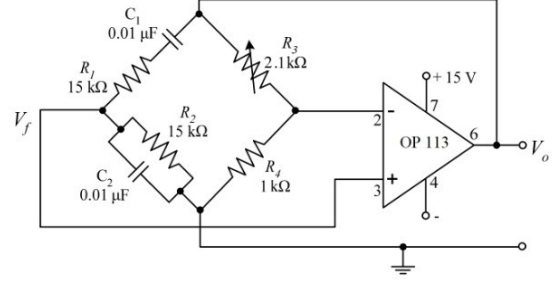


Fig. 2. Wien bridge oscillator

$$\text{feedback gain, } \beta = \frac{R_s C}{(R_s C)^2 + 3R_s C + 1} \quad (4)$$

$$\text{gain, } A_v = \frac{V_o(s)}{V_f(s)} = 1 + \frac{R_3}{R_4} \quad (5)$$

Applying conditions $A_v\beta = 1$ and substituting $s = j\omega$.

$$j\omega \left[\left(1 + \frac{R_3}{R_4} \right) RC - 3RC \right] = 1 - R_2 C^2 \omega^2 \quad (6)$$

To obtain the frequency of oscillation, equate the real part to zero

$$f = \frac{1}{2\pi RC} \quad (7)$$

To obtain the condition for gain at the frequency of oscillation, equate the imaginary part to zero

$$j\omega \left[\left(1 + \frac{R_3}{R_4} \right) RC - 3RC \right] = 0 \quad (8)$$

$$\frac{R_3}{R_4} = 2 \quad (9)$$

Therefore, $R_3 = 2R_4$ is the required condition.

III. CALCULATION

Frequency of Oscillation,

$$f = \frac{1}{2\pi \sqrt{R_1 C_1 R_2 C_2}} \quad (10)$$

$$R_1 = R_2 = R \text{ and } C_1 = C_2 = C \quad (11)$$

$$f = \frac{1}{2\pi RC} \quad (12)$$

Consider $f = 1\text{kHz}$ and $C = 0.01\mu\text{F}$ we get $R = 15.9\text{k}\Omega$ and consider $R_4 = 1\text{k}\Omega$ then $R_3 = 2\text{k}\Omega$ (considering $2.1\text{k}\Omega$)

IV. RESULTS

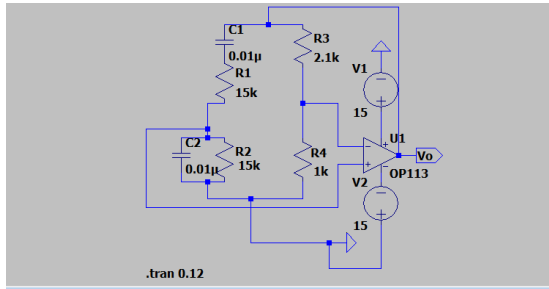


Fig. 3. Wien bridge oscillator circuit

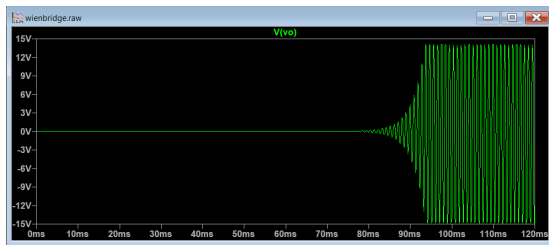


Fig. 4. output graph(voltage vs time)

A Wien bridge oscillator was designed and setup for a frequency of 1kHz and the output waveform is as shown in fig4. Hence, we can observe the sinusoidal output without any input.